



Statement of Volatility – Dell PowerEdge M520 Systems

The Dell PowerEdge M520 system contains both volatile and non-volatile (NV) components. Volatile components lose their data immediately after power is removed from the component. Non-volatile (NV) components continue to retain their data even after power is removed from the component. The following NV components are present on the M520 system.

Item	Reference Designator	Volatility Description	User Accessible for external data	Remedial Action (Action necessary to prevent loss of data)
System BIOS	U_SPI_BIOS	Boot code, system configuration information, UEFI environment, Flash Descriptor, ME.	No	N/A
BMC SPI	U_IDRAC_SPI	iDRAC boot code, and Planar FRU.	No	N/A
eMMC	U_EMMC	iDRAC FW and USC.	No	N/A
SD Controller	On Management Riser Card: U5	Management Riser card monitor and control chip.	No	N/A
Server Complementary Metal-Oxide Semiconductor (CMOS) Memory	U_PBGA	BIOS configurations.	Yes	N/A
BMC Memory	U_IDRAC7_MEM	Graphics buffer and memory of iDRAC	Yes	Data is lost when AC off.
Server RAM	Total 12 DIMMs CPU1: DIMMA1~2 DIMMB1~2 DIMMC1~2 CPU2: DIMME1~2 DIMMF1~2 DIMMG1~2	Main System Memory.	Yes	Data is lost when system is powered down.
Server Processor Memory	J_CPU1 J_CPU2	Processor cache.	Yes	Data is lost when system is powered down.
Trusted Platform Module (TPM)	U TPM	Store firmware for TPM functionality.	No	N/A

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Backplane SEP	On SATA BP: J_SEP_1 On SAS BP: J_SEP_1	Backplane monitor and control chip.	No	No