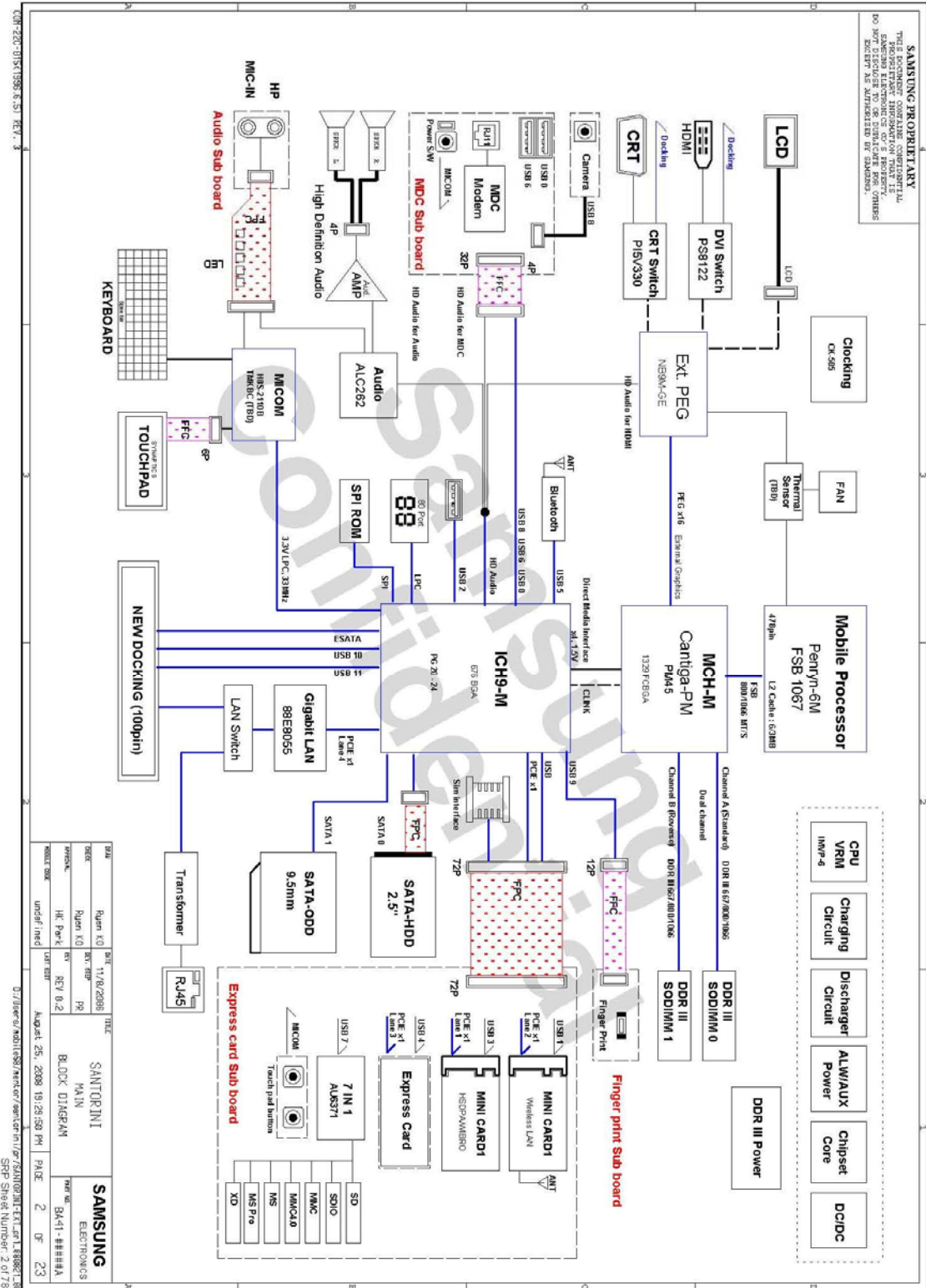


## 8. Block Diagram and Schematic

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<h1>SANTORINI</h1>																																					
CPU	: Intel Penryn-6M (1067/800)MHz																																				
Chip Set	: Intel Cantiga & ICH9M																																				
Remarks	: Montevina Platform																																				
<p>Model Name : SANTORINI - EXT                  PBA Name : MAIN                  PCB Code : BA41-00944A (CNY)                  Dev. Step : PR                  Revision : 1.1                  T.R. Date : 2008.08.28</p>																																					
Design	CHECK APPROVAL																																				
Ryan	Ryan HK Park																																				
<p>Owner : SEC Mobile R &amp; D Signature : X</p>																																					
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<p>Sub1 : MDC Sub board                  Sub2 : Audio Sub board</p>																																					
<table border="1"> <tr> <td>DESIGNER</td> <td>Ryan KO</td> <td>DATE</td> <td>11/9/2005</td> </tr> <tr> <td>CHECKER</td> <td>Ryan KO</td> <td>REV</td> <td>0.2</td> </tr> <tr> <td>APPROVER</td> <td>HK Park</td> <td>REV</td> <td>0.2</td> </tr> <tr> <td>DATE</td> <td>11/9/2005</td> <td>DATE</td> <td>August 25, 2008 19:29:59 PM</td> </tr> </table>	DESIGNER	Ryan KO	DATE	11/9/2005	CHECKER	Ryan KO	REV	0.2	APPROVER	HK Park	REV	0.2	DATE	11/9/2005	DATE	August 25, 2008 19:29:59 PM	<table border="1"> <tr> <td>PROJECT</td> <td>SANTORINI</td> <td>DATE</td> <td>11/9/2005</td> </tr> <tr> <td>DESIGNER</td> <td>Ryan KO</td> <td>DATE</td> <td>11/9/2005</td> </tr> <tr> <td>CHECKER</td> <td>Ryan KO</td> <td>DATE</td> <td>11/9/2005</td> </tr> <tr> <td>APPROVER</td> <td>HK Park</td> <td>DATE</td> <td>11/9/2005</td> </tr> <tr> <td>DATE</td> <td>11/9/2005</td> <td>DATE</td> <td>August 25, 2008 19:29:59 PM</td> </tr> </table>	PROJECT	SANTORINI	DATE	11/9/2005	DESIGNER	Ryan KO	DATE	11/9/2005	CHECKER	Ryan KO	DATE	11/9/2005	APPROVER	HK Park	DATE	11/9/2005	DATE	11/9/2005	DATE	August 25, 2008 19:29:59 PM
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<p>SRP Sheet Number: 1 of 78</p>																																					

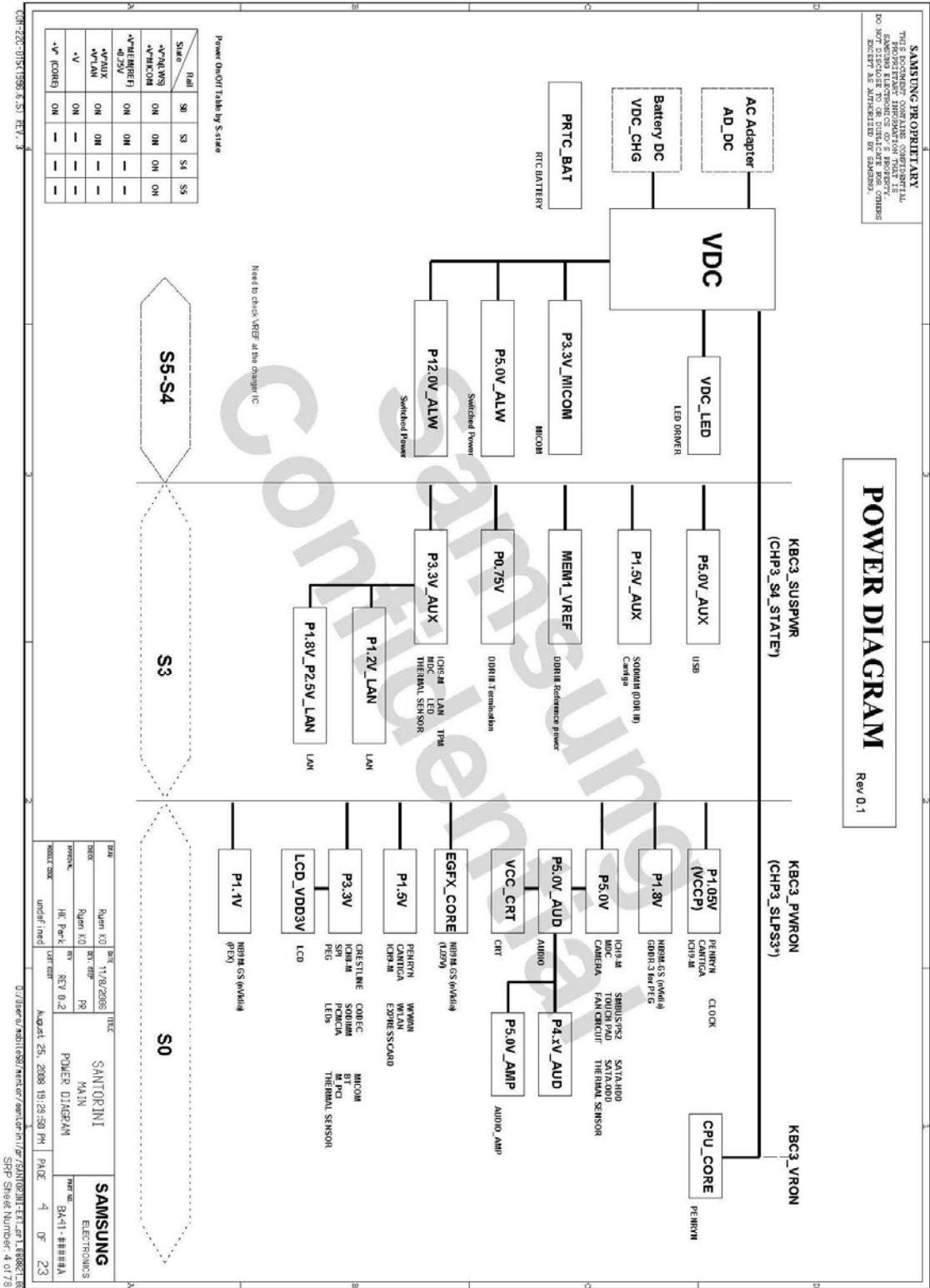
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## 8. Block Diagram and Schematic



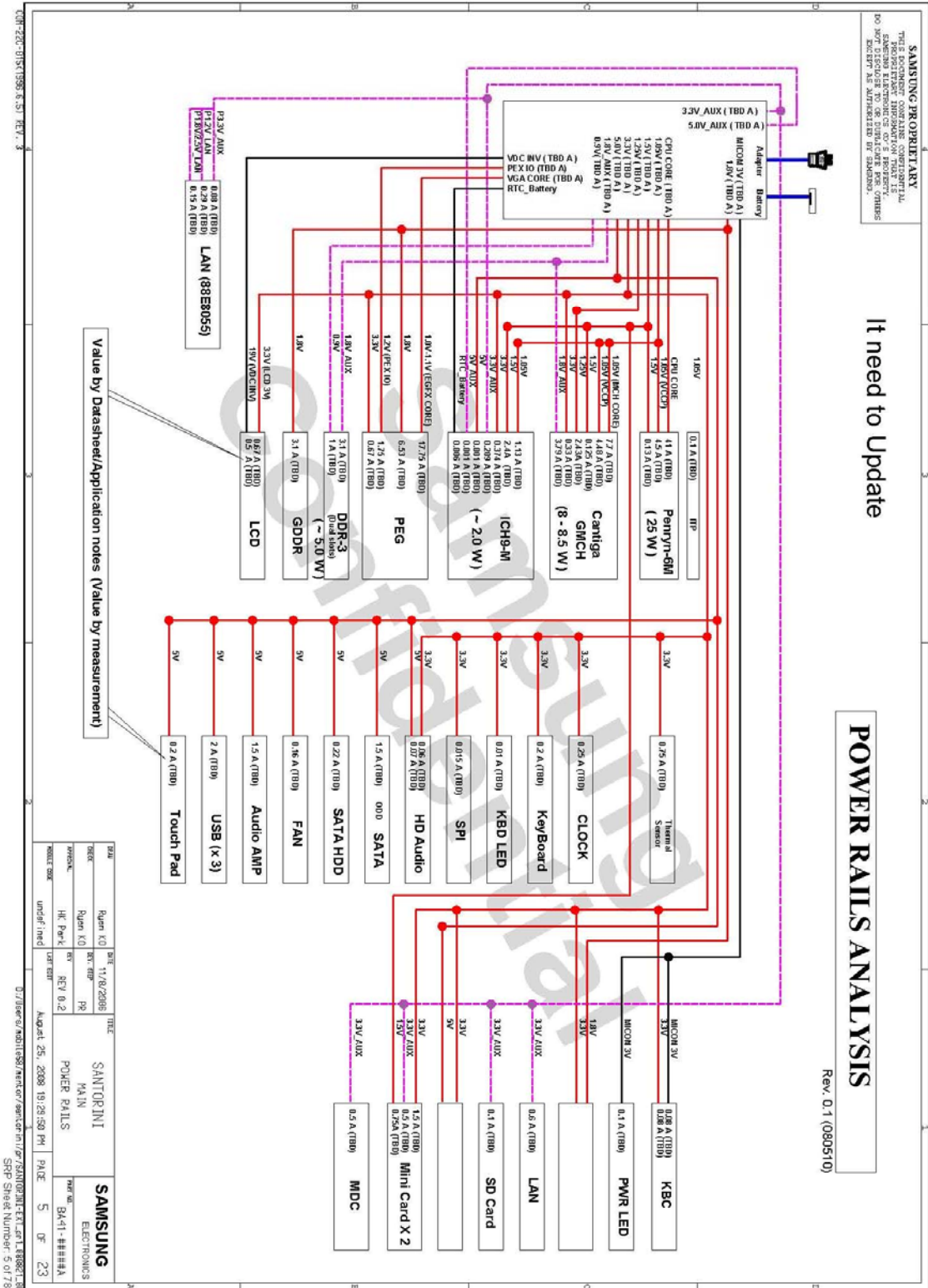


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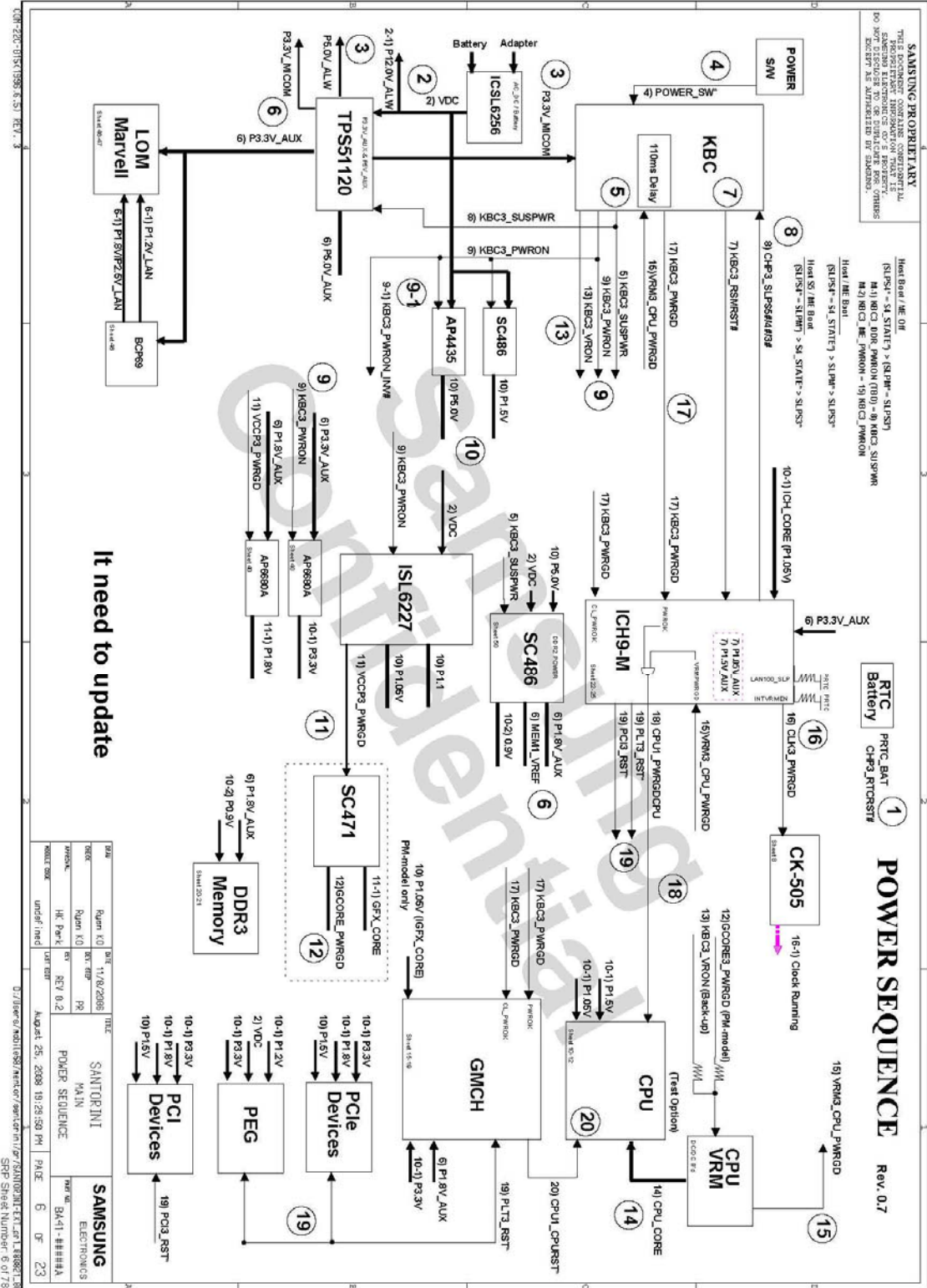
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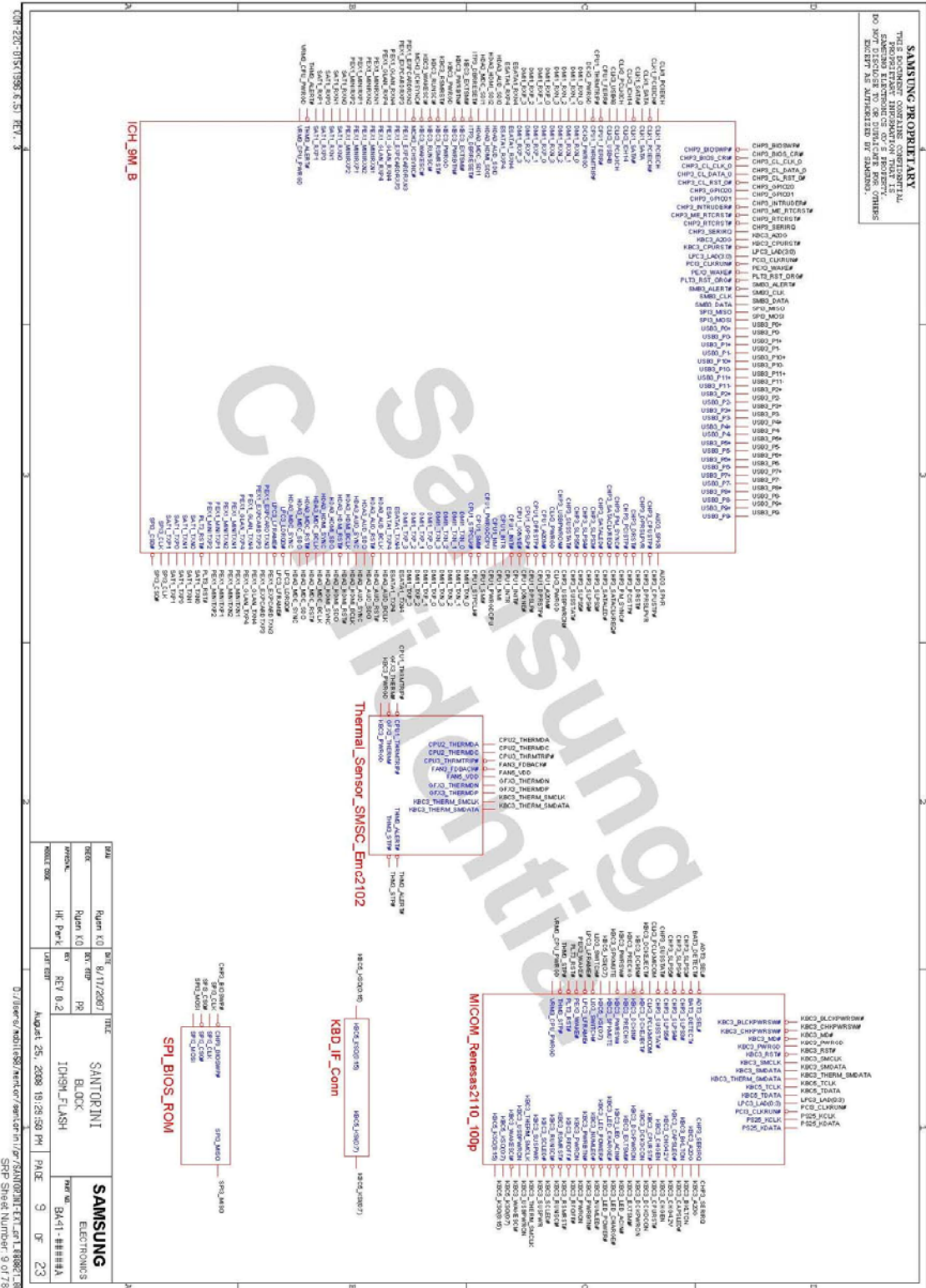






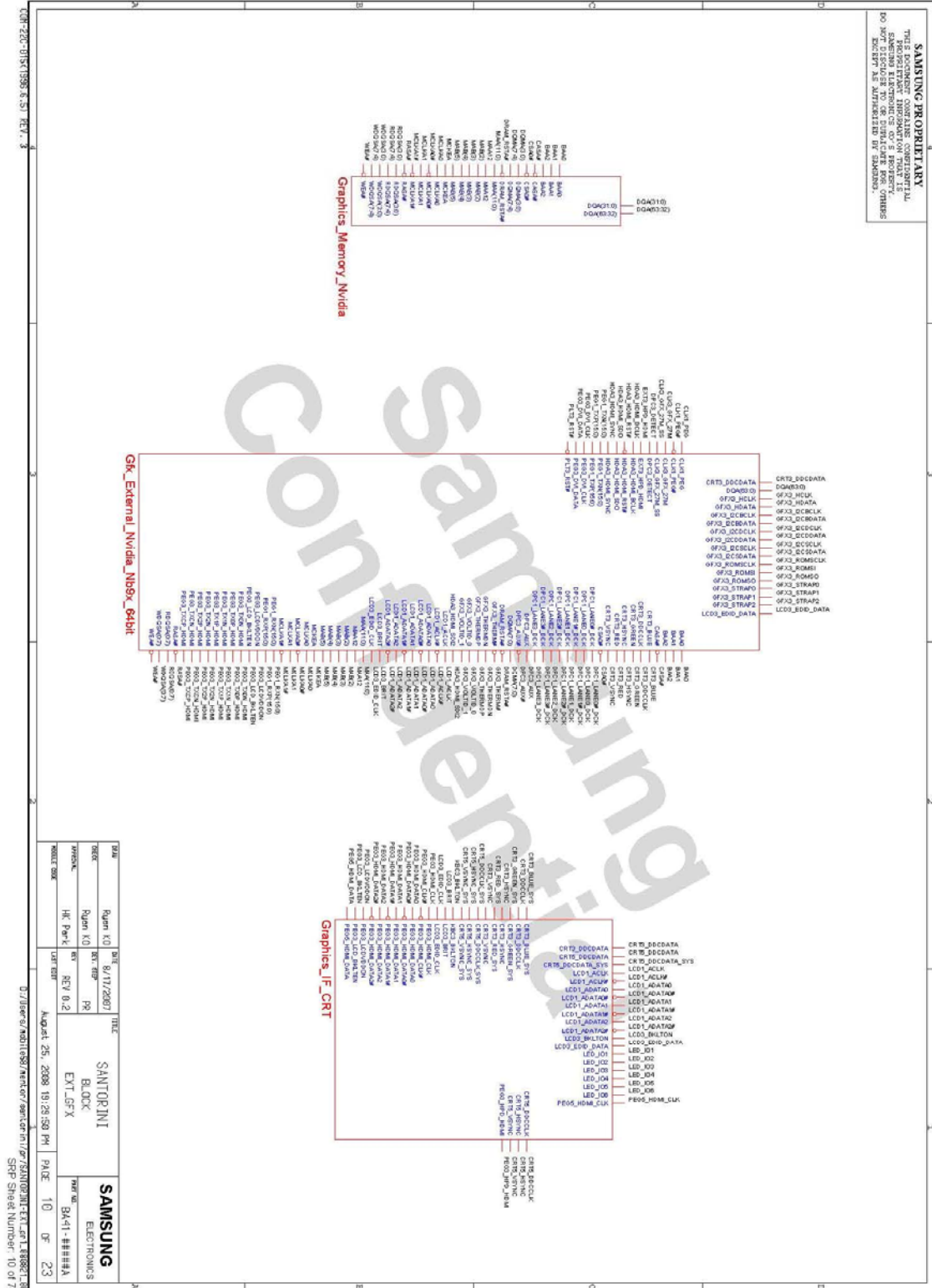
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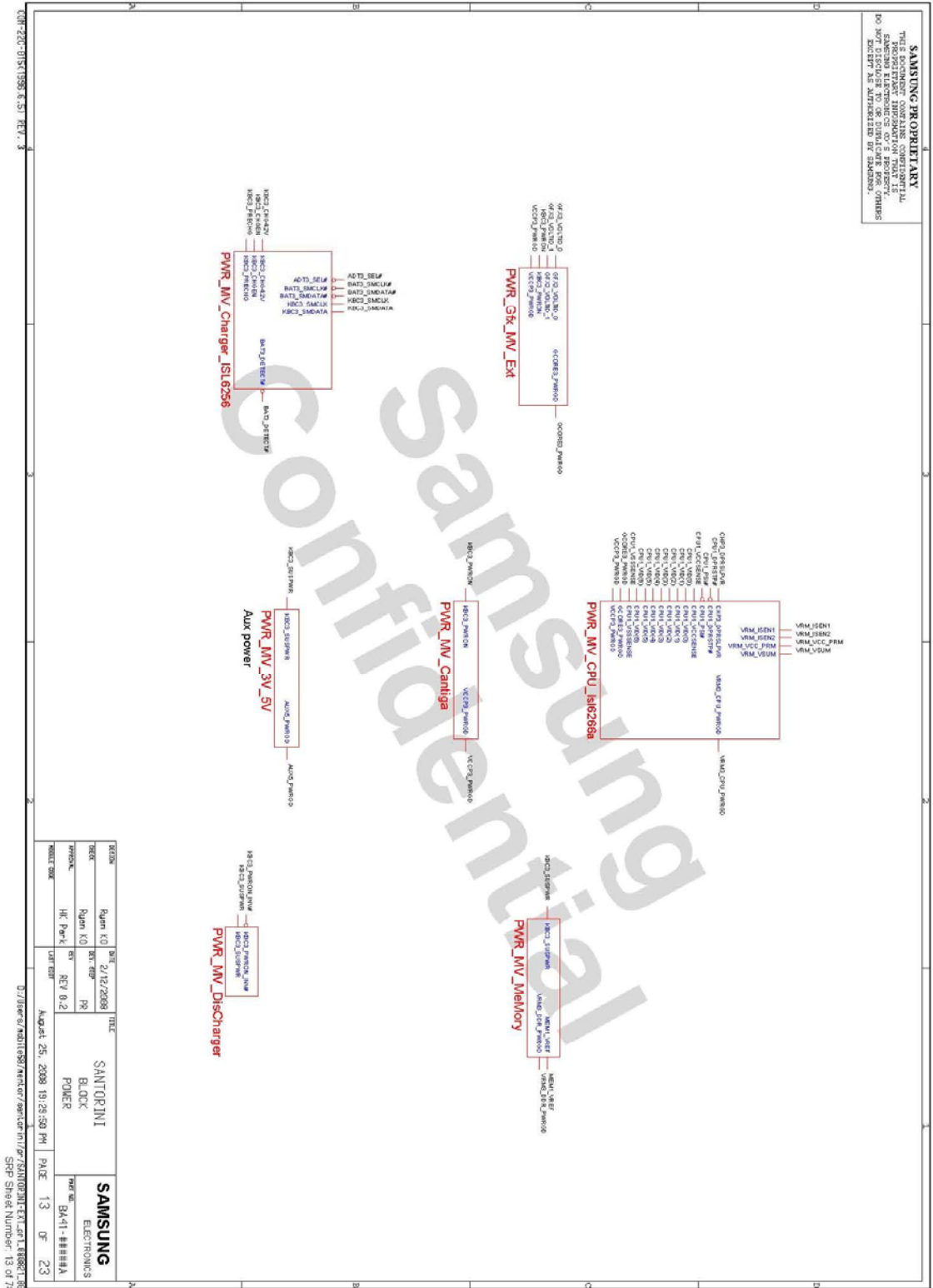






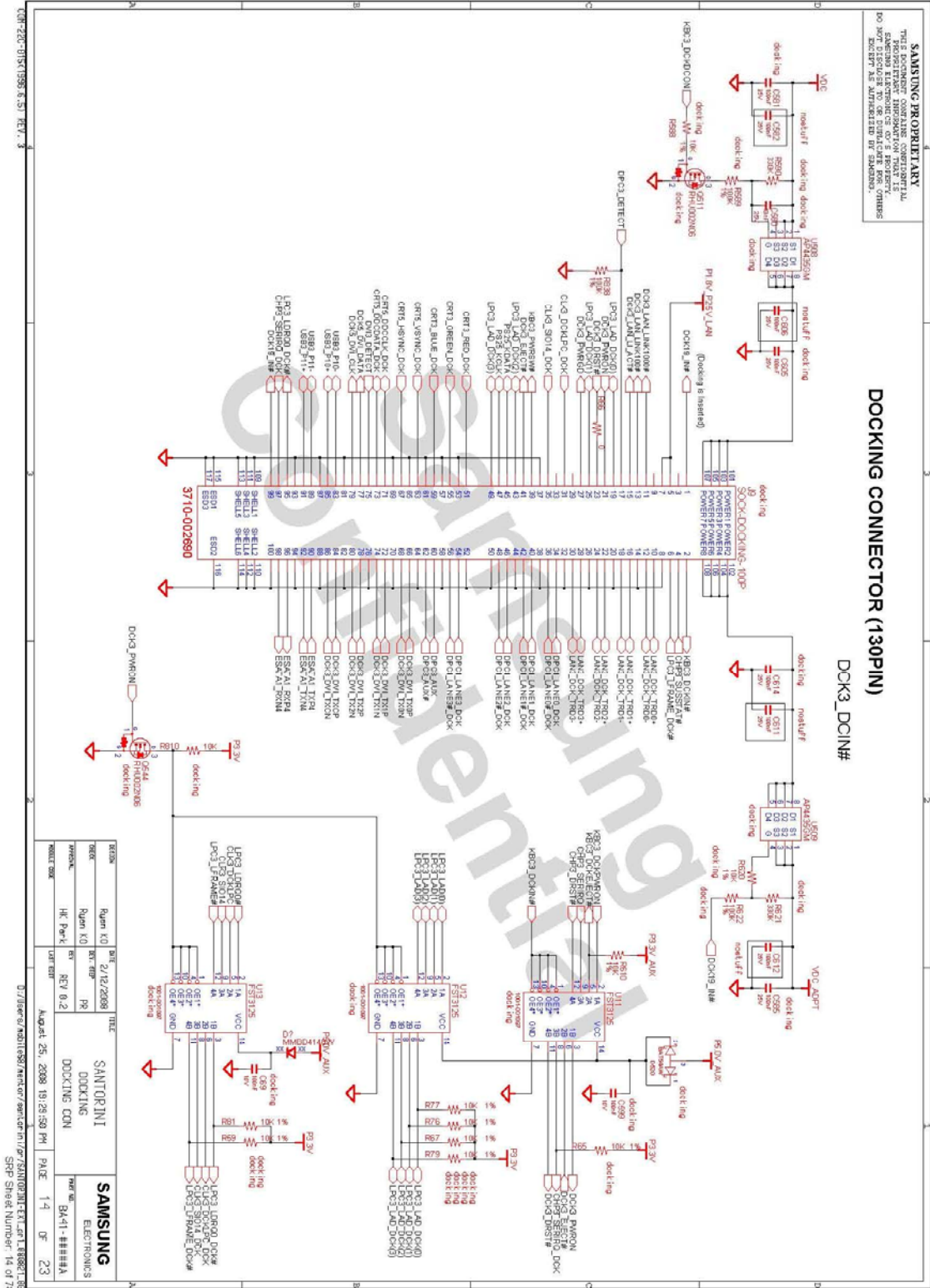
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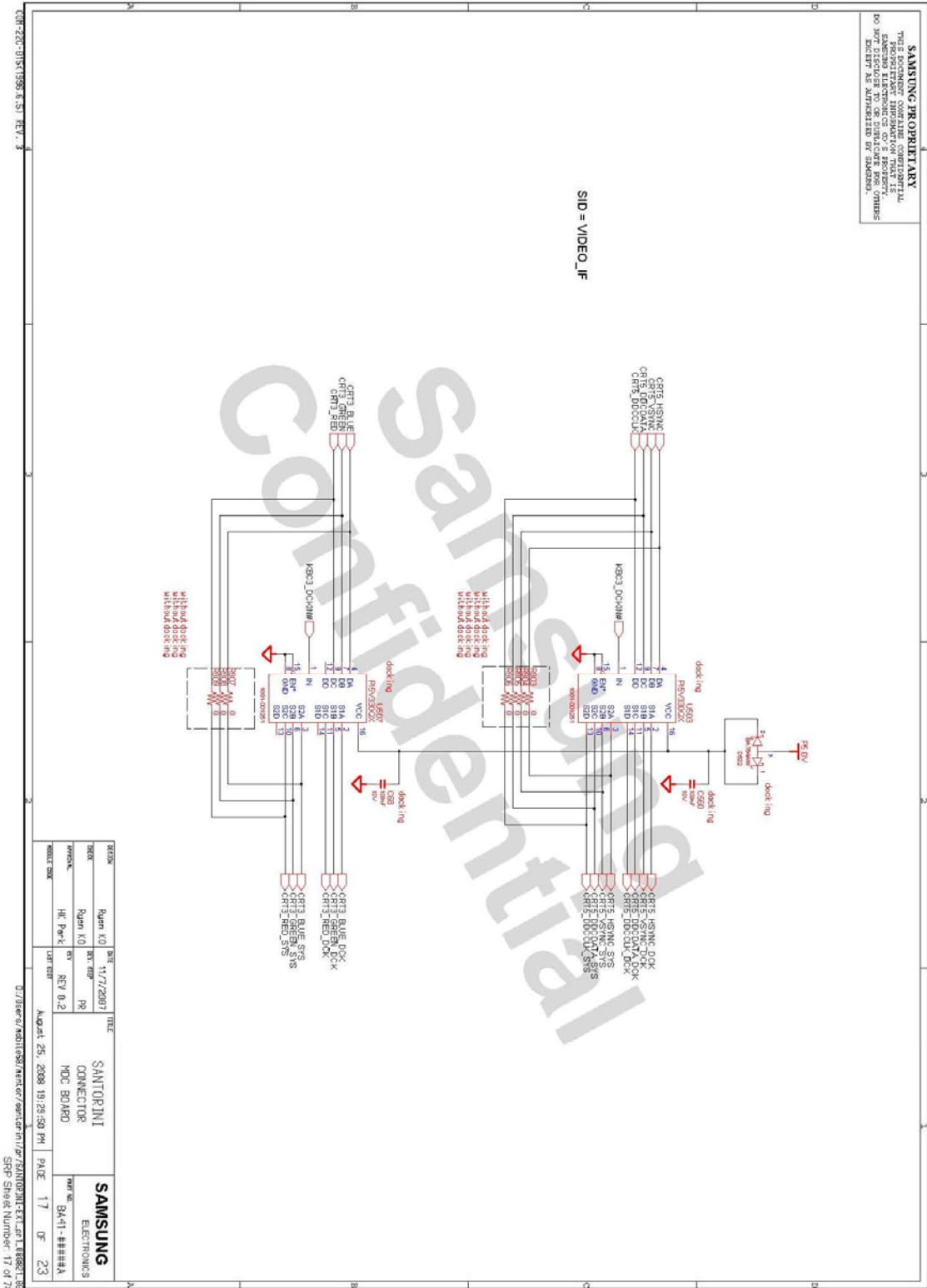






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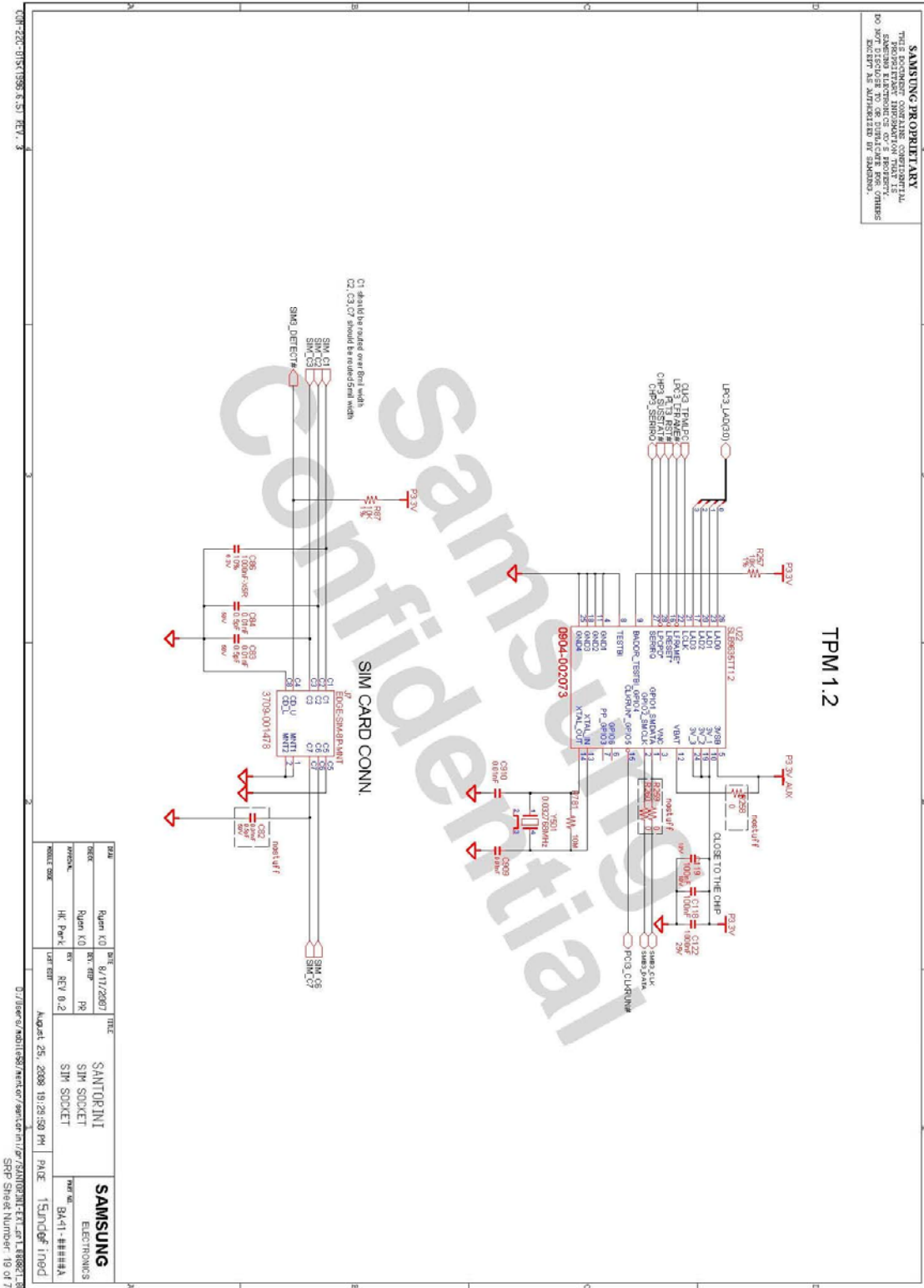
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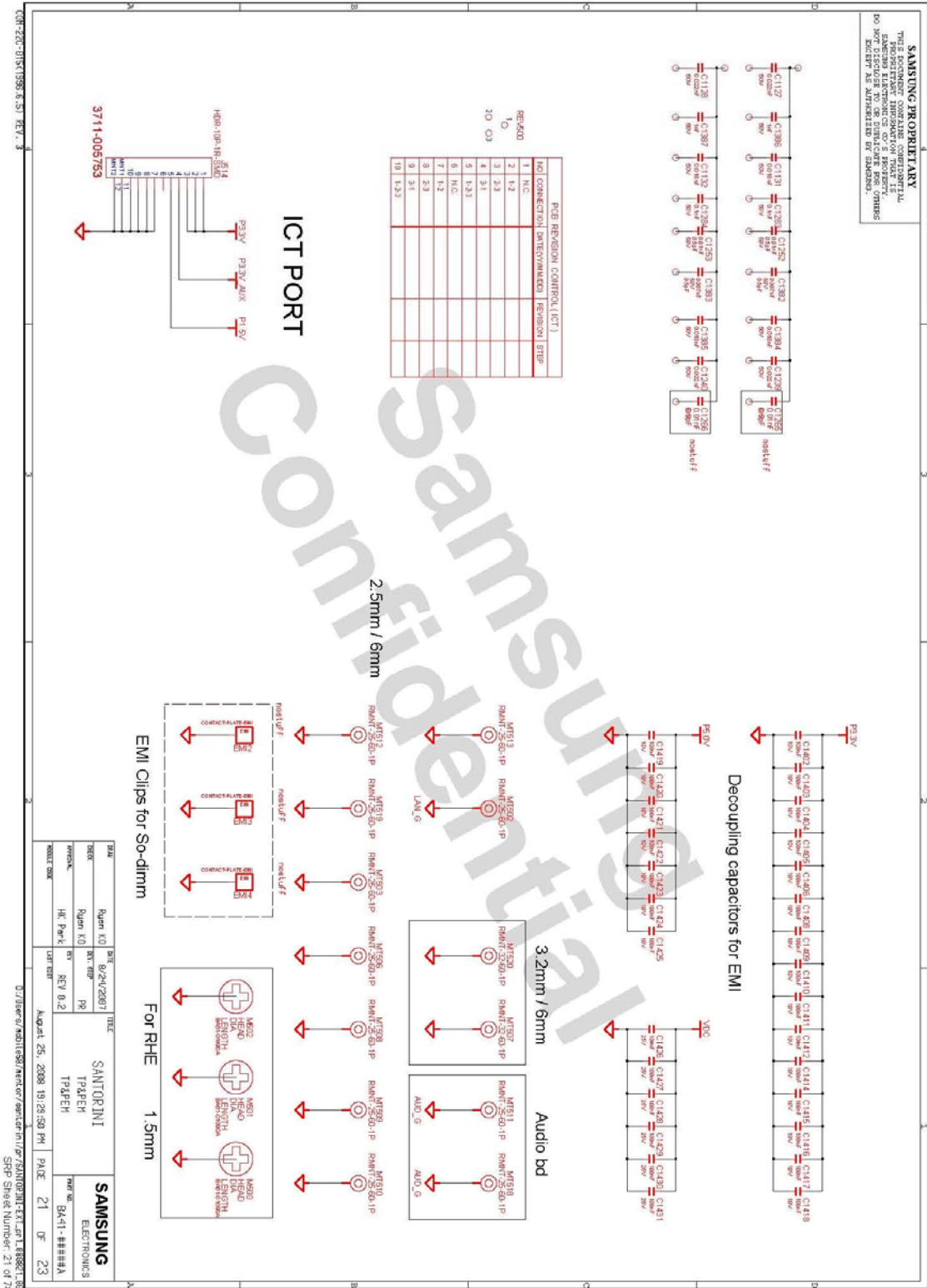
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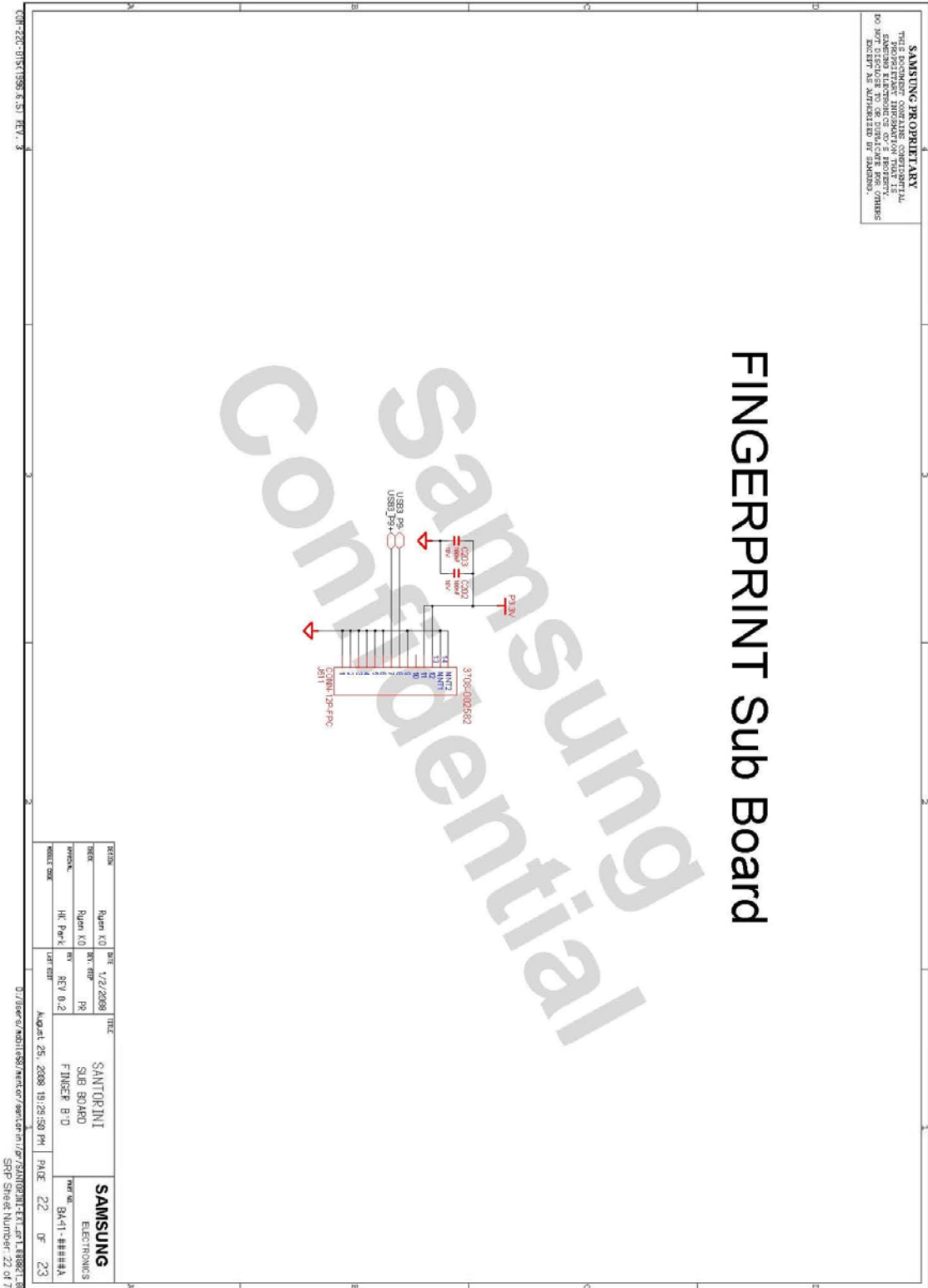
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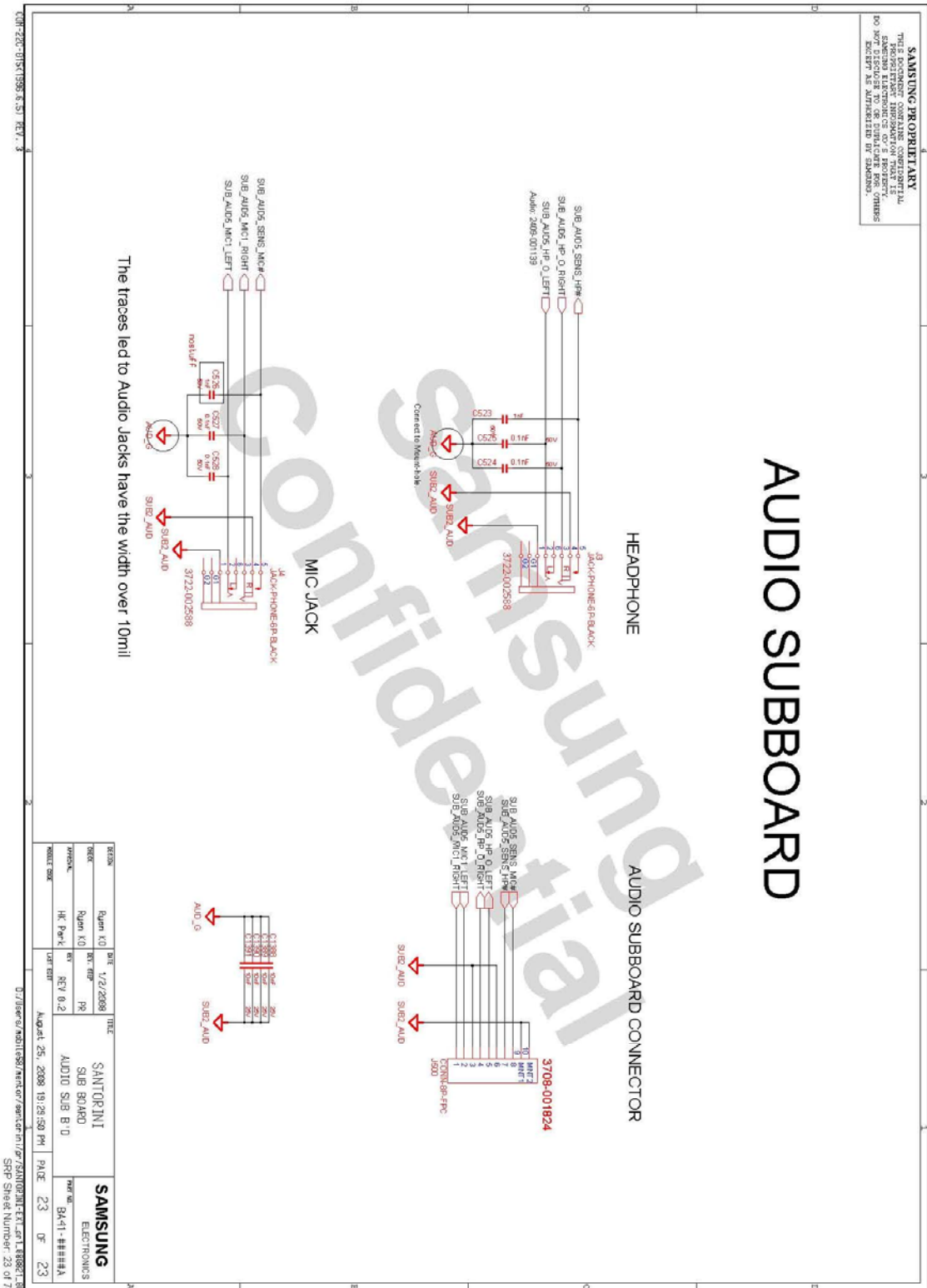
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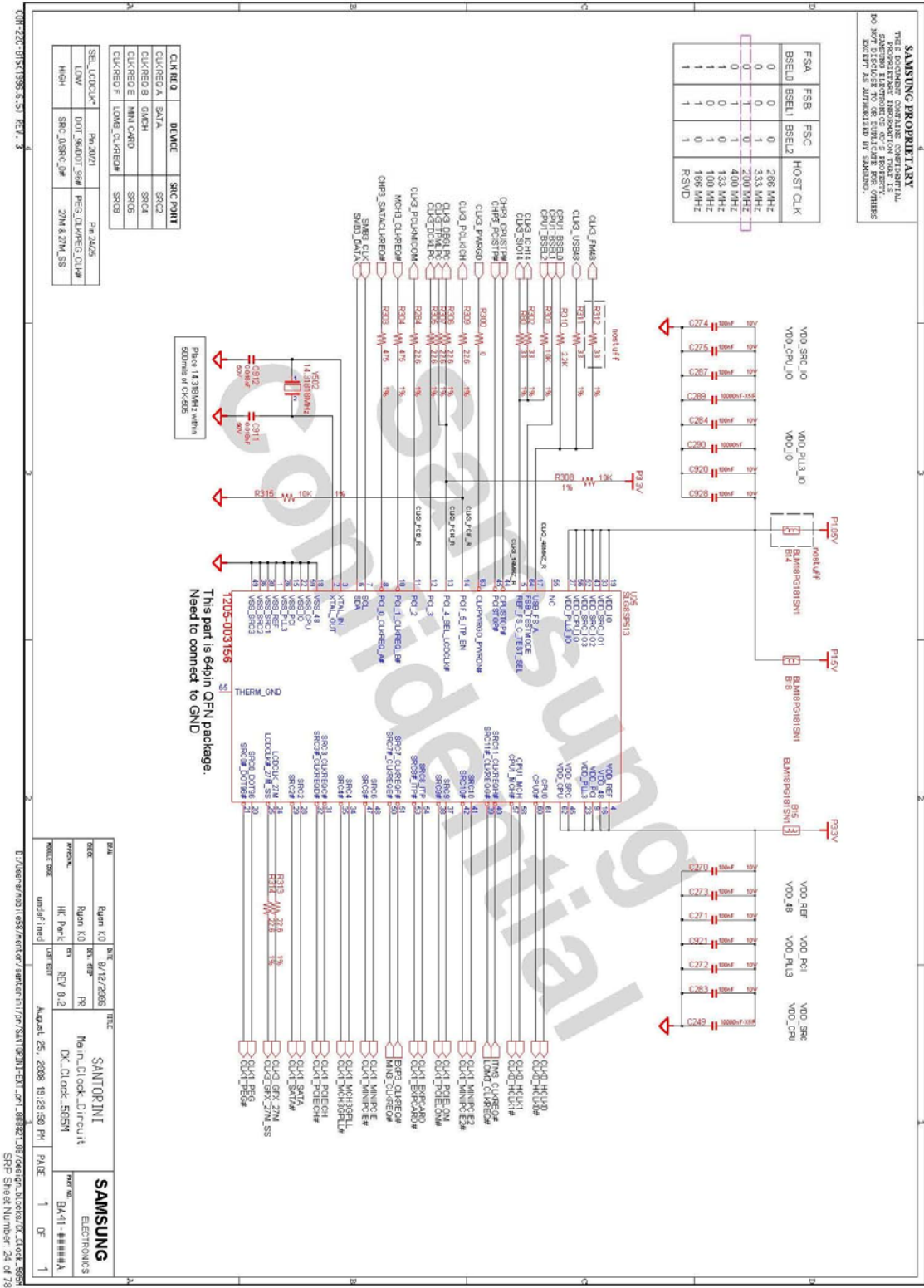
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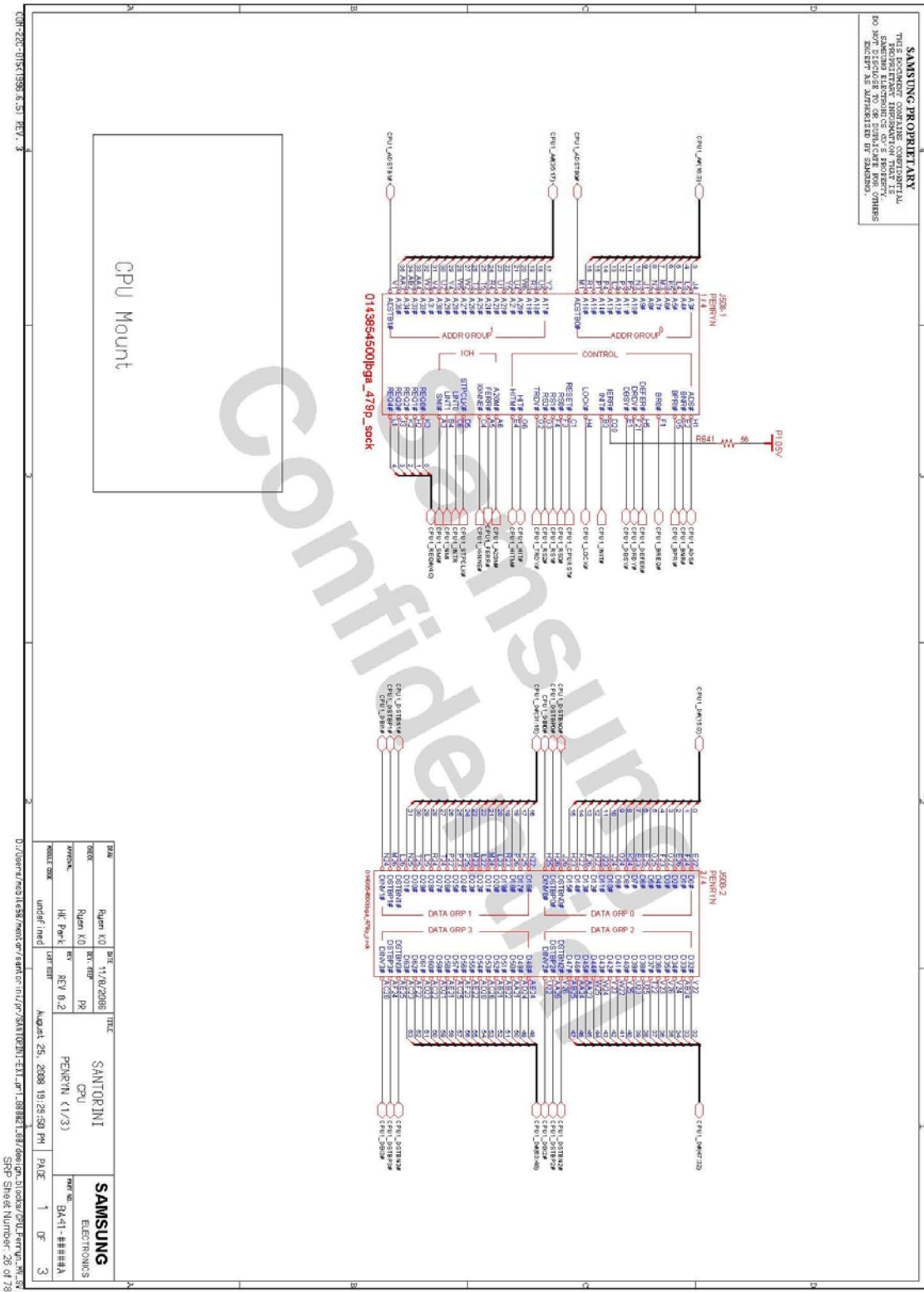






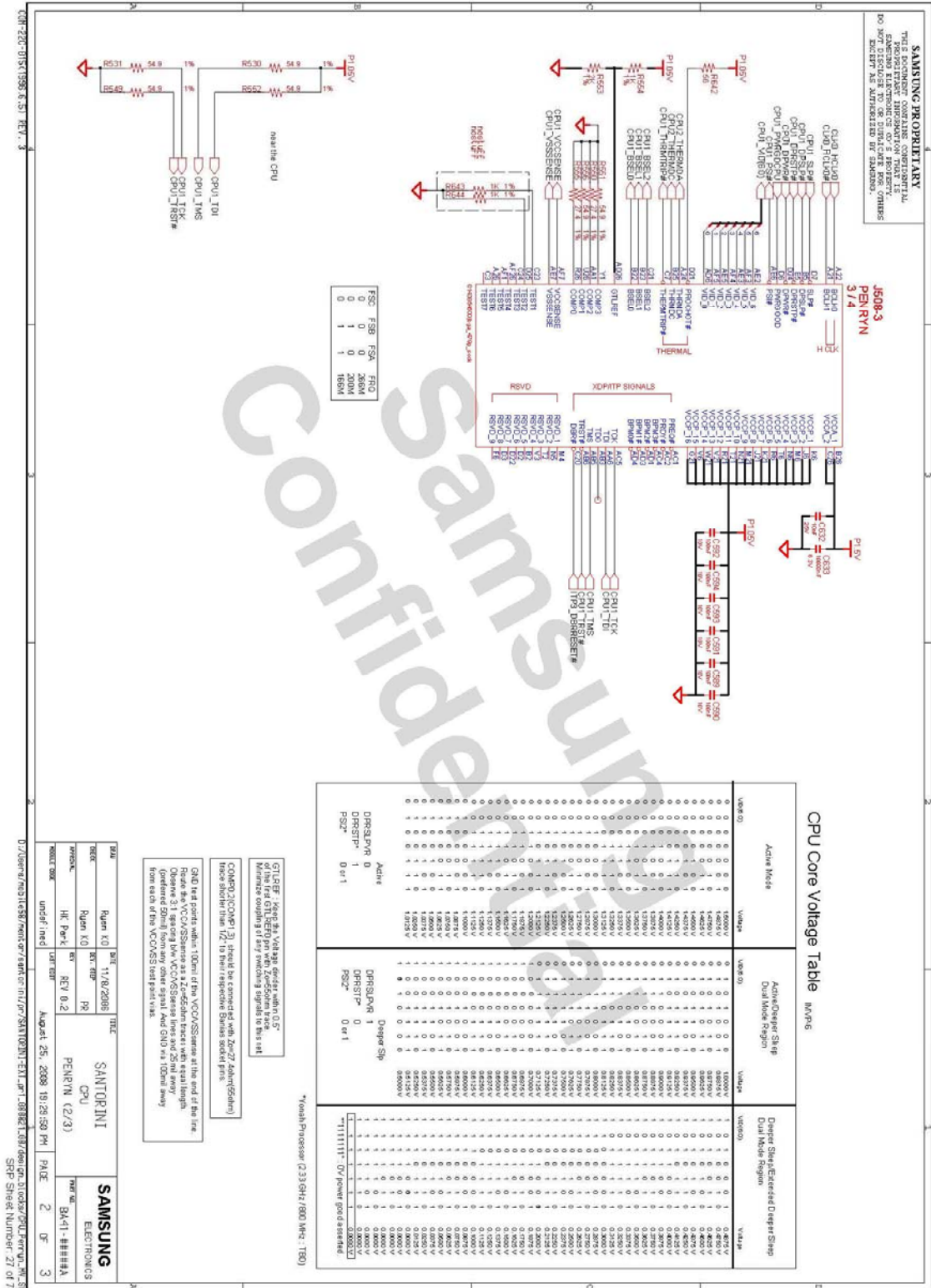
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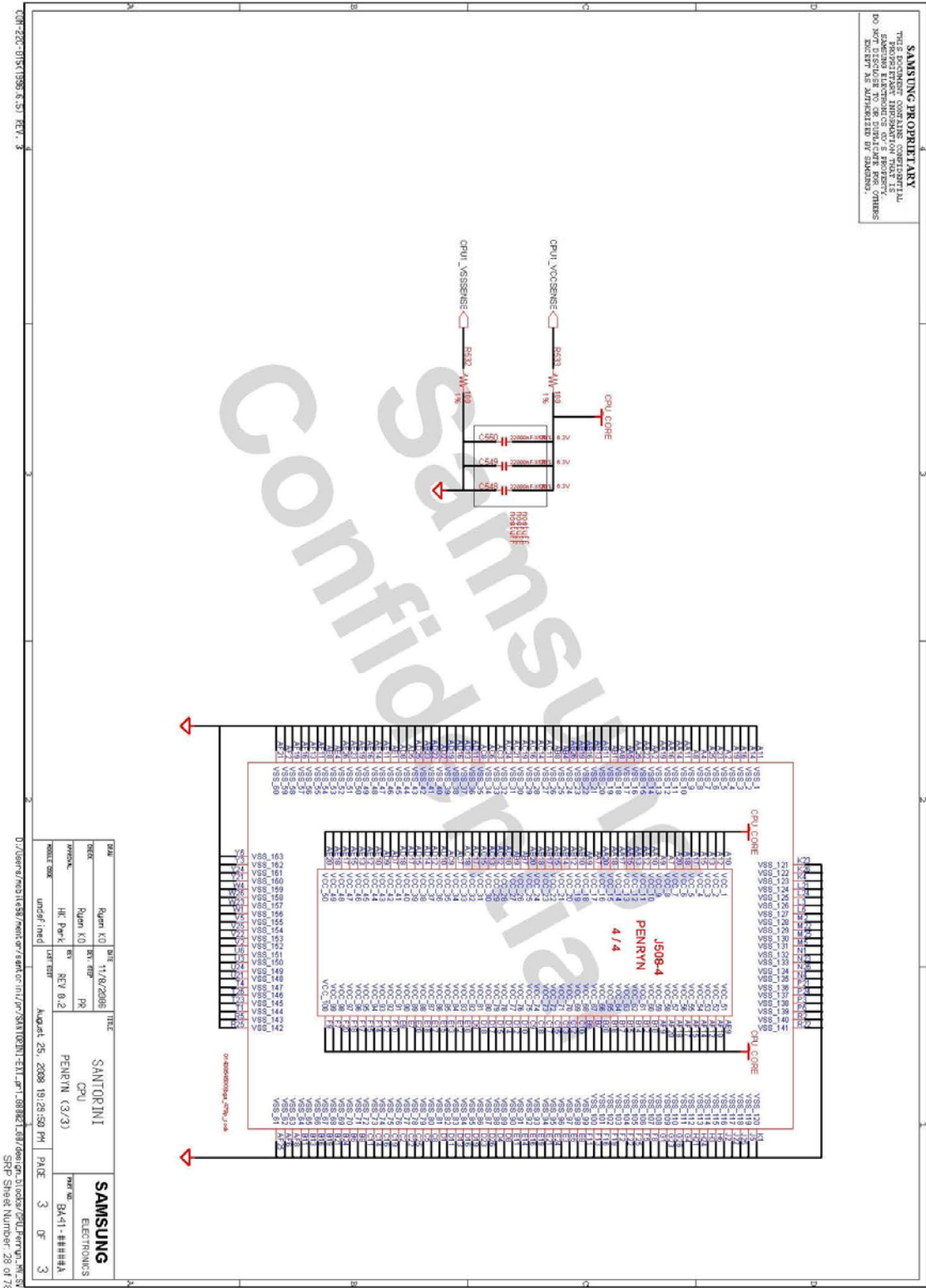
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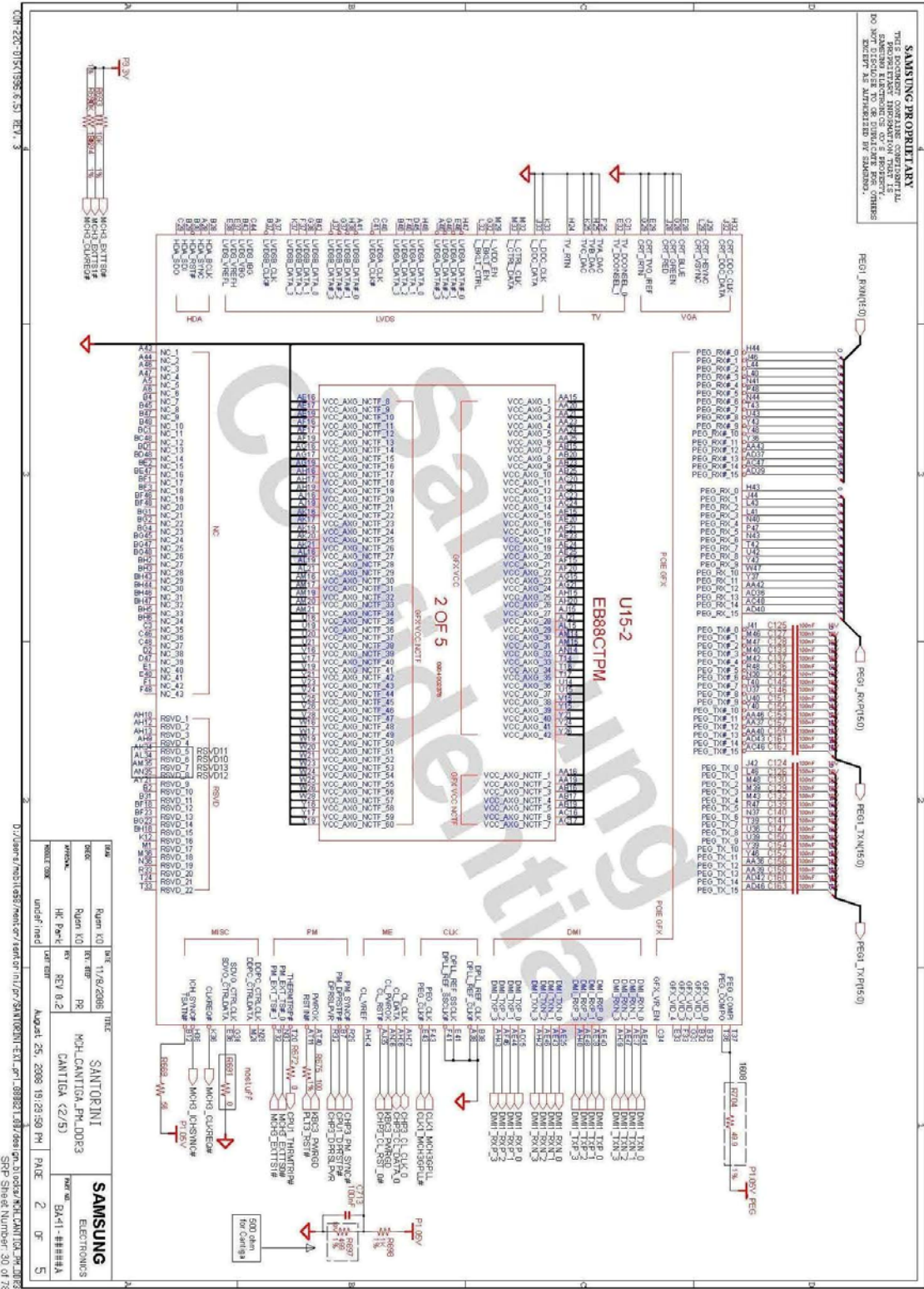
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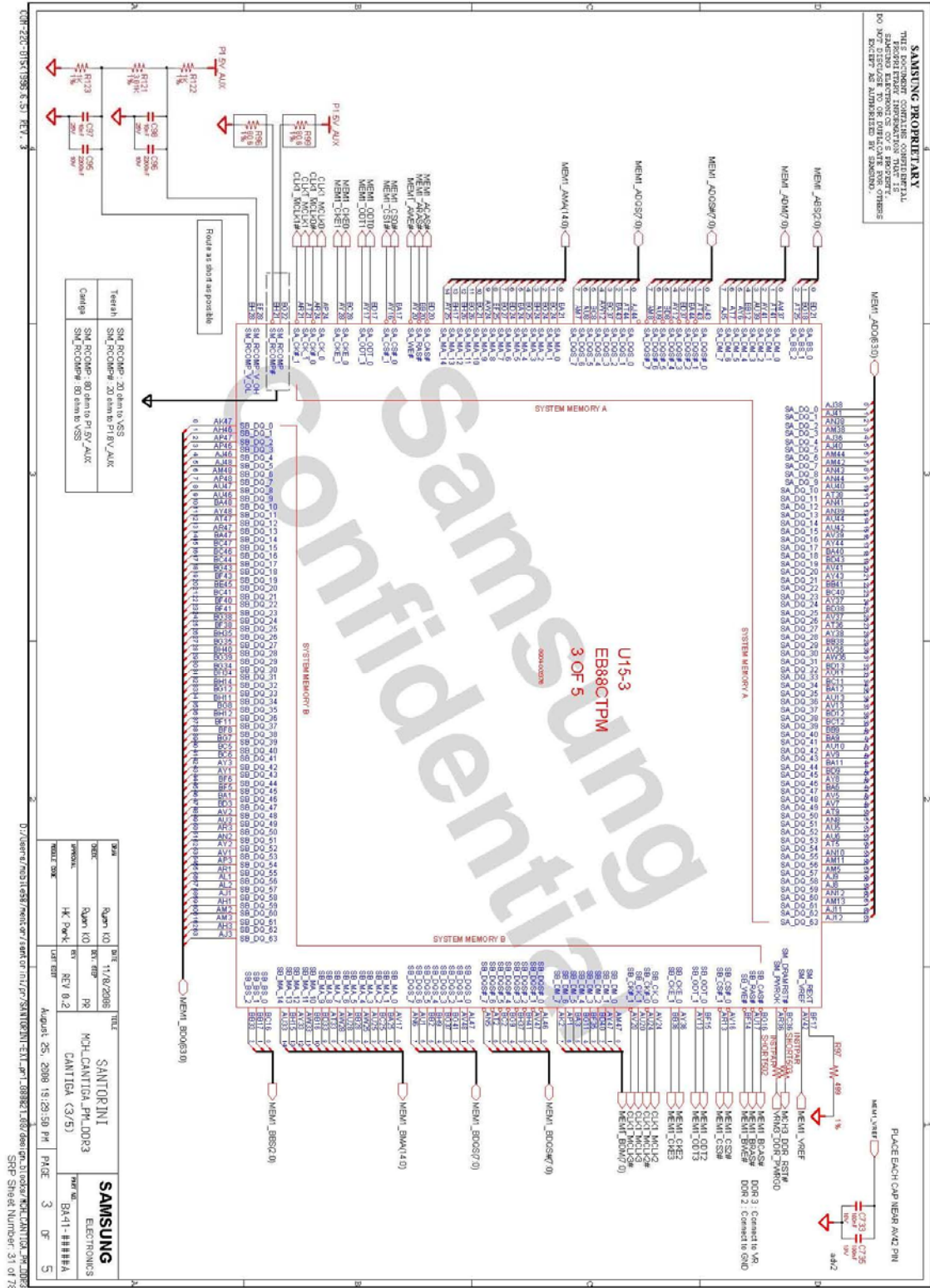
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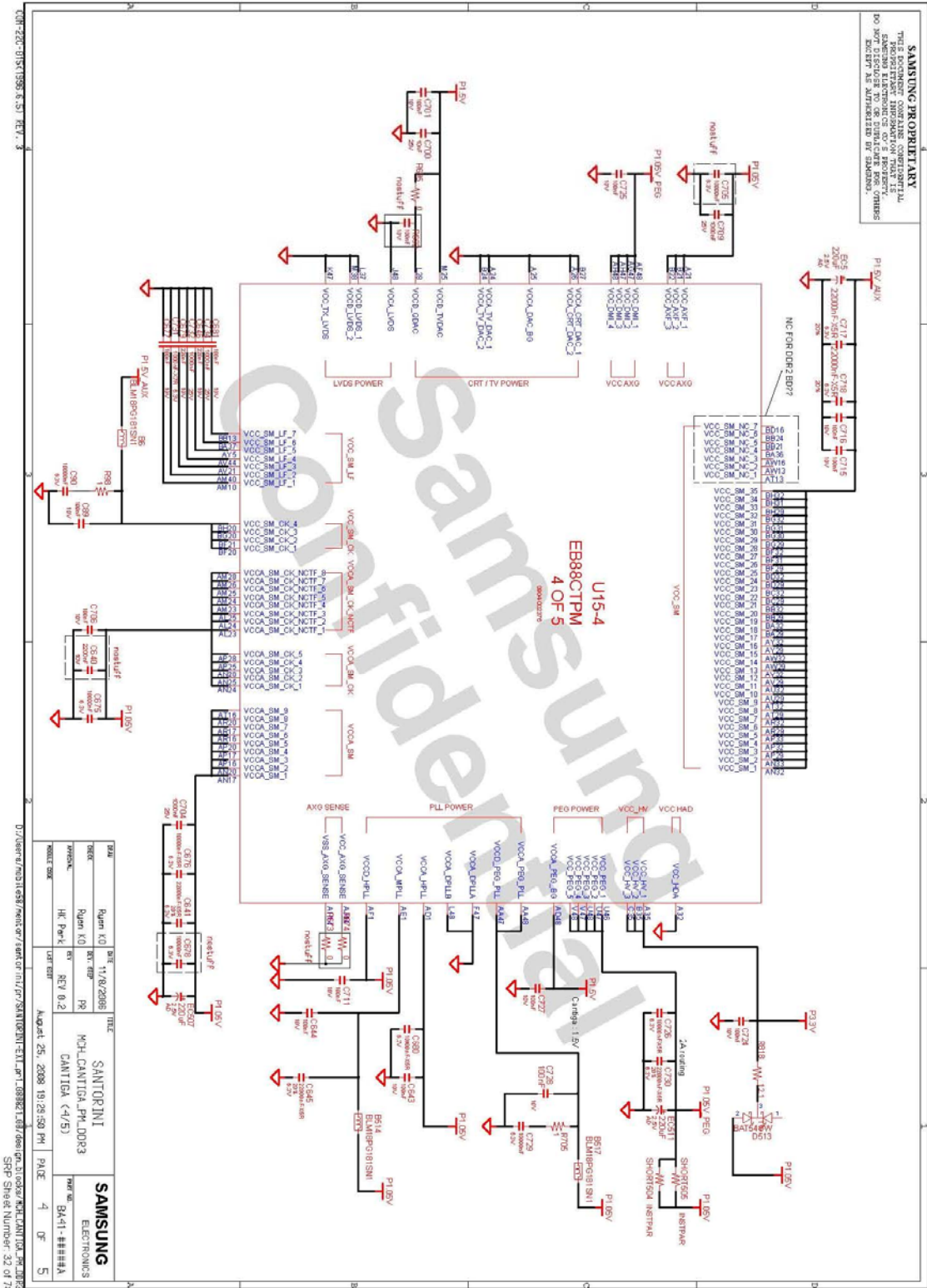
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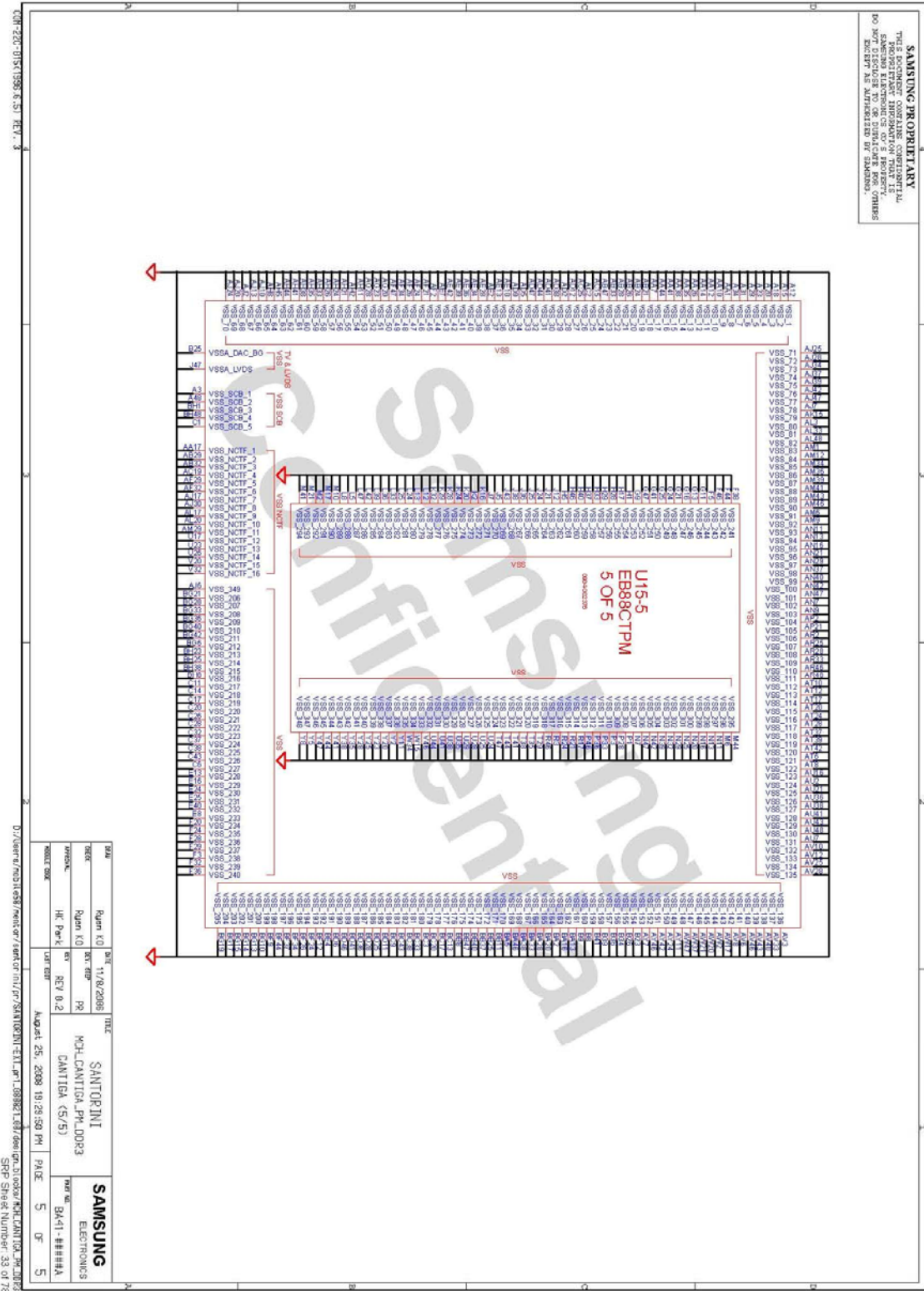
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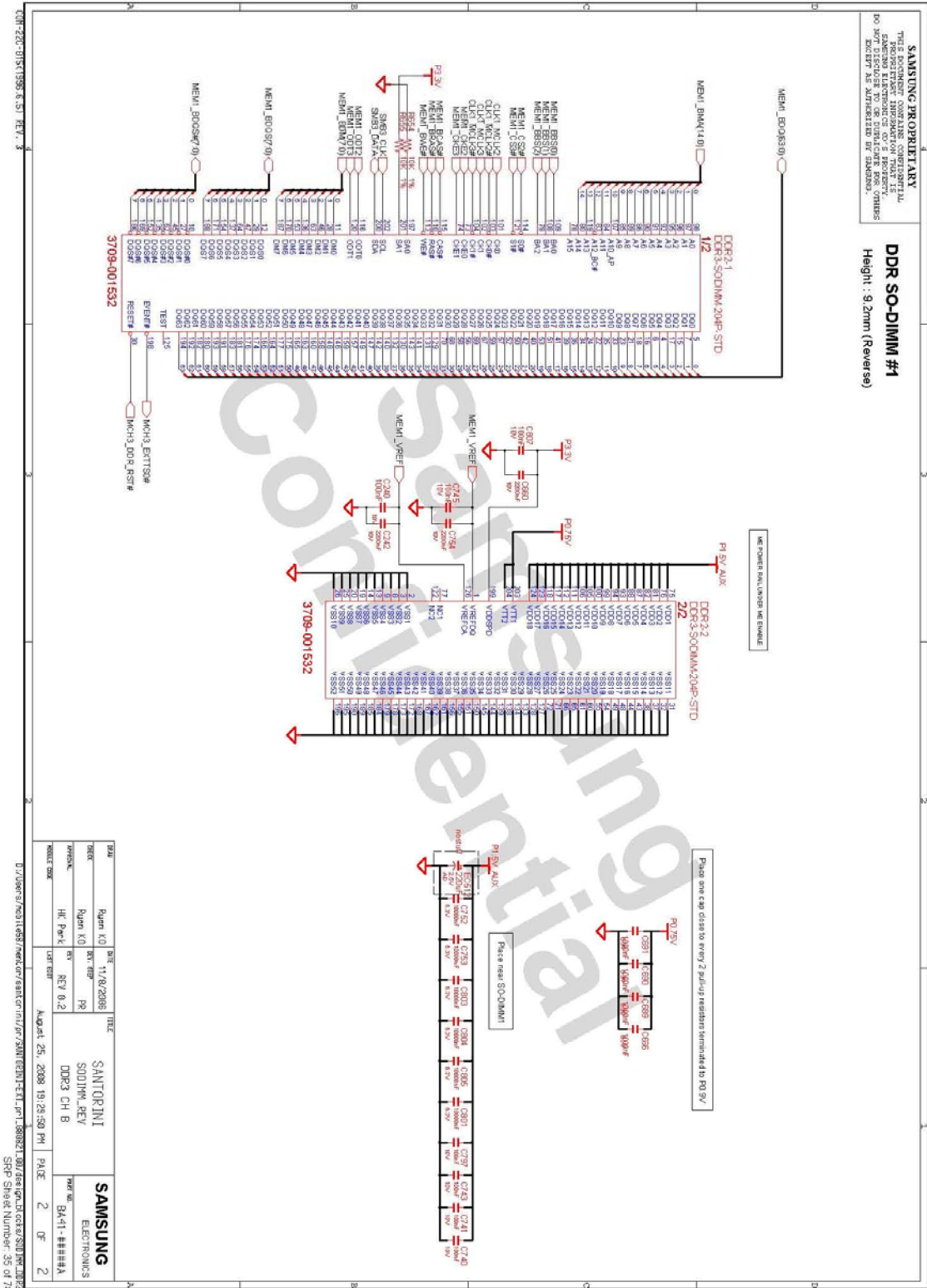
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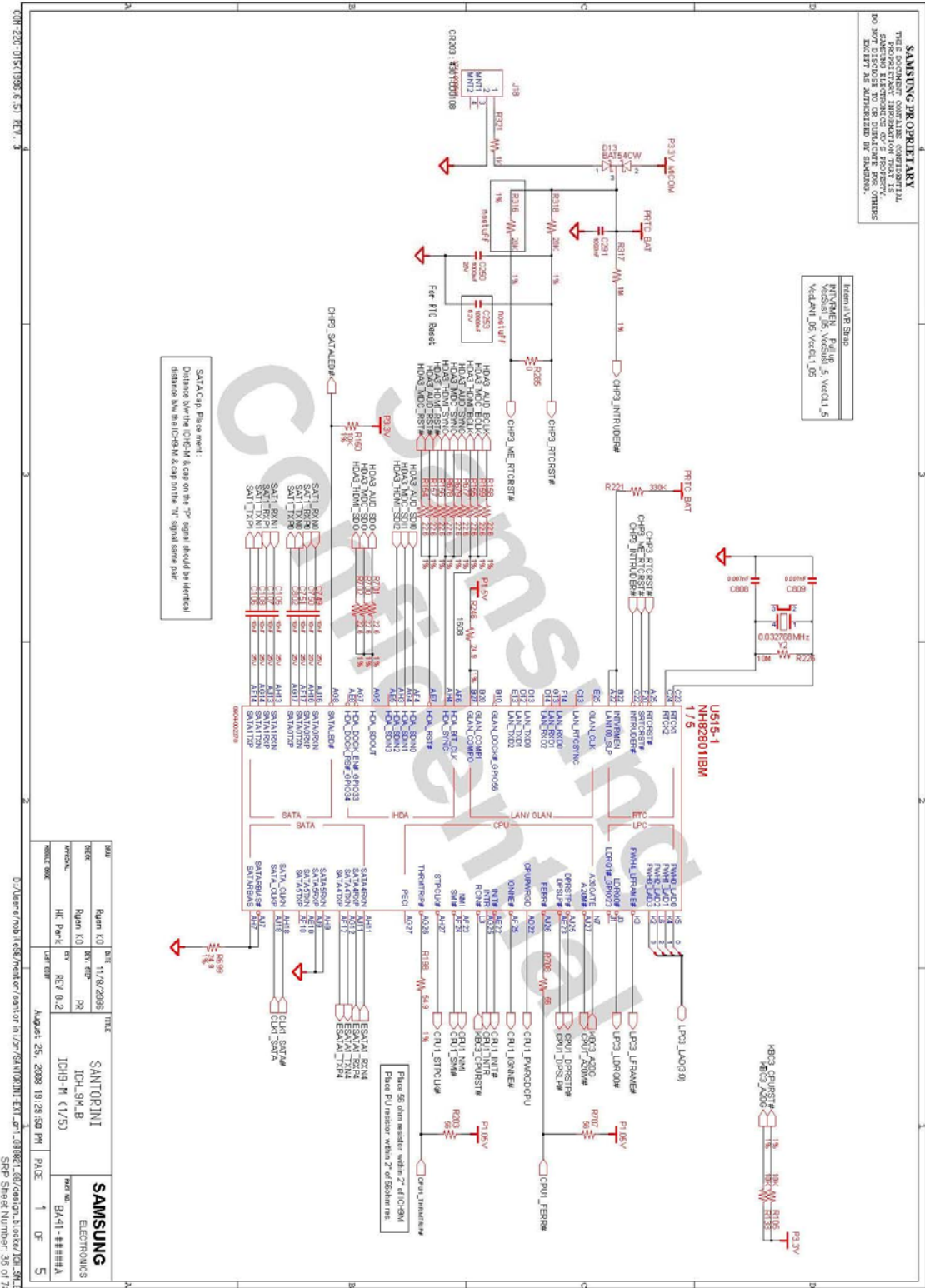
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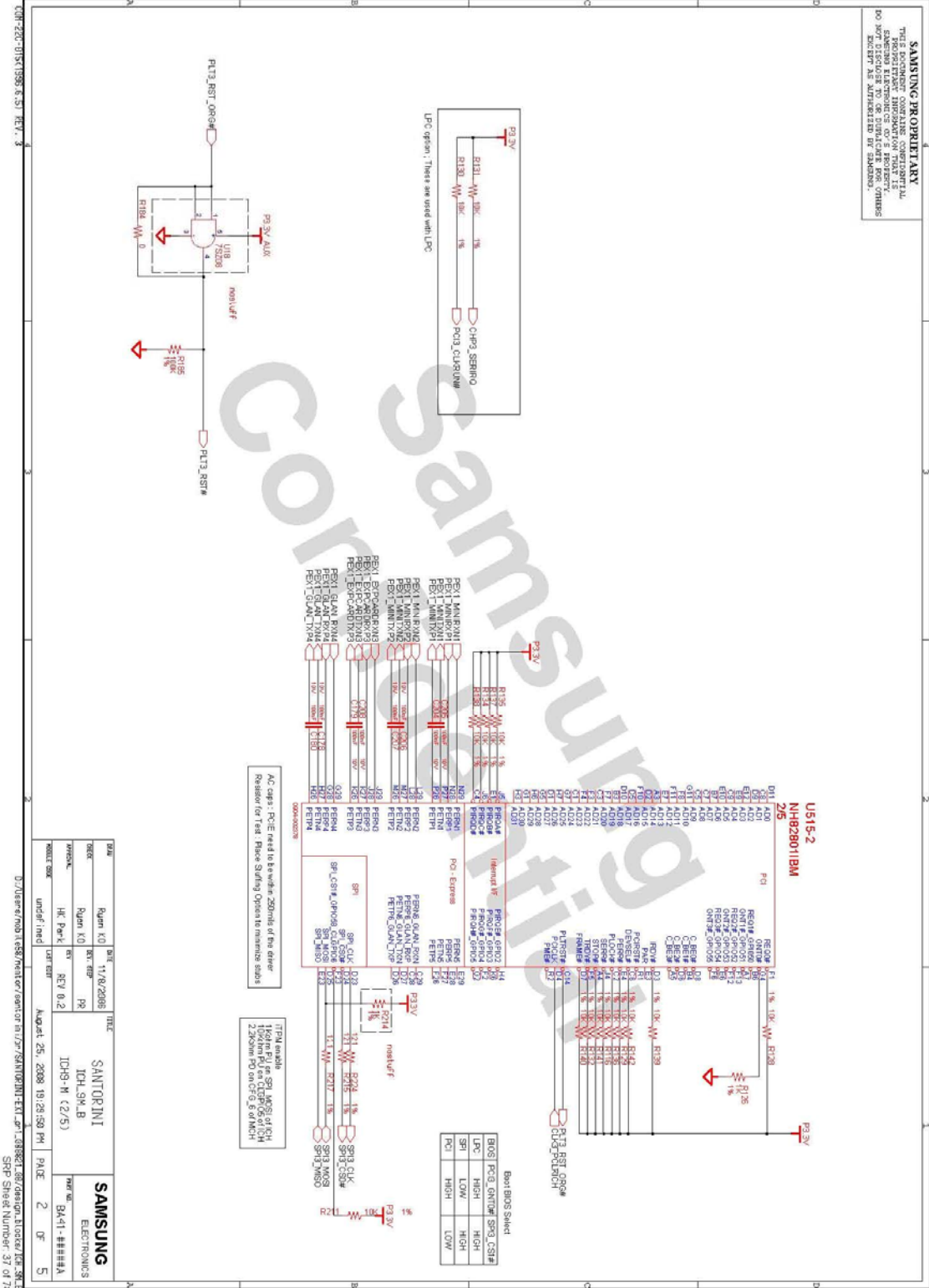
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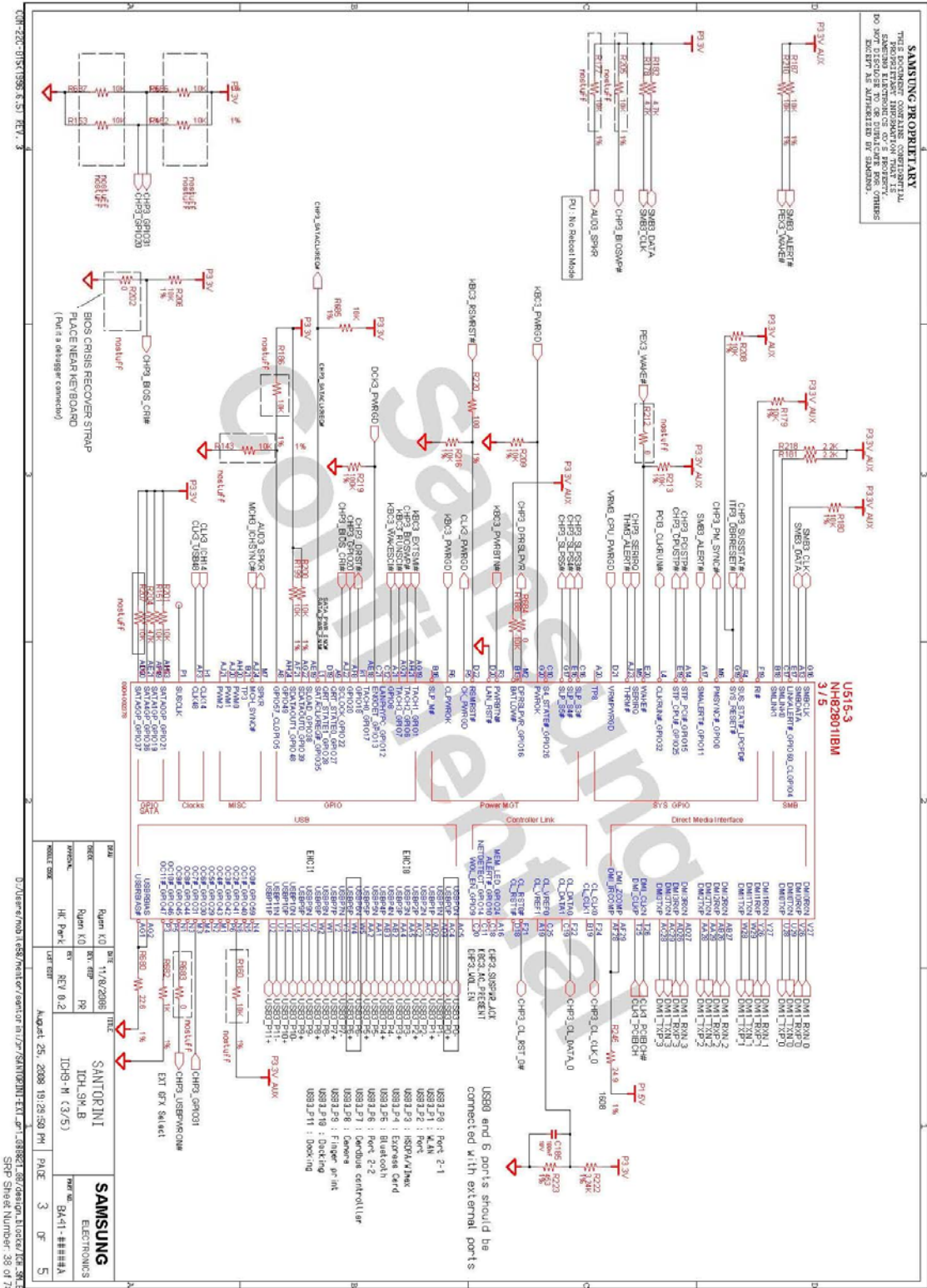
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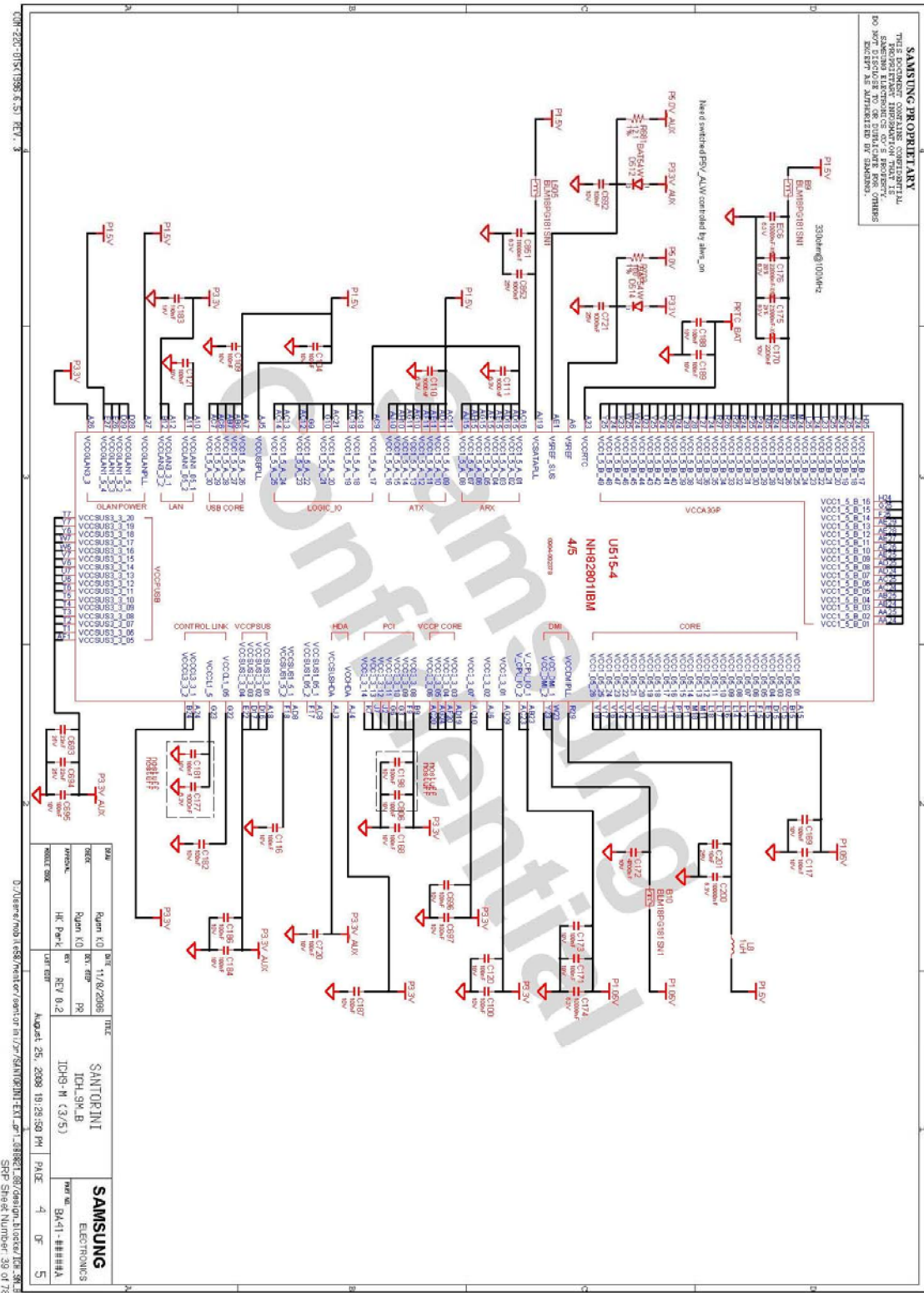
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**- SPI ROM LIST -**  
 Macro# : M325L8005M2C-15G  
 -> 1107-001646

<p>02 VERIFY REAL MODE                  03 DISABLE NMI                  04 GET CPU TYPE                  06 INIT SYSTEMHW                  08 INIT CHIPSET REG.                  09 SET IN POST FLAG                  0A INIT CPU REG                  0B CPU CACHE ON                  0C INIT CACHE TO POST                  0E INIT IO VALUE                  0F ENABLE THE L-BUS IDE                  10 INIT POWERMANAGER                  11 LOAD/ALTERNATE REG.                  13 PC BUSMASTER RESET WITH INITIAL POST VALUE                  14 INIT MEMBOARD CONTROLLER                  16 CHECK CHECKSUM                  18 8257TIMER INIT                  1A 8237 DMA CONTROLLER INIT                  1C RESET INTERRUPT-CONTROLLER                  20 TEST DRAM REFRESH                  22 TEST 8742 KEYBOARD CONTROLLER                  24 SET ES SEGMENT REG. TO 4GB                  26 EMULATE A20                  28 AUTO SETTING DRAM                  32 COMPUTE THE CPU SPEED                  34 TEST CMOS RAM                  38 SHADOW SYSTEM BIOS ROM                  3A AUTO SETTING CACHE                  3C CONFIGURE ADVANCED CHIPSET REGS                  3D LOAD/ALTER REG. WITH CMOS VALUE                  42 INIT INTERRUPT VECTOR                  44 INIT BIOS INTERRUPT                  46 CHECK ROM COPYRIGHT NOTICE                  47 INIT I20 SUPPORT IF INSTALLED                  48 CHECK VIDEO CONFIGURE AGAINST CMOS                  49 INIT PCI BUS AND DEVICE                  4A INIT ALL VIDEO BIOS ROM                  4C SHADOW VIDEO BIOS ROM                  50 DISPLAY CPU TYPE AND SPEED                  52 TEST KEYBOARD                  54 SET KEYBOARD IF ENABLED                  56 ENABLE KEYBOARD                  58 TEST FOR UNEXPECTED INTERRUPTS                  5A DISPLAY "PRESS... SETUP"                  5C TEST RAM BETWEEN 512K AND 640K                  62 TEST EXTENDED MEMORY                  64 JUMP TO USER PATCH 1</p>	<p>66 CONFIGURE ADVANCE CACHE REG.                  6A DISPLAY EXTERNAL CACHE SIZE                  6C DISPLAY SHADOW MESSAGE                  6E DISPLAY NONDISPOSABLE SEGMENT                  70 DISPLAY ERROR MESSAGE                  72 CHECK FOR CONFIGURATION ERROR                  74 TEST REAL TIME CLOCK                  76 CHECK FOR KEYBOARD ERROR                  7C SETUP HARDWARE INTERRUPT VECTOR                  7E TEST COPROCESSER IF PRESENT                  80 DISABLE ON BOARD IO PORT                  82 DETECT AND INSTALL EXT RS232C                  84 DETECT AND INSTALL EXT PARALLEL                  86 REINIT. ONBOARD IO PORT                  88 INIT BIOS DATA ROM                  8A INIT EXTENDED BIOS DATA AREA                  8C INIT F80 CONTROLLER                  9A SHADOW OPTION ROMS                  9C SETUP POWER MANAGEMENT                  9E ENABLE HW INTERRUPT                  A0 SET TIME OF DAY                  A4 INIT TYPENATIC RATE                  A8 ERASE F2 PROMPT                  AA SCAN FOR F2 KEY STROKE                  AC ENTER SETUP                  AE CLEAR IN POST FLAG                  B0 CHECK FOR ERRORS                  B2 POST DONE PREPARE TO BOOT O/S                  B4 ONE BEEP                  B6 CHECK PASSWORD (OPTION)                  B7 ACPI INIT                  B8 DM INIT                  BE CLEAR SCREEN                  C0 TRY BOOT WITH INT'9                  D0 INTERRUPT HANDLER ERROR                  D2 UNKNOWN INTERRUPT ERROR                  D4 PENDING INTERRUPT ERROR                  D6 SHUTDOWN 5                  D8 SHUTDOWN ERROR                  DA EXTENDED BLOCK/MOVE                  DC SHUTDOWN 10                  DE EMULATE NMI                  E0 INIT HDD CONTROLLER                  E1 INIT LOCAL BUS HDD CONTROLLER                  E2 JUMP TO USER PATCH 2                  E4 DISABLE A20 ADDRESS LINE                  E6 CLEAR HUGE ES SEGMENT REG.                  E8 SEARCH FOR OPTION ROMS</p>
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REV	DATE	BY	TITLE
03A	04/18/2008	Rajun KJ	SANTORINI
03B	04/18/2008	Rajun KJ	SPL BIOS ROM
03C	04/18/2008	Rajun KJ	SPL BIOS ROM
03D	04/18/2008	Rajun KJ	SPL BIOS ROM
03E	04/18/2008	Rajun KJ	SPL BIOS ROM
03F	04/18/2008	Rajun KJ	SPL BIOS ROM
040	04/18/2008	Rajun KJ	SPL BIOS ROM
041	04/18/2008	Rajun KJ	SPL BIOS ROM
042	04/18/2008	Rajun KJ	SPL BIOS ROM
043	04/18/2008	Rajun KJ	SPL BIOS ROM
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045	04/18/2008	Rajun KJ	SPL BIOS ROM
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064	04/18/2008	Rajun KJ	SPL BIOS ROM

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046	04/18/2008	Rajun KJ	SPL BIOS ROM
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048	04/18/2008	Rajun KJ	SPL BIOS ROM
049	04/18/2008	Rajun KJ	SPL BIOS ROM
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062	04/18/2008	Rajun KJ	SPL BIOS ROM
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064	04/18/2008	Rajun KJ	SPL BIOS ROM

SAMSUNG  
ELECTRONICS

SANTORINI  
SPL BIOS ROM

REV 0.2

DATE 04/18/2008

BY Rajun KJ

TITLE SANTORINI

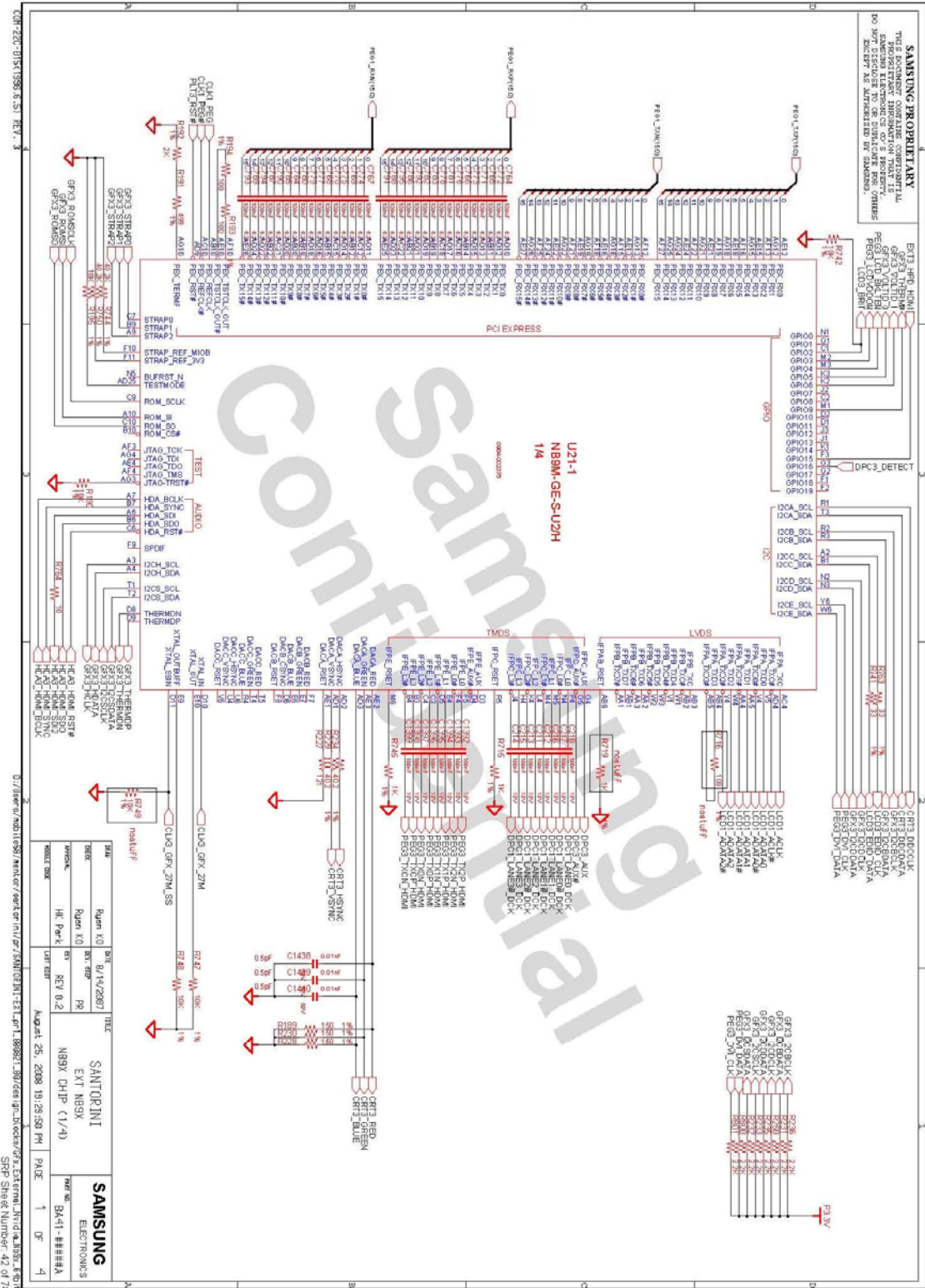
PART NO. BA41-####A

PAGE 1 OF 1

SPP Sheet Number: 41 of 78

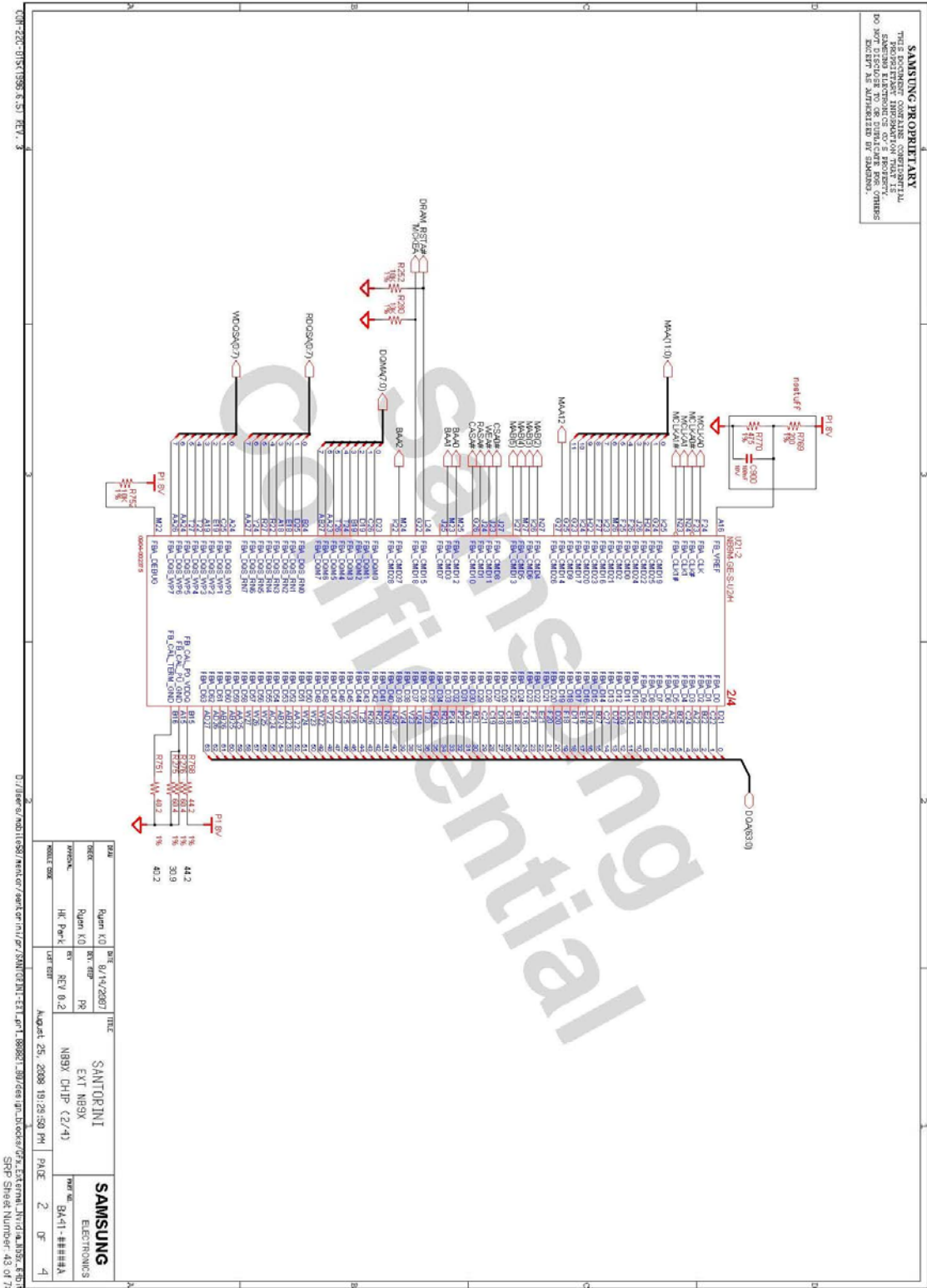
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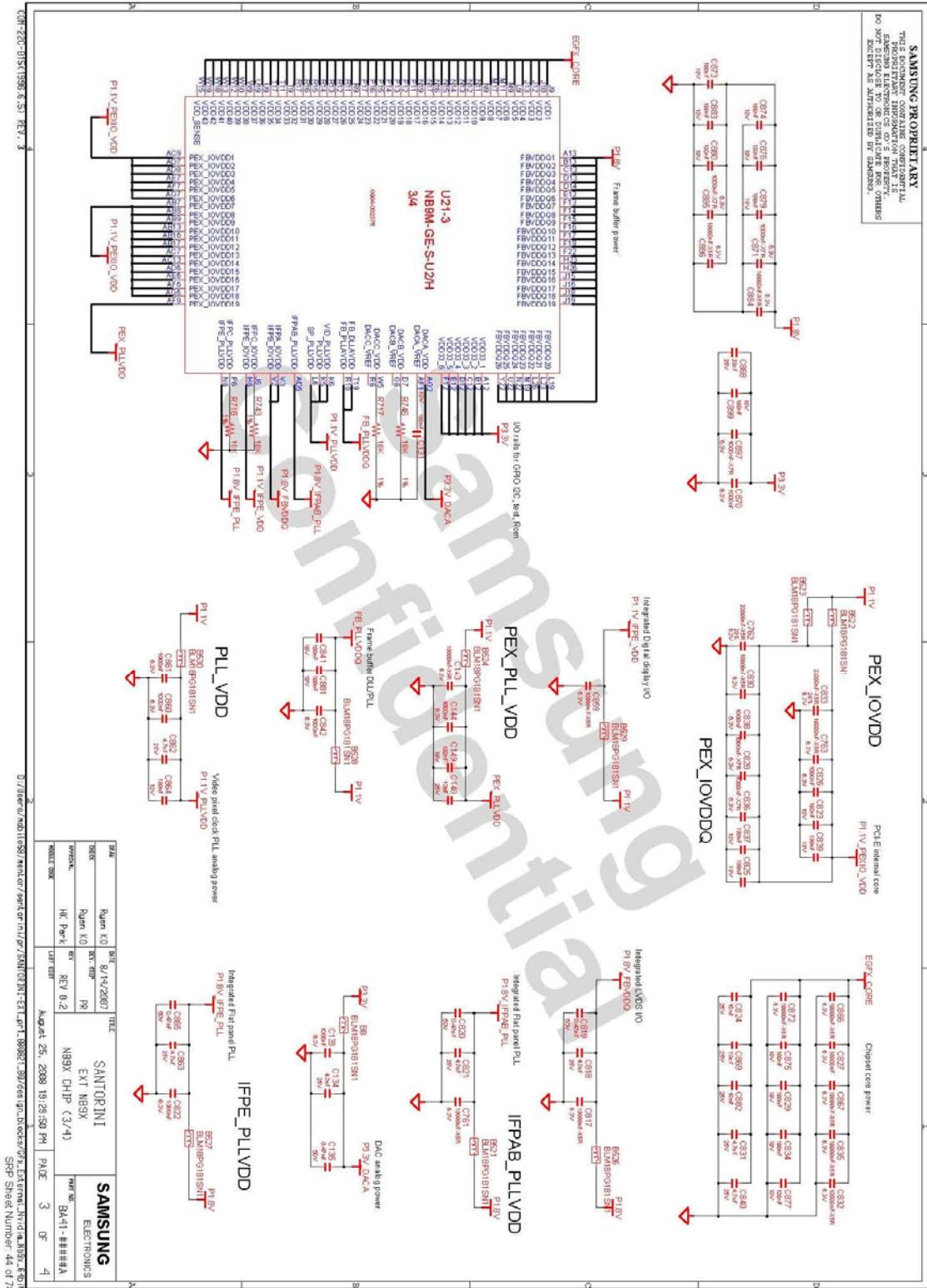


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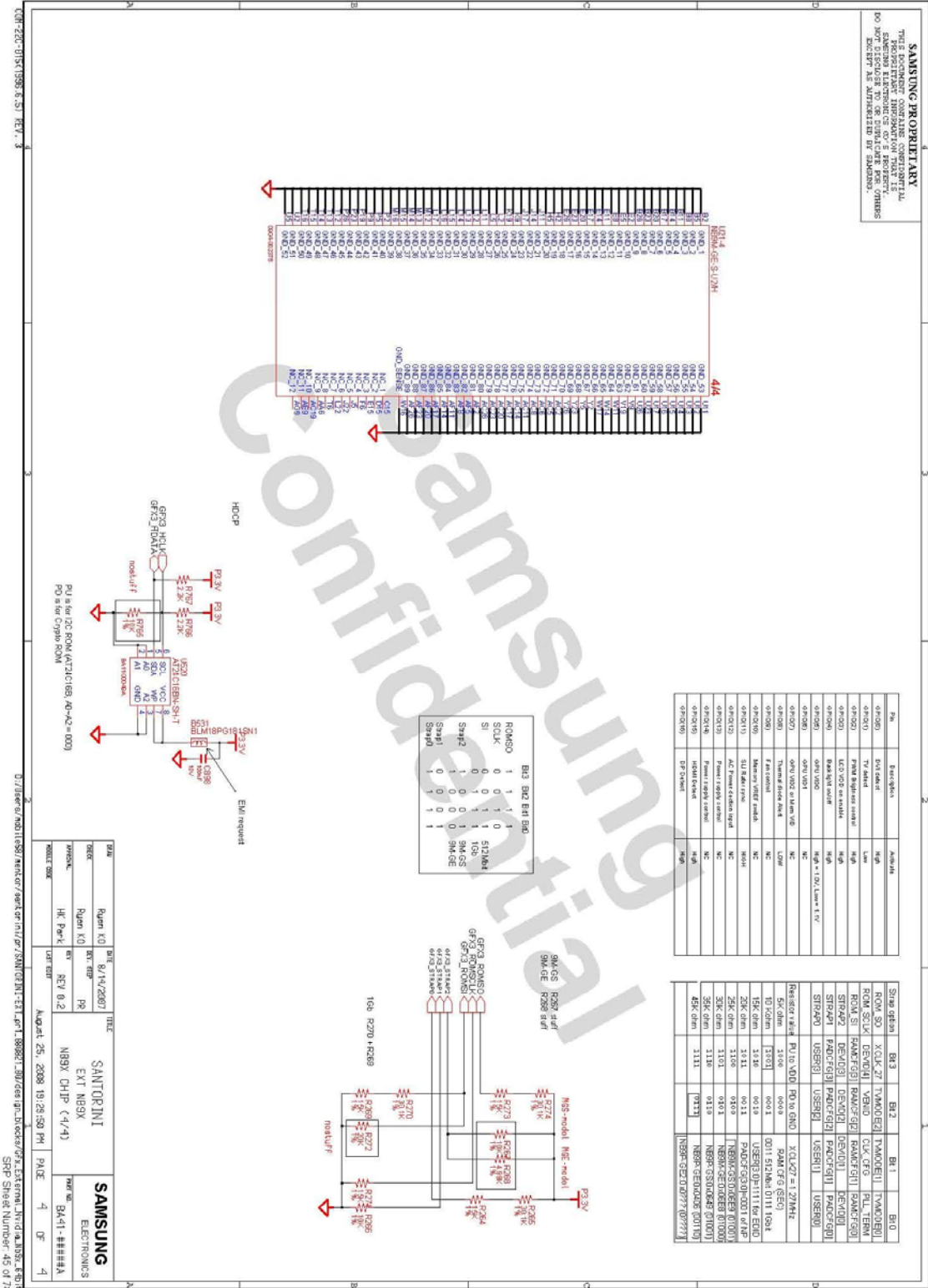


## 8. Block Diagram and Schematic



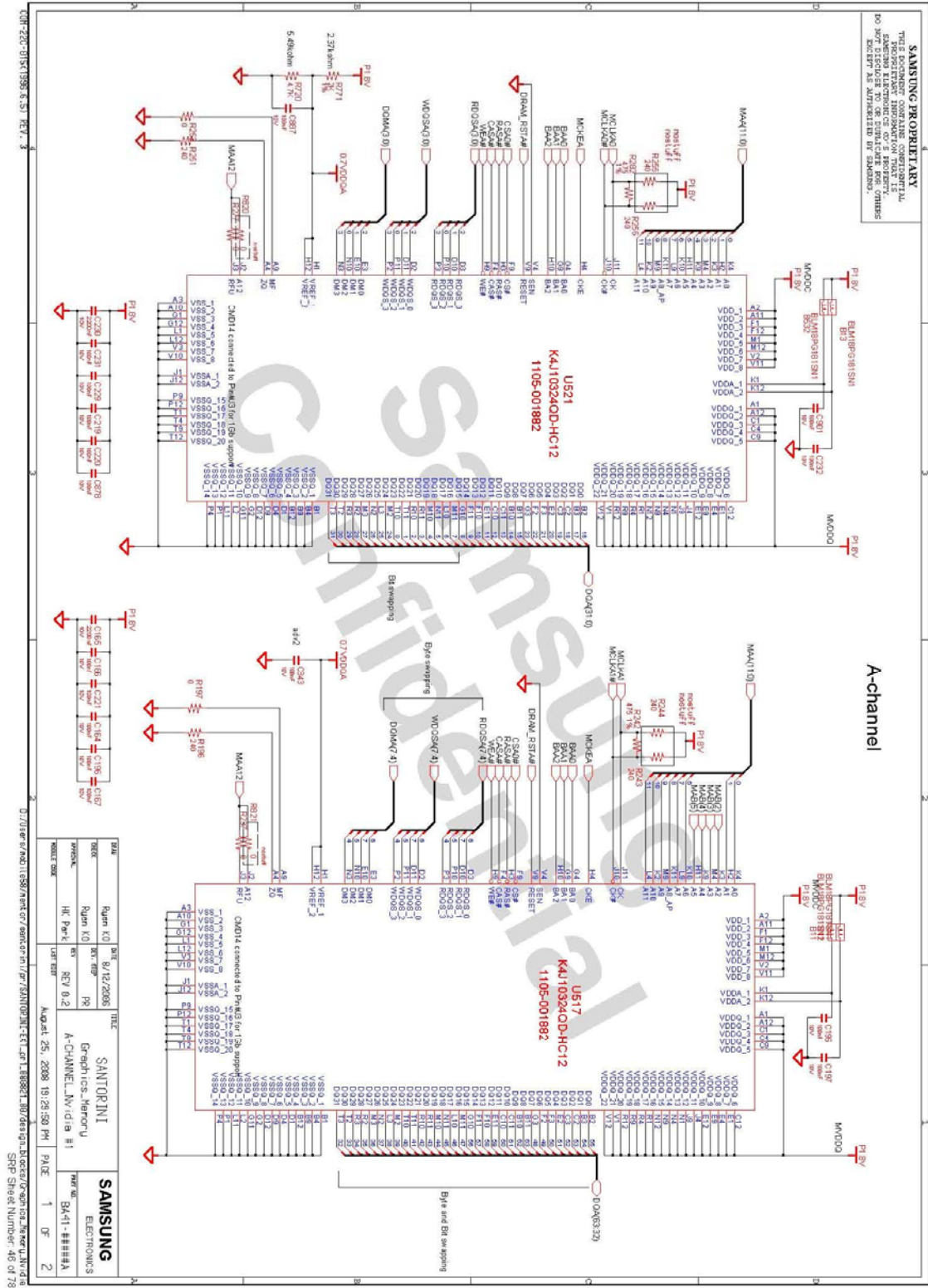
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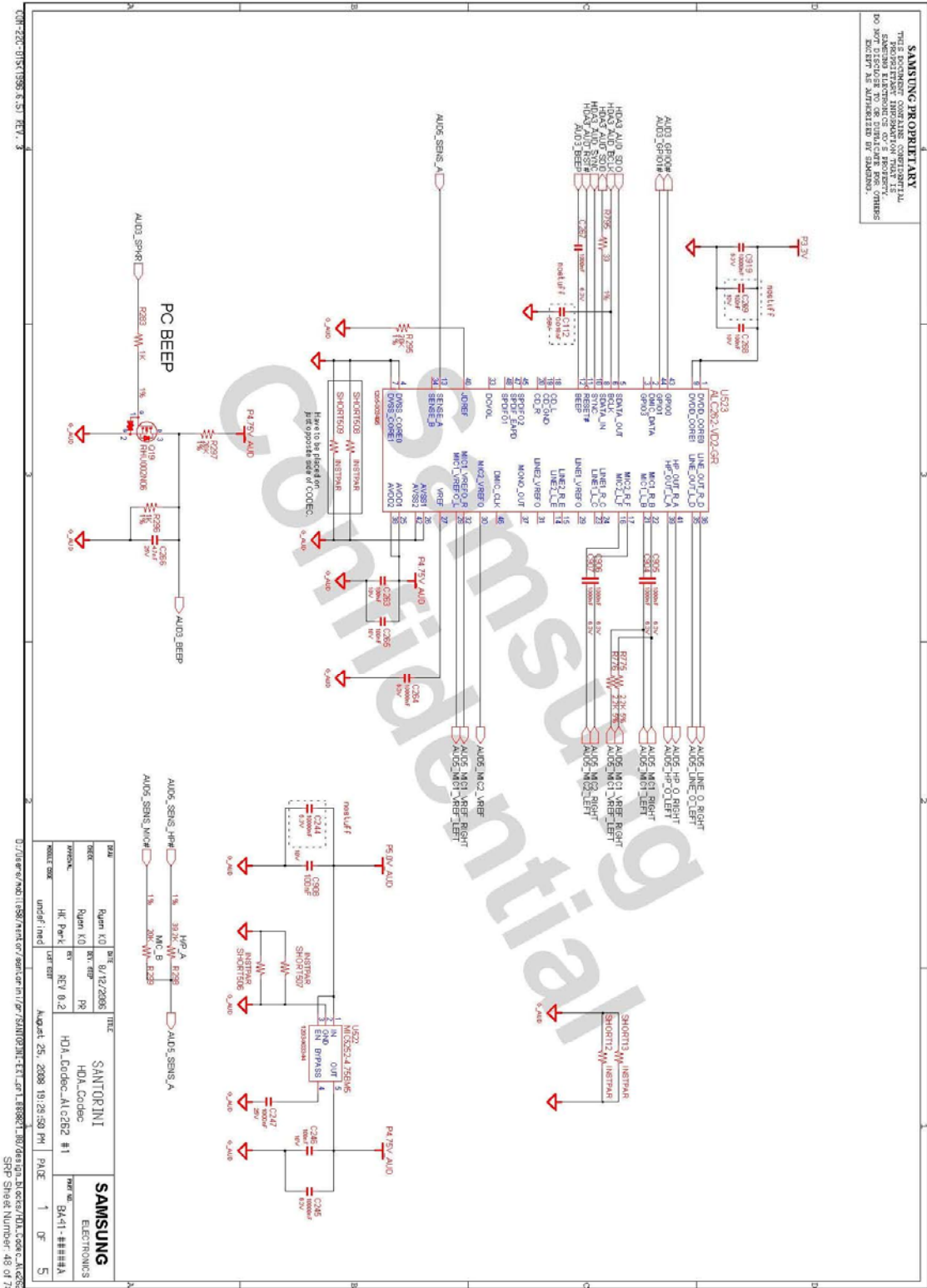
Samsung Confidential

<b>DATE</b>	Rapun KO	REV	8/12/2008	<b>TITLE</b>	SANTORINI	<b>SAMSUNG</b>
<b>DESIGNER</b>	Rapun KO	<b>APPR</b>	PR	Graphics-Memory		<b>ELECTRONICS</b>
<b>SYMBOL</b>	HK-PwrX	<b>REV</b>	REV 0.2	TERMINATION.Nvidia #3		<b>HW #</b> BK41-#####A
<b>MODEL CODE</b>		<b>DATE</b>	August 25, 2008 10:29:50 PM	<b>PAGE</b>	2	<b>OF</b> 2

D:\01dev\mod\1158\ref\ref\gsm\cmn\ref\SANTORINI-ET-CP11888821.00\design\block\ref\gsm\ca\mem\Nvidia  
 SRP Sheet Number: 41 of 78

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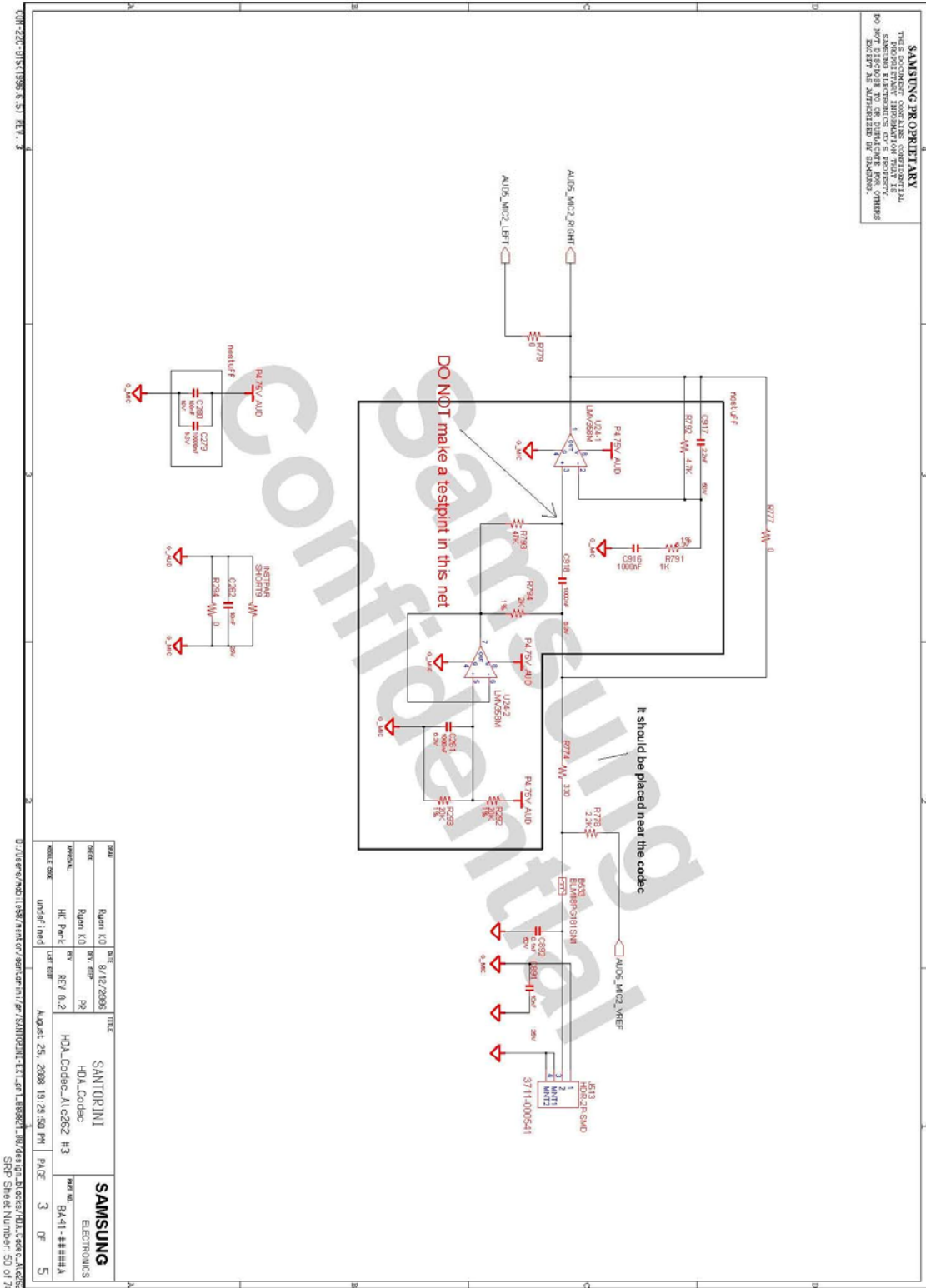






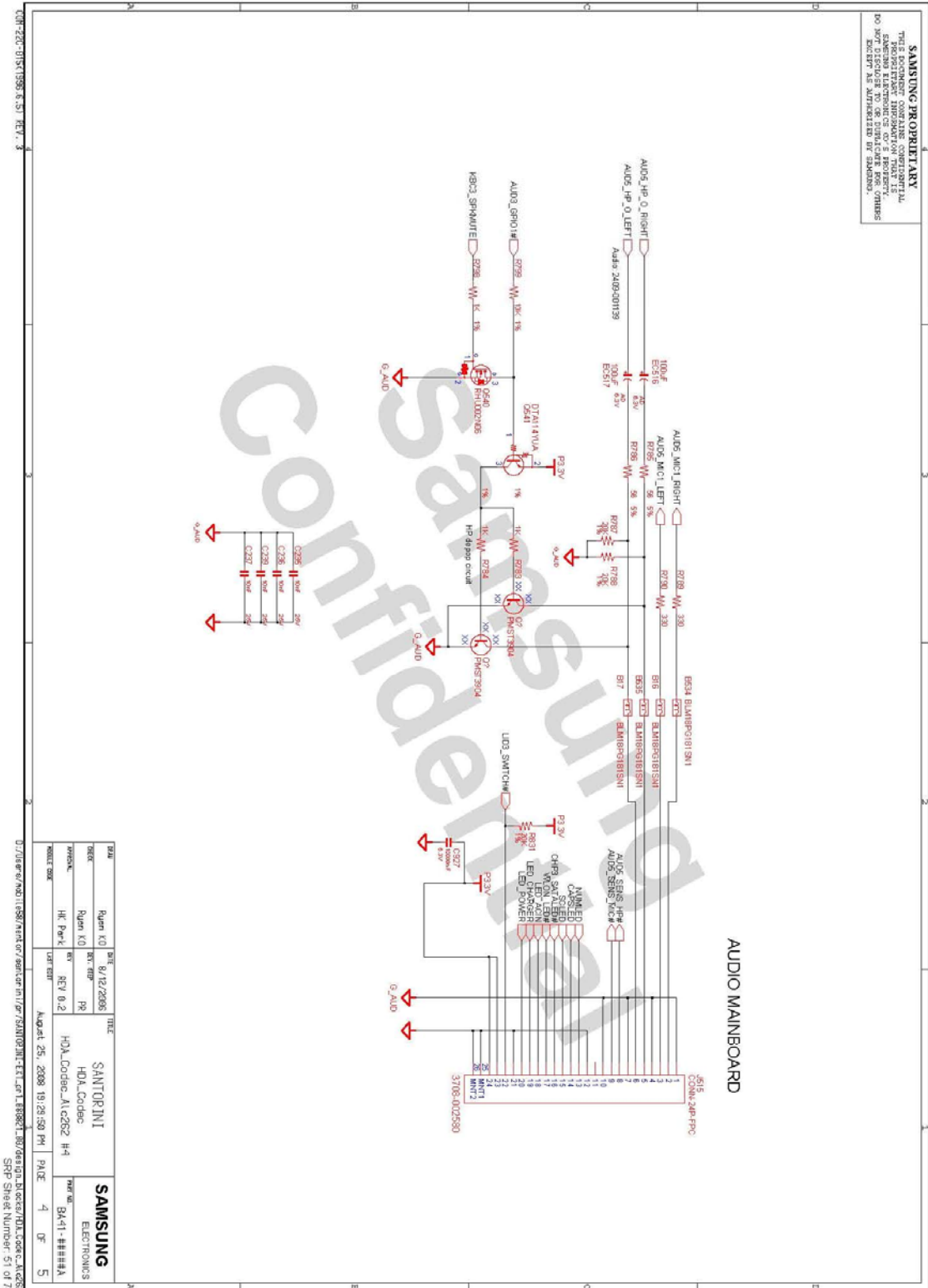
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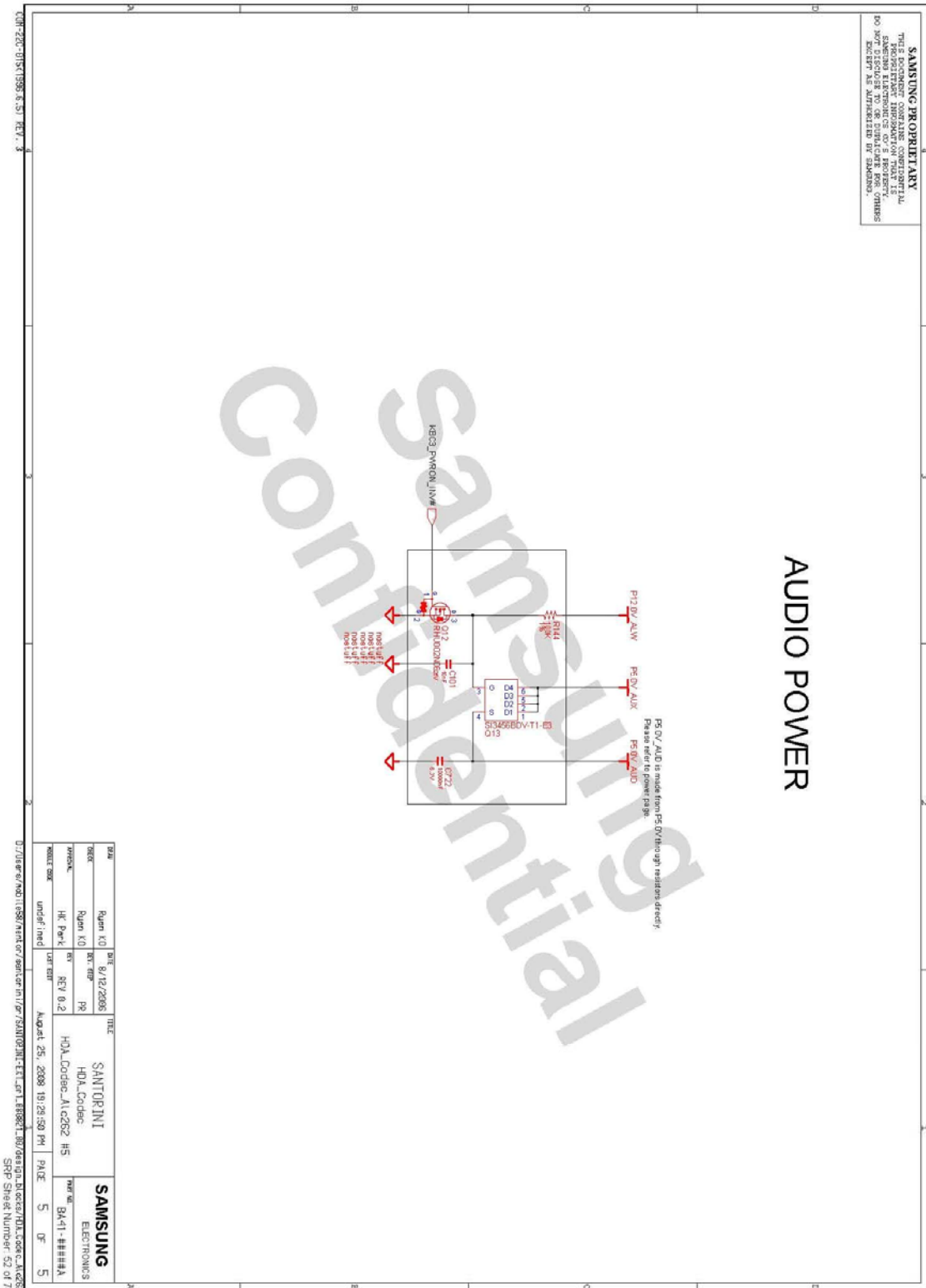
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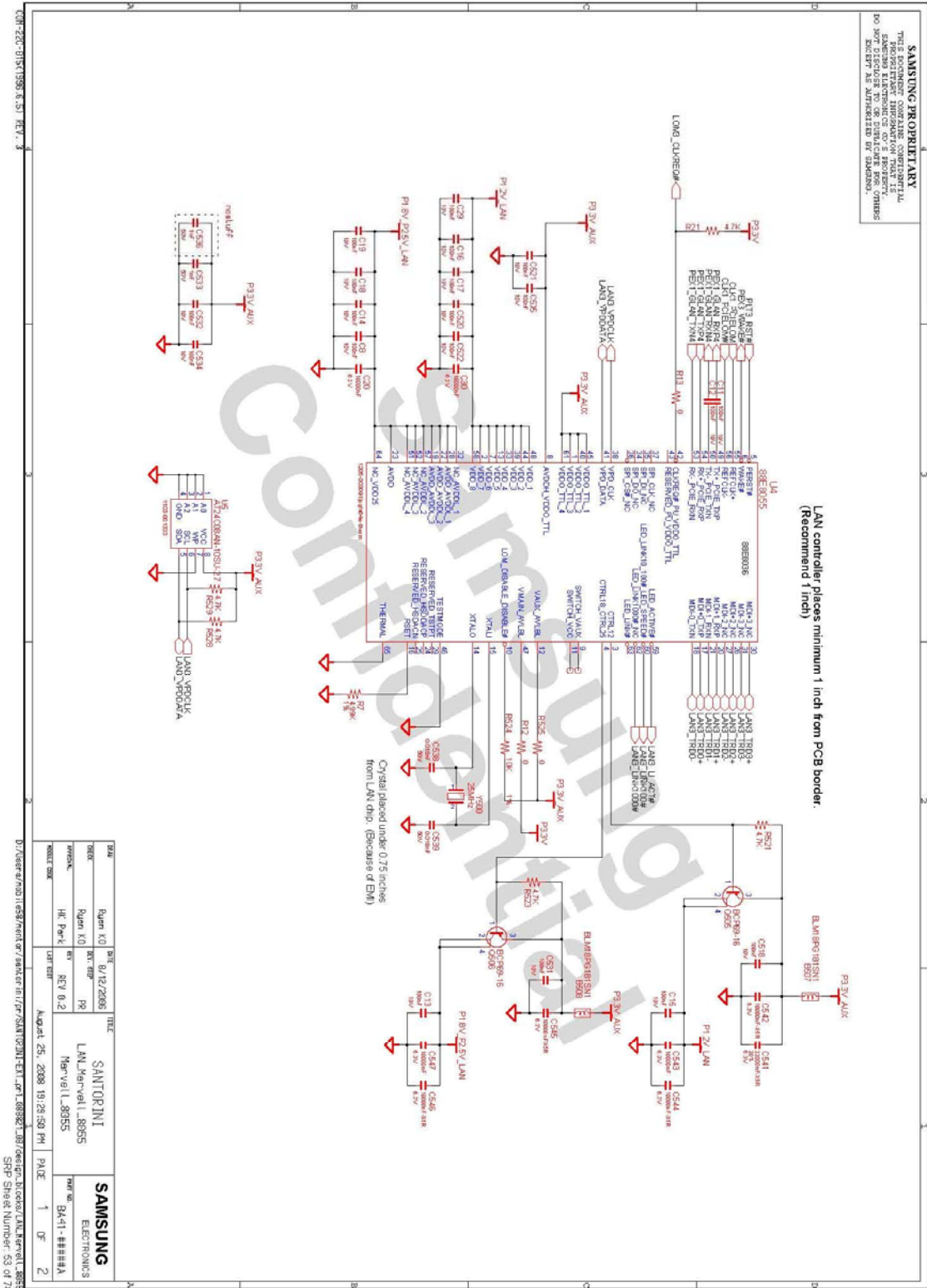
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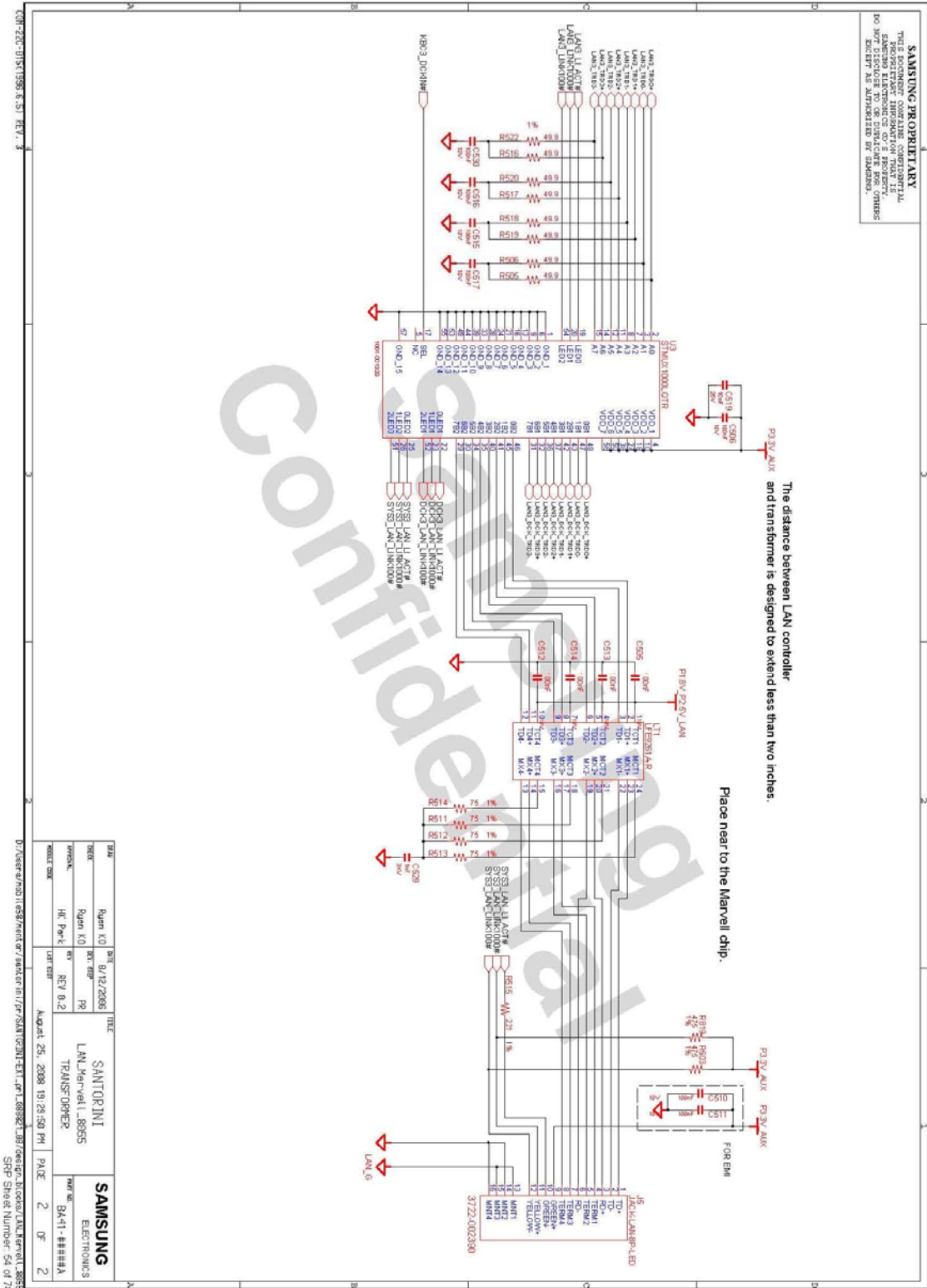
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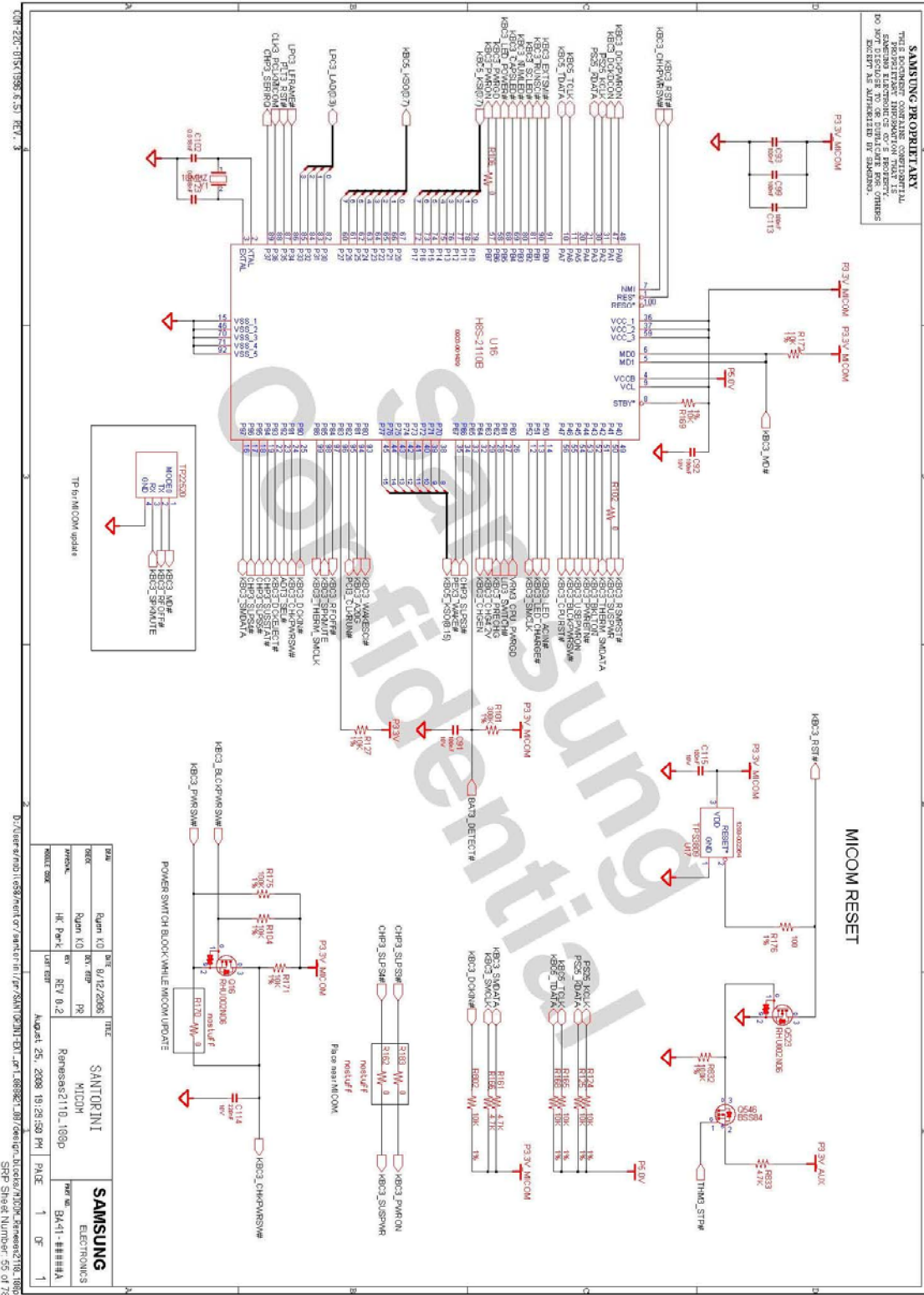
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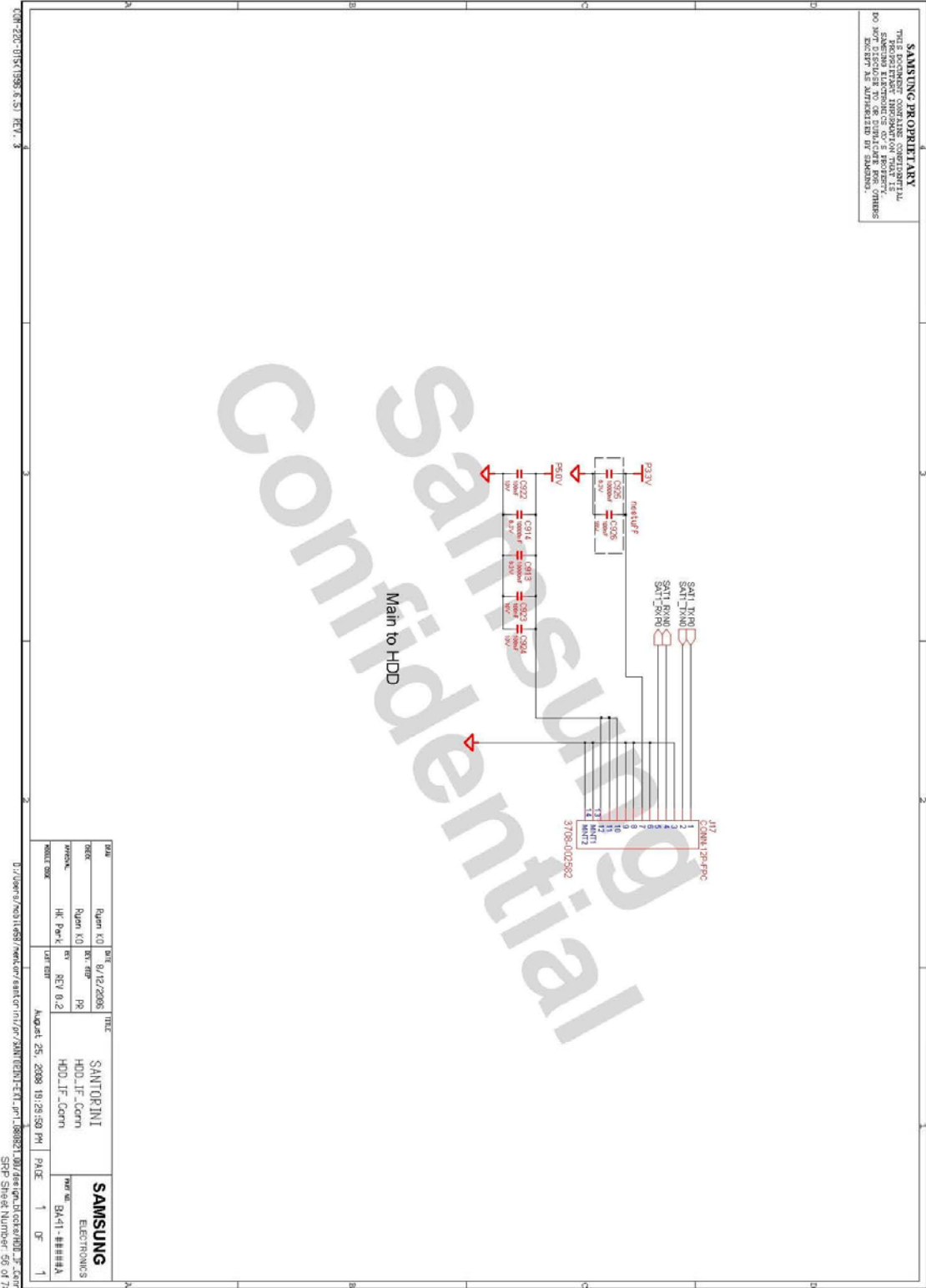
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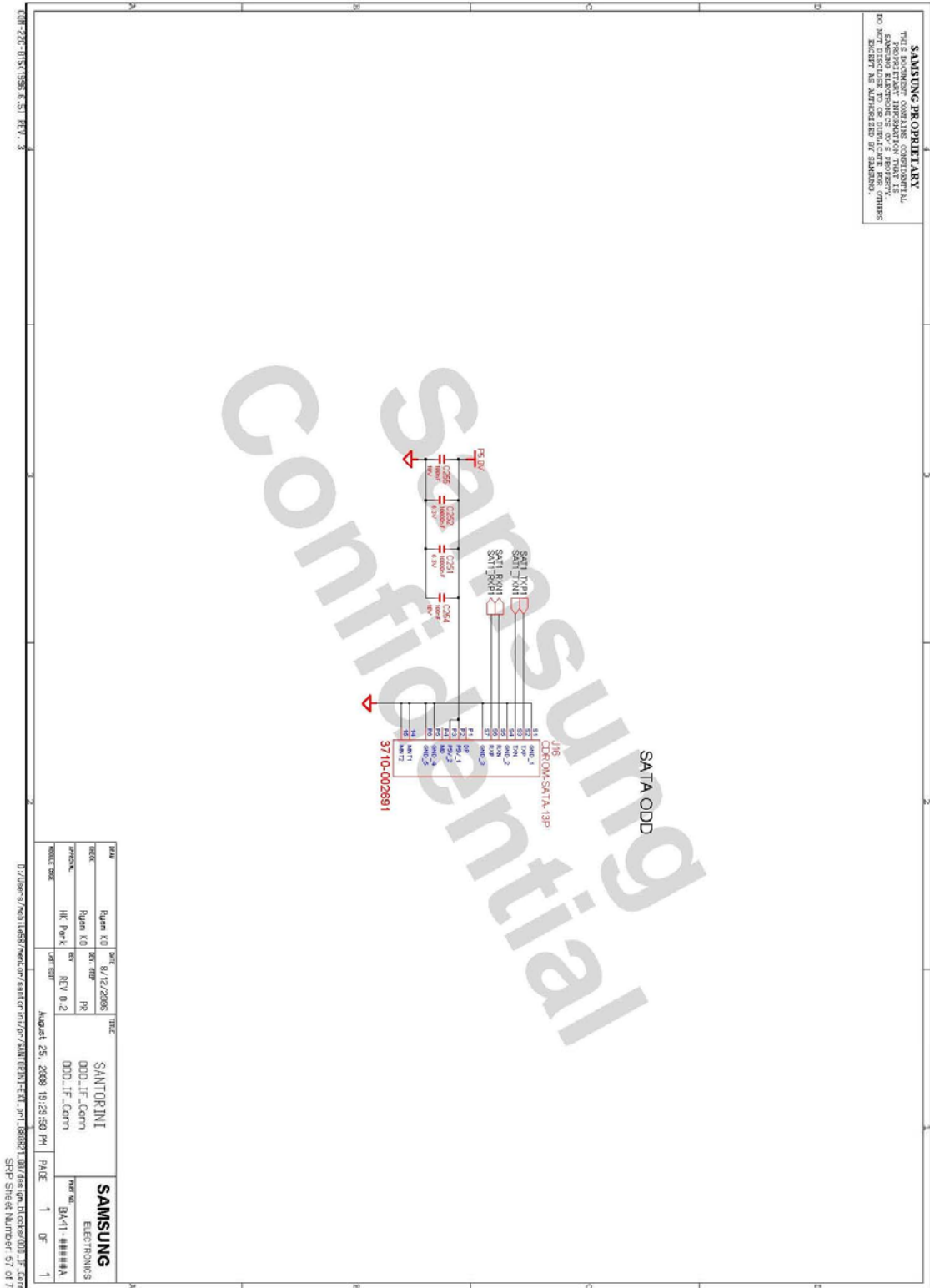
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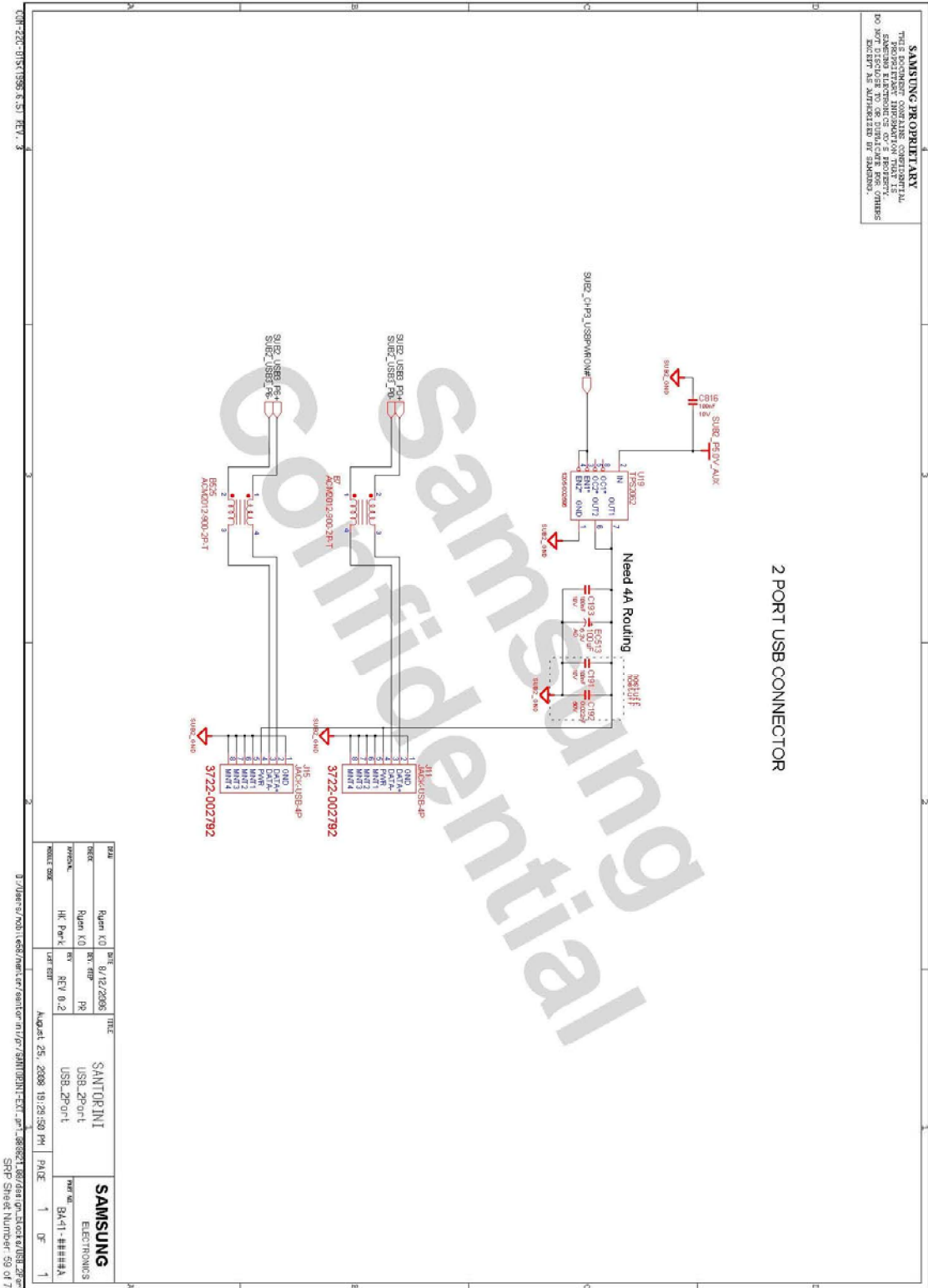
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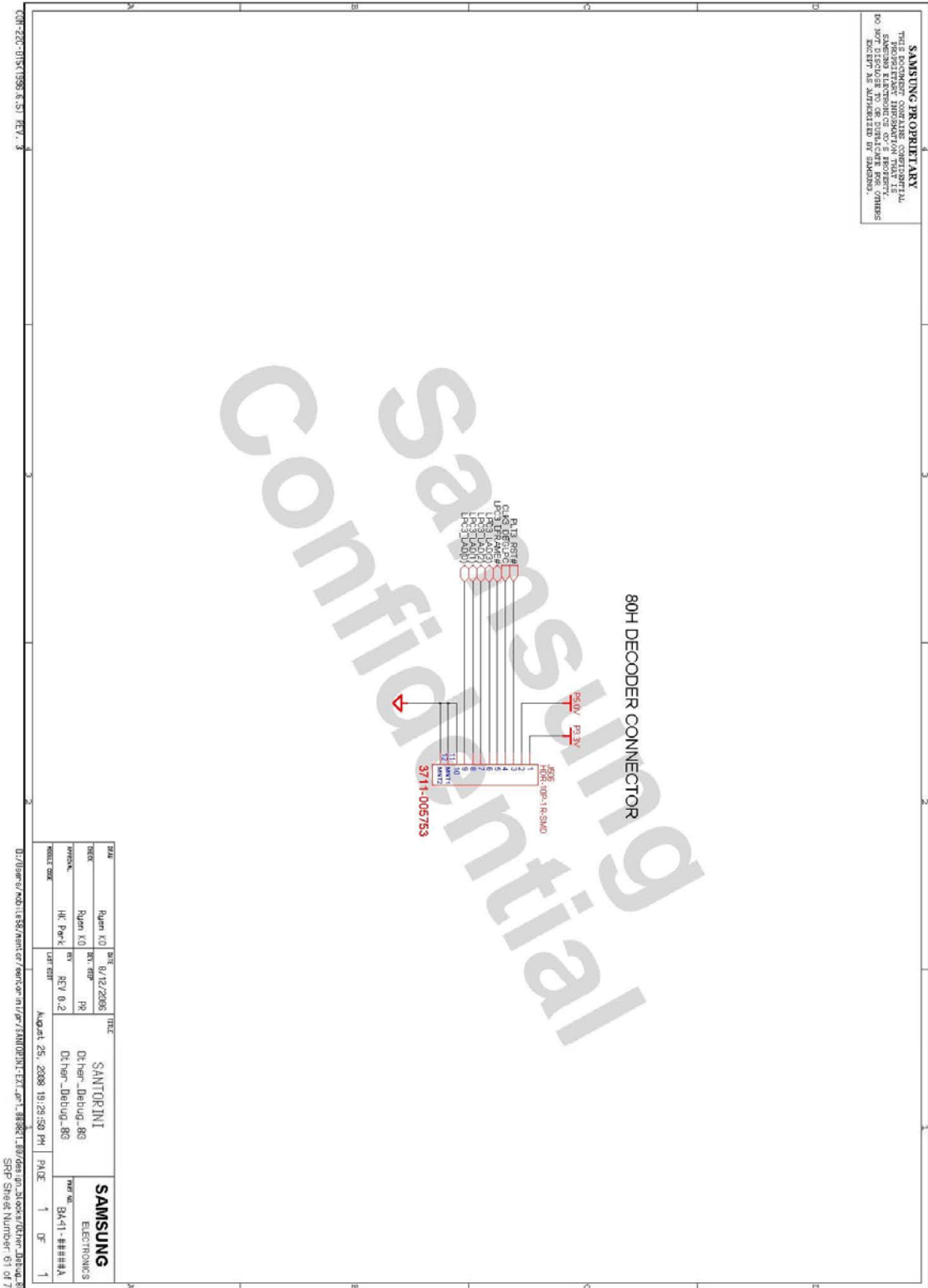
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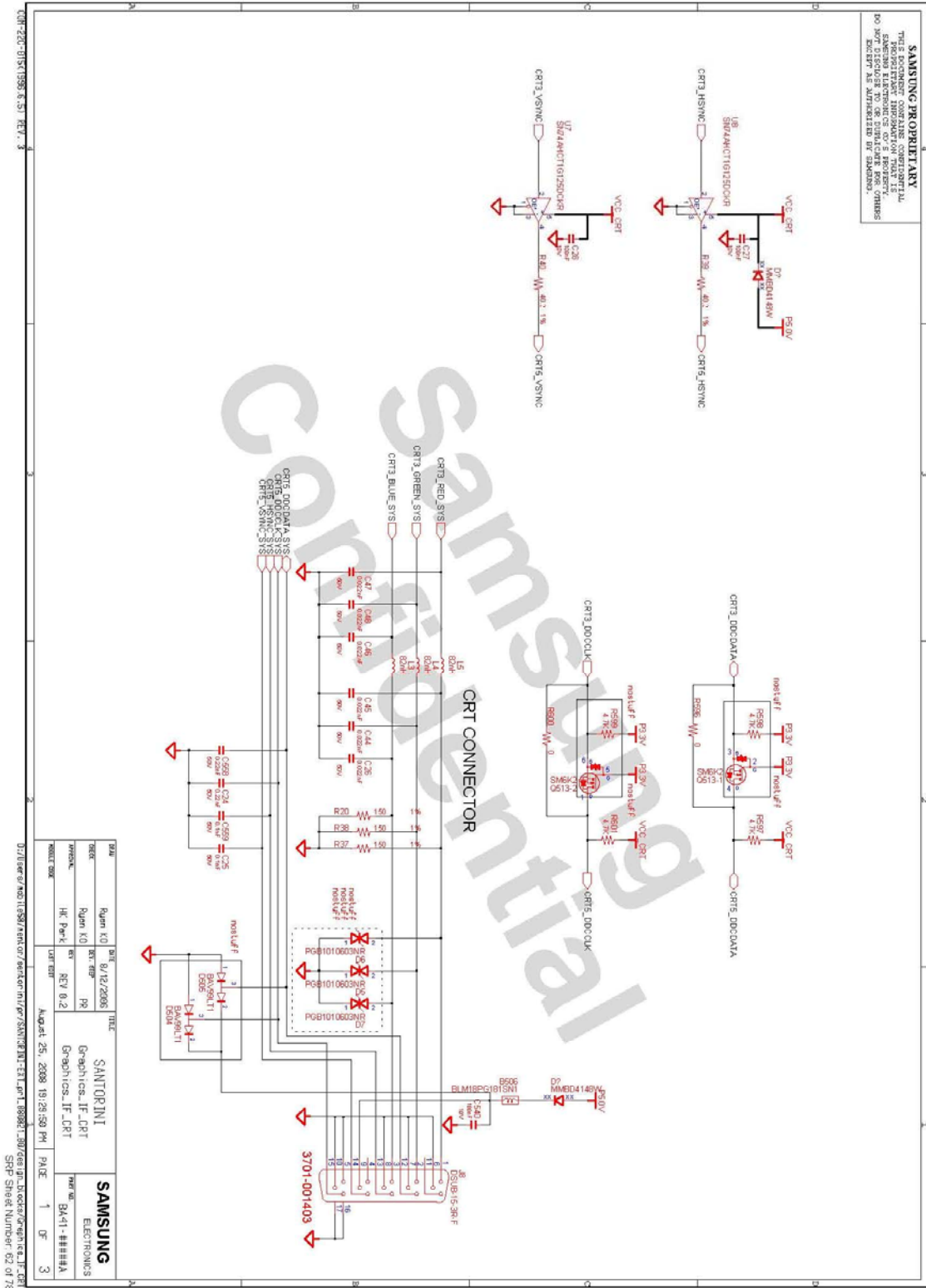
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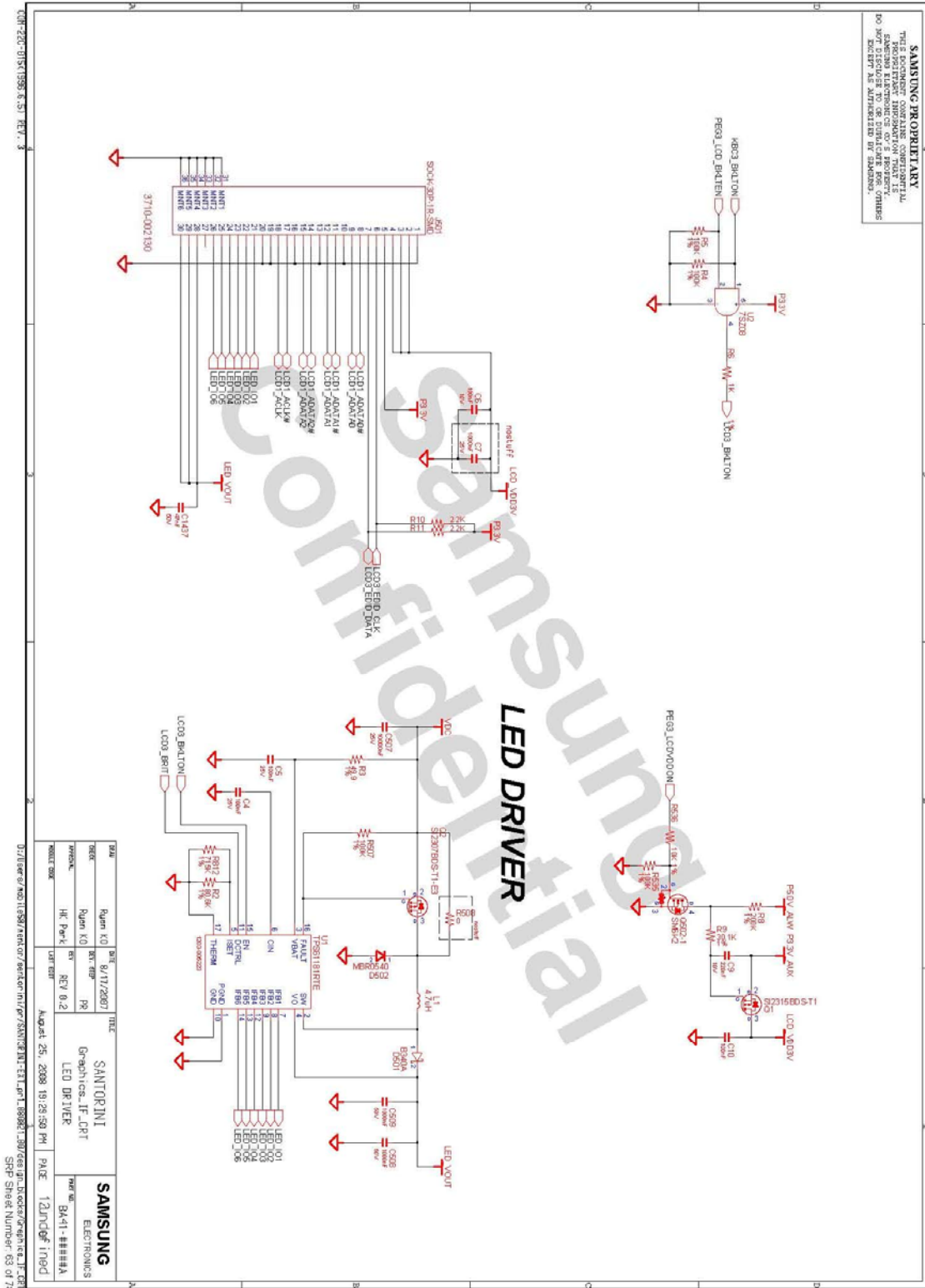
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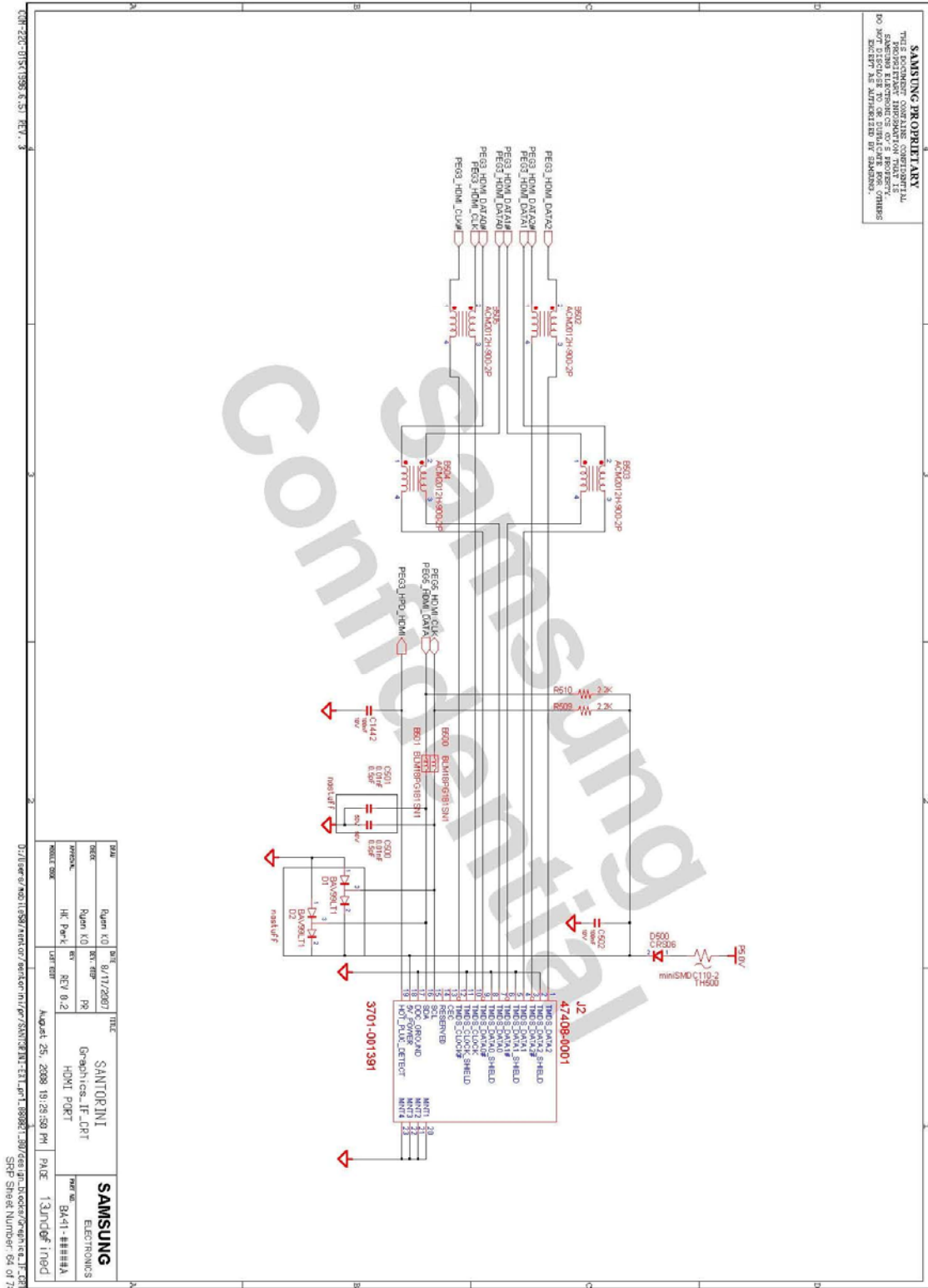
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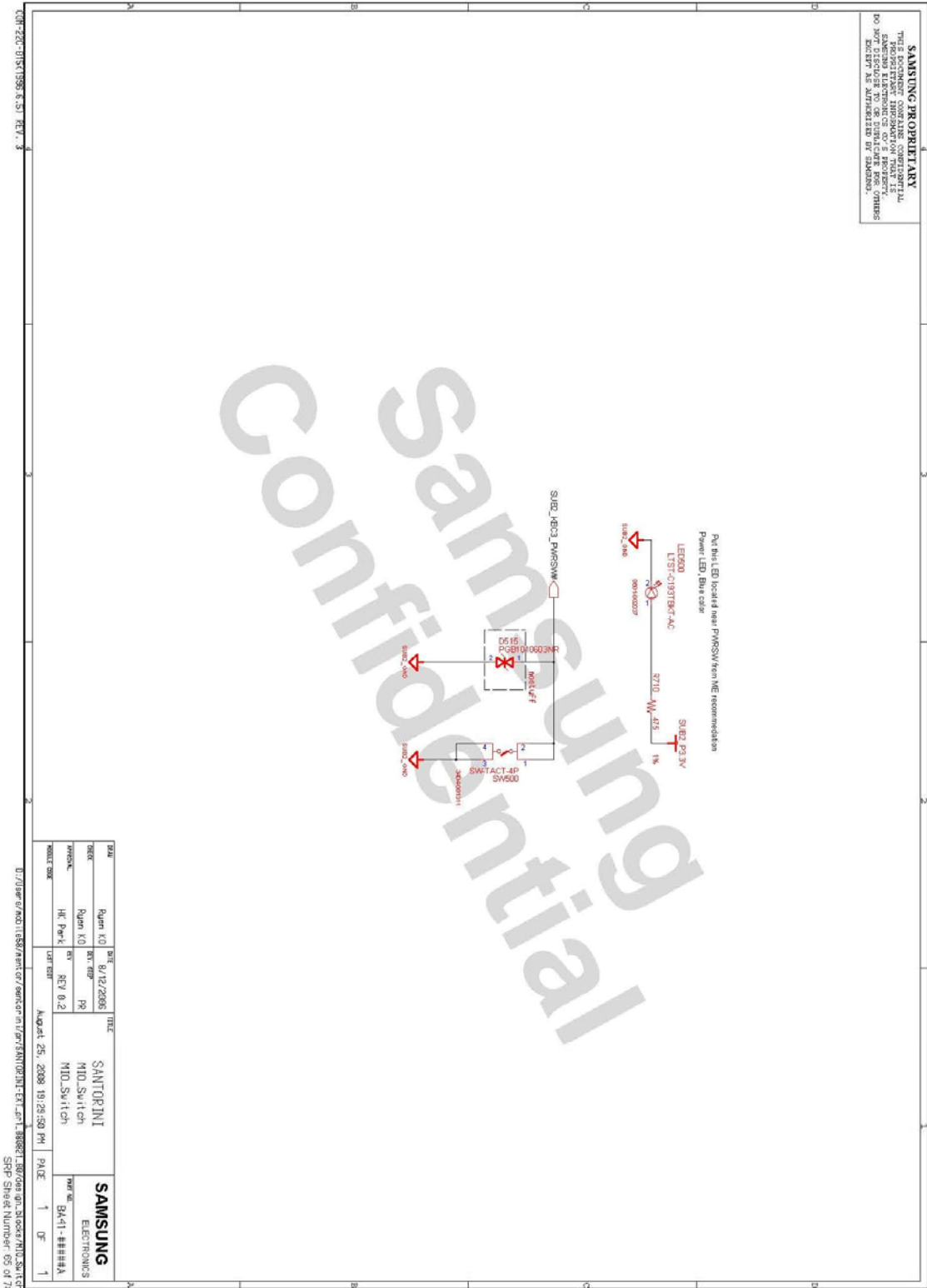
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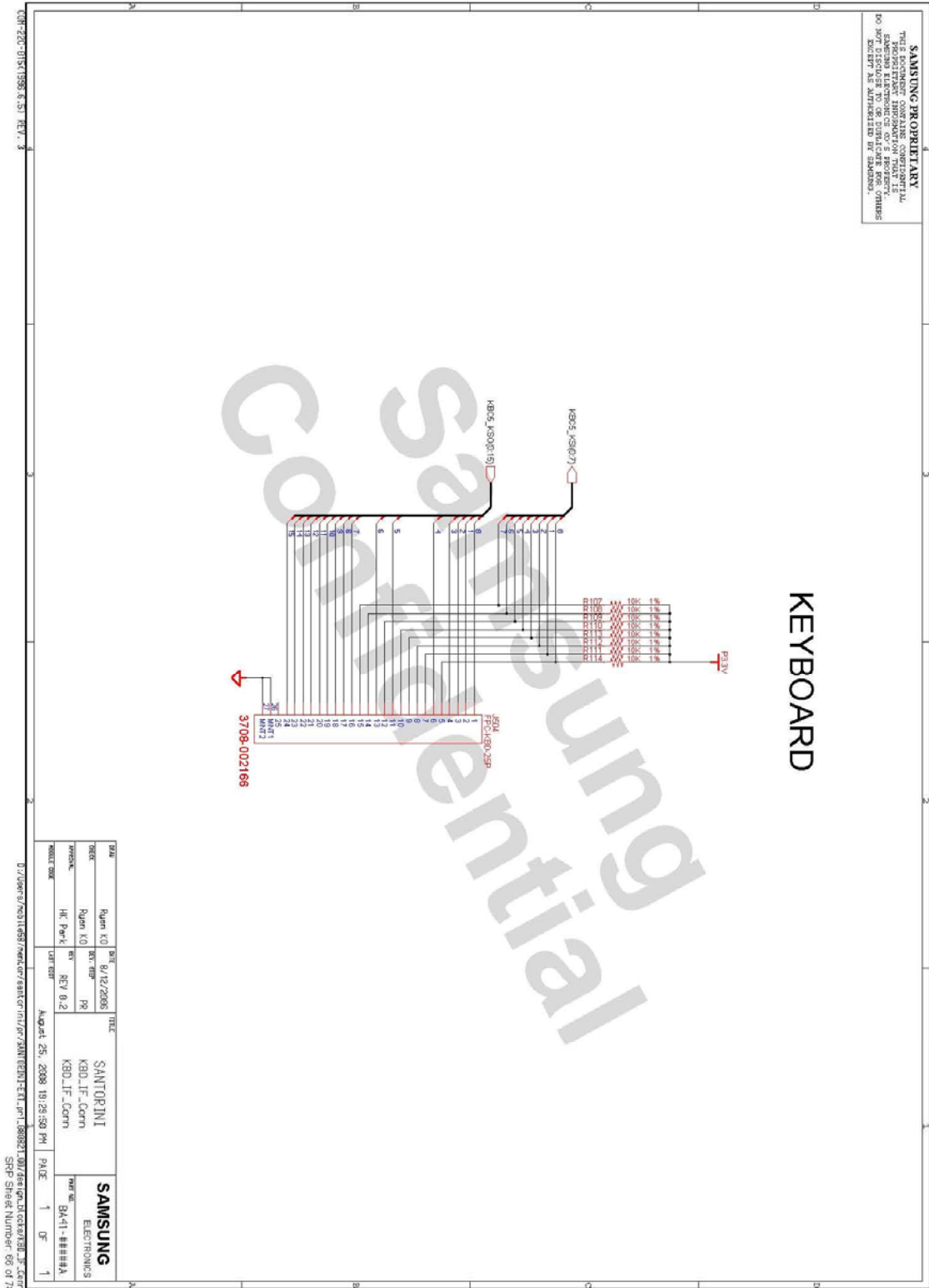
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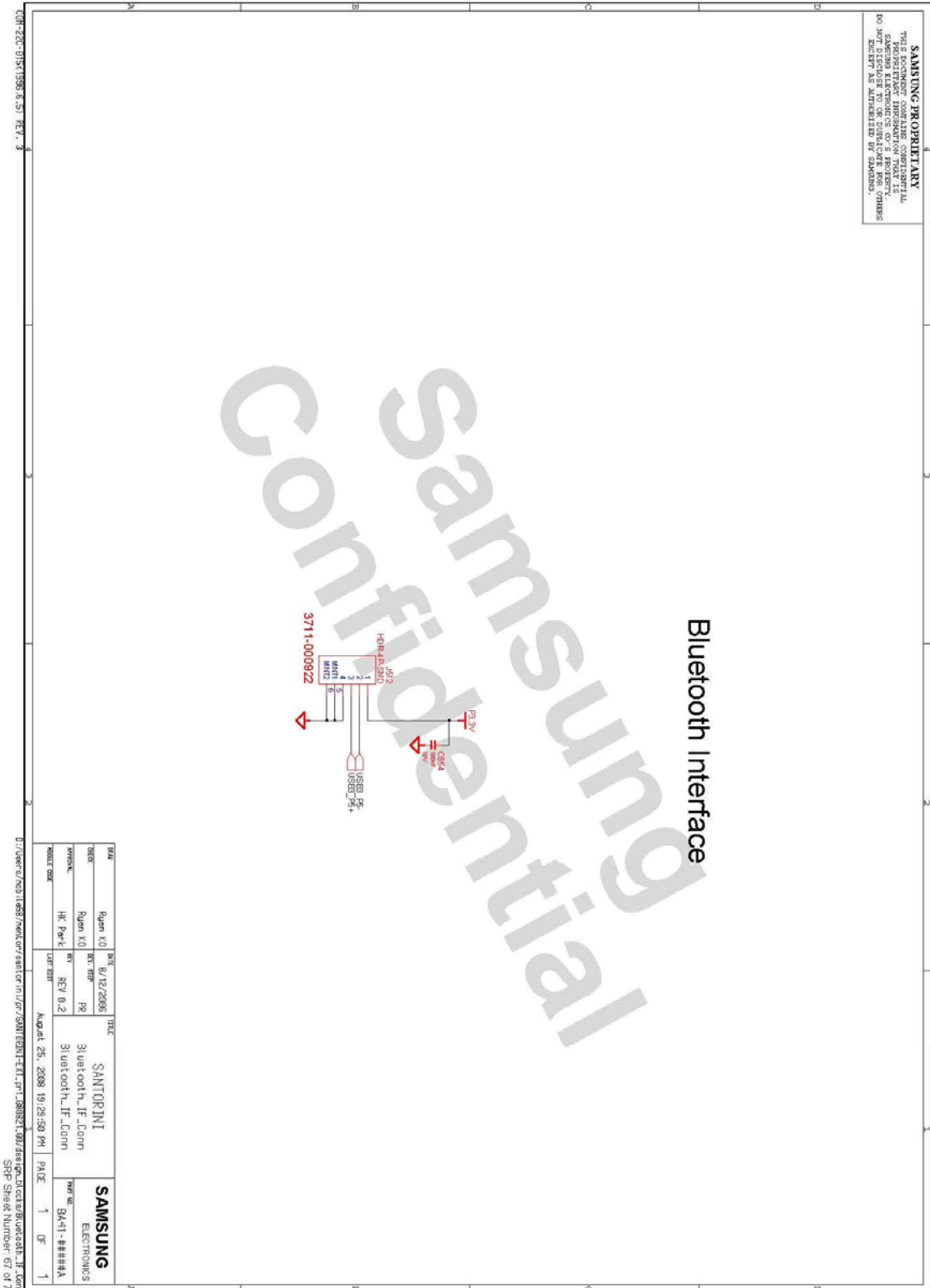
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**CAMERA**

Commercial

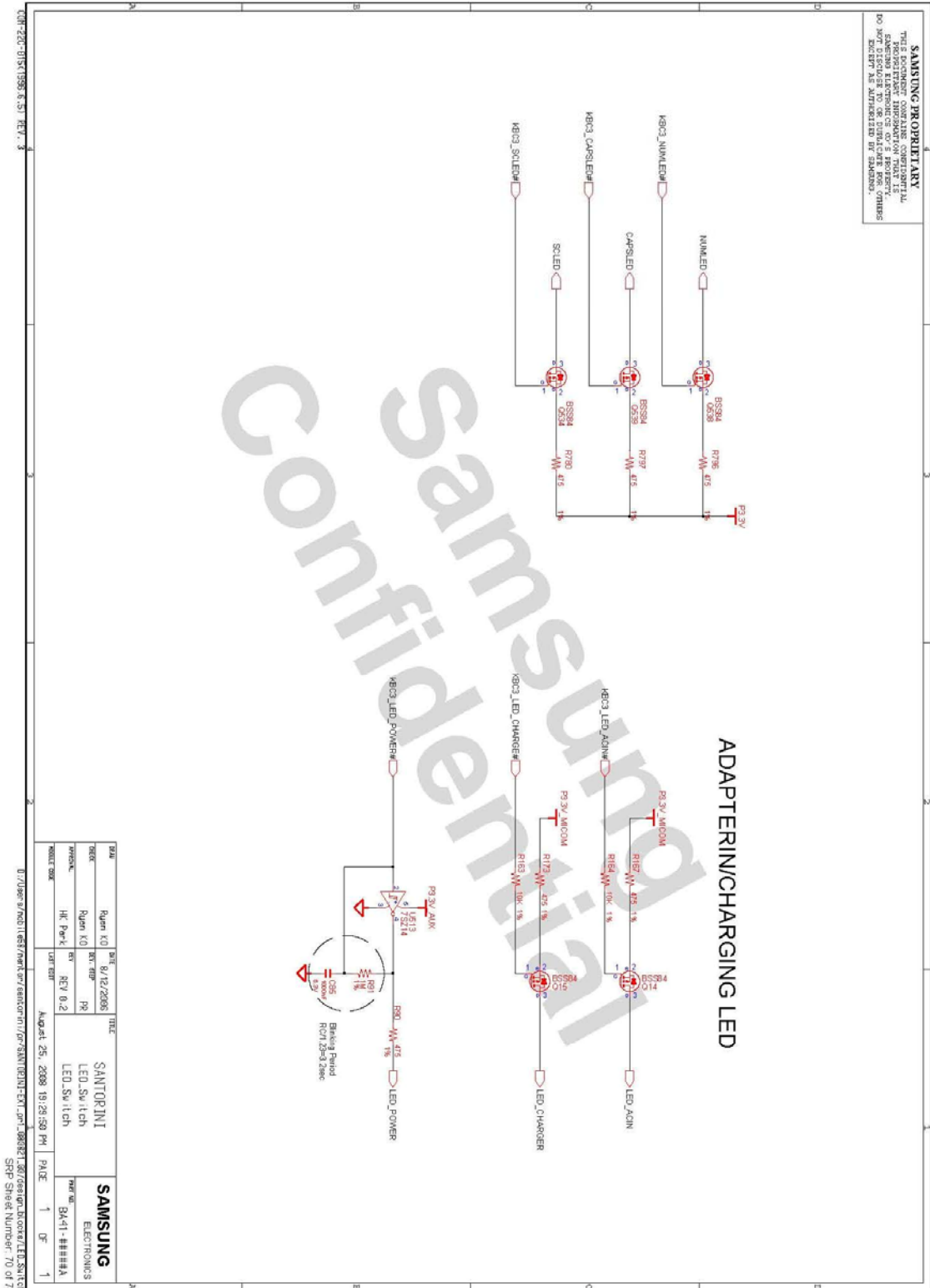
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DATE	DATE	TIME	SANTORINI		SAMSUNG	
RYUN K.O	8/12/2008		Camera_If_Conn		ELECTRONICS	
TYPECHK	RYUN K.O	PR	Camera_If_Conn		PART NO.	BA41-####A
TABLE NO.	HK Park	REV 0.2			DATE	1 OF 1
		August 25, 2008 10:29:50 PM			PAGE	1 OF 1

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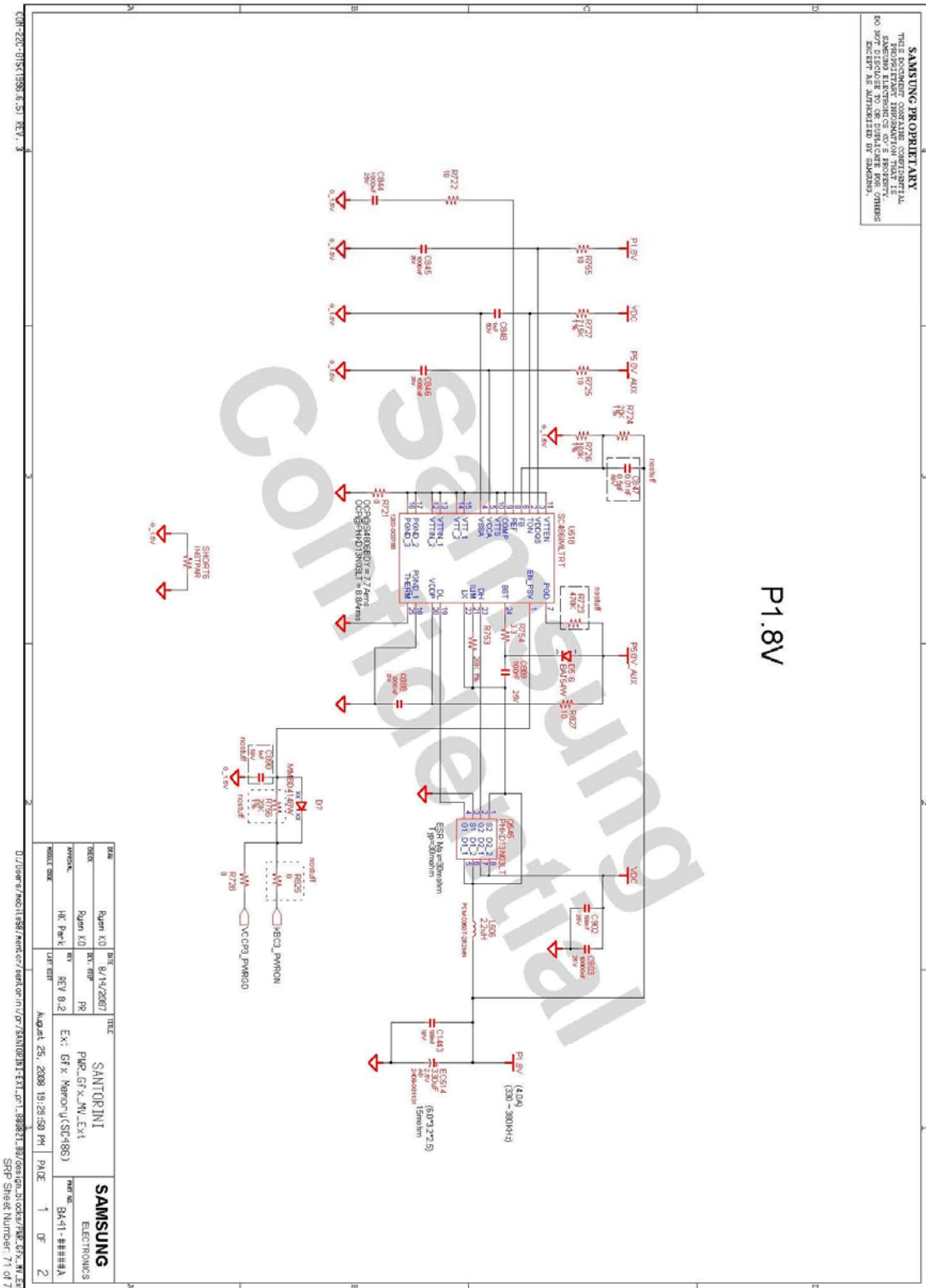
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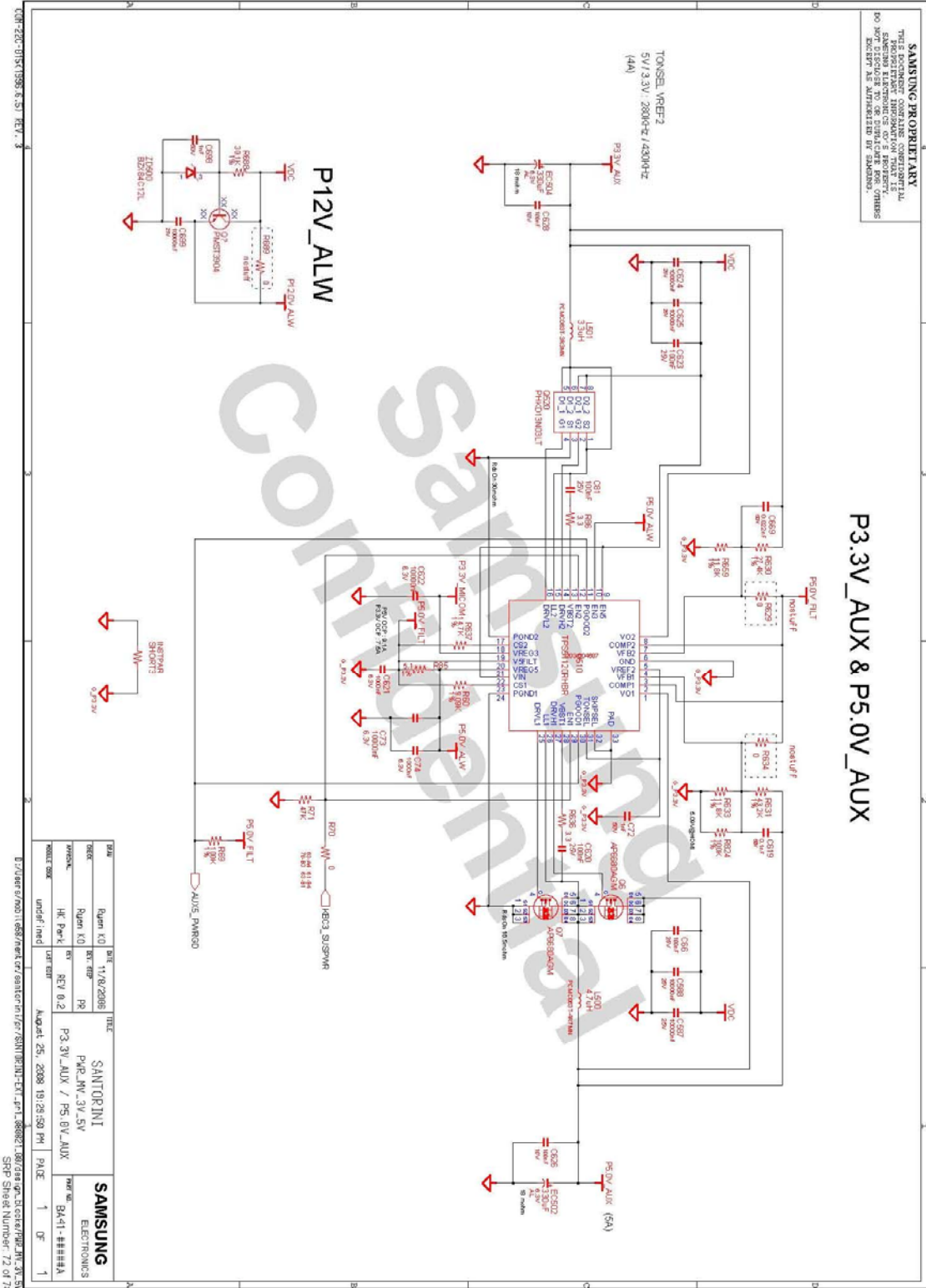


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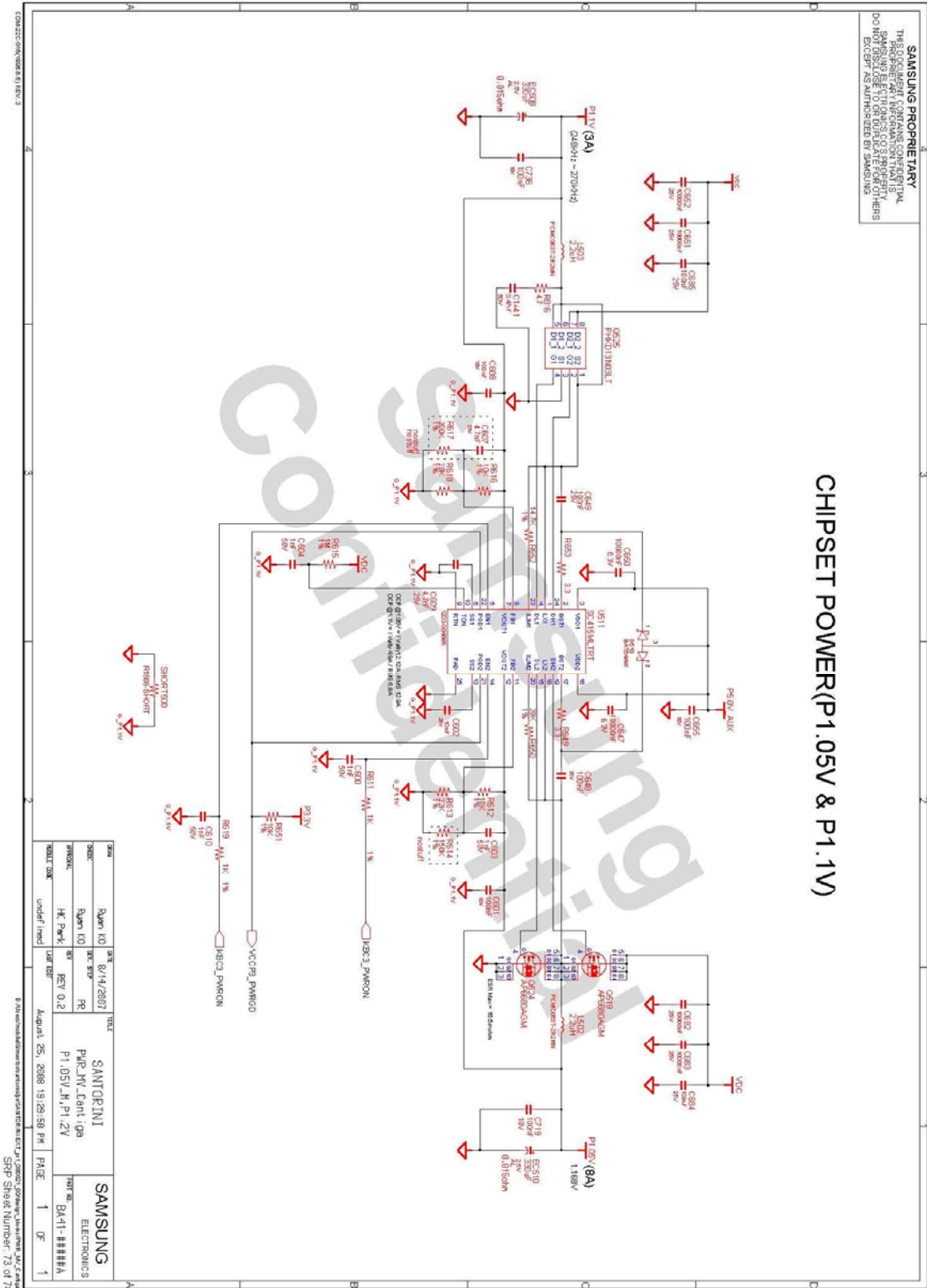
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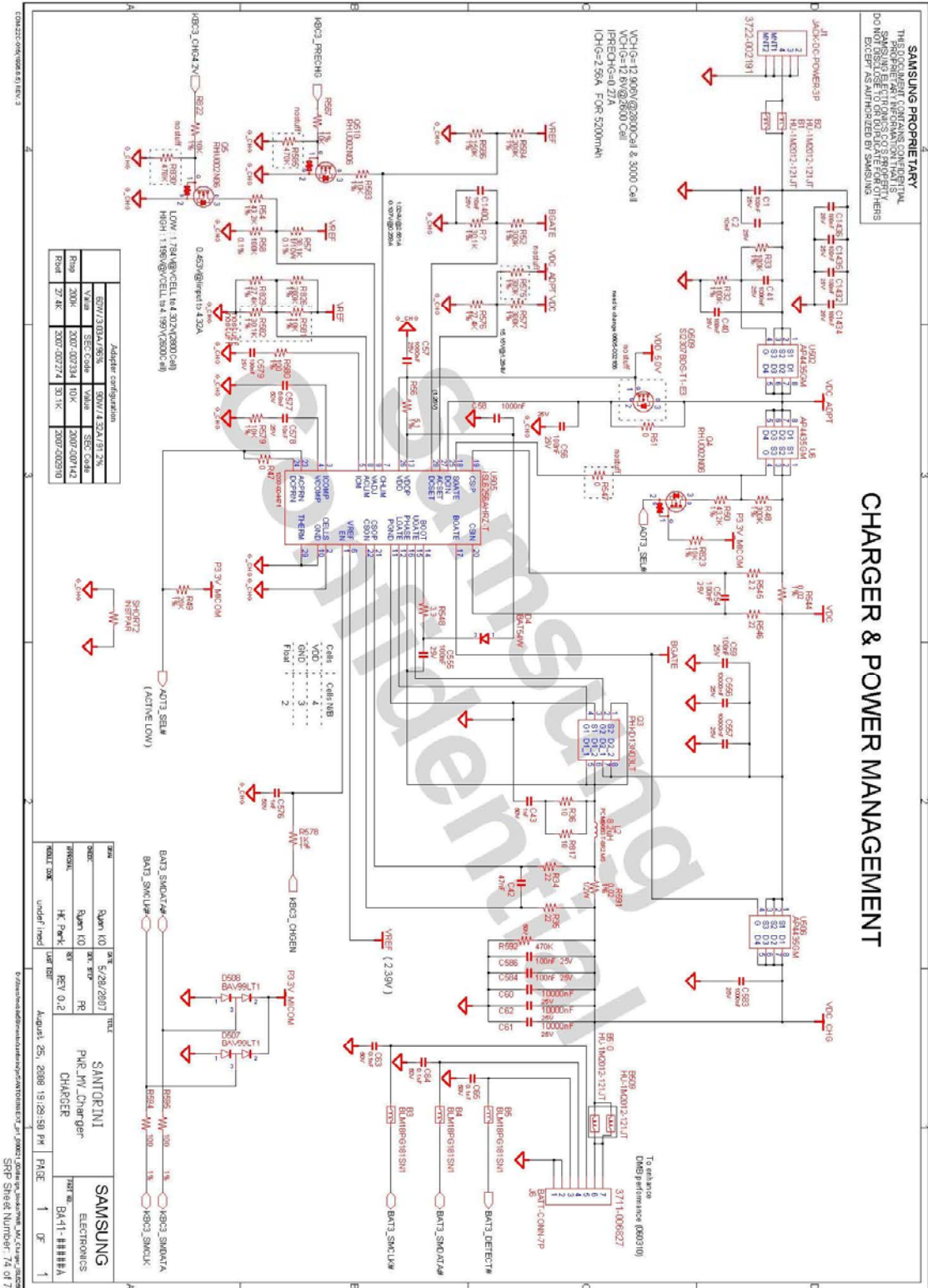
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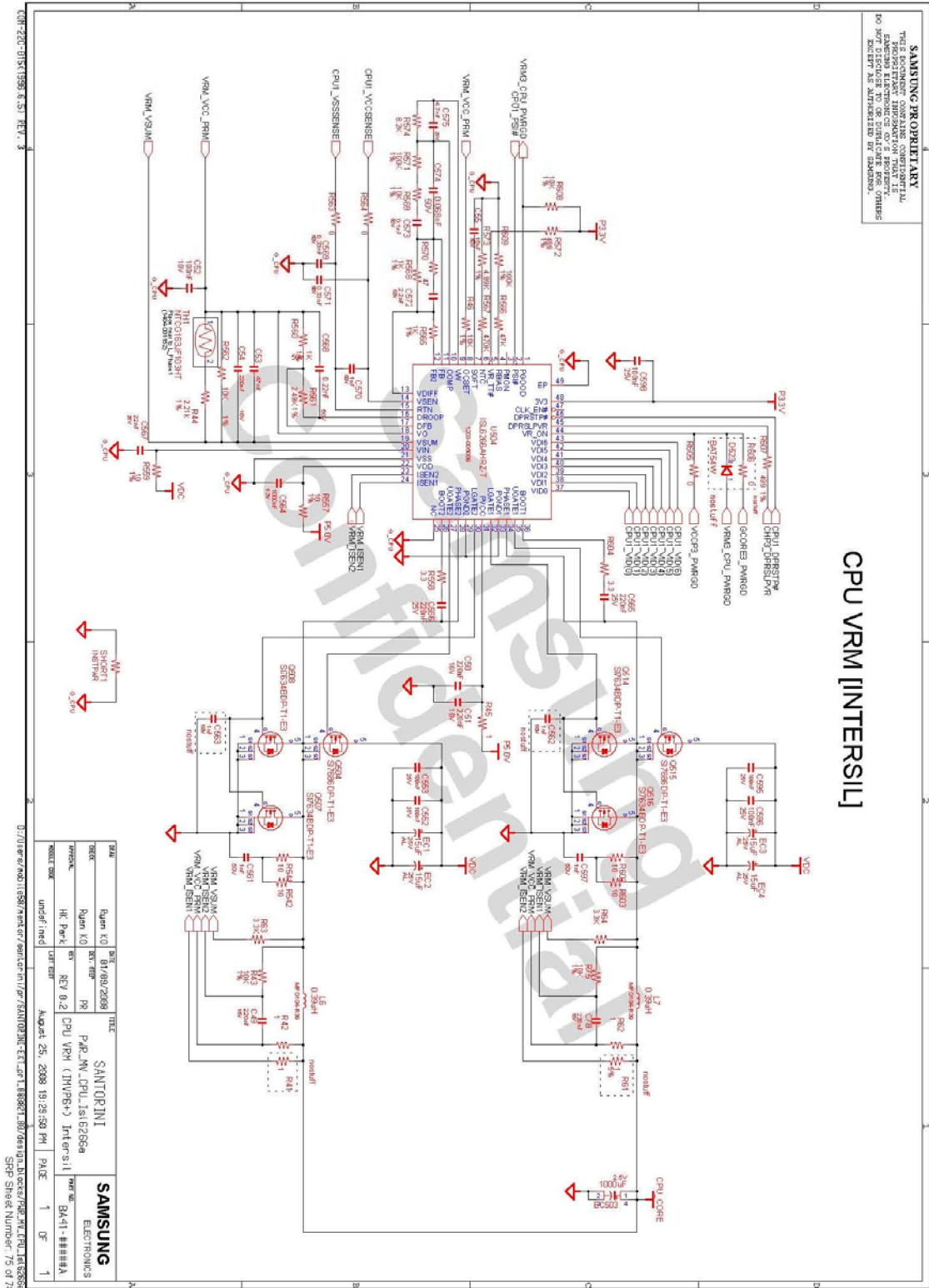
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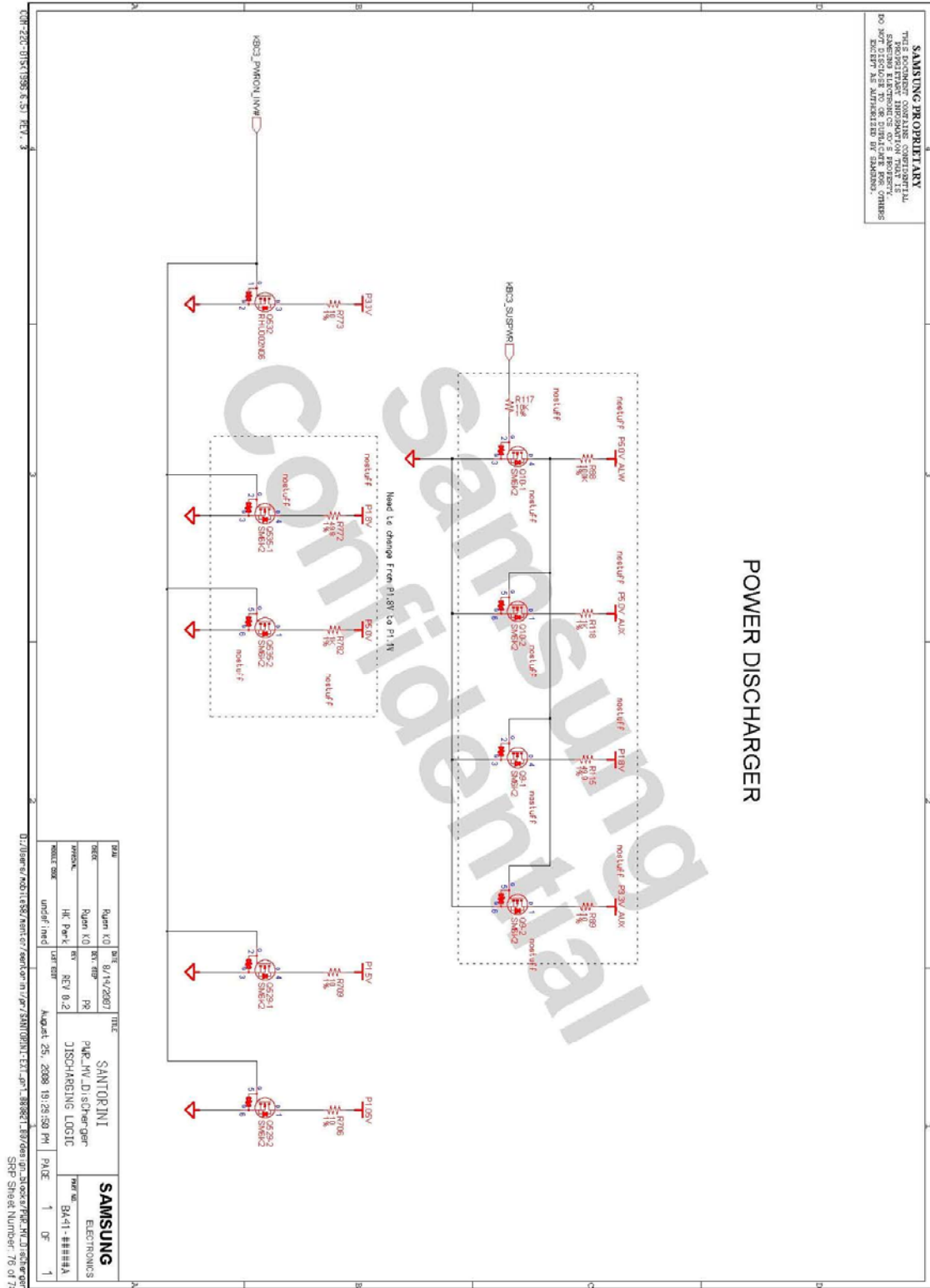
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