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| 12 | CALIST (VSS) 7/7 | 0.20 | 04/01 | 47 | SYS Power(+1_5V/+1_05V) | 0.20 | 04/01 |
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| 15 | DDR2(Termination) 3/3 | 0.20 | 04/01 | 50 | Others power plan | 0.20 | 04/01 |
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| 17 | CRT | 0.20 | 04/01 | 52 | History (1) | 0.20 | 04/01 |
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| 20 | CLOCK GEN | 0.20 | 04/01 | 55 | | | |
| 21 | ICH7-M(PCI/USB) 1/5 | 0.20 | 04/01 | 56 | | | |
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| 26 | SATA HDD/CD-ROM | 0.20 | 04/01 | | | | |
| 27 | EC+KBC | 0.20 | 04/01 | | | | |
| 28 | Flash ROM/X-Bus | 0.20 | 04/01 | | | | |
| 29 | LED/Touch PAD | 0.20 | 04/01 | | | | |
| 30 | Mini-PCIE Card | 0.20 | 04/01 | | | | |
| 31 | FAN | 0.20 | 04/01 | | | | |
| 32 | OIDE | 0.20 | 04/01 | | | | |
| 33 | AUDIO(CODEC & POWER) | 0.20 | 04/01 | | | | |
| 34 | AUDIO(AMP & HP & SPK) | 0.20 | 04/01 | | | | |
| 35 | AUDIO(EXTMIC) | 0.20 | 04/01 | | | | |

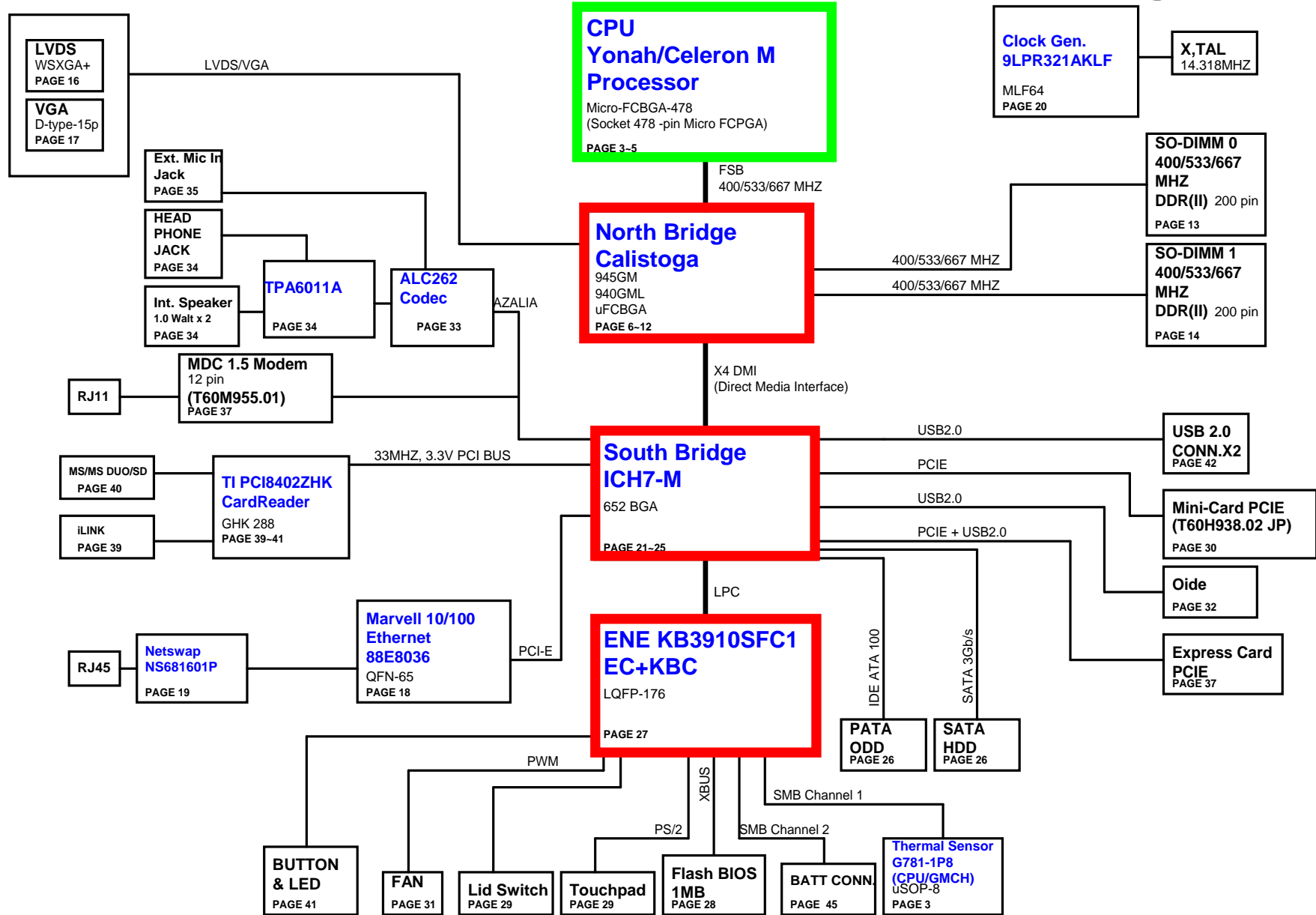
| | | |
|-----------|----------|-----------|
| P. Leader | Check by | Design by |
| | | |

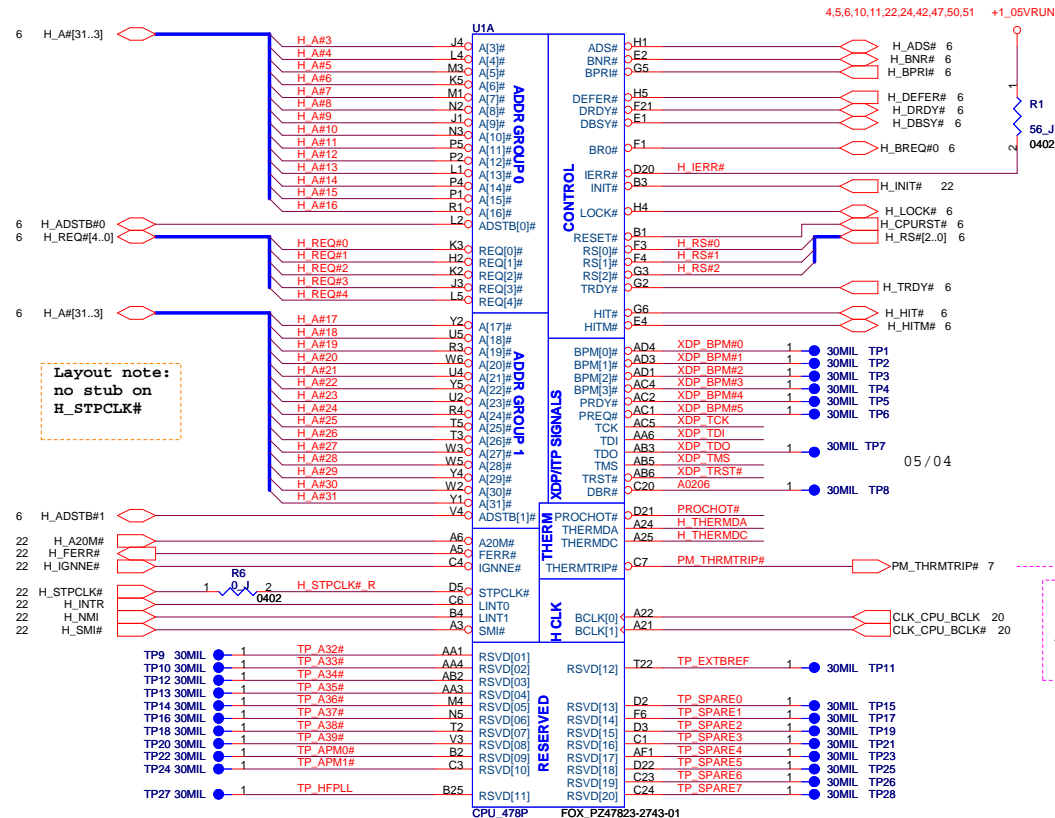
Project Code & Schematics Subject: MS70 Main Board

PCB P/N: (FUBAI) 1P-0063100-60SA
 (NA YA) 1P-0063200-60SA
 (HANSTAR) 1P-0063500-60SA

| | | | |
|----------------|-------------------------|--|---------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division | |
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RAPTOR/MS70(CALISTOGA GM/GML Block Diagram)

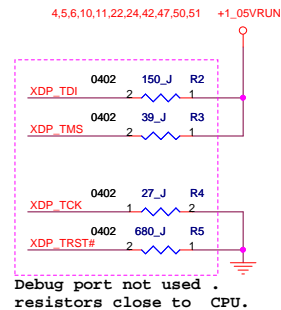




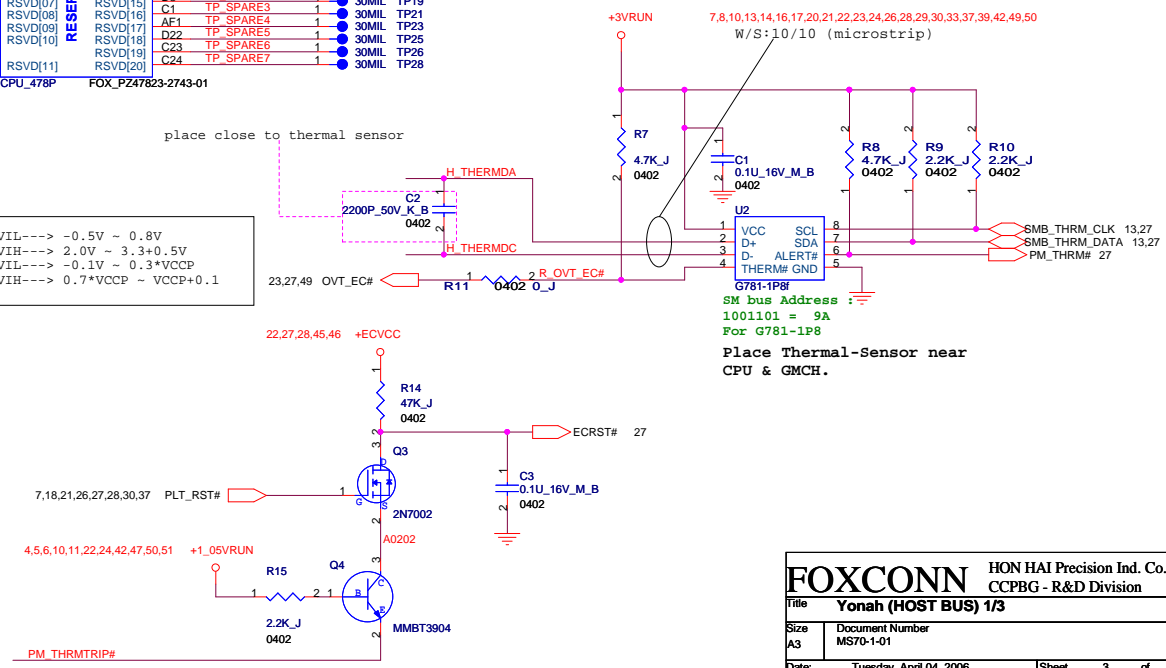
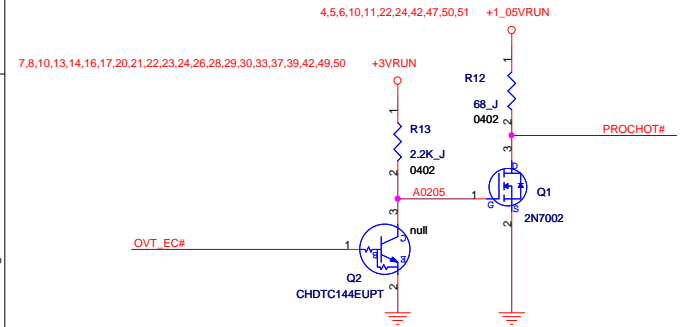
Layout note:
no stub on
H_STPCLK#

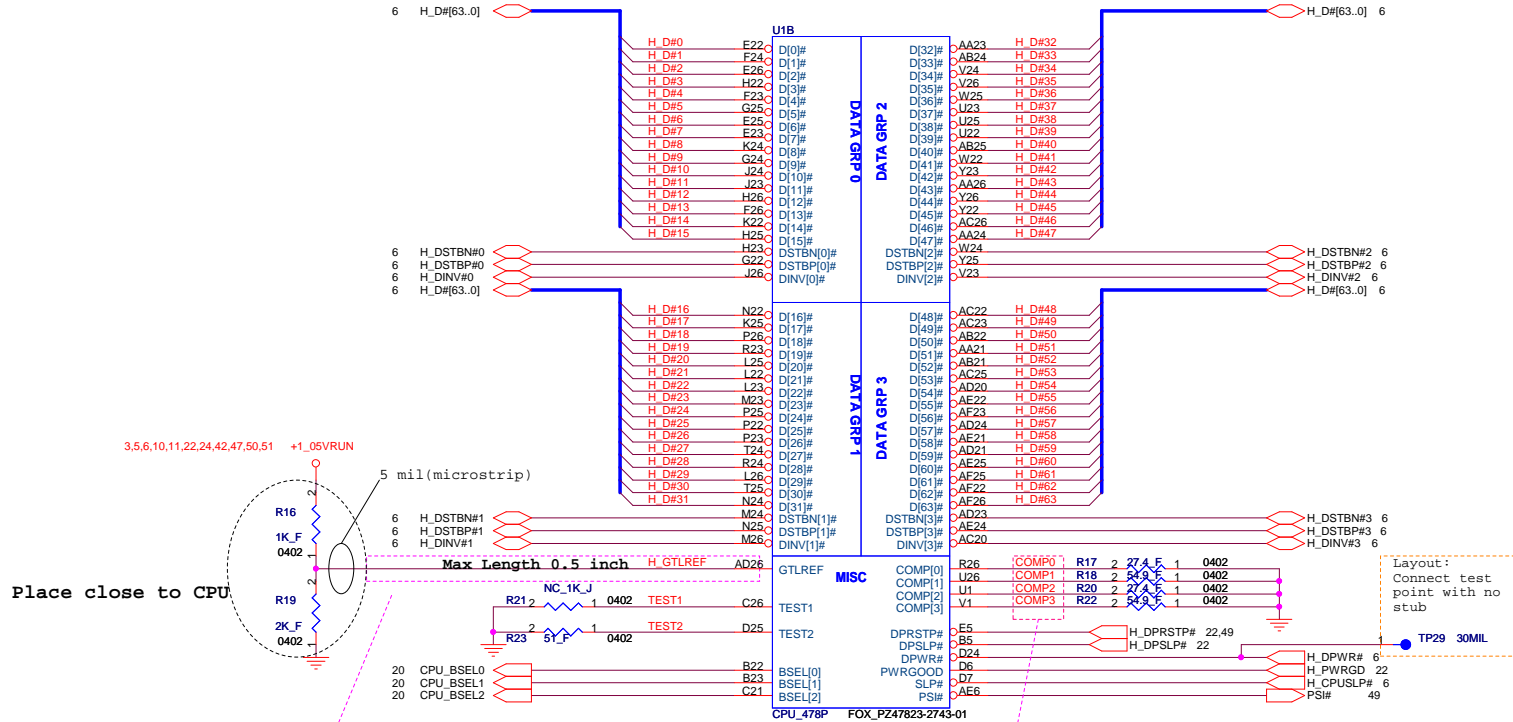
A#[32-39], APM#[0-1]:
Leave escape routing
on for future
functionality

ICH7M's GPIO12: VIL----> -0.5V ~ 0.8V
VIH----> 2.0V ~ 3.3+0.5V
YONAH's PROCHOT#: VIL----> -0.1V ~ 0.3*VCCP
VIH----> 0.7*VCCP ~ VCCP+0.1



PM_THRMTRIP#
should connect to
ICH7-M and GMCH
without T-ing (No
stub)





3.5,6,10,11,22,24,42,47,50,51 +1.05VRUN

5 mil (microstrip)

Place close to CPU

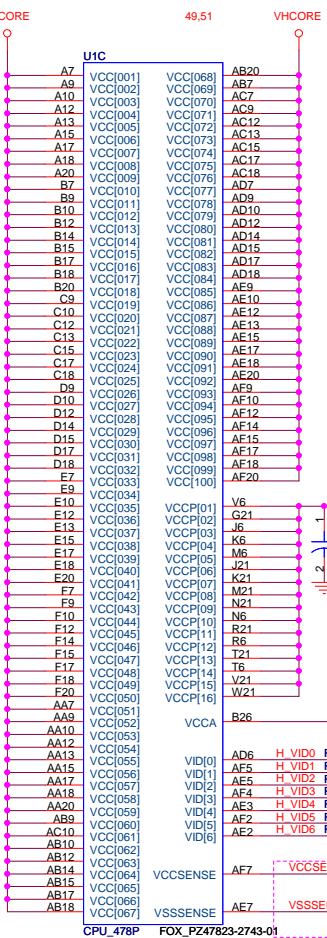
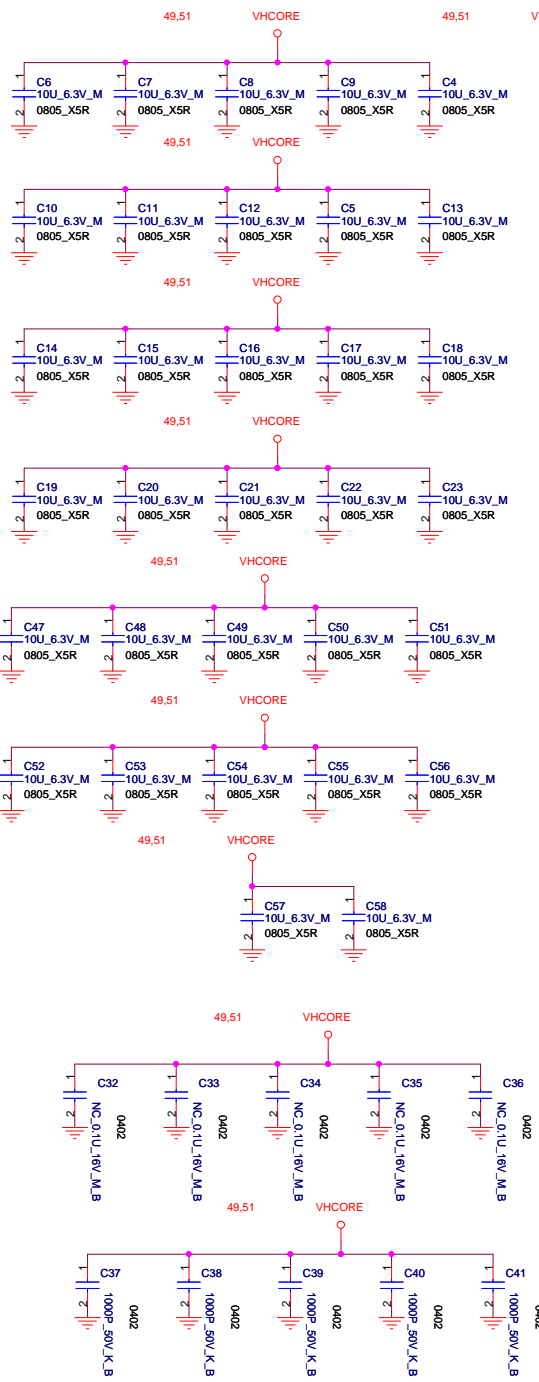
Max Length 0.5 inch H_GTLREF

Layout Note:
Zo=55 ohm, 0.5"
max for GTLREF.

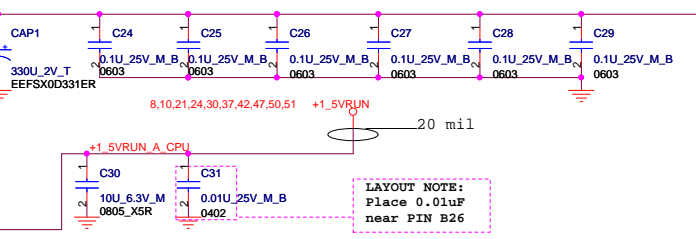
Layout Note:
Comp0,2 connect with Zo=27.4 ohm, make
trace length shorter then 0.5".
Comp1,3 connect with Zo=55 ohm, make
trace length shorter then 0.5".

IMVP6 (max8736)
cpu PSI# <-> max8736 PSI#
max8736: VIHmin=0.67V
 VILmax=0.33V
(ref. max8736 datasheet)

Layout:
Connect test
point with no
stub

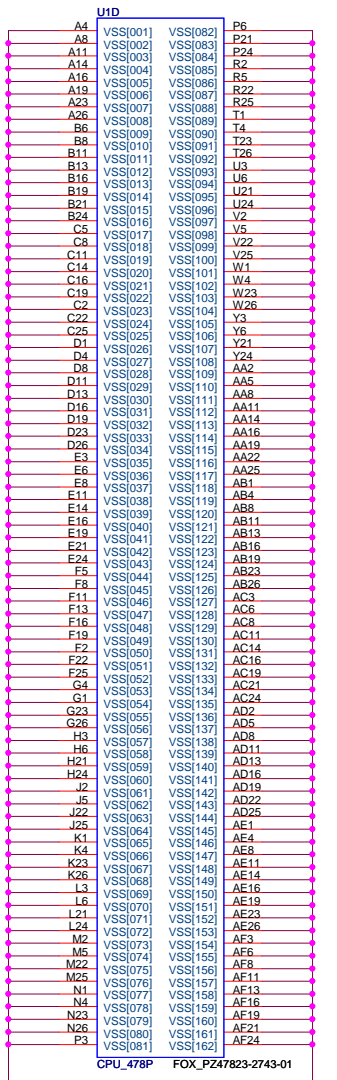
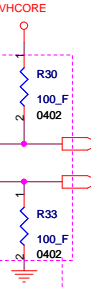
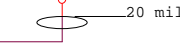
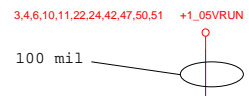


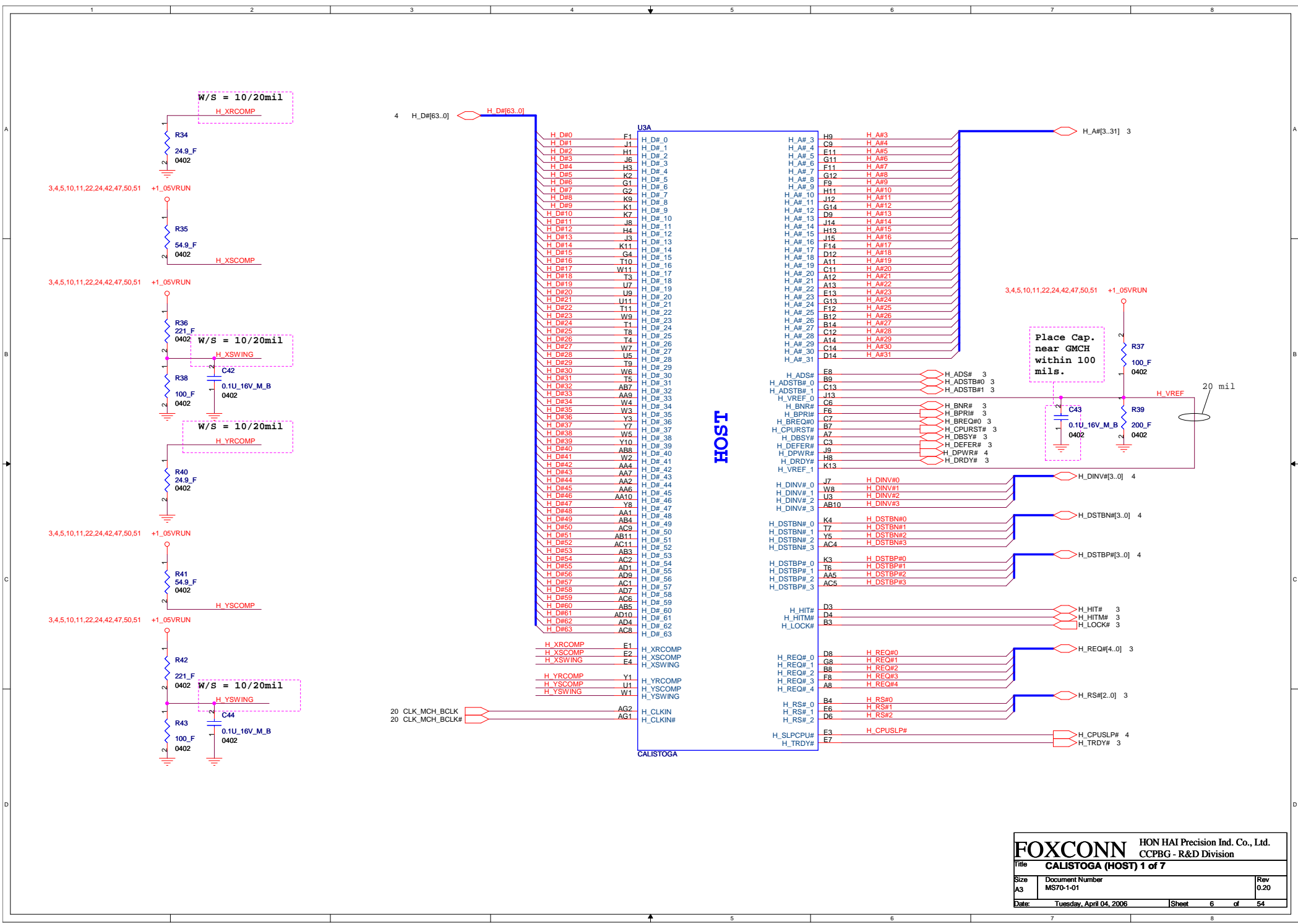
CPU_VCCA----->120mA
 CPU_VCCP----->2.5A
 CPU_VCC----->36A

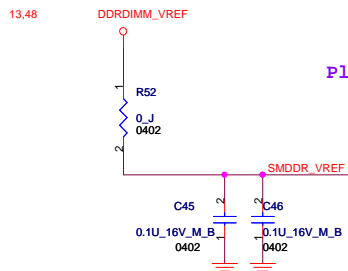
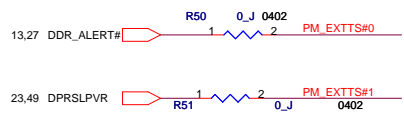
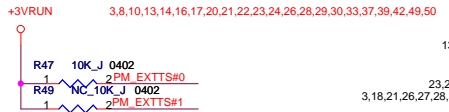
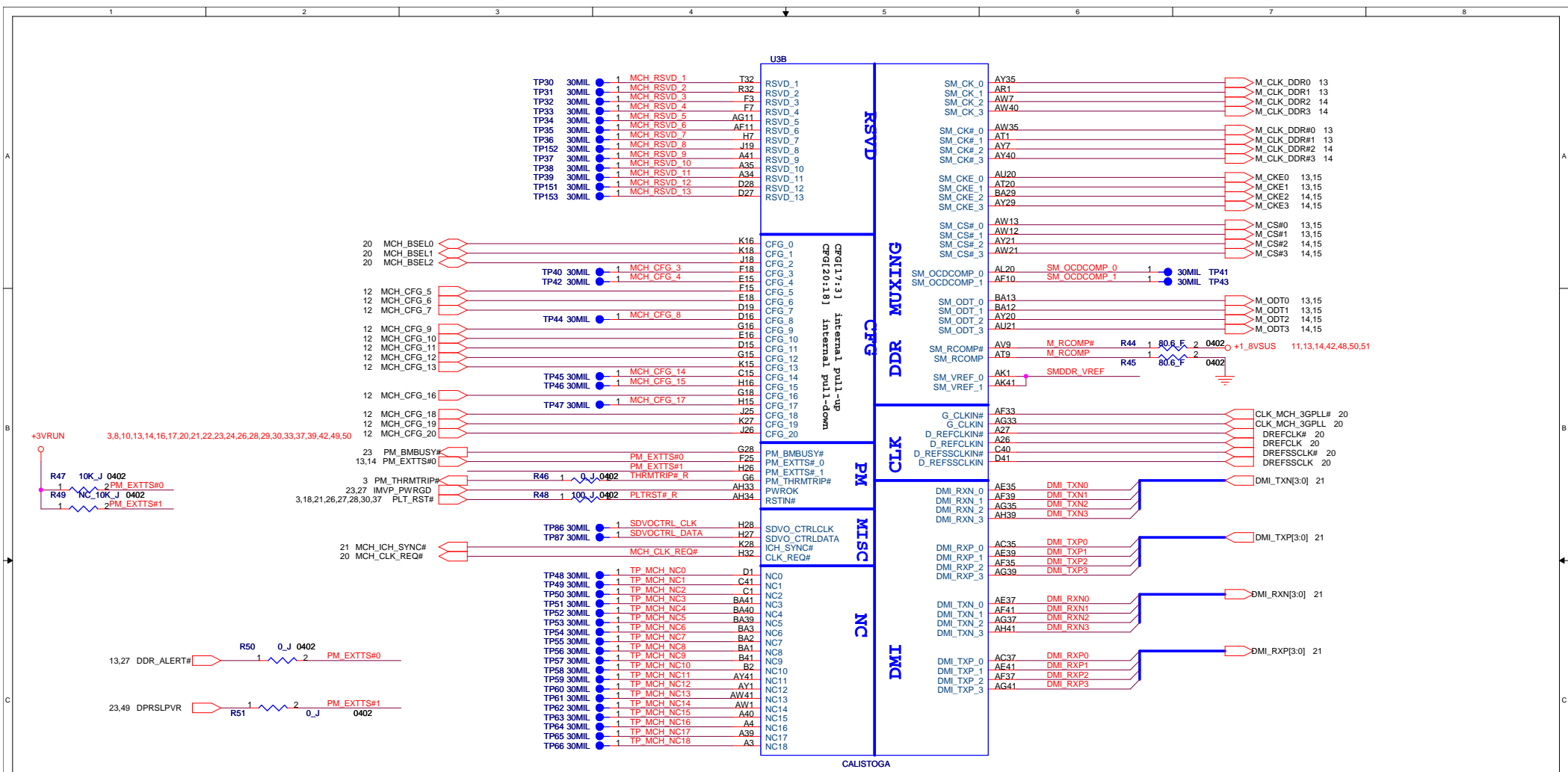


LAYOUT NOTE:
 Place 0.01µF
 near PIN B26

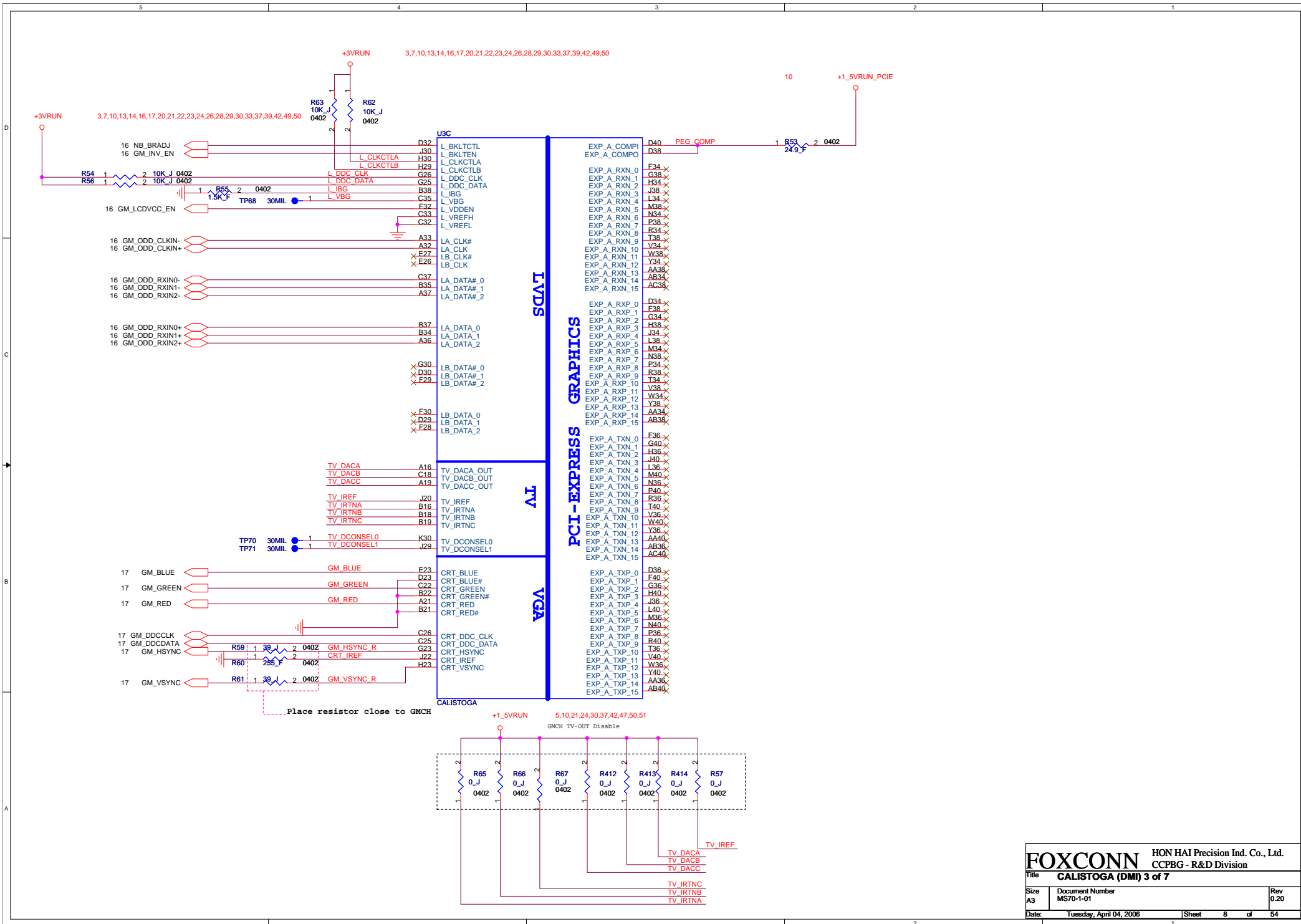
LAYOUT NOTE: Route
 VCCSENSE traces at 27.4
 Ohms with 50 mil spacing.
 Place PU and PD within 1
 inch of cpu.
 width=18 mil
 spacing=7 mil







Place close to chipset



13 M_A_DQ[63..0]

U3D

M_A DQ0 AJ35 SA_DQ0
 M_A DQ1 AJ34 SA_DQ1
 M_A DQ2 AM31 SA_DQ2
 M_A DQ3 AM33 SA_DQ3
 M_A DQ4 AJ36 SA_DQ4
 M_A DQ5 AK35 SA_DQ5
 M_A DQ6 AJ38 SA_DQ6
 M_A DQ7 AH31 SA_DQ7
 M_A DQ8 AN35 SA_DQ8
 M_A DQ9 AP33 SA_DQ9
 M_A DQ10 AR31 SA_DQ10
 M_A DQ11 AP31 SA_DQ11
 M_A DQ12 AN38 SA_DQ12
 M_A DQ13 AM36 SA_DQ13
 M_A DQ14 AM34 SA_DQ14
 M_A DQ15 AN33 SA_DQ15
 M_A DQ16 AK26 SA_DQ16
 M_A DQ17 AL27 SA_DQ17
 M_A DQ18 AM26 SA_DQ18
 M_A DQ19 AN24 SA_DQ19
 M_A DQ20 AK28 SA_DQ20
 M_A DQ21 AL28 SA_DQ21
 M_A DQ22 AM24 SA_DQ22
 M_A DQ23 AP26 SA_DQ23
 M_A DQ24 AP23 SA_DQ24
 M_A DQ25 AL22 SA_DQ25
 M_A DQ26 AP21 SA_DQ26
 M_A DQ27 AN20 SA_DQ27
 M_A DQ28 AL23 SA_DQ28
 M_A DQ29 AP24 SA_DQ29
 M_A DQ30 AP20 SA_DQ30
 M_A DQ31 AT21 SA_DQ31
 M_A DQ32 AR12 SA_DQ32
 M_A DQ33 AR14 SA_DQ33
 M_A DQ34 AP13 SA_DQ34
 M_A DQ35 AP12 SA_DQ35
 M_A DQ36 AT13 SA_DQ36
 M_A DQ37 AT12 SA_DQ37
 M_A DQ38 AL14 SA_DQ38
 M_A DQ39 AL12 SA_DQ39
 M_A DQ40 AK9 SA_DQ40
 M_A DQ41 AN7 SA_DQ41
 M_A DQ42 AK8 SA_DQ42
 M_A DQ43 AK7 SA_DQ43
 M_A DQ44 AP9 SA_DQ44
 M_A DQ45 AN9 SA_DQ45
 M_A DQ46 AT5 SA_DQ46
 M_A DQ47 AL5 SA_DQ47
 M_A DQ48 AY2 SA_DQ48
 M_A DQ49 AW2 SA_DQ49
 M_A DQ50 AP1 SA_DQ50
 M_A DQ51 AN2 SA_DQ51
 M_A DQ52 AY2 SA_DQ52
 M_A DQ53 AT3 SA_DQ53
 M_A DQ54 AN1 SA_DQ54
 M_A DQ55 AL2 SA_DQ55
 M_A DQ56 AG7 SA_DQ56
 M_A DQ57 AF9 SA_DQ57
 M_A DQ58 AG4 SA_DQ58
 M_A DQ59 AF6 SA_DQ59
 M_A DQ60 AG9 SA_DQ60
 M_A DQ61 AH6 SA_DQ61
 M_A DQ62 AF4 SA_DQ62
 M_A DQ63 AF8 SA_DQ63

DDR SYSTEM MEMORY A

SA_BS_0 AY12 M_A_BS0 13,15
 SA_BS_1 AY14 M_A_BS1 13,15
 SA_BS_2 BA20 M_A_BS2 13,15
 SA_CAS# AY13 M_A_CAS# 13,15
 SA_DM_0 M_B_DQ4 AJ38 M_A_DM[7..0] 13
 SA_DM_1 M_B_DQ5 AK36
 SA_DM_2 AL26 M_A_DM2
 SA_DM_3 AN22 M_A_DM3
 SA_DM_4 AM14 M_A_DM4
 SA_DM_5 AL9 M_A_DM5
 SA_DM_6 AR3 M_A_DM6
 SA_DM_7 AH4 M_A_DM7
 SA_DQS_0 AK33 M_A_DQS0
 SA_DQS_1 AT33 M_A_DQS1
 SA_DQS_2 AN28 M_A_DQS2
 SA_DQS_3 AM22 M_A_DQS3
 SA_DQS_4 AN12 M_A_DQS4
 SA_DQS_5 AN8 M_A_DQS5
 SA_DQS_6 AP3 M_A_DQS6
 SA_DQS_7 AG5 M_A_DQS7
 SA_DQS#_0 AK32 M_A_DQS#0
 SA_DQS#_1 AU33 M_A_DQS#1
 SA_DQS#_2 AN27 M_A_DQS#2
 SA_DQS#_3 AM21 M_A_DQS#3
 SA_DQS#_4 AM12 M_A_DQS#4
 SA_DQS#_5 AL8 M_A_DQS#5
 SA_DQS#_6 AN3 M_A_DQS#6
 SA_DQS#_7 AH5 M_A_DQS#7
 SA_MA_0 AY16 M_A_A0
 SA_MA_1 AU14 M_A_A1
 SA_MA_2 AW16 M_A_A2
 SA_MA_3 BA16 M_A_A3
 SA_MA_4 BA17 M_A_A4
 SA_MA_5 AU16 M_A_A5
 SA_MA_6 AV17 M_A_A6
 SA_MA_7 AU17 M_A_A7
 SA_MA_8 AU17 M_A_A8
 SA_MA_9 AT16 M_A_A9
 SA_MA_10 AU13 M_A_A10
 SA_MA_11 AT17 M_A_A11
 SA_MA_12 AV20 M_A_A12
 SA_MA_13 AV12 M_A_A13
 SA_RAS# AW14 M_A_RAS# 13,15
 SA_RCVENIN# AK23 TP MA RCVENIN#
 SA_RCVENOUT# AK24 TP MA RCVENOUT#
 SA_WE# AY14 M_A_WE# 13,15

14 M_B_DQ[63..0]

U3E

M_B DQ0 AK39 SB_DQ0
 M_B DQ1 AJ37 SB_DQ1
 M_B DQ2 AP39 SB_DQ2
 M_B DQ3 AR41 SB_DQ3
 M_B DQ4 AJ38 SB_DQ4
 M_B DQ5 AK36 SB_DQ5
 M_B DQ6 AN41 SB_DQ6
 M_B DQ7 AP41 SB_DQ7
 M_B DQ8 AT40 SB_DQ8
 M_B DQ9 AV41 SB_DQ9
 M_B DQ10 AU38 SB_DQ10
 M_B DQ11 AV38 SB_DQ11
 M_B DQ12 AP38 SB_DQ12
 M_B DQ13 AR40 SB_DQ13
 M_B DQ14 AW38 SB_DQ14
 M_B DQ15 AV36 SB_DQ15
 M_B DQ16 BA38 SB_DQ16
 M_B DQ17 AV36 SB_DQ17
 M_B DQ18 AR36 SB_DQ18
 M_B DQ19 AP36 SB_DQ19
 M_B DQ20 BA36 SB_DQ20
 M_B DQ21 AU36 SB_DQ21
 M_B DQ22 AP35 SB_DQ22
 M_B DQ23 AR36 SB_DQ23
 M_B DQ24 AY33 SB_DQ24
 M_B DQ25 BA33 SB_DQ25
 M_B DQ26 AT31 SB_DQ26
 M_B DQ27 AU29 SB_DQ27
 M_B DQ28 AU31 SB_DQ28
 M_B DQ29 AW31 SB_DQ29
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 M_B DQ38 AP15 SB_DQ38
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 M_B DQ42 AJ9 SB_DQ42
 M_B DQ43 AN10 SB_DQ43
 M_B DQ44 AK13 SB_DQ44
 M_B DQ45 AH11 SB_DQ45
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 M_B DQ51 AW4 SB_DQ51
 M_B DQ52 AY10 SB_DQ52
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 M_B DQ54 AW5 SB_DQ54
 M_B DQ55 AY5 SB_DQ55
 M_B DQ56 AV4 SB_DQ56
 M_B DQ57 AR5 SB_DQ57
 M_B DQ58 AK4 SB_DQ58
 M_B DQ59 AK3 SB_DQ59
 M_B DQ60 AT4 SB_DQ60
 M_B DQ61 AK5 SB_DQ61
 M_B DQ62 AJ5 SB_DQ62
 M_B DQ63 AJ3 SB_DQ63

DDR SYSTEM MEMORY B

SB_BS_0 AT24 M_B_BS0 14,15
 SB_BS_1 AV23 M_B_BS1 14,15
 SB_BS_2 AY28 M_B_BS2 14,15
 SB_CAS# AR24 M_B_CAS# 14,15
 SB_DM_0 AK36 M_B_DM0
 SB_DM_1 AR38 M_B_DM1
 SB_DM_2 AT36 M_B_DM2
 SB_DM_3 BA31 M_B_DM3
 SB_DM_4 AL17 M_B_DM4
 SB_DM_5 AH8 M_B_DM5
 SB_DM_6 BA5 M_B_DM6
 SB_DM_7 AN4 M_B_DM7
 SB_DQS_0 AM39 M_B_DQS0
 SB_DQS_1 AT39 M_B_DQS1
 SB_DQS_2 AU35 M_B_DQS2
 SB_DQS_3 AR29 M_B_DQS3
 SB_DQS_4 AR16 M_B_DQS4
 SB_DQS_5 AR10 M_B_DQS5
 SB_DQS_6 ARZ M_B_DQS6
 SB_DQS_7 AN5 M_B_DQS7
 SB_DQS#_0 AM40 M_B_DQS#0
 SB_DQS#_1 AU39 M_B_DQS#1
 SB_DQS#_2 AT35 M_B_DQS#2
 SB_DQS#_3 AP29 M_B_DQS#3
 SB_DQS#_4 AP16 M_B_DQS#4
 SB_DQS#_5 AT10 M_B_DQS#5
 SB_DQS#_6 AT7 M_B_DQS#6
 SB_DQS#_7 AP5 M_B_DQS#7
 SB_MA_0 AY23 M_B_A0
 SB_MA_1 AW24 M_B_A1
 SB_MA_2 AY24 M_B_A2
 SB_MA_3 AR28 M_B_A3
 SB_MA_4 AT27 M_B_A4
 SB_MA_5 AT28 M_B_A5
 SB_MA_6 AU27 M_B_A6
 SB_MA_7 AV28 M_B_A7
 SB_MA_8 AV27 M_B_A8
 SB_MA_9 AW27 M_B_A9
 SB_MA_10 AV24 M_B_A10
 SB_MA_11 BA27 M_B_A11
 SB_MA_12 AY27 M_B_A12
 SB_MA_13 AR23 M_B_A13
 SB_RAS# AU23 M_B_RAS# 14,15
 SB_RCVENIN# AK16 TP MB RCVENIN#
 SB_RCVENOUT# AK18 TP MB RCVENOUT#
 SB_WE# AR27 M_B_WE# 14,15

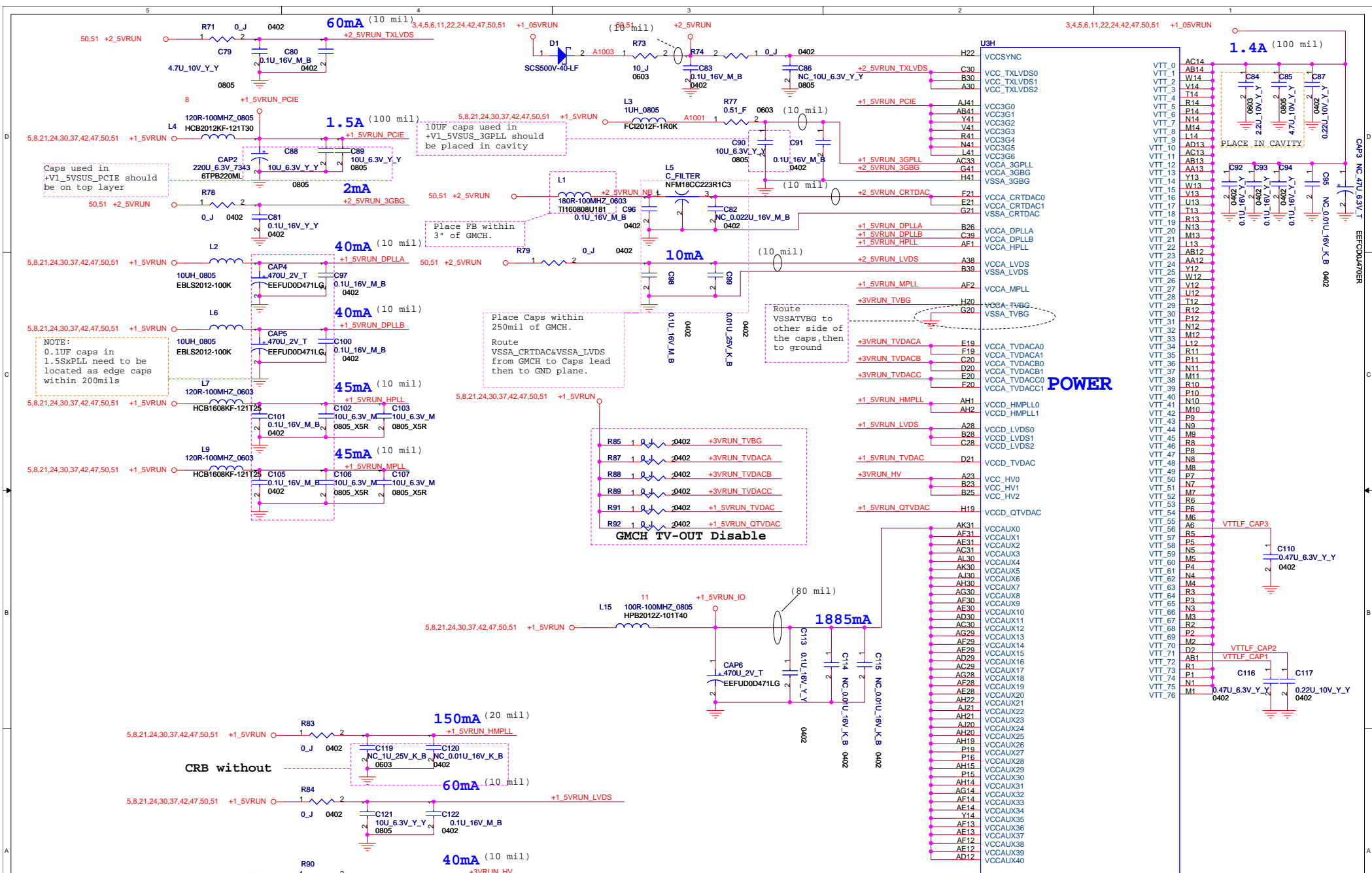
CALISTOGA

CALISTOGA

FOXCONN HON HAI Precision Ind. Co., Ltd.
 CCPBG - R&D Division

Title: **CALISTOGA (DDRII) 4 of 7**

| | | |
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POWER

FOXCONN HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

Title: **CALISTOGA(POWER,VCC) 5 of 7**

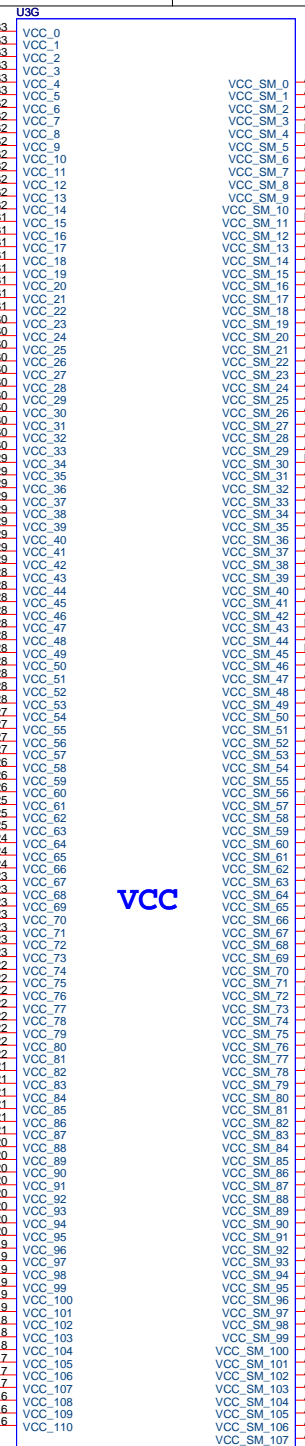
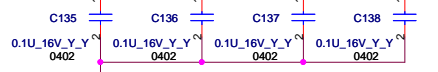
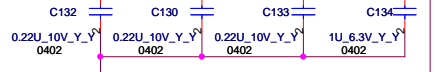
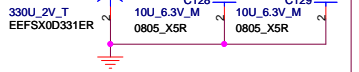
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|-------------------------------|----------------------------|-----------|
| Size: A3 | Document Number: MS70-1-01 | Rev: 0.20 |
| Date: Tuesday, April 04, 2006 | Sheet: 10 | of 54 |

3,4,5,6,10,22,24,42,47,50,51 +1.05VRUN

4.6A

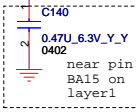
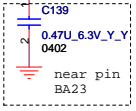
(200 mil)

CAP7
330U_2V_T
EFSX0D331ER



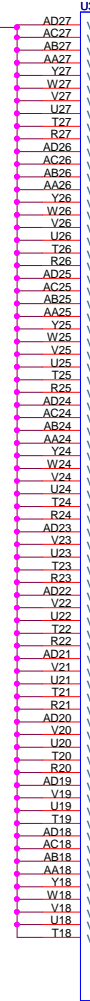
VCC

Note: All VCCSM pins shorted internally.



3,4,5,6,10,22,24,42,47,50,51 +1.05VRUN

C127
NC_0.1uF_16V_Y_Y
0402



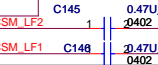
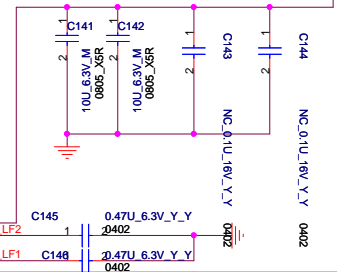
NCTF

CALISTOGA

7,13,14,42,48,50,51 +1.8VSUS

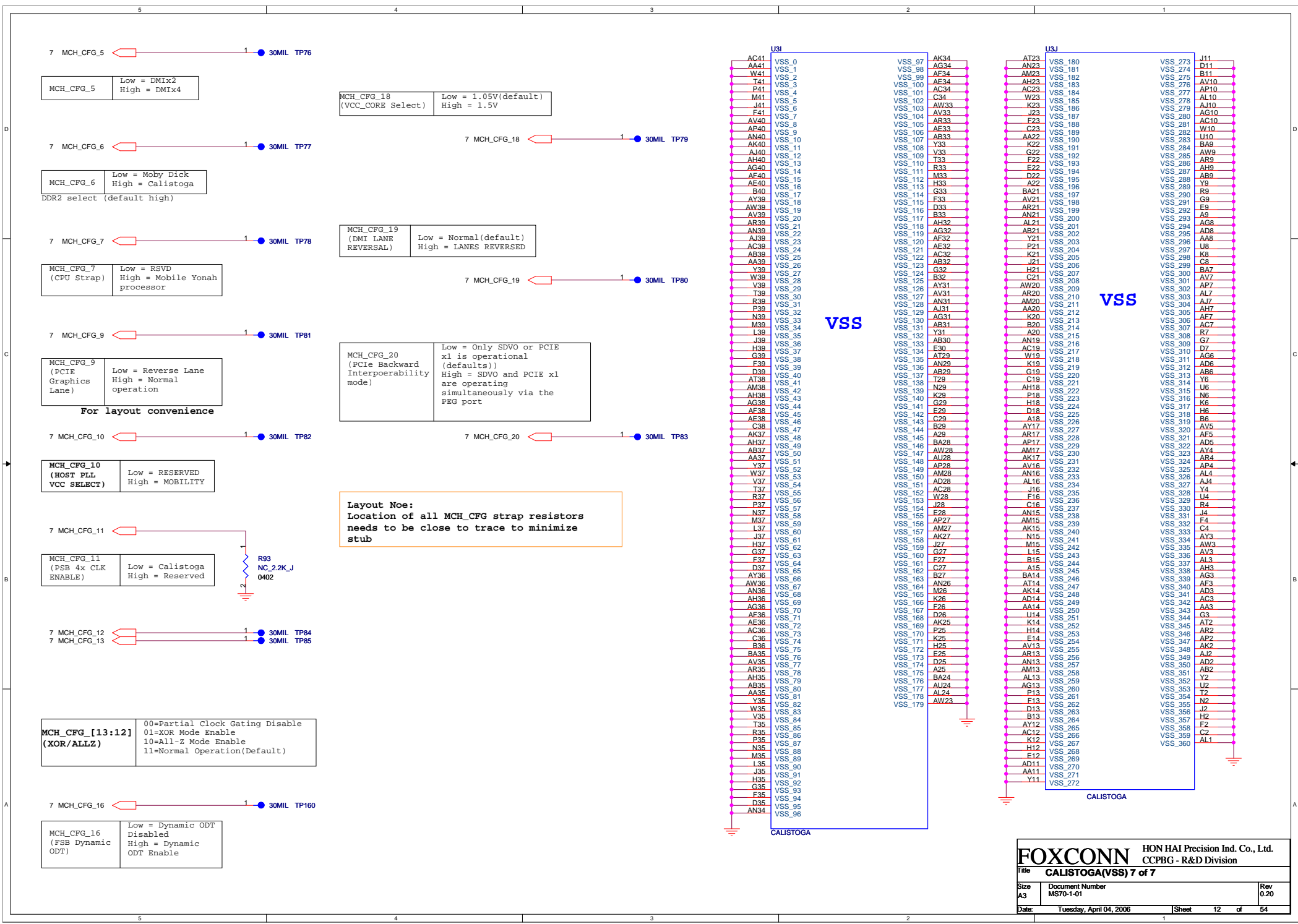
3.1A

(150 mil)

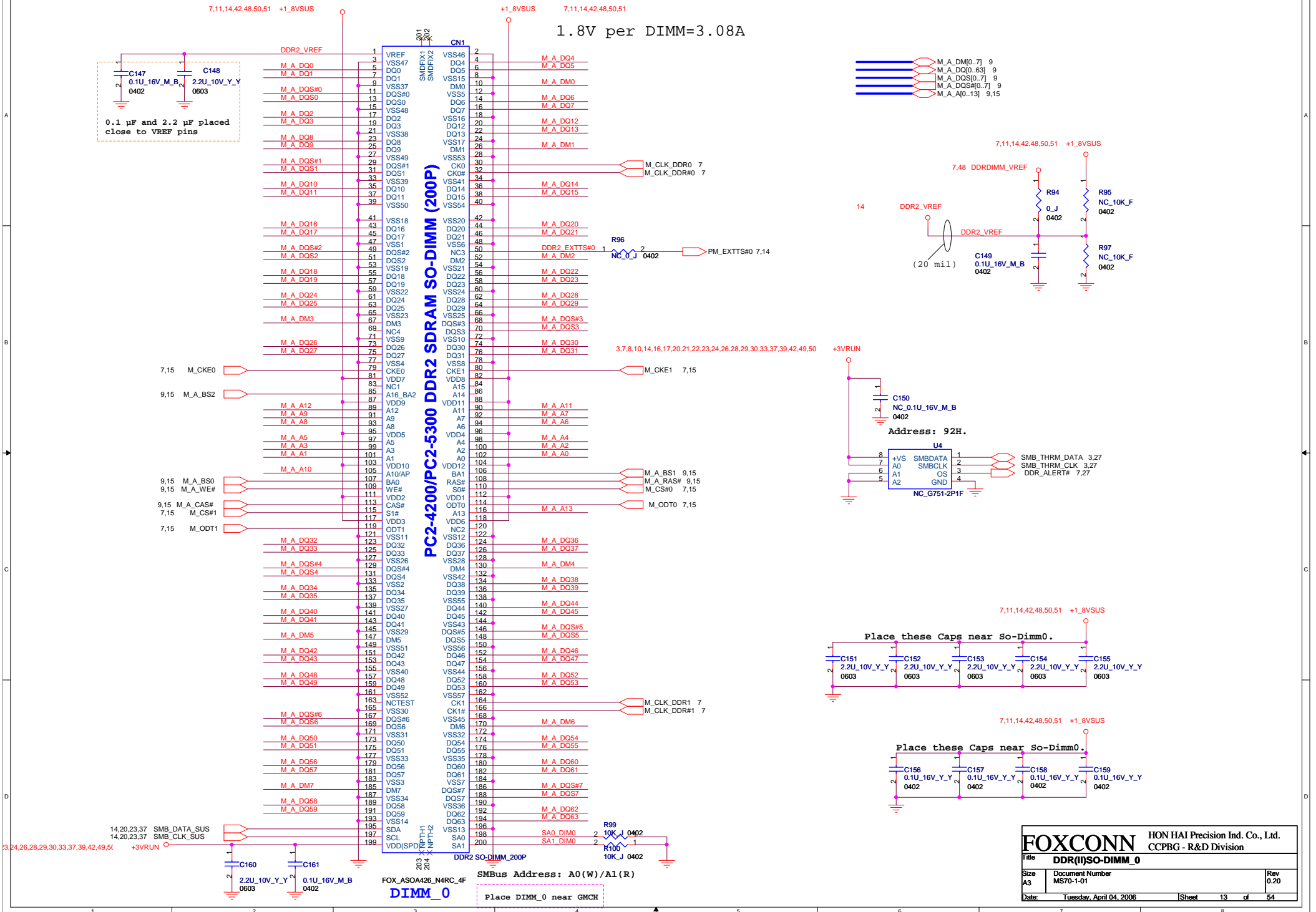
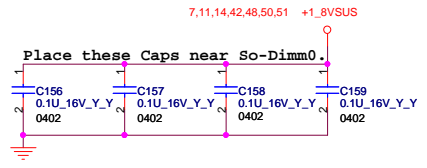
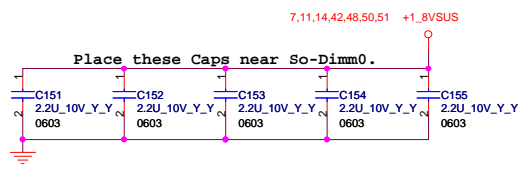
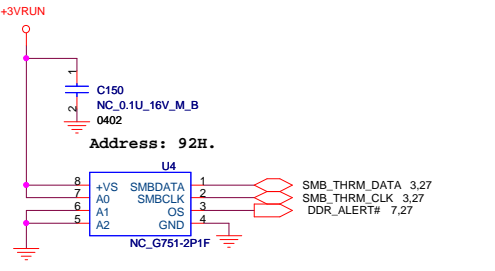
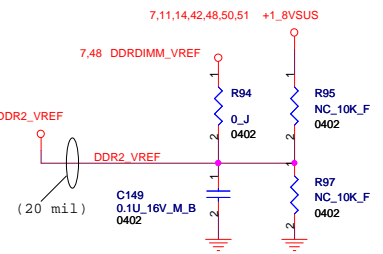
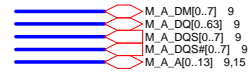
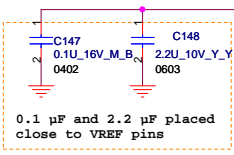


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CCPBG - R&D Division

| | | |
|-----------------------------------|----------------------------|-----------|
| Title: CALISTOGA(VCC CORE) 6 of 7 | | |
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| Date: Tuesday, April 04, 2006 | Sheet: 11 | of 54 |



1.8V per DIMM=3.08A



FOXCONN HON HAI Precision Ind. Co., Ltd.
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Title: **DDR(I)SO-DIMM_0**

| | | |
|-------------------------------|---------------------------|----------|
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| Date: Tuesday, April 04, 2006 | Sheet 13 | of 54 |

13

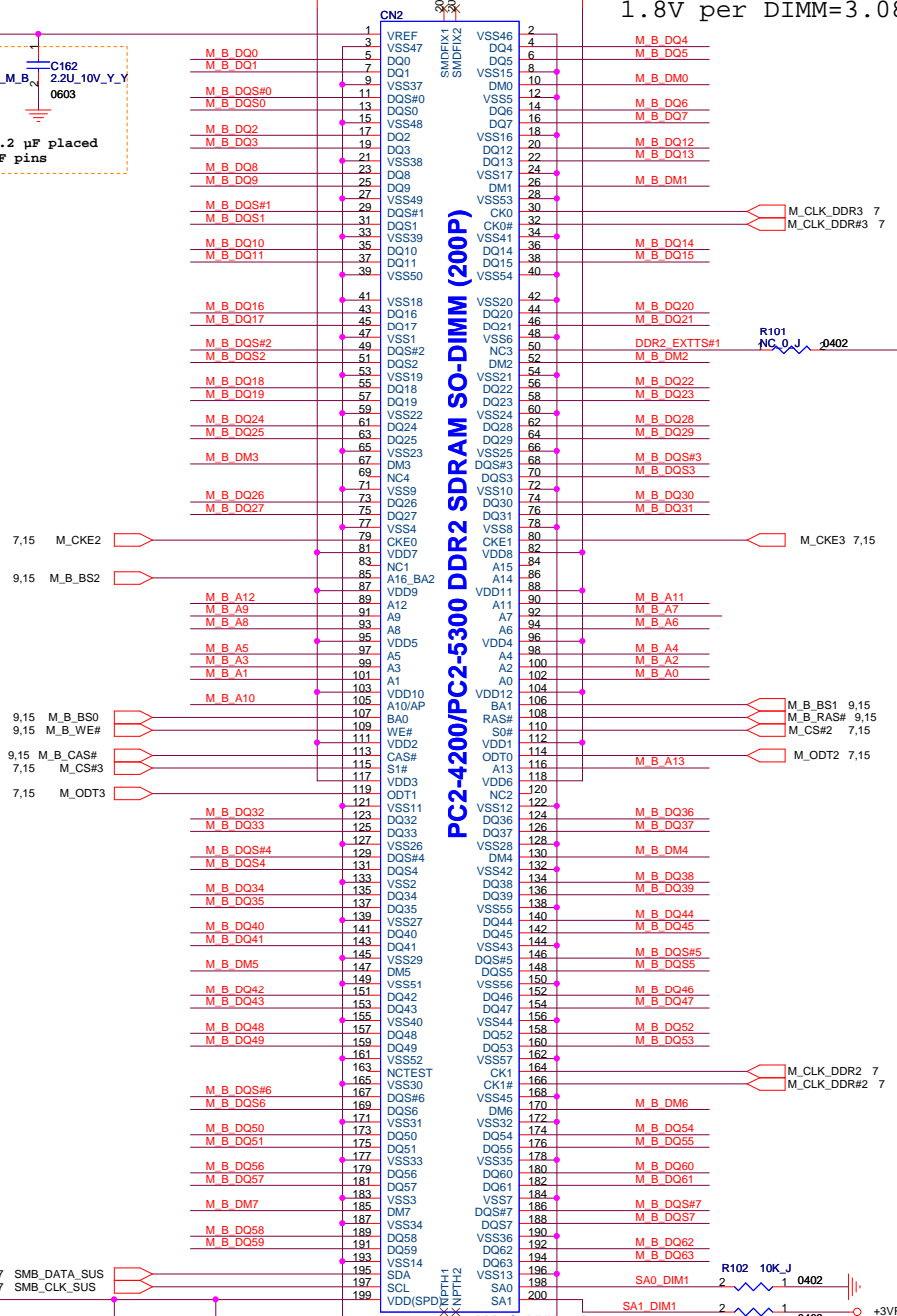
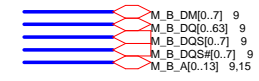
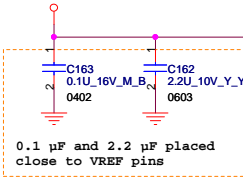
DDR2_VREF

7,11,13,42,48,50,51 +1.8VSUS

+1.8VSUS

7,11,13,42,48,50,51

1.8V per DIMM=3.08A



PM_EXTTS#0 7,13

M_CKE3 7,15

7,15 M_CKE2

9,15 M_B_BS2

9,15 M_B_BS0

9,15 M_B_WE#

9,15 M_B_CAS#

7,15 M_CS#3

7,15 M_ODT3

M_B_DQ32

M_B_DQ33

M_B_DQS#4

M_B_DQ34

M_B_DQ35

M_B_DQ40

M_B_DQ41

M_B_DM5

M_B_DQ42

M_B_DQ43

M_B_DQ48

M_B_DQ49

M_B_DQS#6

M_B_DQS6

M_B_DQ50

M_B_DQ51

M_B_DQ56

M_B_DQ57

M_B_DM7

M_B_DQ58

M_B_DQ59

SMB_DATA_SUS

SMB_CLK_SUS

+3VRUN

2.2u_10V_Y_Y

0.1u_16V_M_B

0603

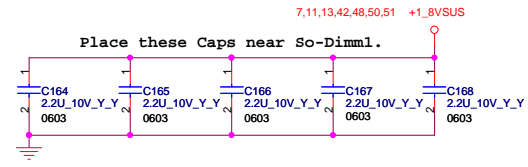
0402

FOX_ASOA426_N4SC_4F

DIMM_1

SMBus Address: A4(W)/A5 (R)

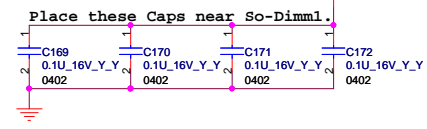
DIMM_1 is placed farther from the GMCH than DIMM_0



7,11,13,42,48,50,51 +1.8VSUS

Place these Caps near So-Dimm1.

7,11,13,42,48,50,51 +1.8VSUS



7,11,13,42,48,50,51 +1.8VSUS

Place these Caps near So-Dimm1.

8,7,8,10,13,16,17,20,21,22,23,24,26,28,29,30,33,37,39,42,49,50

+3VRUN

SMB_DATA_SUS

SMB_CLK_SUS

2.2u_10V_Y_Y

0.1u_16V_M_B

0603

0402

FOX_ASOA426_N4SC_4F

DIMM_1

SMBus Address: A4(W)/A5 (R)

DIMM_1 is placed farther from the GMCH than DIMM_0

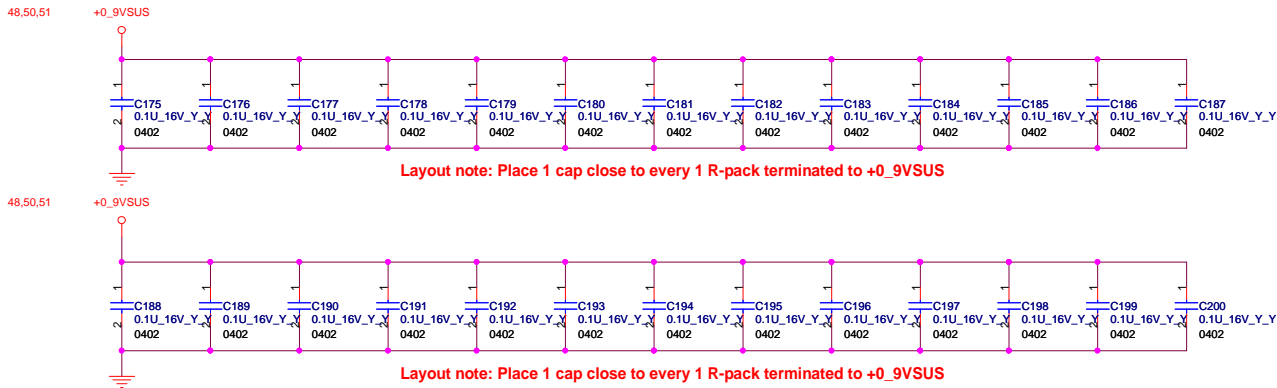
FOX_ASOA426_N4SC_4F

DIMM_1

SMBus Address: A4(W)/A5 (R)

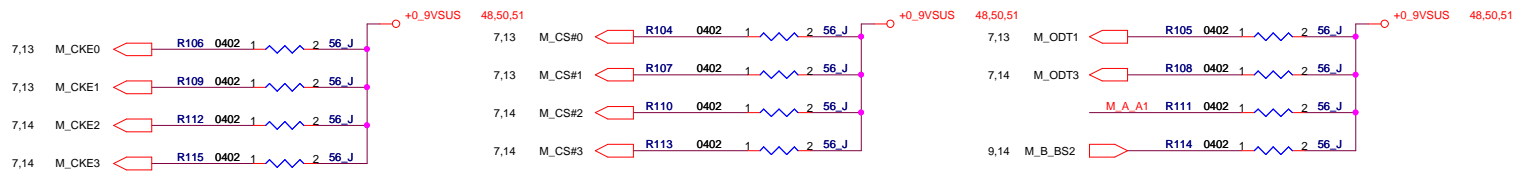
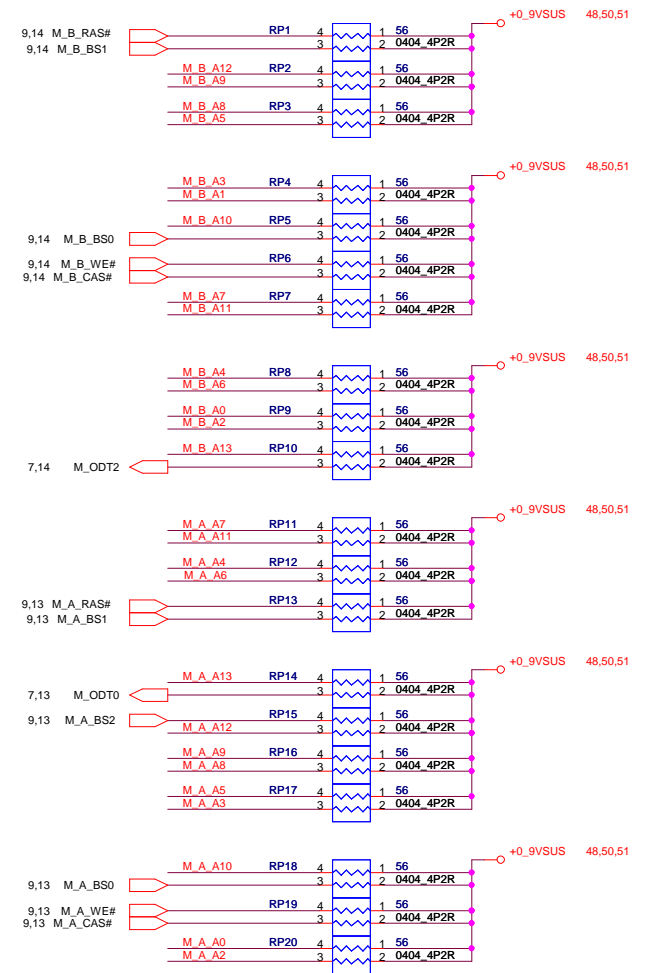
DIMM_1 is placed farther from the GMCH than DIMM_0

| | | |
|---|---------------------------|----------|
| FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division | | |
| Title DDR(II)SO-DIMM_1 | | |
| Size A3 | Document Number MS70-1-01 | Rev 0.20 |
| Date: Tuesday, April 04, 2006 | Sheet 14 | of 54 |

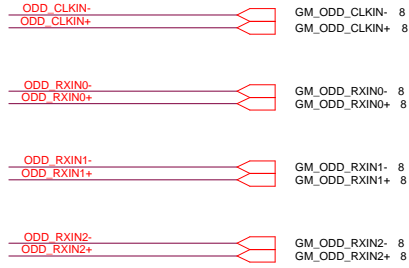


9,13 M_A_A[0.13]

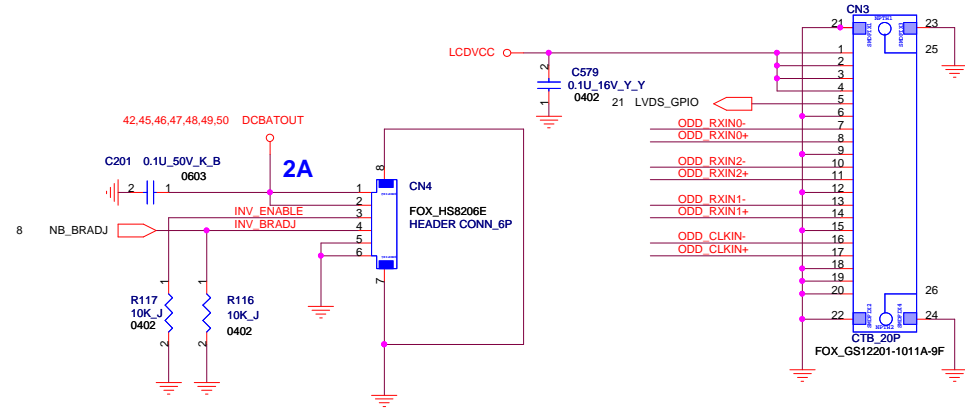
9,14 M_B_A[0.13]



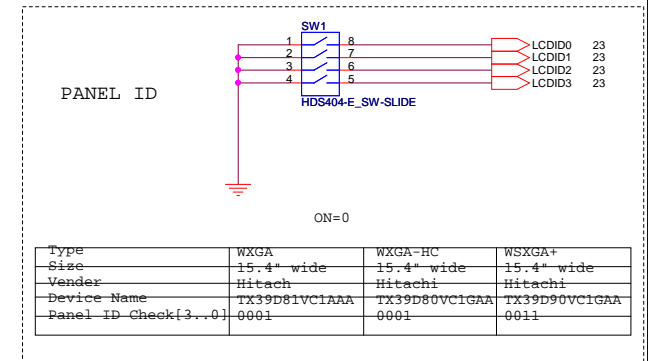
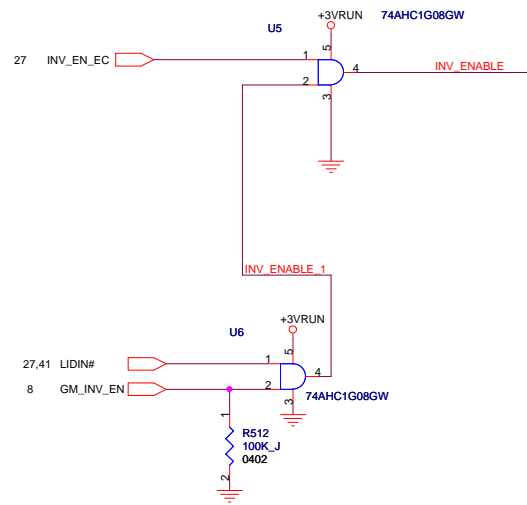
LVDS



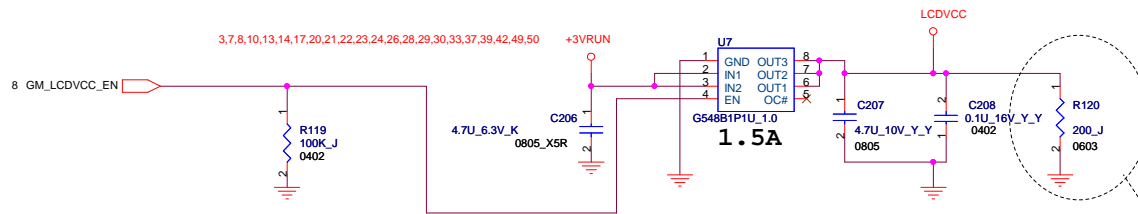
LVDS CONNECTOR



INVERTER CONNECTOR



| Type | WXGA | WXGA-HC | WSXGA+ |
|-----------------------|---------------|---------------|---------------|
| Size | 15.4" wide | 15.4" wide | 15.4" wide |
| Vendor | Hitach | Hitachi | Hitachi |
| Device Name | TX39D81VC1AAA | TX39D80VC1GAA | TX39D90VC1GAA |
| Panel ID Check[3...0] | 0001 | 0001 | 0011 |



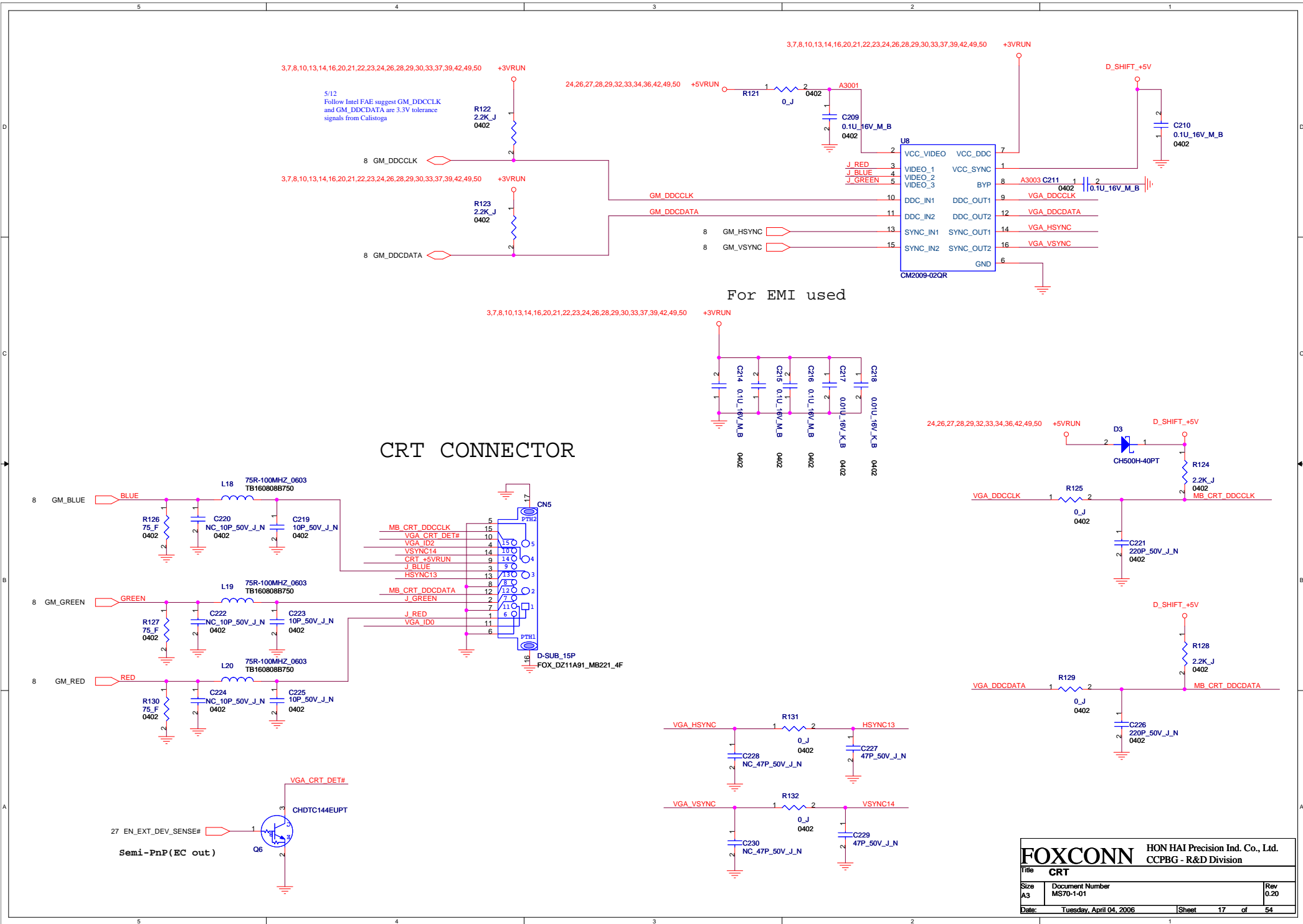
DISCHARGE

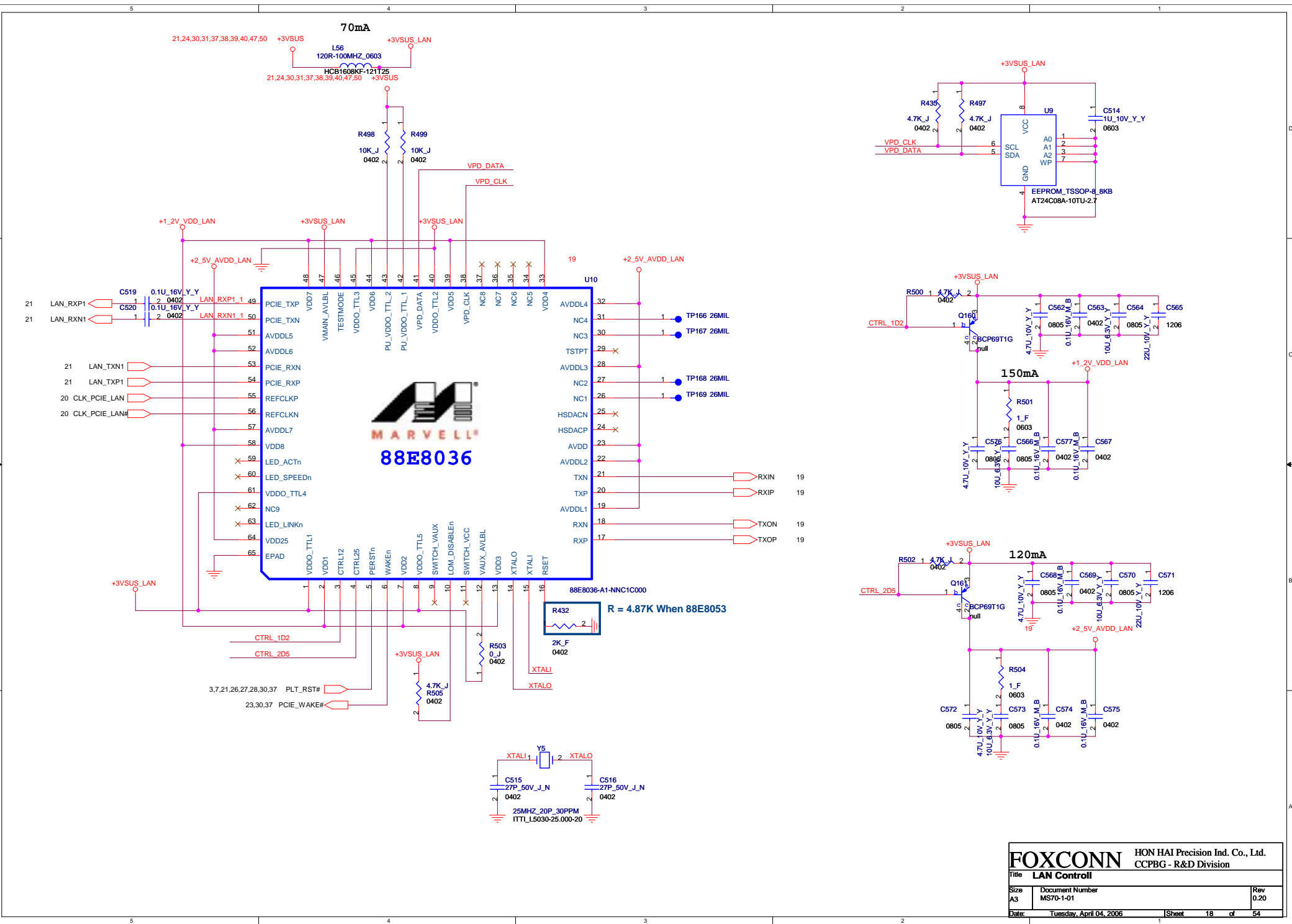
The R461 will consume about 0.054 Watt (3.3x3.3/200 = 0.054W). We changed resistor to 0603 size (1/8 Watt)

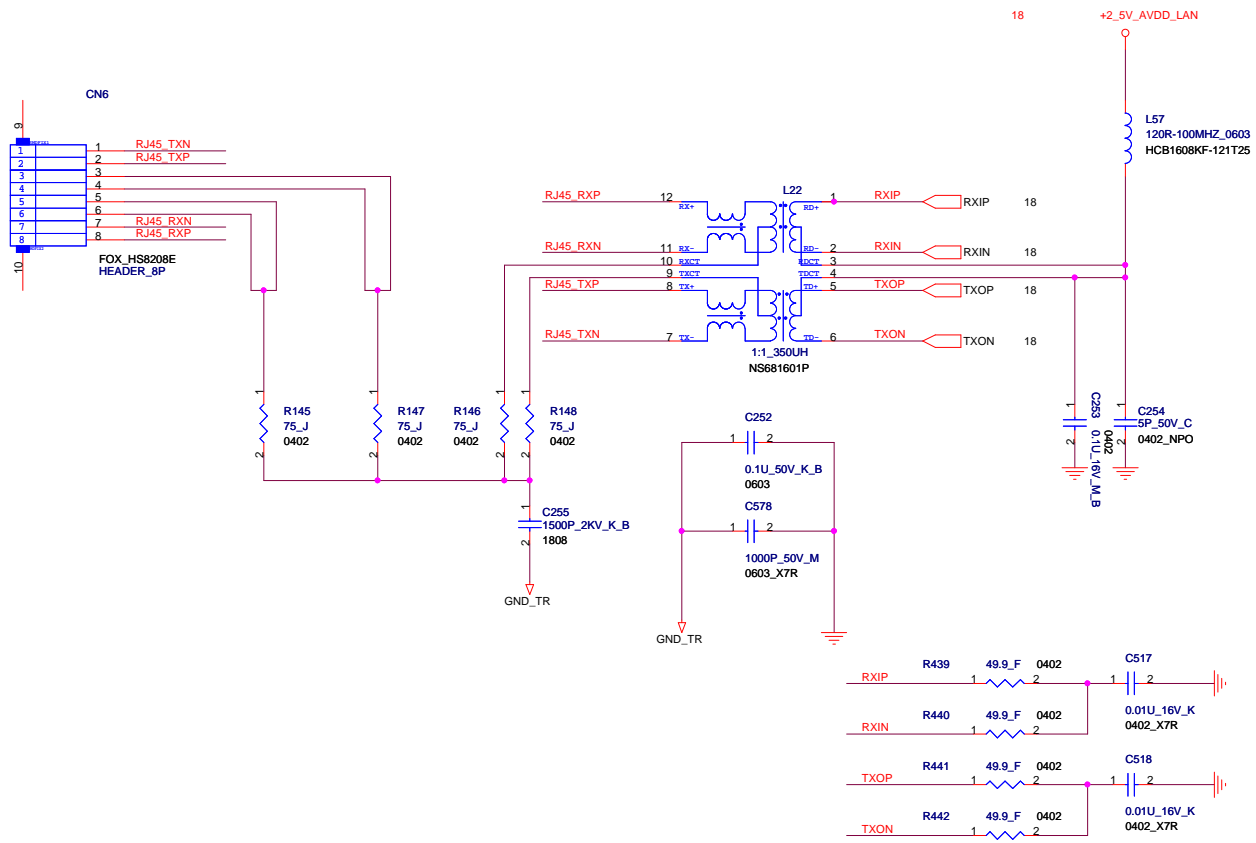
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Title: **LVDS**

| | | |
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| Size A3 | Document Number MS70-1-01 | Rev 0.20 |
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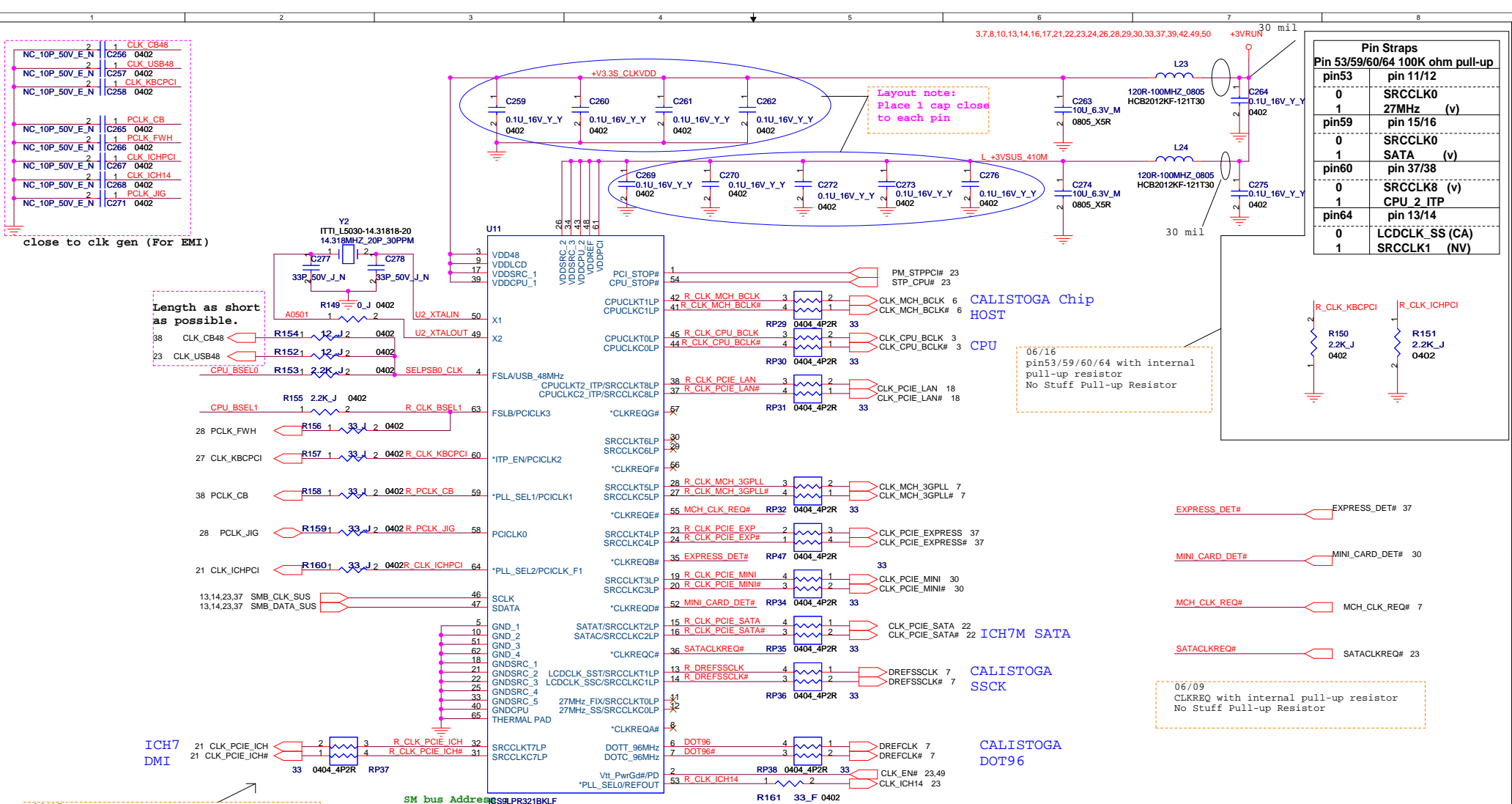




| | | | |
|----------------|---|---|------------|
| NC_10P_50V_E_N | 2 | 1 | CLK_CB48 |
| NC_10P_50V_E_N | 2 | 1 | CLK_USB48 |
| NC_10P_50V_E_N | 2 | 1 | CLK_KBCPCI |
| NC_10P_50V_E_N | 2 | 1 | CLK_ICHPCI |
| NC_10P_50V_E_N | 2 | 1 | CLK_IH14 |
| NC_10P_50V_E_N | 2 | 1 | CLK_JIG |
| NC_10P_50V_E_N | 2 | 1 | CLK_JIG |

close to clk gen (For EMI)

Length as short as possible.



| Pin Straps | |
|----------------------------------|----------------|
| Pin 53/59/60/64 100K ohm pull-up | |
| pin53 | pin 11/12 |
| 0 | SRCLK0 |
| 1 | 27MHz (v) |
| pin59 | pin 15/16 |
| 0 | SRCLK0 |
| 1 | SATA (v) |
| pin60 | pin 37/38 |
| 0 | SRCLK8 (v) |
| 1 | CPU 2 ITP |
| pin64 | pin 13/14 |
| 0 | LCDCLK_SS (CA) |
| 1 | SRCLK1 (NV) |

06/16 pin53/59/60/64 with internal pull-up resistor No Stuff Pull-up Resistor

06/09 CLKREQ with internal pull-up resistor No Stuff Pull-up Resistor

06/17 CLK_PCIE_ICH changed to SRCLK7 CLK_DOCK_LAN changed to SRCLK8 SW Note: datasheet page13 Byte8.1 => SRCLK7 should be configured as "Not Controlled"

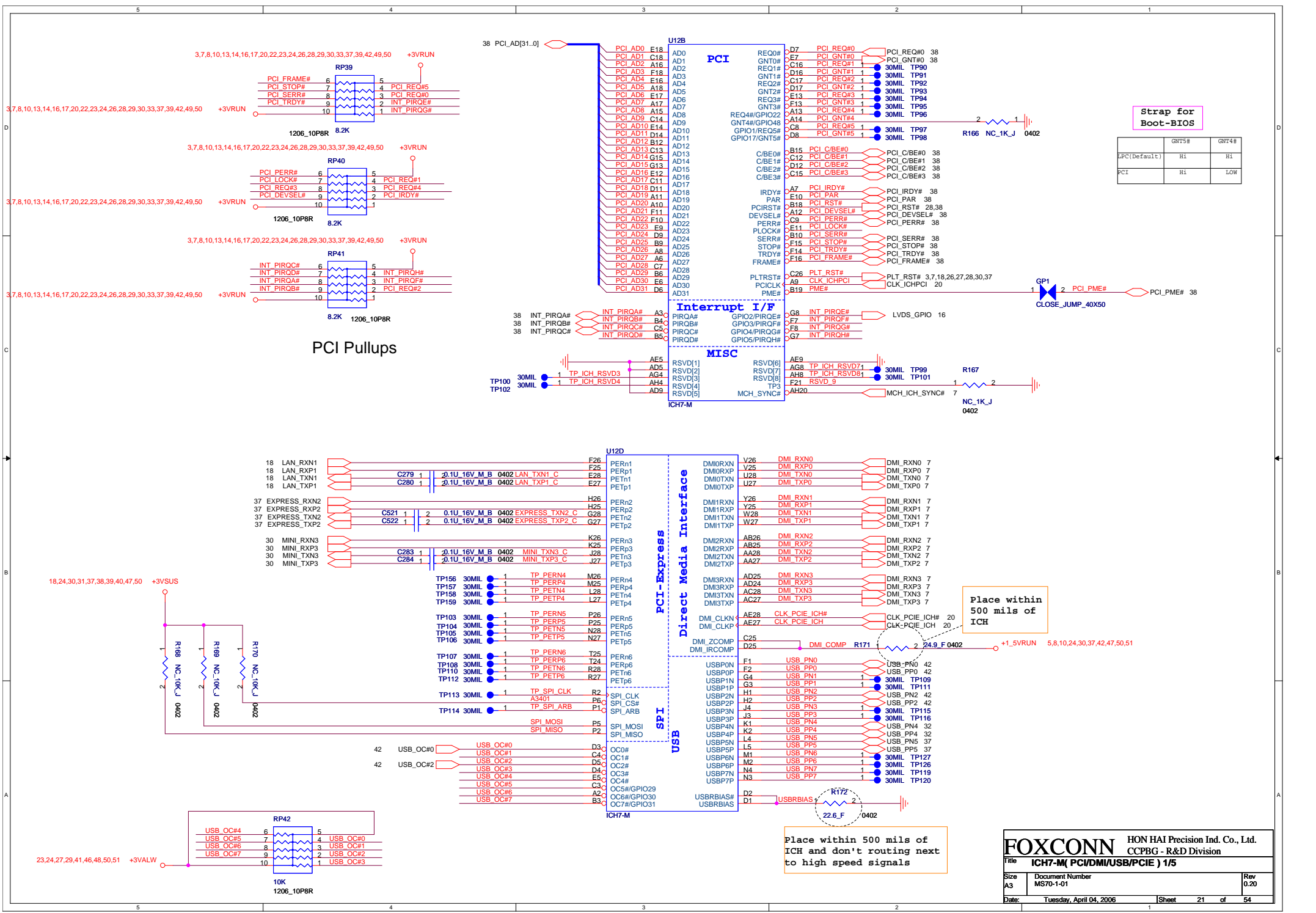
06/09 DEL pull-up resistor R80-82 pull-down resistor R85,R88 Del R84,R87,R90

06/16 ICS have recognized, FSLA/FSLB setting is different from CK410M spec. But MS10 will not use 100MHz. For test purpose, please move R91 from MCH_BSEL2 to MCH_BSEL0, and mount R89.

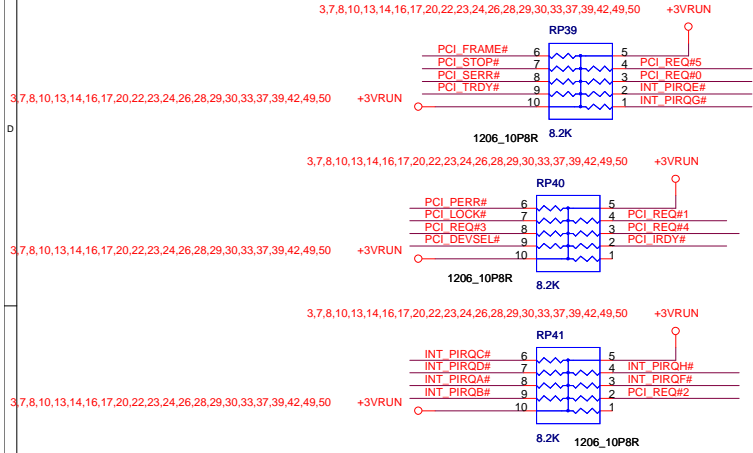
FSB Frequency Table:

| FSLB | FSLA | CPU | SRC[7:0] | PCI |
|------|------|-----|----------|-----|
| 0 | 0 | 100 | 100 | 33 |
| 0 | 1 | 133 | 100 | 33 |
| 1 | 0 | 200 | 100 | 33 |
| 1 | 1 | 166 | 100 | 33 |

| | | | |
|------------------------|-------------------------|----------------------------------|----------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| | | CCPBG - R&D Division | |
| Title CLOCK GEN | | | |
| Size | Document Number | Rev | |
| A3 | MS70-1-01 | 020 | |
| Date: | Tuesday, April 04, 2006 | Sheet | 20 of 54 |



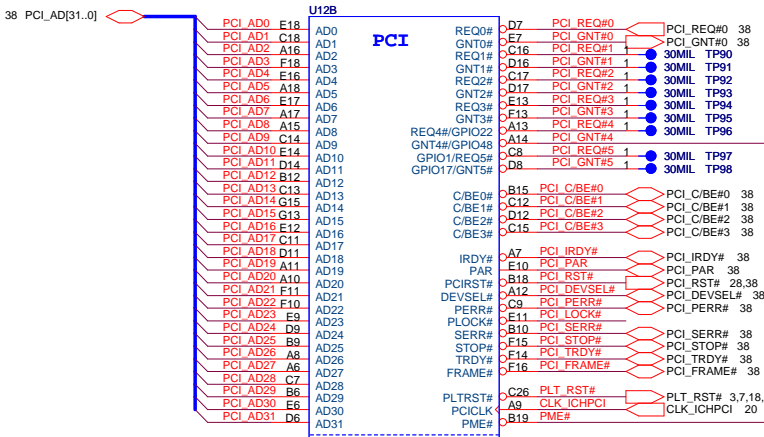
PCI Pullups



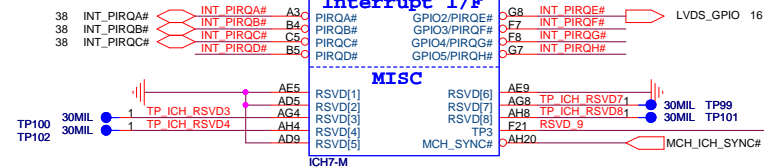
Strap for Boot-BIOS

| | GNT5# | GNT4# |
|--------------|-------|-------|
| IPC(Default) | HI | HI |
| PCI | HI | LOW |

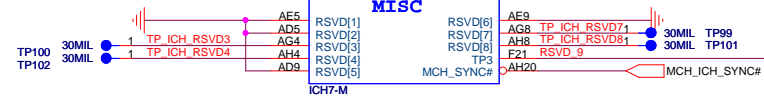
PCI



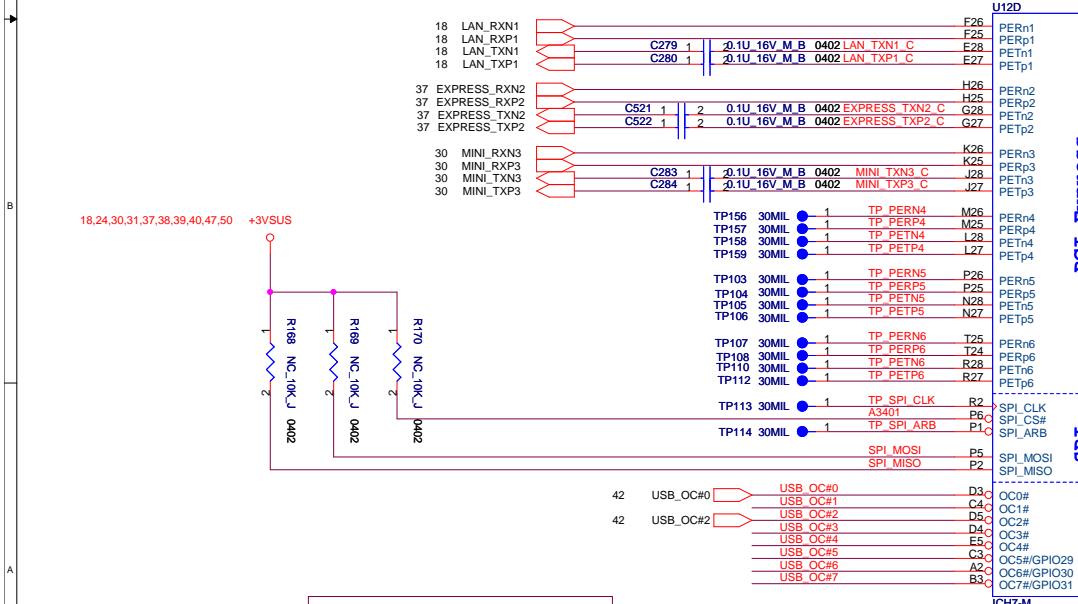
Interrupt I/F



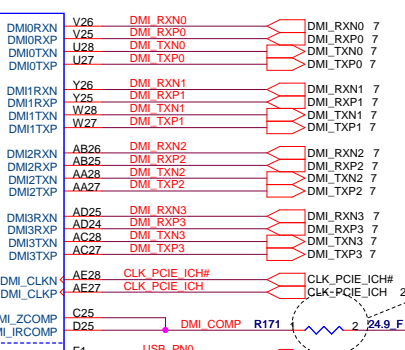
MISC



U12D

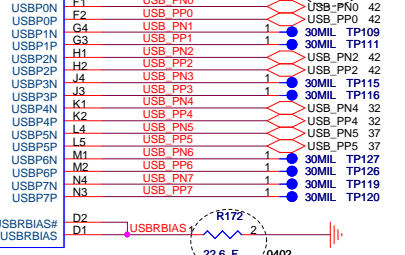


PCI-Express



Place within 500 mils of ICH

SPI



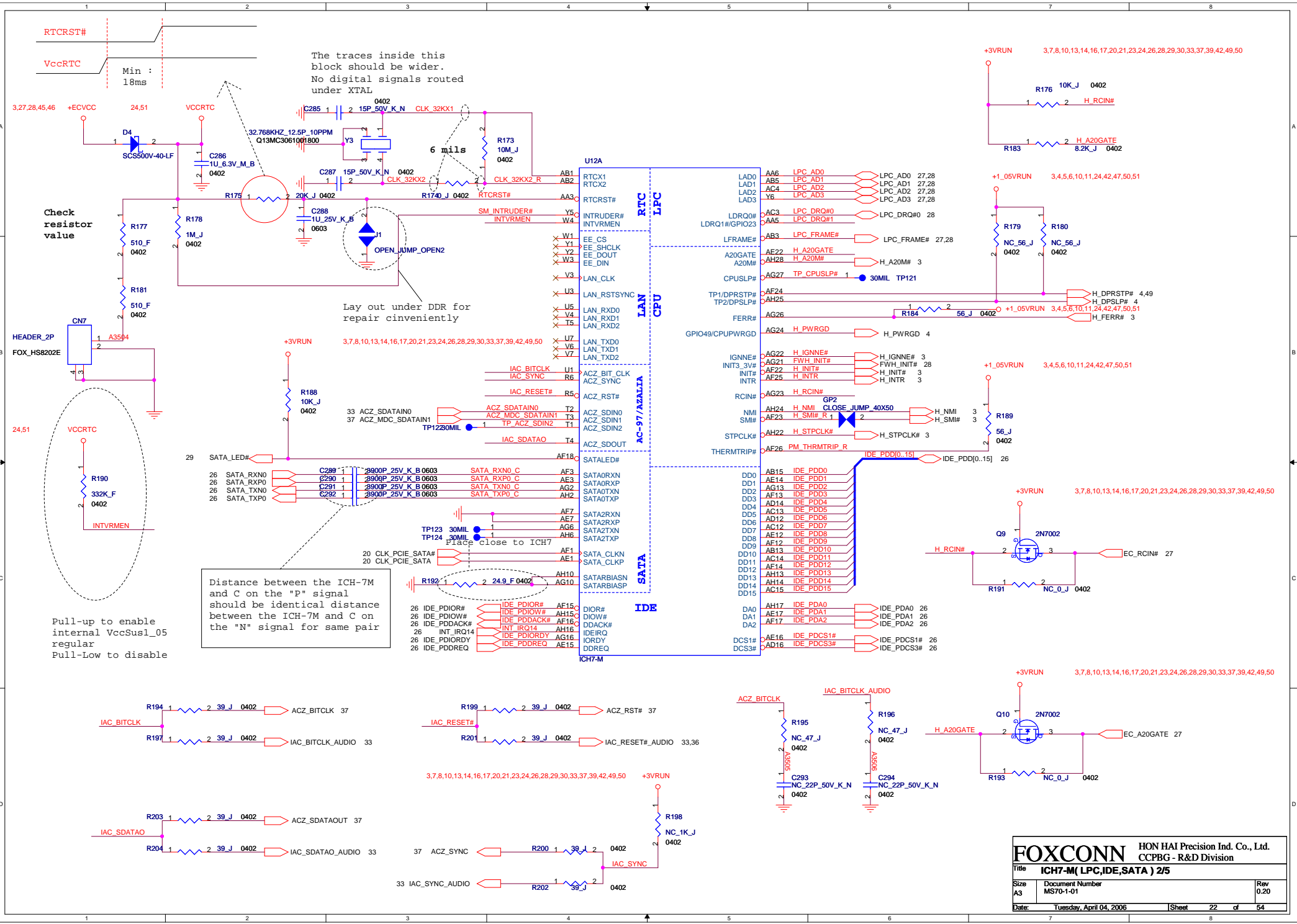
Place within 500 mils of ICH and don't routing next to high speed signals

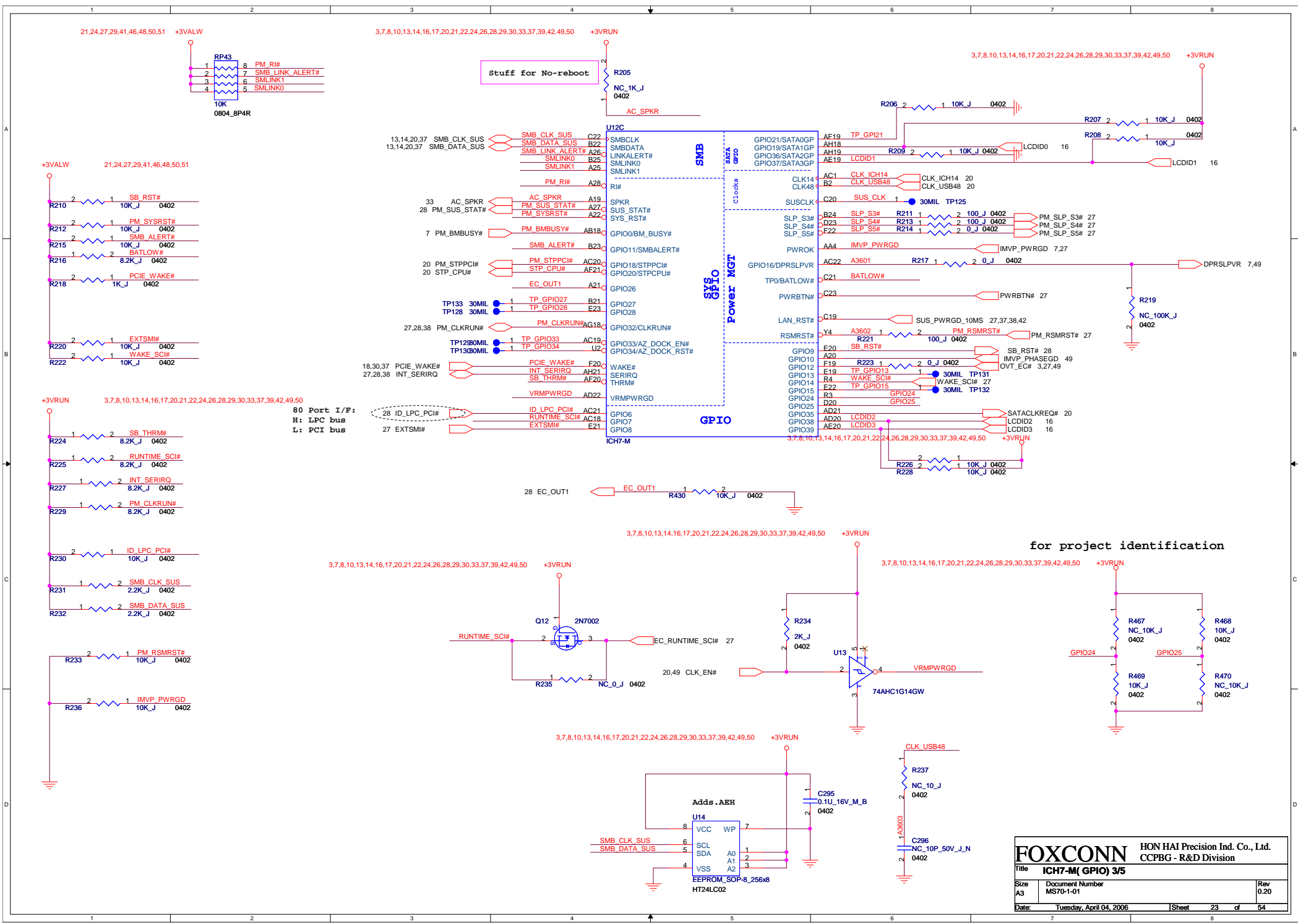
FOXCONN HON HAI Precision Ind. Co., Ltd.
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Title: **ICH7-M (PCI/DMI/USB/PCIE) 1/5**

| | | |
|---------|---------------------------|----------|
| Size A3 | Document Number M570-1-01 | Rev 0.20 |
|---------|---------------------------|----------|

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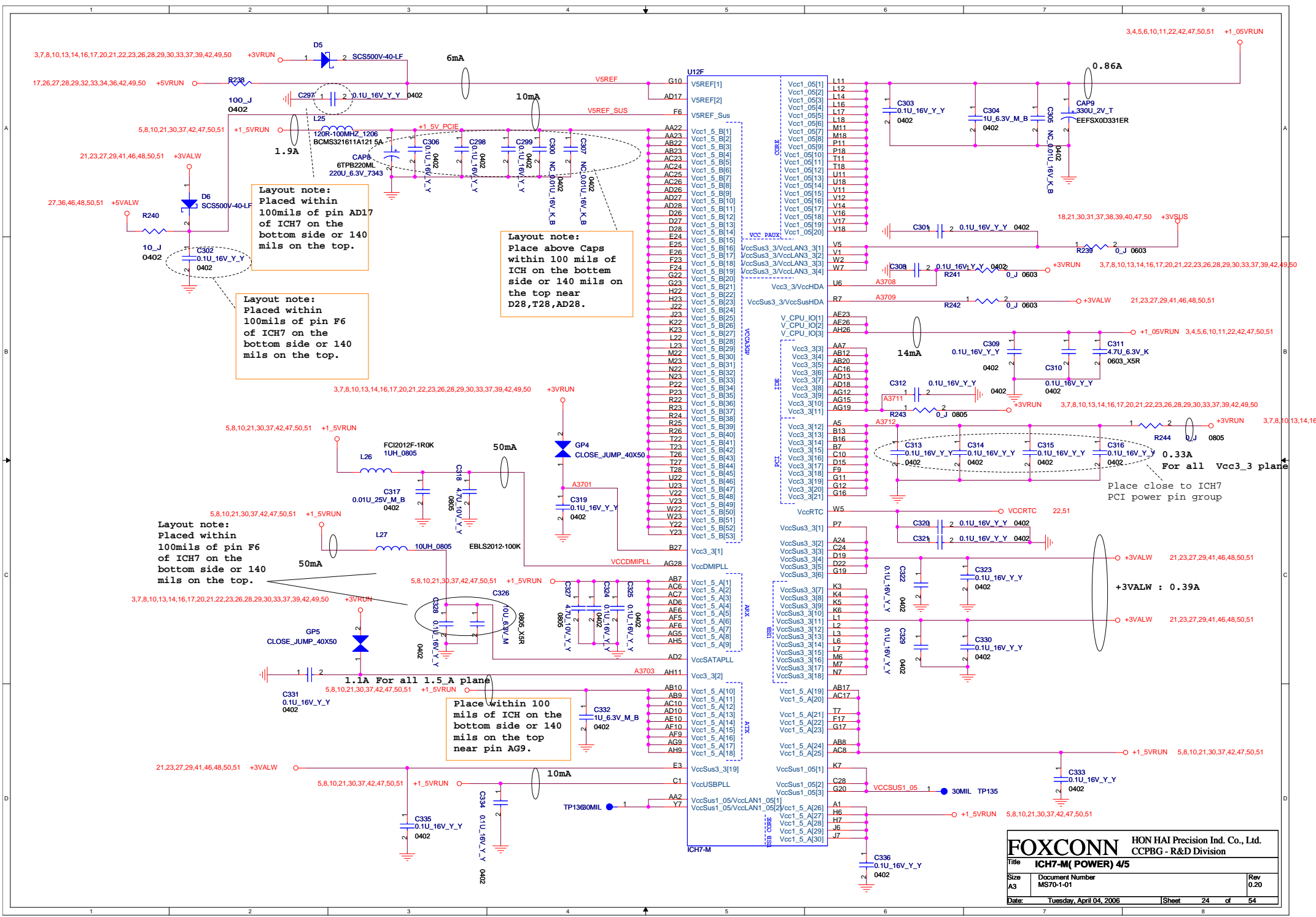




80 Port I/F:
 H: LPC bus
 L: PCI bus

for project identification

| | | |
|---|---------------------------|----------|
| FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division | | |
| Title ICH7-M (GPIO) 3/5 | | |
| Size A3 | Document Number MS70-1-01 | Rev 0.20 |
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Layout note:
Placed within 100mils of pin AD17 of ICH7 on the bottom side or 140 mils on the top.

Layout note:
Placed within 100mils of pin F6 of ICH7 on the bottom side or 140 mils on the top.

Layout note:
Place above Caps within 100 mils of ICH on the bottom side or 140 mils on the top near D28, T28, AD28.

Layout note:
Placed within 100mils of pin F6 of ICH7 on the bottom side or 140 mils on the top.

Layout note:
Place within 100 mils of ICH on the bottom side or 140 mils on the top near pin AG9.

Place close to ICH7 PCI power pin group

+3VALW : 0.39A

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Title: **ICH7-M (POWER) 4/5**

| | | |
|-------------------------------|----------------------------|-----------|
| Size: A3 | Document Number: MS70-1-01 | Rev: 0.20 |
| Date: Tuesday, April 04, 2006 | Sheet: 24 | of 54 |

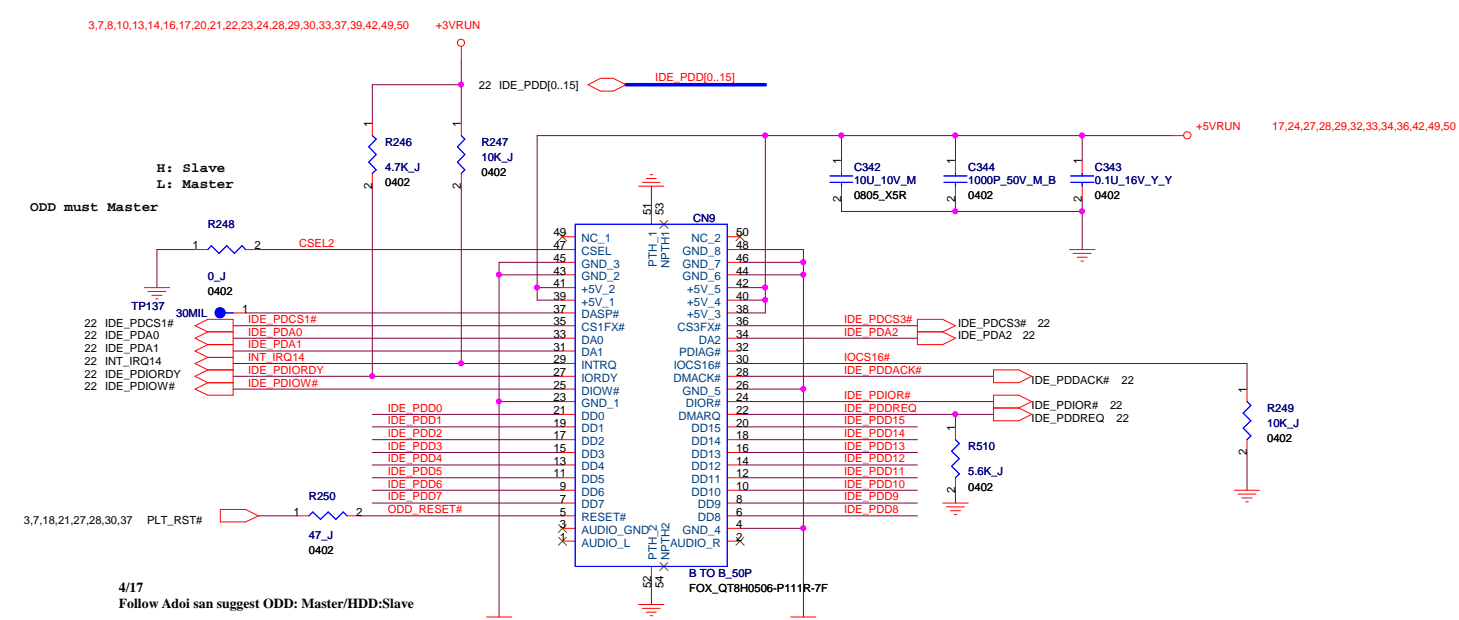
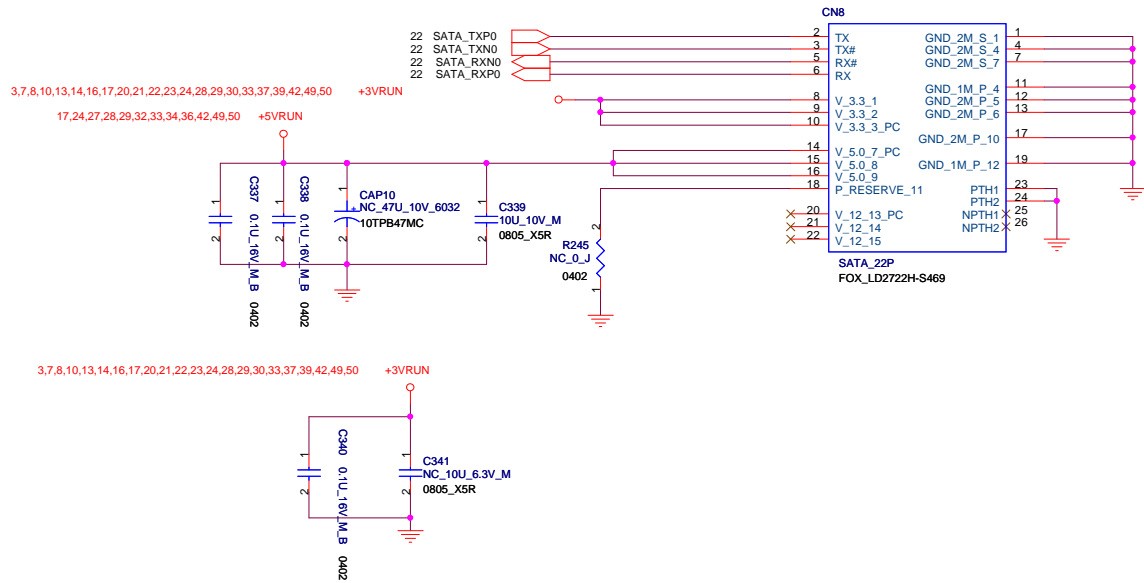
U12E

| | | | |
|-----|-------|--------|------|
| A4 | VSS1 | VSS98 | P28 |
| A23 | VSS2 | VSS99 | R1 |
| B1 | VSS3 | VSS100 | R11 |
| B8 | VSS4 | VSS101 | R12 |
| B11 | VSS5 | VSS102 | R13 |
| B14 | VSS6 | VSS103 | R14 |
| B17 | VSS7 | VSS104 | R15 |
| B20 | VSS8 | VSS105 | R16 |
| B26 | VSS9 | VSS106 | R17 |
| B28 | VSS10 | VSS107 | R18 |
| C2 | VSS11 | VSS108 | T6 |
| C6 | VSS12 | VSS109 | T12 |
| C27 | VSS13 | VSS110 | T13 |
| D10 | VSS14 | VSS111 | T14 |
| D13 | VSS15 | VSS112 | T15 |
| D18 | VSS16 | VSS113 | T16 |
| D21 | VSS17 | VSS114 | T17 |
| D24 | VSS18 | VSS115 | U4 |
| E1 | VSS19 | VSS116 | U12 |
| E2 | VSS20 | VSS117 | U13 |
| E4 | VSS21 | VSS118 | U14 |
| E8 | VSS22 | VSS119 | U15 |
| E15 | VSS23 | VSS120 | U16 |
| F3 | VSS24 | VSS121 | U17 |
| F4 | VSS25 | VSS122 | U24 |
| F5 | VSS26 | VSS123 | U25 |
| F12 | VSS27 | VSS124 | U26 |
| F27 | VSS28 | VSS125 | V2 |
| F28 | VSS29 | VSS126 | V13 |
| G1 | VSS30 | VSS127 | V15 |
| G2 | VSS31 | VSS128 | V24 |
| G5 | VSS32 | VSS129 | V27 |
| G6 | VSS33 | VSS130 | V28 |
| G9 | VSS34 | VSS131 | W6 |
| G14 | VSS35 | VSS132 | W24 |
| G18 | VSS36 | VSS133 | W25 |
| G21 | VSS37 | VSS134 | W26 |
| G24 | VSS38 | VSS135 | Y3 |
| G25 | VSS39 | VSS136 | Y24 |
| G26 | VSS40 | VSS137 | Y27 |
| H3 | VSS41 | VSS138 | Y28 |
| H4 | VSS42 | VSS139 | AA1 |
| H5 | VSS43 | VSS140 | AA24 |
| H24 | VSS44 | VSS141 | AA25 |
| H27 | VSS45 | VSS142 | AA26 |
| H28 | VSS46 | VSS143 | AB4 |
| J1 | VSS47 | VSS144 | AB6 |
| J2 | VSS48 | VSS145 | AB11 |
| J5 | VSS49 | VSS146 | AB14 |
| J24 | VSS50 | VSS147 | AB16 |
| J25 | VSS51 | VSS148 | AB19 |
| J26 | VSS52 | VSS149 | AB21 |
| K24 | VSS53 | VSS150 | AB24 |
| K27 | VSS54 | VSS151 | AB27 |
| K28 | VSS55 | VSS152 | AB28 |
| L13 | VSS56 | VSS153 | AC2 |
| L15 | VSS57 | VSS154 | AC5 |
| L24 | VSS58 | VSS155 | AC9 |
| L25 | VSS59 | VSS156 | AC11 |
| L26 | VSS60 | VSS157 | AD1 |
| M3 | VSS61 | VSS158 | AD3 |
| M4 | VSS62 | VSS159 | AD4 |
| M5 | VSS63 | VSS160 | AD7 |
| M12 | VSS64 | VSS161 | AD8 |
| M13 | VSS65 | VSS162 | AD11 |
| M14 | VSS66 | VSS163 | AD15 |
| M15 | VSS67 | VSS164 | AD19 |
| M16 | VSS68 | VSS165 | AD23 |
| M17 | VSS69 | VSS166 | AE2 |
| M24 | VSS70 | VSS167 | AE4 |
| M27 | VSS71 | VSS168 | AE8 |
| M28 | VSS72 | VSS169 | AE11 |
| N1 | VSS73 | VSS170 | AE13 |
| N2 | VSS74 | VSS171 | AE18 |
| N5 | VSS75 | VSS172 | AE21 |
| N6 | VSS76 | VSS173 | AE24 |
| N11 | VSS77 | VSS174 | AE25 |
| N12 | VSS78 | VSS175 | AF2 |
| N13 | VSS79 | VSS176 | AF4 |
| N14 | VSS80 | VSS177 | AF8 |
| N15 | VSS81 | VSS178 | AF11 |
| N16 | VSS82 | VSS179 | AF27 |
| N17 | VSS83 | VSS180 | AF28 |
| N18 | VSS84 | VSS181 | AG1 |
| N24 | VSS85 | VSS182 | AG3 |
| N25 | VSS86 | VSS183 | AG7 |
| N26 | VSS87 | VSS184 | AG11 |
| P3 | VSS88 | VSS185 | AG14 |
| P4 | VSS89 | VSS186 | AG17 |
| P12 | VSS90 | VSS187 | AG20 |
| P13 | VSS91 | VSS188 | AG25 |
| P14 | VSS92 | VSS189 | AH1 |
| P15 | VSS93 | VSS190 | AH3 |
| P16 | VSS94 | VSS191 | AH7 |
| P17 | VSS95 | VSS192 | AH12 |
| P24 | VSS96 | VSS193 | AH23 |
| P27 | VSS97 | VSS194 | AH27 |

ICH7-M

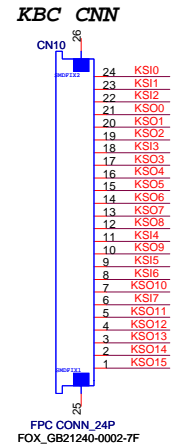
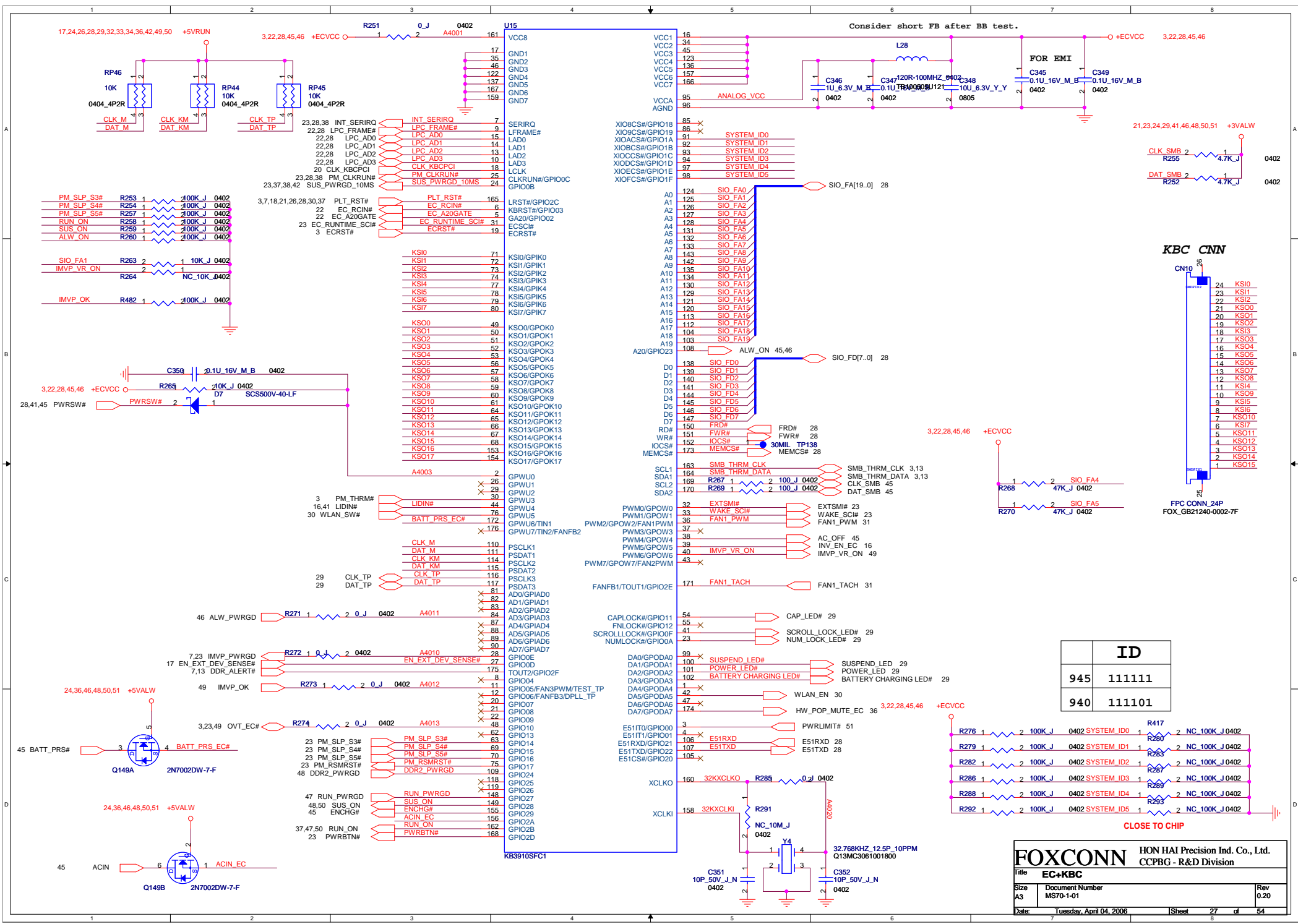
| | | | |
|------------------------------|-------------------------|----------------------------------|----------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| | | CCPBG - R&D Division | |
| Title ICH7-M(GND) 5/5 | | | |
| Size | Document Number | Rev | |
| A3 | MS70-1-01 | 0.20 | |
| Date: | Tuesday, April 04, 2006 | Sheet | 25 of 54 |

SATA HDD CONN

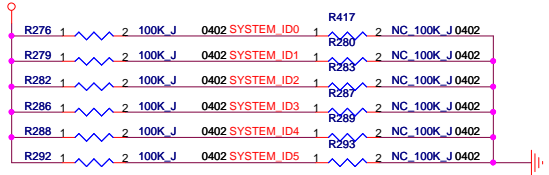


CD-ROM CONN

| | | | | | |
|------------------------------|-------------------------|-------|--|----|------|
| FOXCONN | | | HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division | | |
| Title SATA HDD/CD-ROM | | | | | |
| Size | Document Number | | | | Rev |
| A3 | MS70-1-01 | | | | 0.20 |
| Date: | Tuesday, April 04, 2006 | Sheet | 26 | of | 54 |



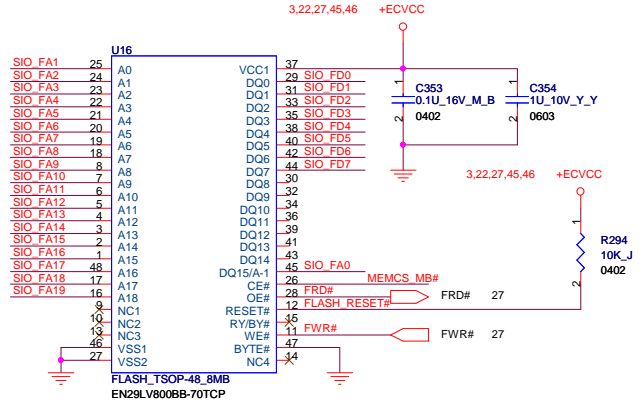
| ID | |
|-----|--------|
| 945 | 111111 |
| 940 | 111101 |



FOXCONN HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

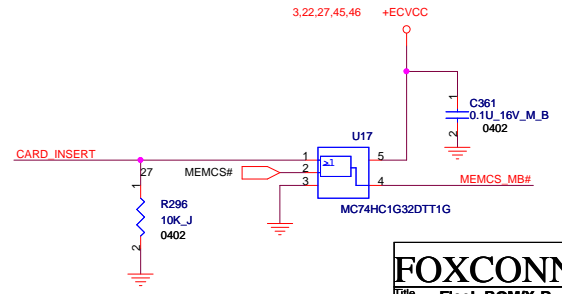
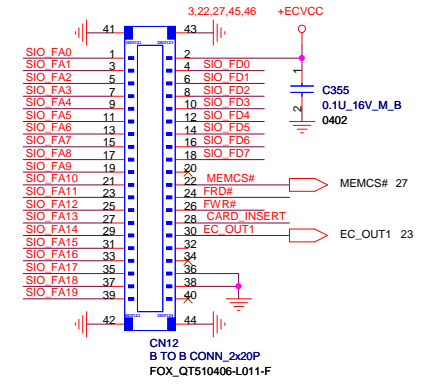
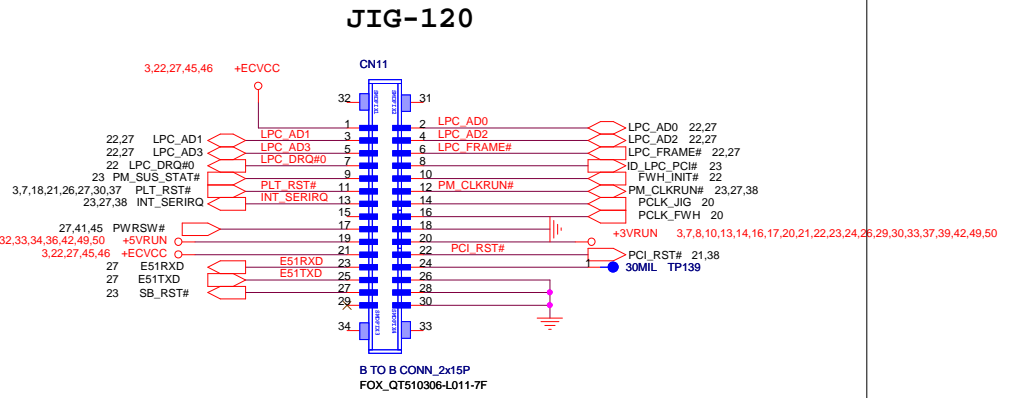
| | | |
|-------|-------------------------|----------------|
| Title | EC+KBC | |
| Size | Document Number | Rev |
| A3 | MS70-1-01 | 020 |
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27 SIO_FA[19..0]
27 SIO_FD[7..0]



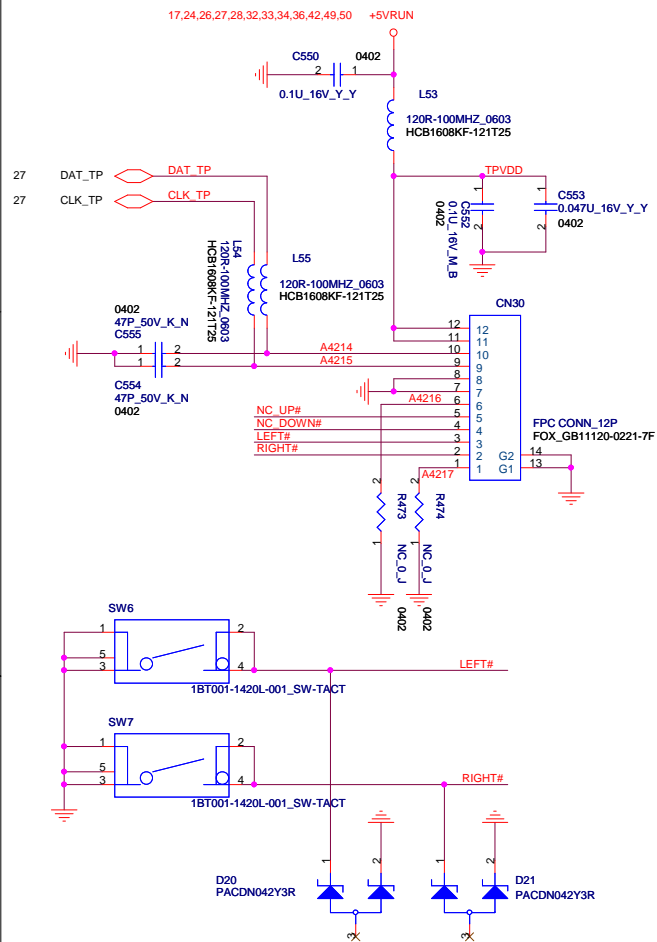
FLASH BIOS

X-BUS

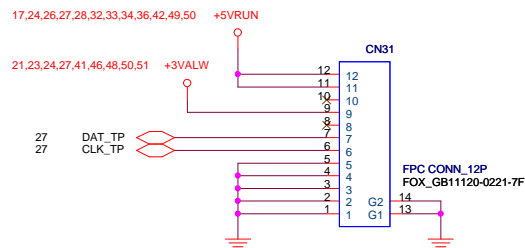


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|---|-------------------------|----------------|
| FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division | | |
| Title | Flash ROM/X-Bus/LID SW# | |
| Size | Document Number | Rev |
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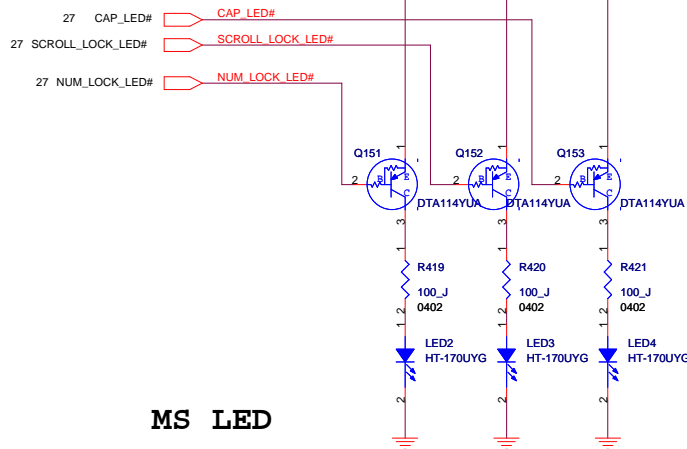
Touch Pad Board



Touch Pad Daughter Board Conn

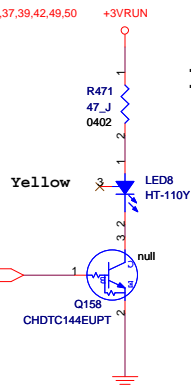


CAP_LED# SCROLL_LOCK_LED# NUM_LOCK_LED#

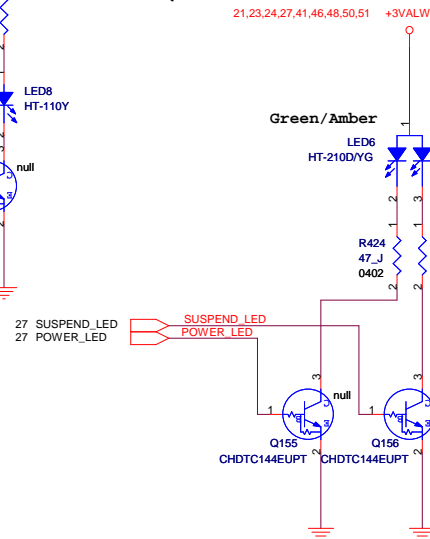


MS LED

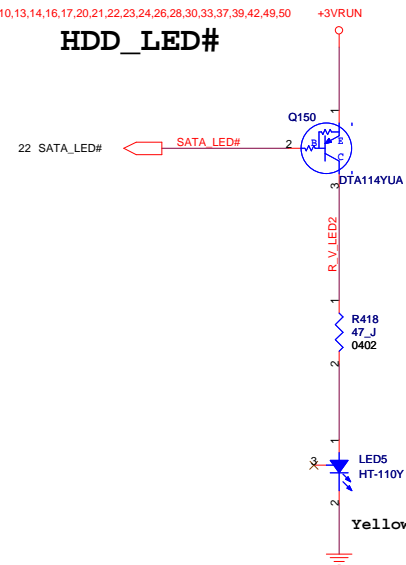
3,7,8,10,13,14,16,17,20,21,22,23,24,26,28,30,33,37,39,42,49,50



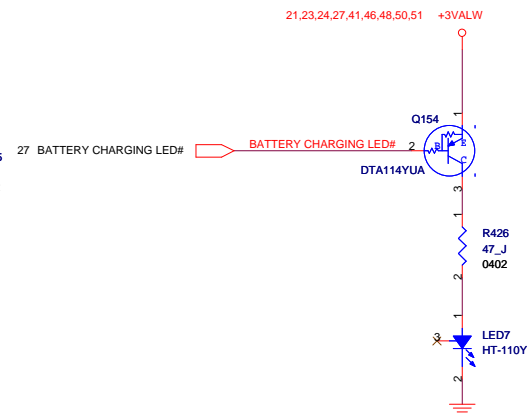
POWER/SUSPEND LED



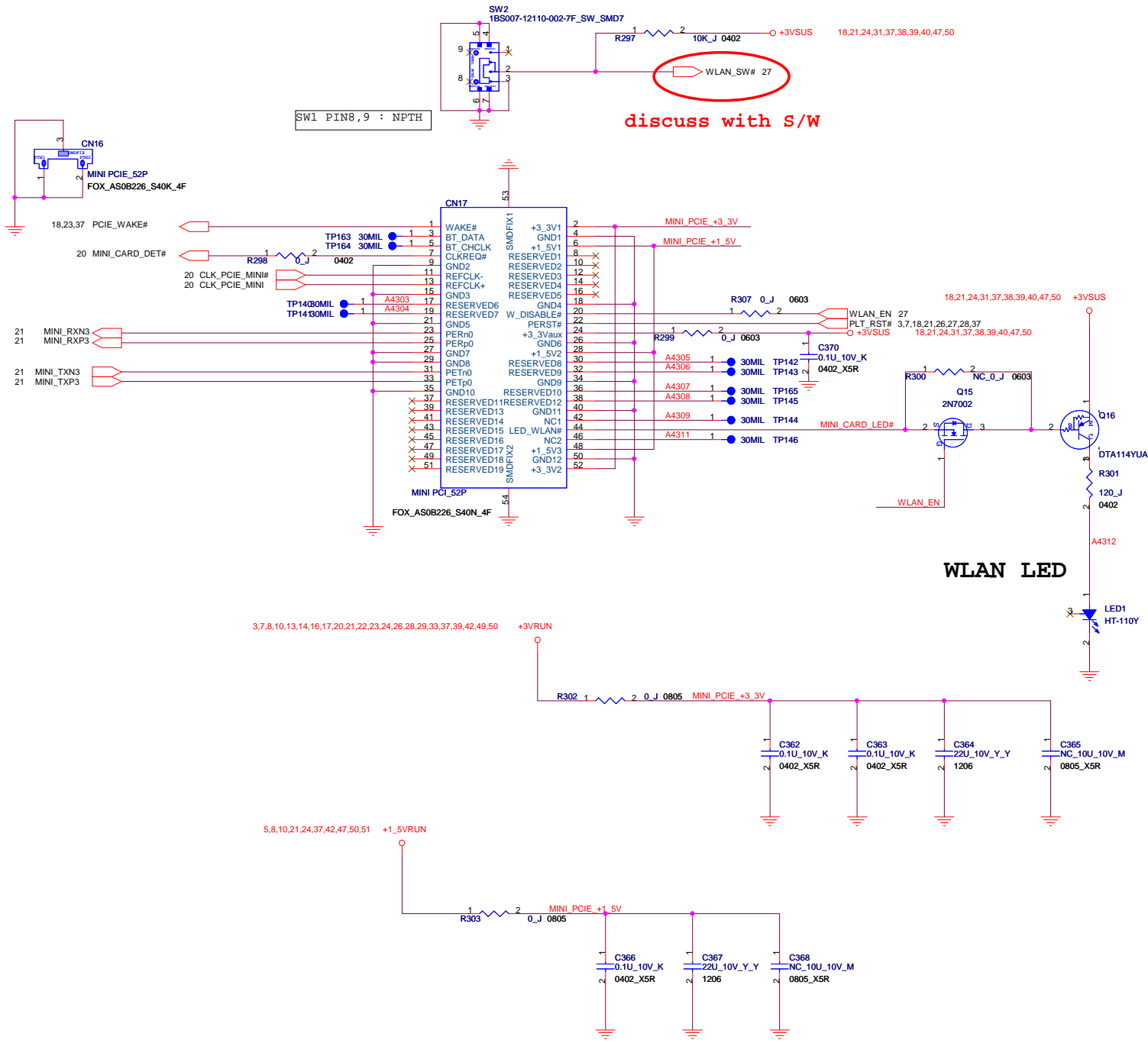
HDD_LED#



BATTERY CHARGING LED#



| | | | |
|-------------------------------|----------------------------|----------------------------------|----|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title: LED/Touch PAD | | CCPBG - R&D Division | |
| Size: A3 | Document Number: MS70-1-01 | Rev: 0.20 | |
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SW1 PIN8,9 : NPTH

discuss with S/W

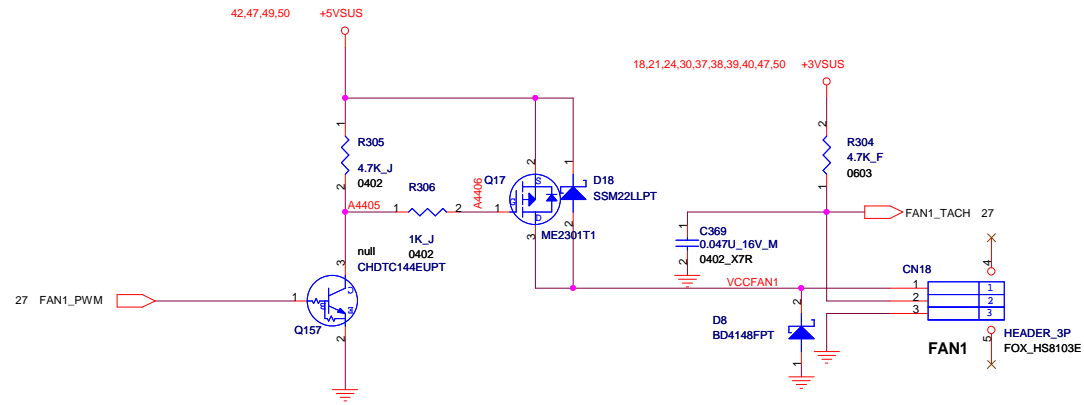
WLAN LED

LED IF SPEC:
20mA (TYP) , 30mA (MAX)

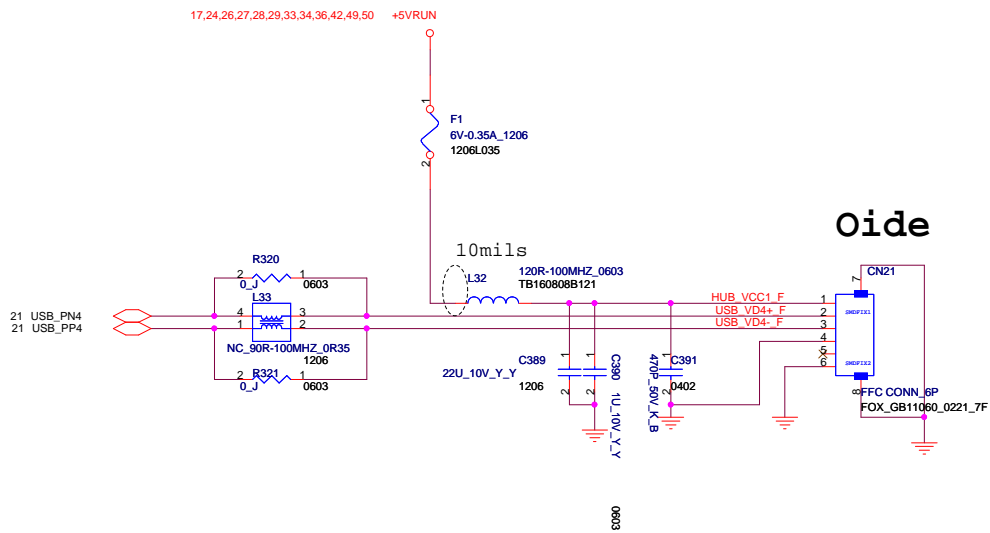
Green

| | | | |
|-------------------------------|---------------------------|----------------------------------|--|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title Mini-PCIE Card | | CCPBG - R&D Division | |
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FAN1

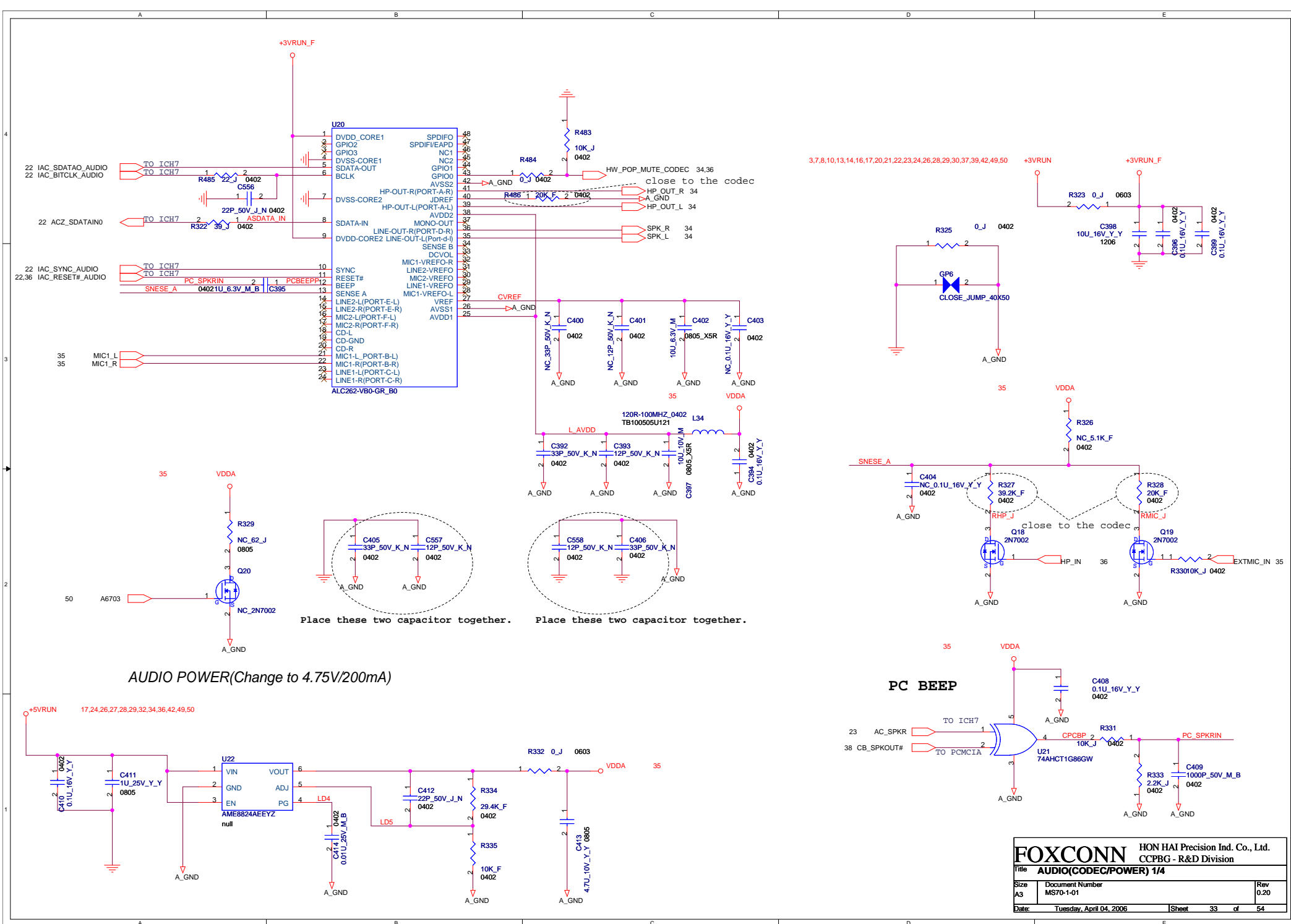


| | | | |
|------------------|-------------------------|----------------------------------|----------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title FAN | | CCPBG - R&D Division | |
| Size | Document Number | Rev | |
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Oide

| | | | |
|----------------------------------|------------------------------|----------------------------------|--|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title OIDE | | CCPBG - R&D Division | |
| Size A3 | Document Number MS70-1-01 | Rev 0.20 | |
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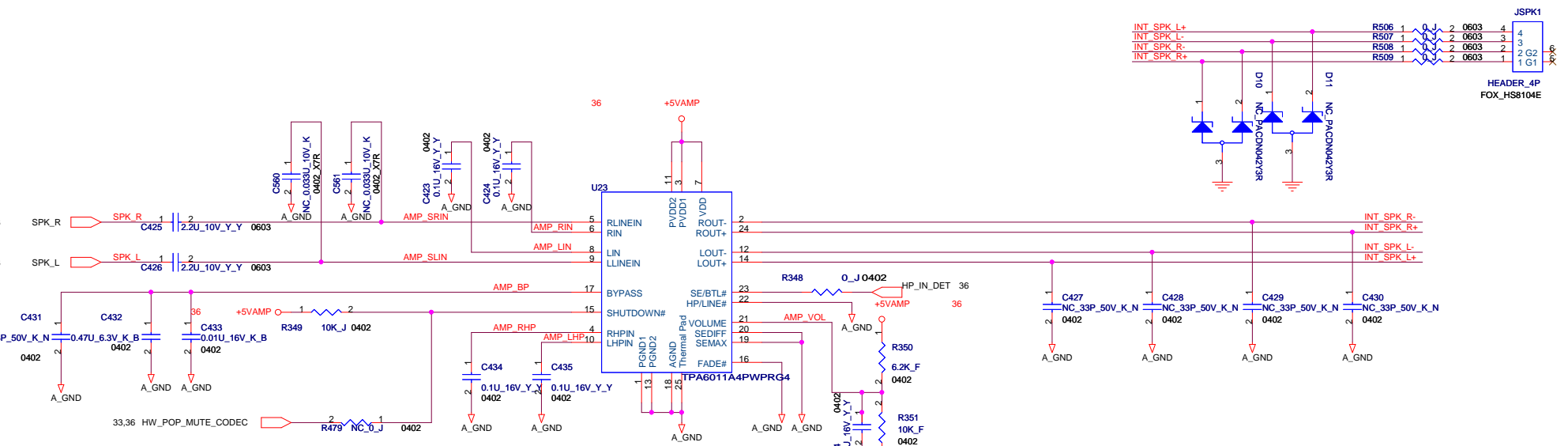
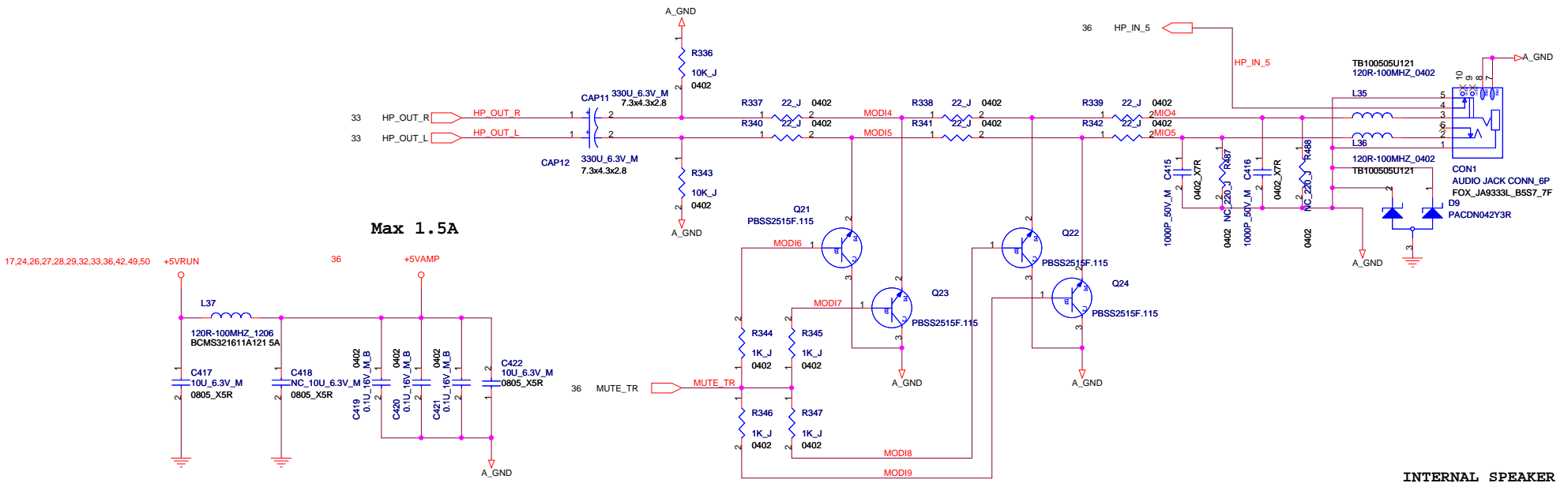


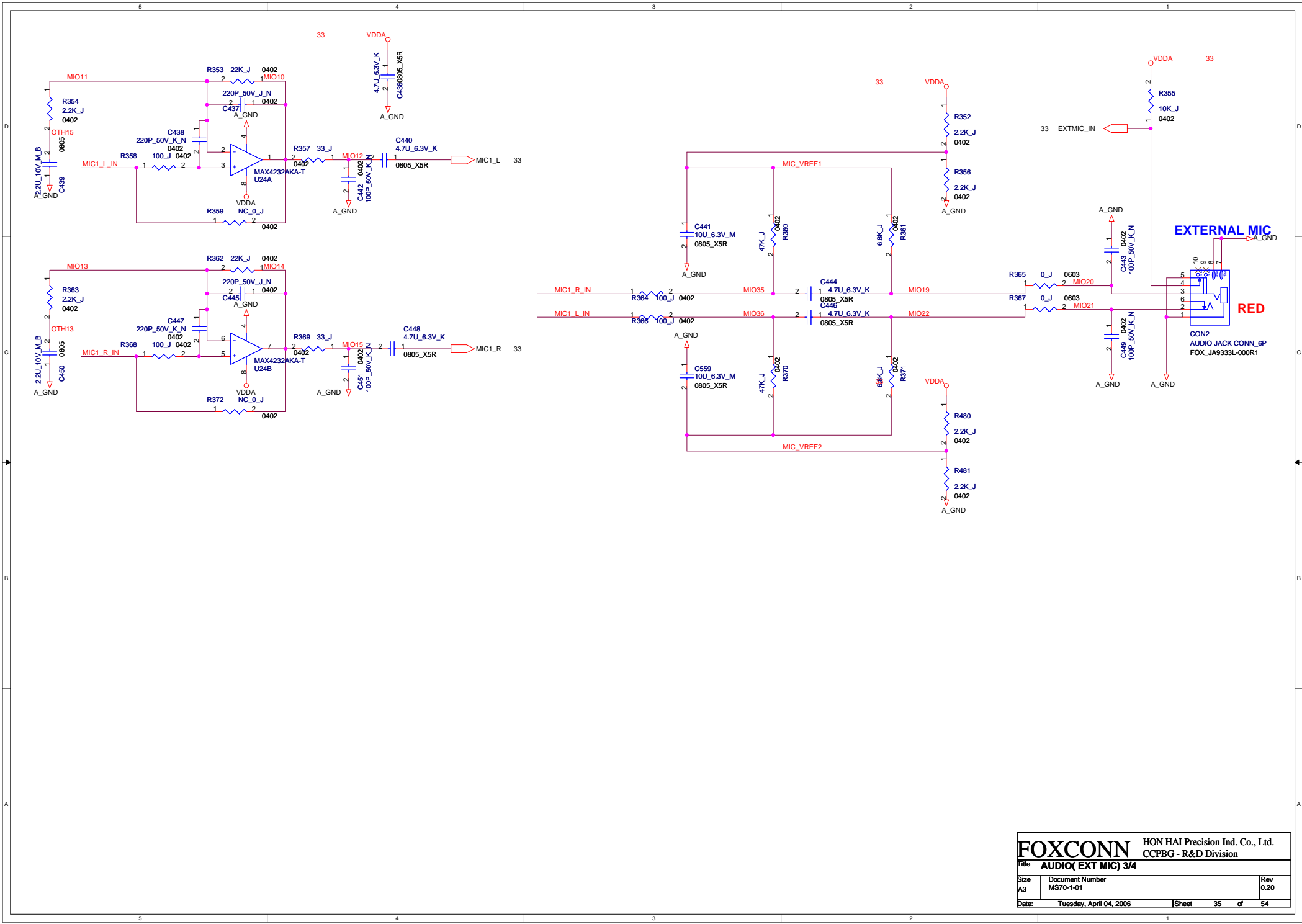
Place these two capacitor together. Place these two capacitor together.

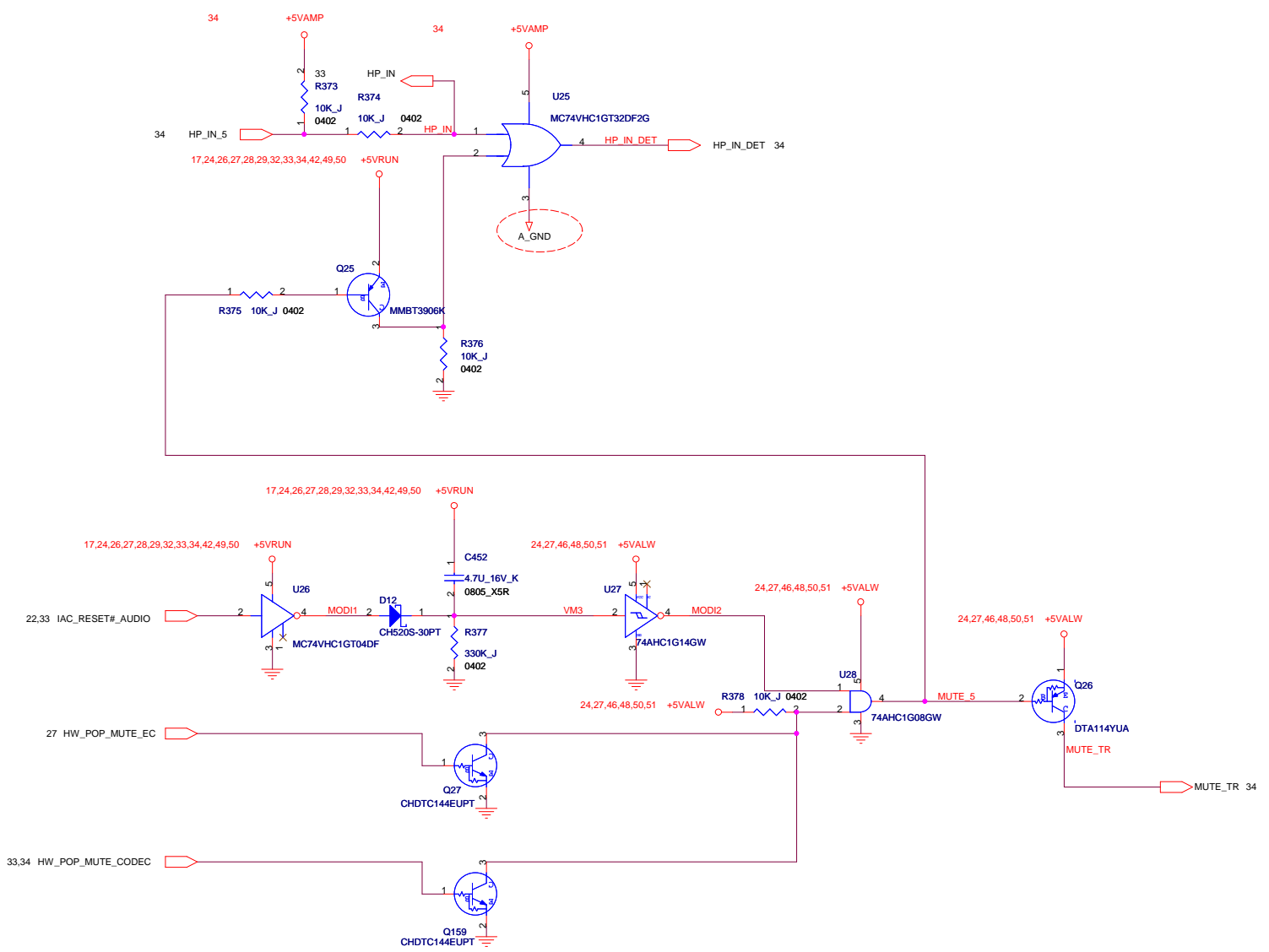
AUDIO POWER(Change to 4.75V/200mA)

PC BEEP

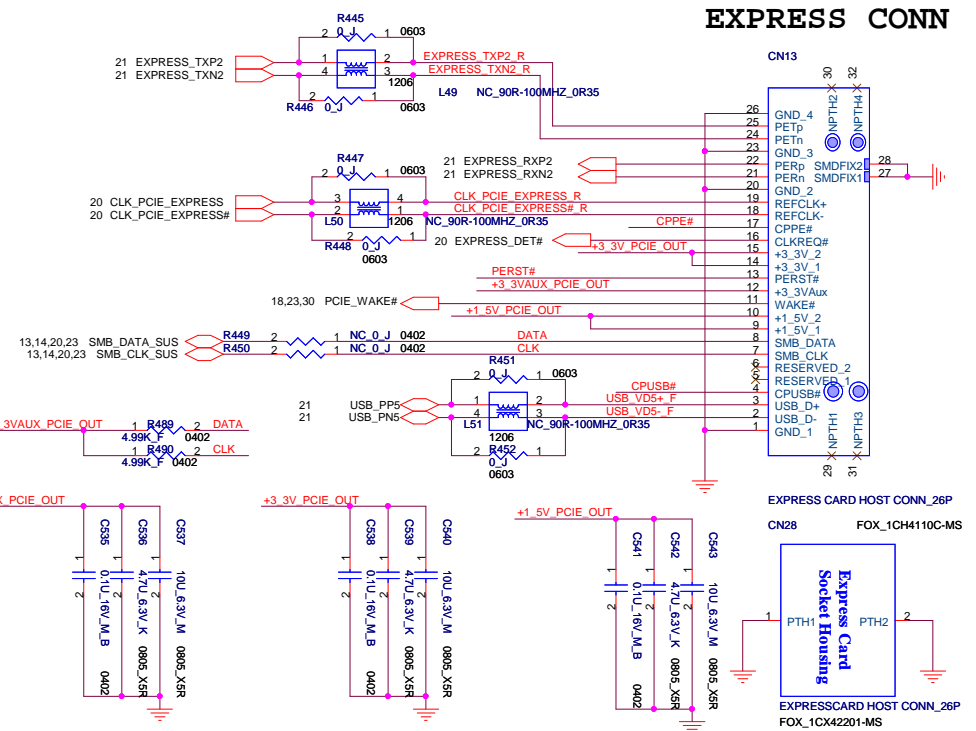
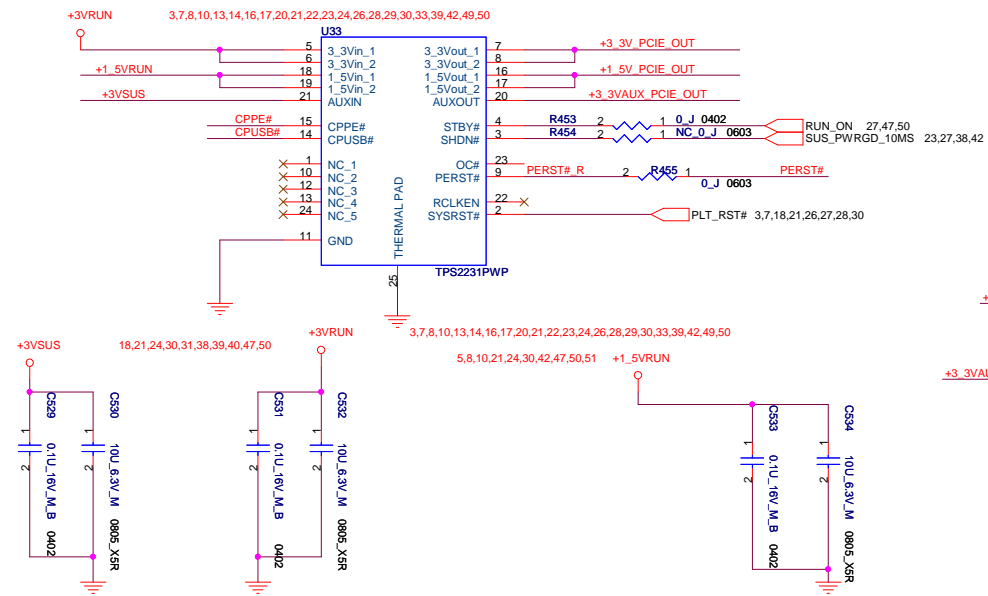
| | | |
|---|-------------------------|----------------|
| FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division | | |
| Title | AUDIO(CODEC/POWER) 1/4 | |
| Size | Document Number | Rev |
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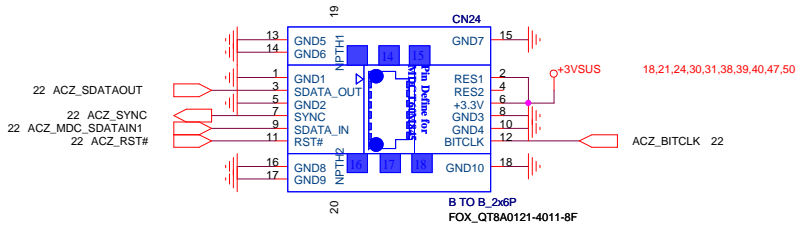




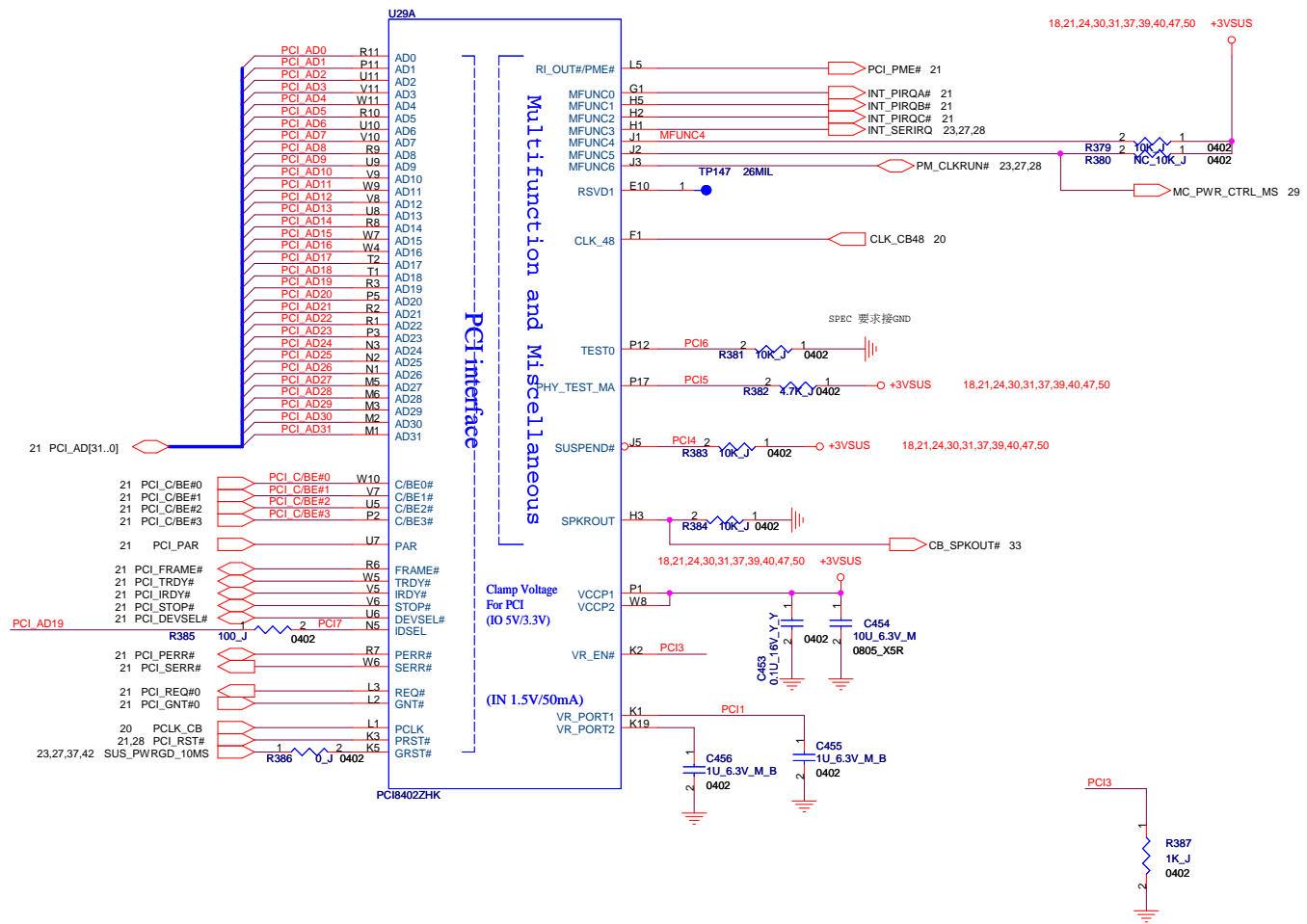
EXPRESS CONN

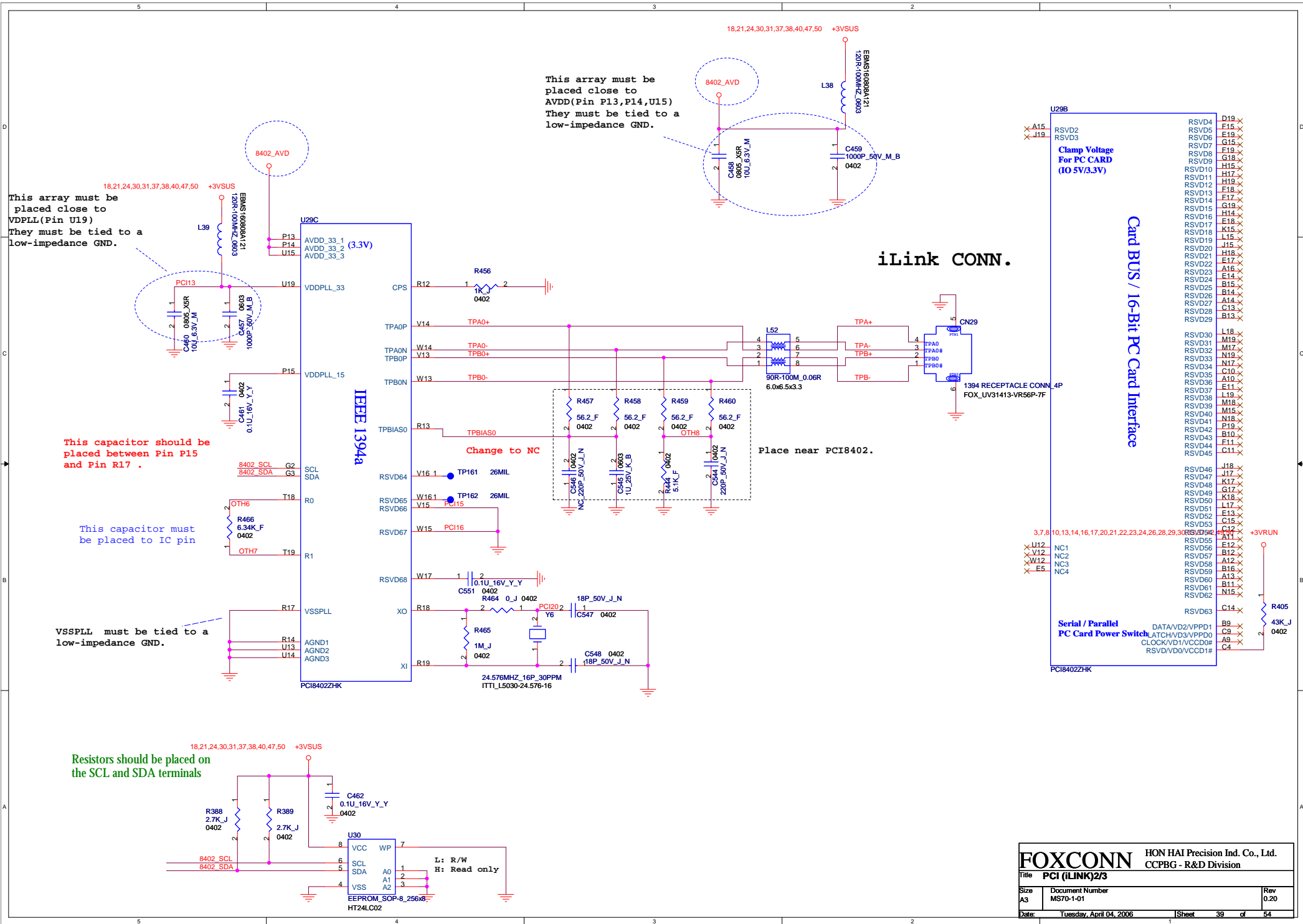


MDC CONN.



| | | | |
|---------------------------|------------------------------|----------------------------------|----------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title EXPRESS CARD | | CCPBG - R&D Division | |
| Size A3 | Document Number MS70-1-01 | Rev 0.20 | |
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18,21,24,30,31,37,38,40,47,50 +3VSUS
 This array must be placed close to VDDPLL (Pin U19) They must be tied to a low-impedance GND.

This array must be placed close to AVDD (Pin P13, P14, U15) They must be tied to a low-impedance GND.

This capacitor should be placed between Pin P15 and Pin R17 .

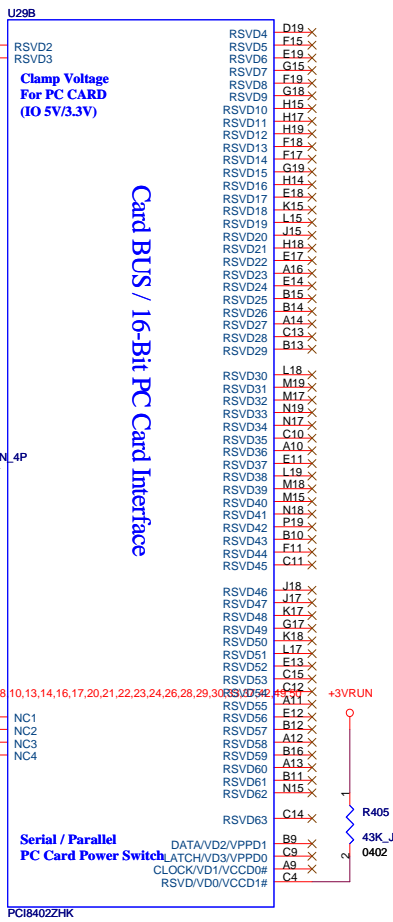
This capacitor must be placed to IC pin

VSSPLL must be tied to a low-impedance GND.

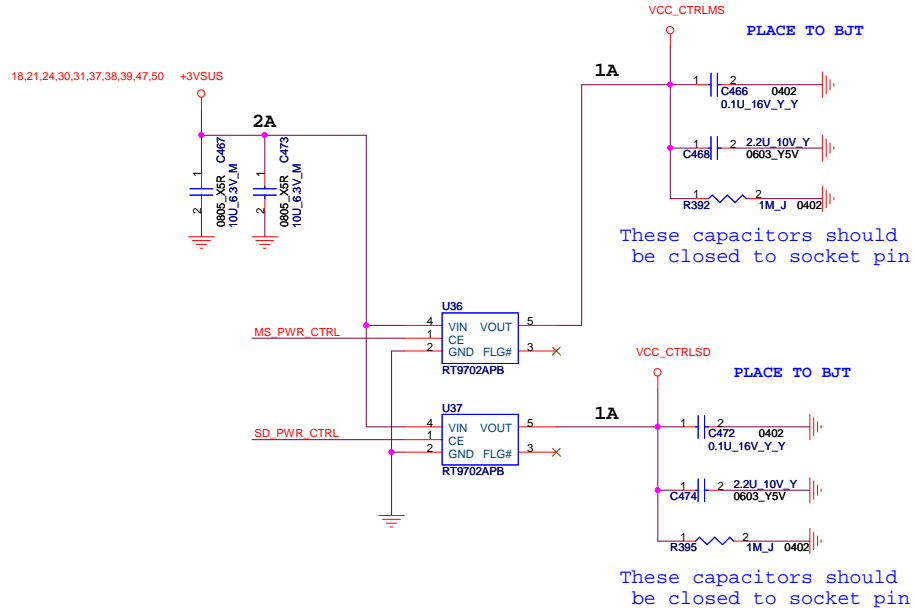
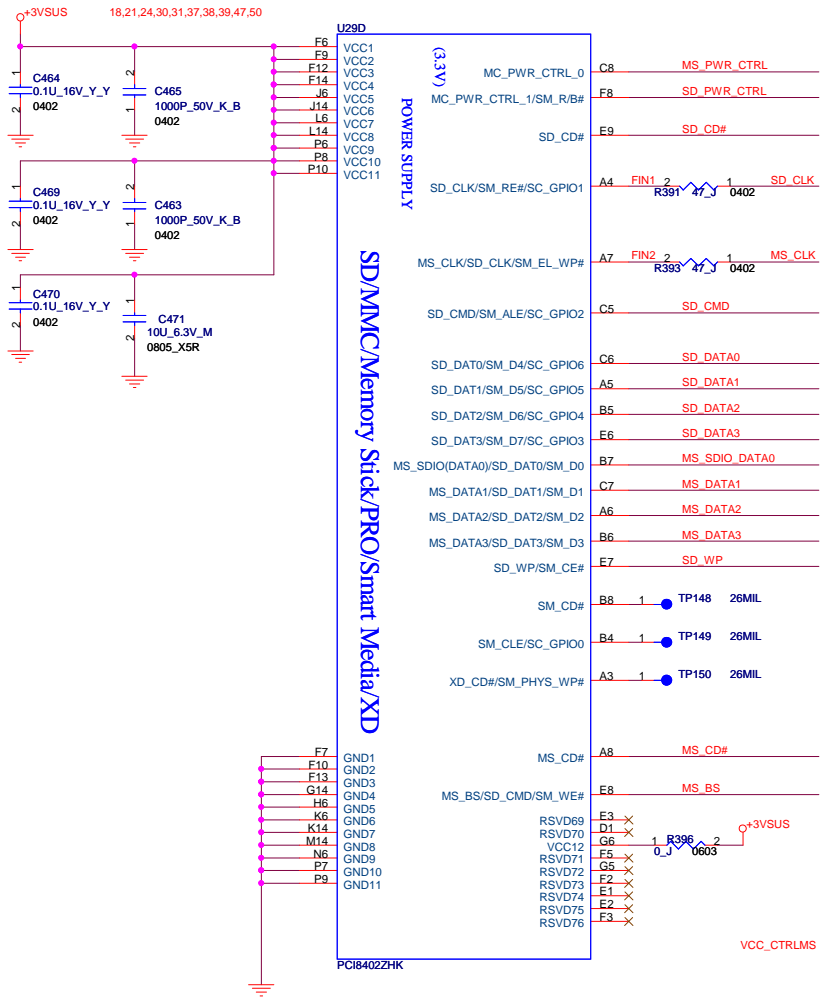
Resistors should be placed on the SCL and SDA terminals

iLink CONN.

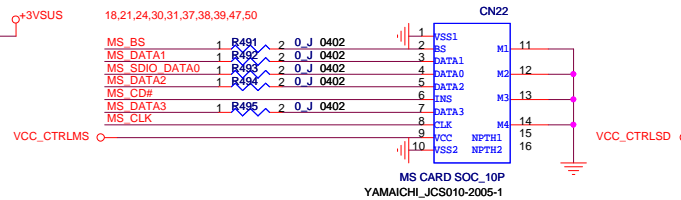
Place near PCI8402.



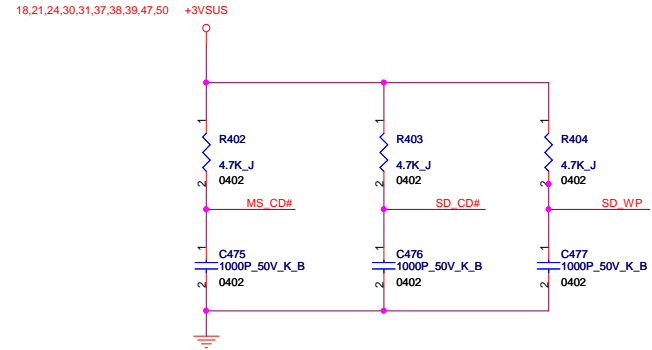
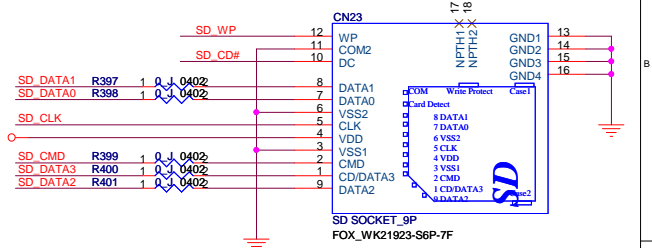
| | | | |
|-----------------------------|-------------------------|----------------------------------|----------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| | | CCPBG - R&D Division | |
| Title PCI (iLINK)2/3 | | | |
| Size | Document Number | Rev | |
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MS STD/DUO CONN.



SD CONN.

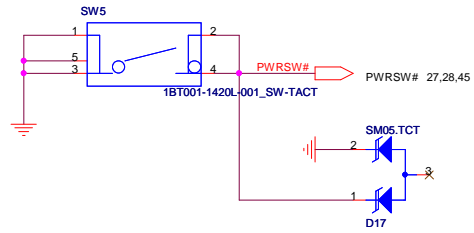


FOXCONN HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

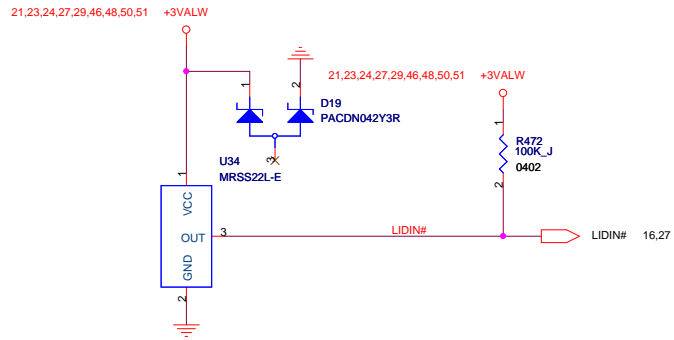
Title: **PCI (MS-DUO/SD/MDC) 3/3**

| | | |
|-------------------------------|----------------------------|-----------|
| Size: A3 | Document Number: MS70-1-01 | Rev: 0.20 |
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POWER BUTTON



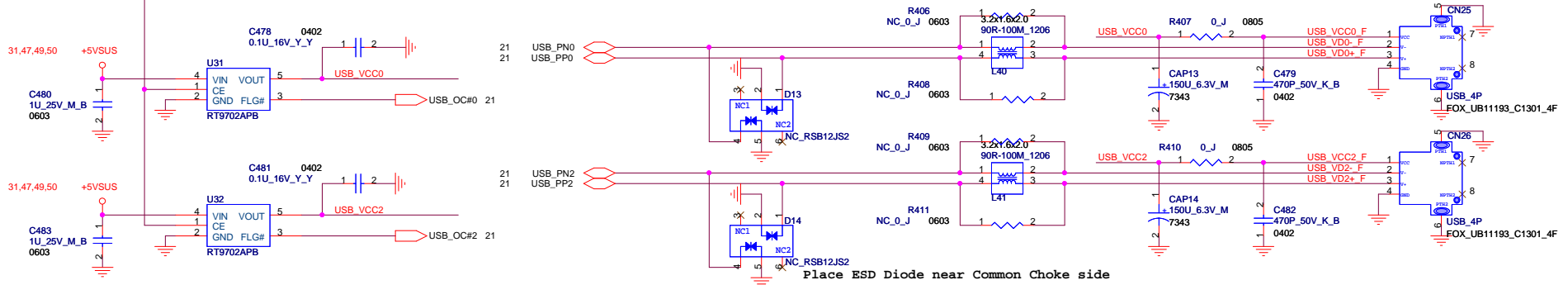
LID Switch



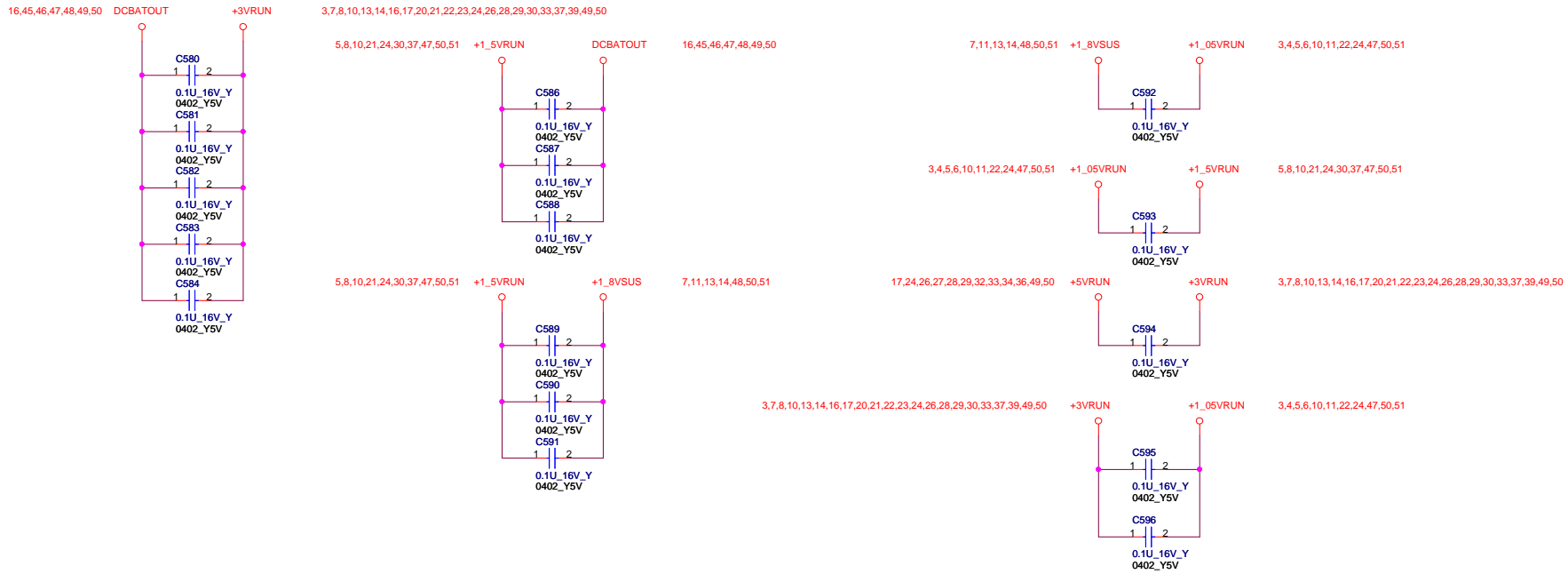
| | | | |
|--------------------------------|-------------------------|----------------------------------|----------|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title Button/LID Switch | | CCPBG - R&D Division | |
| Size | Document Number | Rev | |
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USB CONN X 2

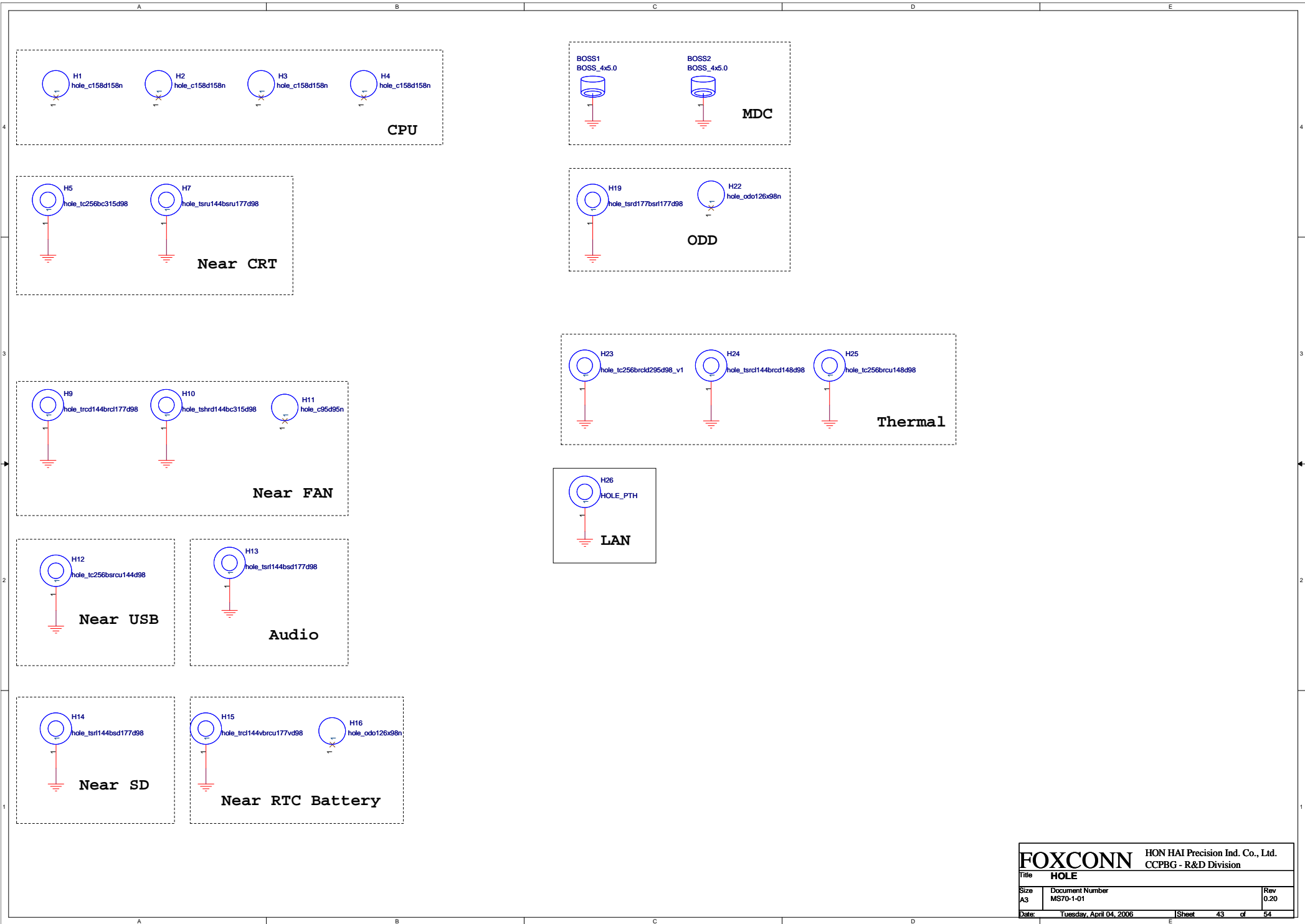
23,27,37,38 SUS_PWRGD_10MS



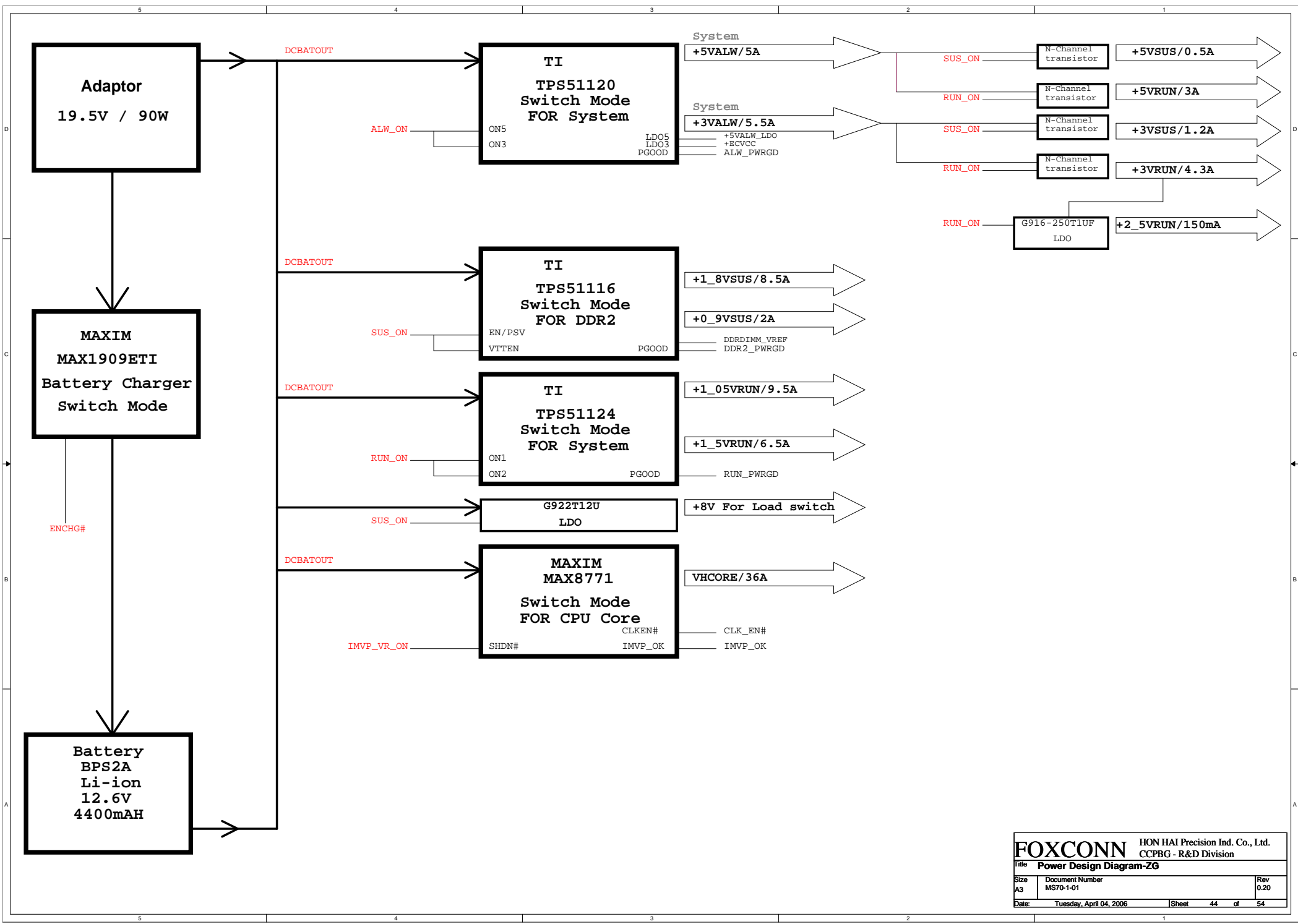
EMI CAP



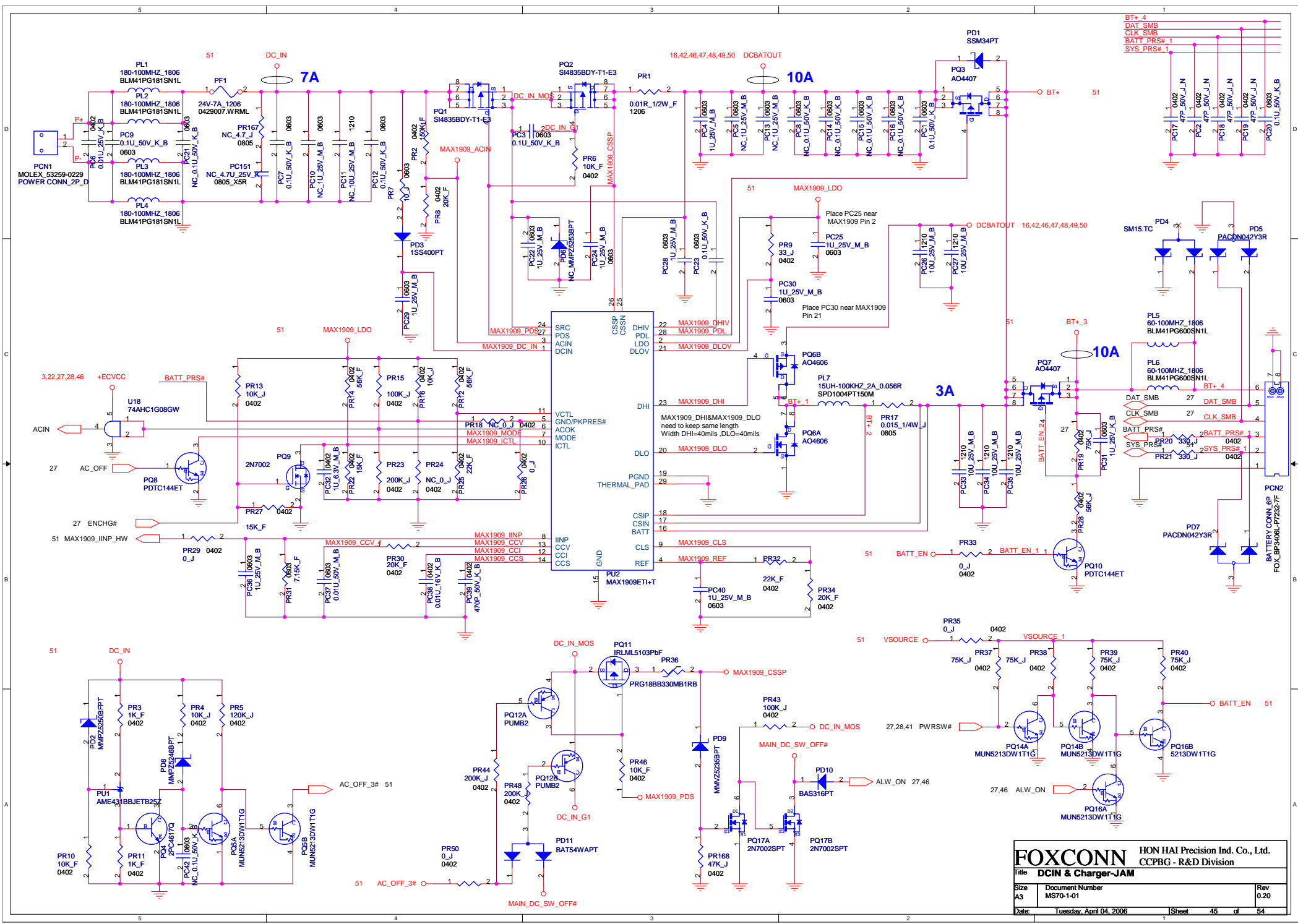
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| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title USB2.0 | | CCPBG - R&D Division | |
| Size A3 | Document Number MS70-1-01 | Rev 0.20 | |
| Date: Tuesday, April 04, 2006 | Sheet 42 | of 54 | |



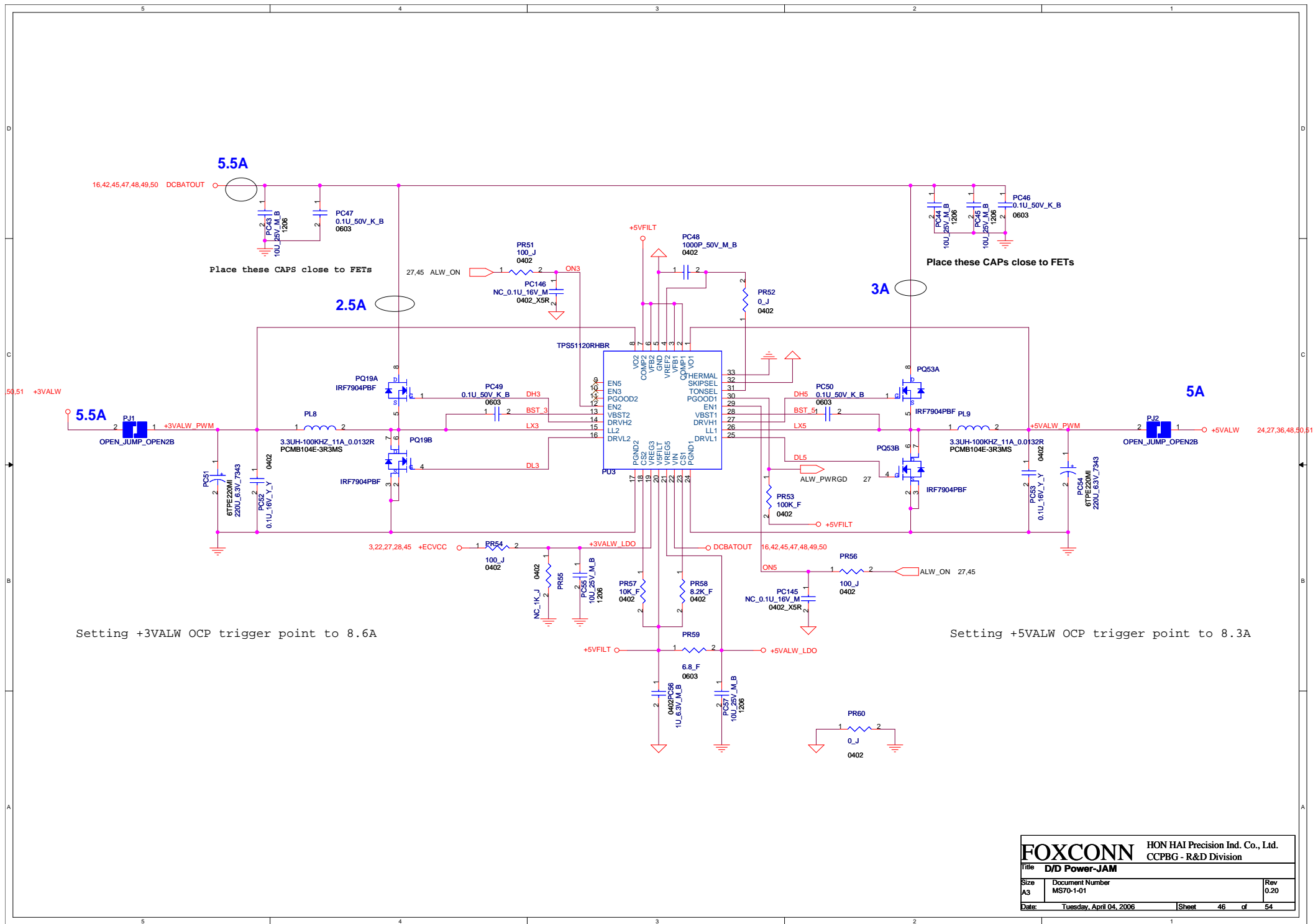
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| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title HOLE | | CCPBG - R&D Division | |
| Size | Document Number | Rev | |
| A3 | MS70-1-01 | 0.20 | |
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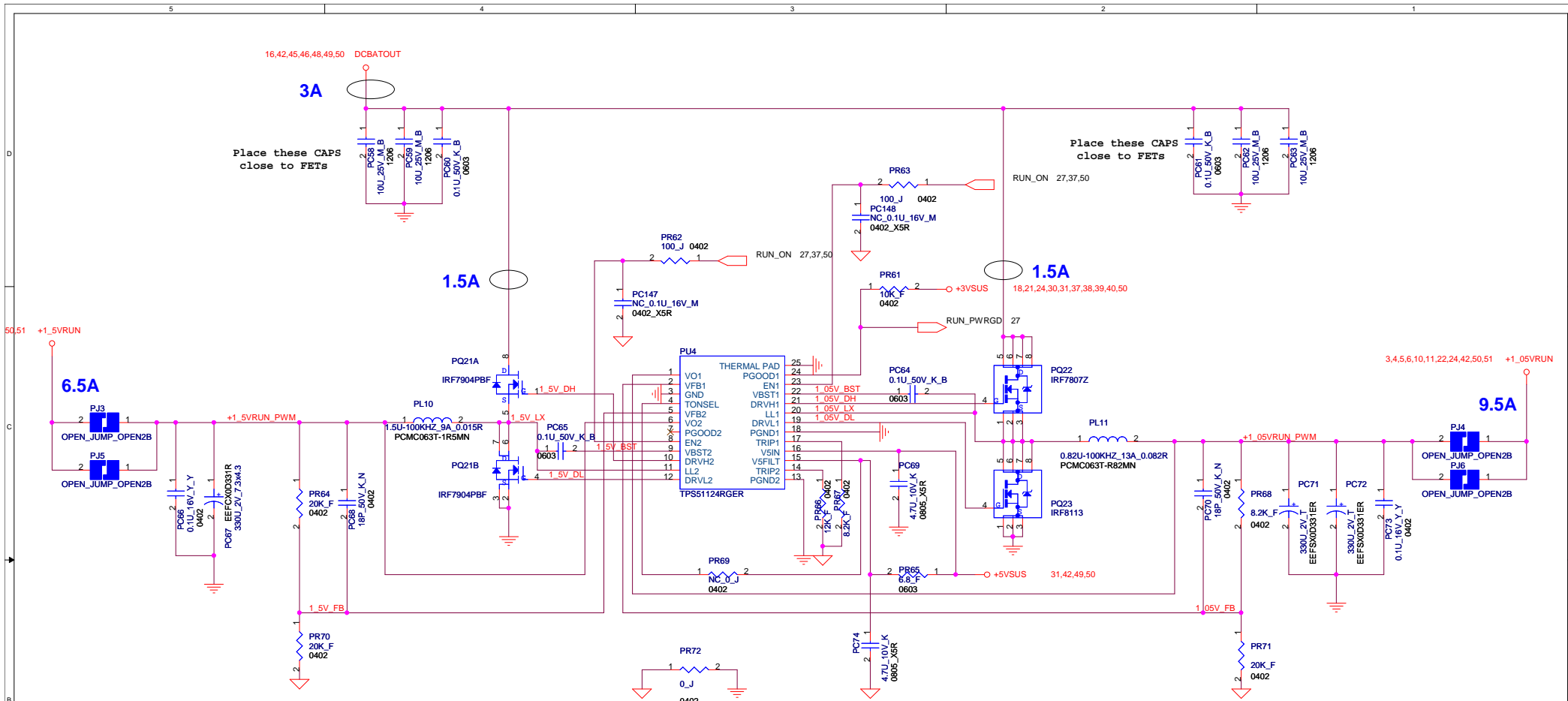


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| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| | | CCPBG - R&D Division | |
| Title Power Design Diagram-ZG | | | |
| Size | Document Number | Document Number | Rev |
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| | | | | | |
|-------------------------------------|-------------------------|-------|----------------------------------|-------|--|
| FOXCONN | | | HON HAI Precision Ind. Co., Ltd. | | |
| Title DCIN & Charger-JAM | | | CCPBG - R&D Division | | |
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16,42,45,46,48,49,50 DCBATOUT
3A
 Place these CAPS close to FETs

Place these CAPS close to FETs

6.5A

1.5A

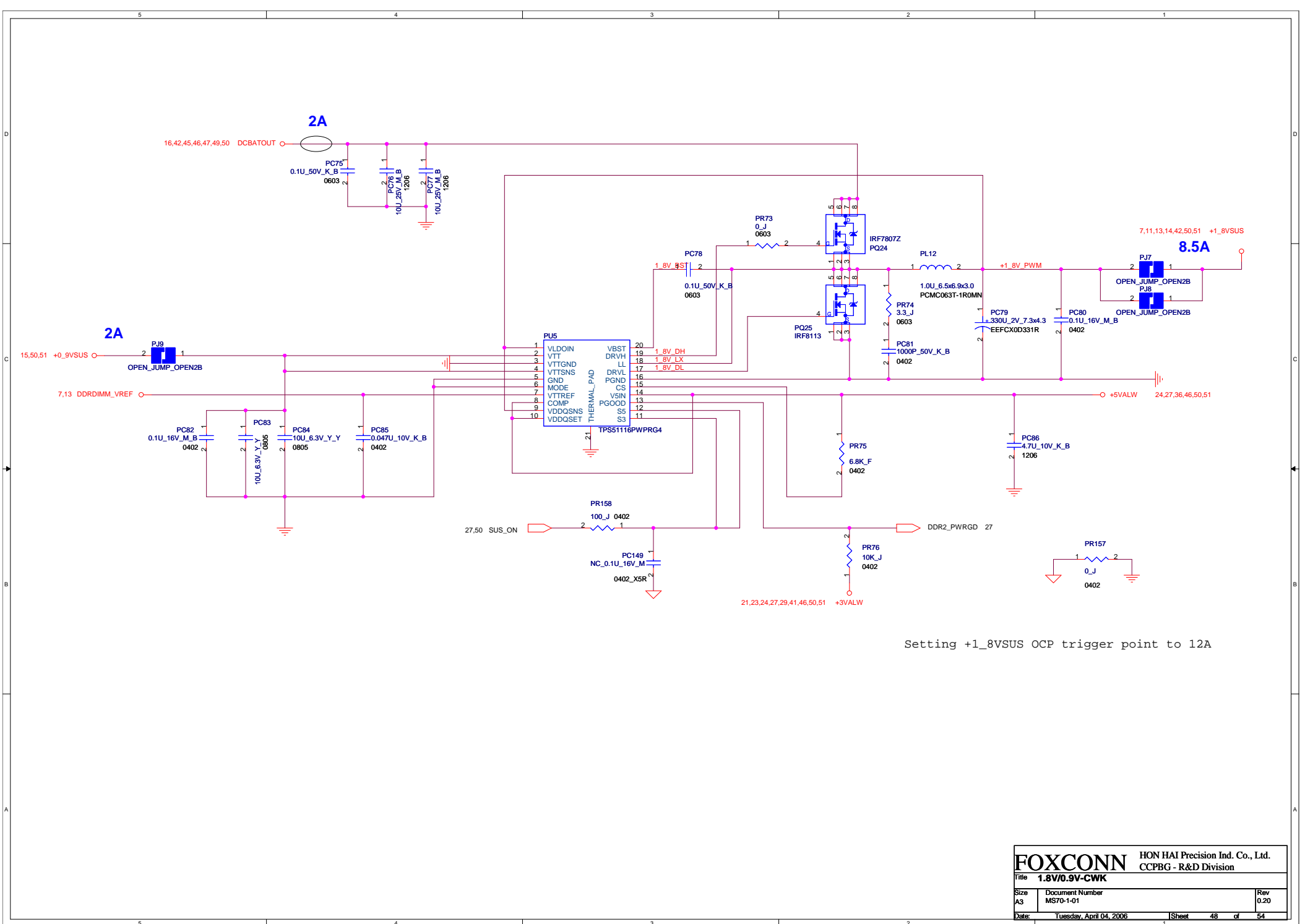
1.5A

9.5A

Setting +1_5VRUN OCP trigger point to 10.5A

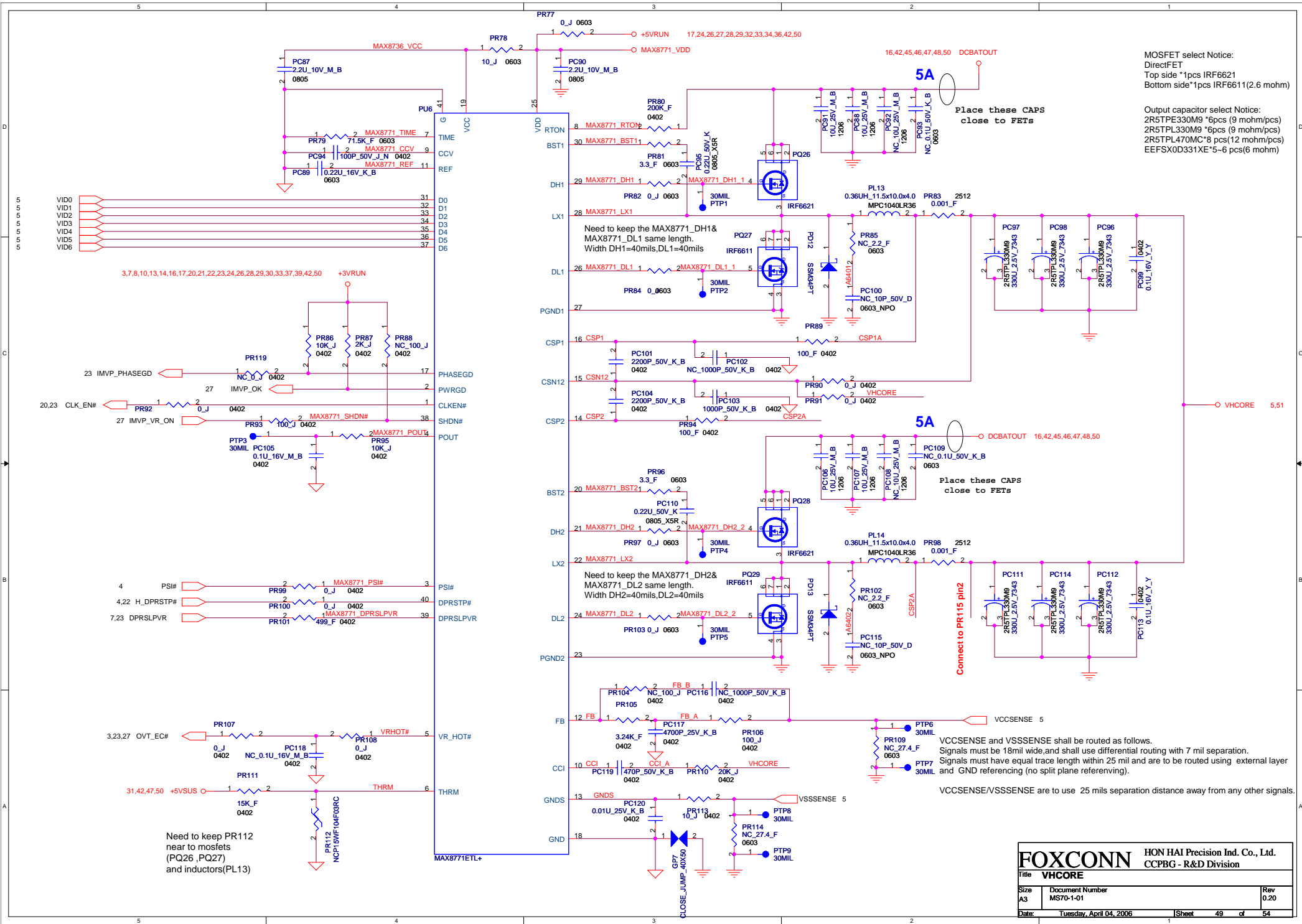
Setting +1_05VRUN OCP trigger point to 14.2A

| | | | | |
|-----------------------------|-------------------------|-------|----------------------------------|-------|
| FOXCONN | | | HON HAI Precision Ind. Co., Ltd. | |
| Title 1.5V/1.05V-JAM | | | CCPBG - R&D Division | |
| Size | Document Number | Rev | | |
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Setting +1_8VSUS OCP trigger point to 12A

| | | | |
|-------------------------------|---------------------------|----------------------------------|--|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| Title 1.8V/0.9V-CWK | | CCPBG - R&D Division | |
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| Date: Tuesday, April 04, 2006 | Sheet 48 | of 54 | |



MOSFET select Notice:
 DirectFET
 Top side *1pcs IRF6621
 Bottom side*1pcs IRF6611(2.6 mohm)

Output capacitor select Notice:
 2R5TPE330M9 *6pcs (9 mohm/pcs)
 2R5TPL330M9 *6pcs (9 mohm/pcs)
 2R5TFL470M*8 pcs(12 mohm/pcs)
 EEFSX0D331XE*5-6 pcs(6 mohm)

Need to keep the MAX8771_DH1&
 MAX8771_DL1 same length.
 Width DH1=40mils,DL1=40mils

Need to keep the MAX8771_DH2&
 MAX8771_DL2 same length.
 Width DH2=40mils,DL2=40mils

Need to keep PR112
 near to mosfets
 (PQ26, PQ27)
 and inductors(PL13)

Place these CAPS
 close to FETs

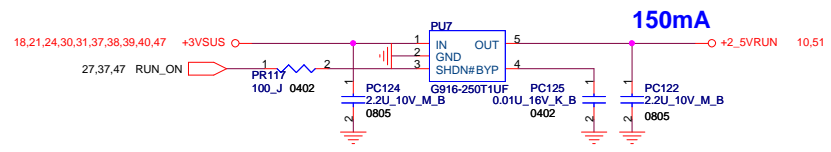
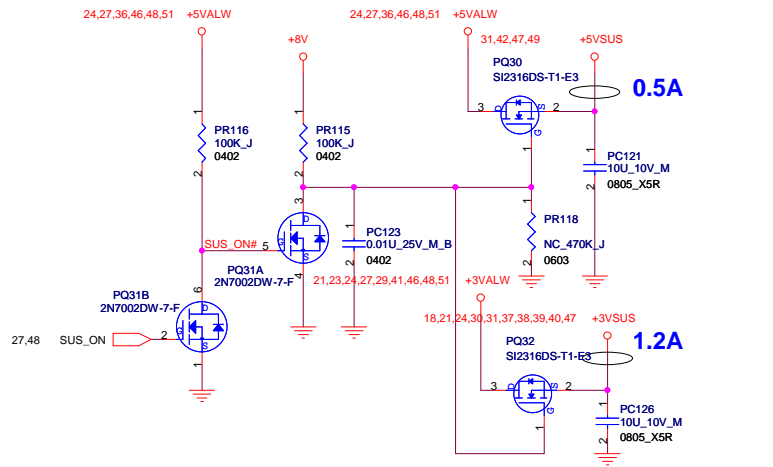
Place these CAPS
 close to FETs

Connect to PR115 pin2

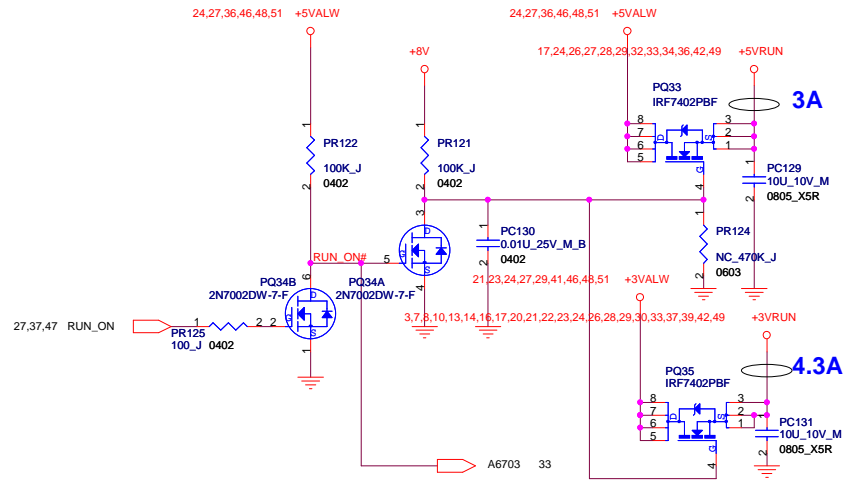
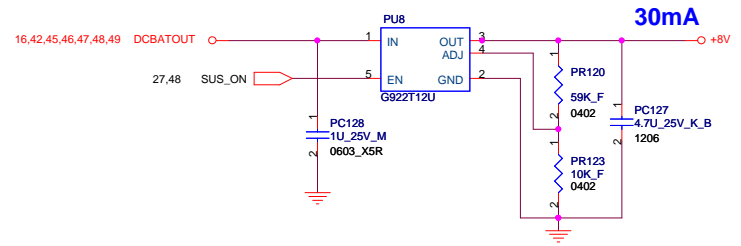
VCCSENSE and VSSSENSE shall be routed as follows.
 Signals must be 18mil wide, and shall use differential routing with 7 mil separation.
 Signals must have equal trace length within 25 mil and are to be routed using external layer
 and GND referencing (no split plane referencing).

VCCSENSE/VSSSENSE are to use 25 mils separation distance away from any other signals.

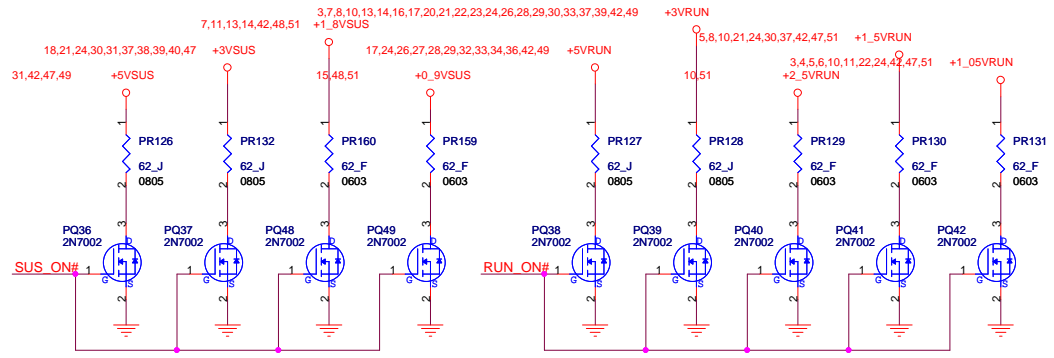
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|----------------------------------|------------------------------|-------------|----------------------------------|
| FOXCONN | | | HON HAI Precision Ind. Co., Ltd. |
| Title VHCORE | | | CCPBG - R&D Division |
| Size A3 | Document Number MS70-1-01 | Rev 0.20 | |
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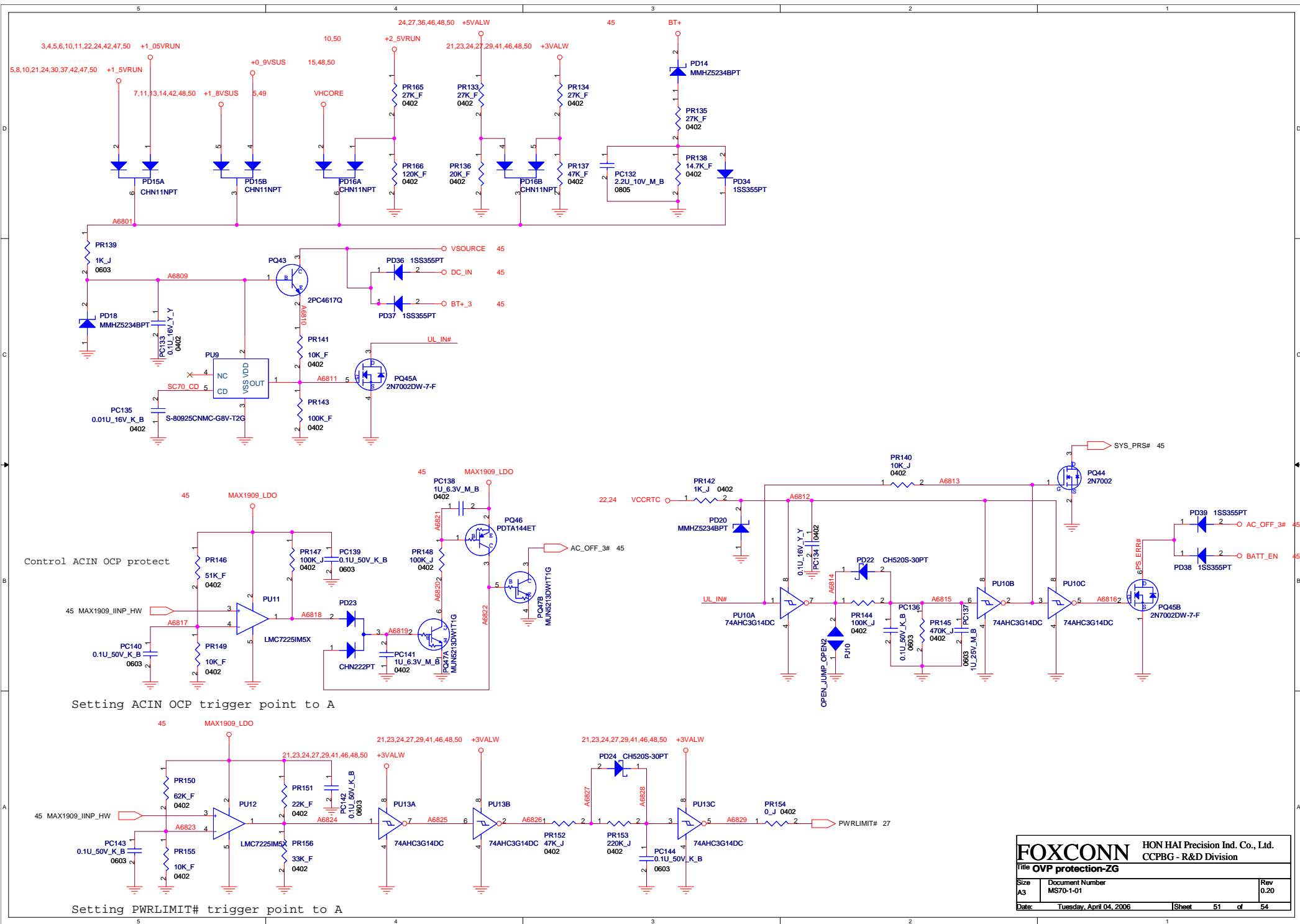
for load switch



Discharge circuit for power-off



| | | | |
|-----------------------------------|----------------------------|----------------------------------|--|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. | |
| | | CCPBG - R&D Division | |
| Title: Other power plan-ZG | | | |
| Size: A3 | Document Number: MS70-1-01 | Rev: 0.20 | |
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HISTORY

| | | |
|----------------|-------------------------|--|
| FOXCONN | | HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division |
| File | History (1) | |
| Size | Document Number | Rev |
| C | MS70-1-01 | 0.20 |
| Date: | Tuesday, April 04, 2006 | Sheet 52 of 54 |