DIGITAL LOGIC & SYSTEM DESIGN

My home ► My courses ► Managed Courses ► Semester 1701 ► 1701-COL215 ► 24 July - 30 July ► Setup and Submission Instructions

NAVIGATION Setup and Submission Instructions My home Site home Setup Instructions: Site pages Please follow the below instructions to setup your unix environment for being able to run Xilinx tools: My profile 1. Logon to the machine using your GCL account. Current course 2. In your home folder, open the .bashrc file and add the below lines (create a new file if .bashrc 1701-COL215 Participants doesn't exist): General export XILINXD LICENSE FILE=2100@10.208.22.170:/extra/xilinxlic/Xilinx.lic 24 July - 30 July source /opt/Xilinx/14.7/ISE_DS/settings64.sh Preferred Lab timings for Group source /opt/Xilinx/Vivado/2016.4/settings64.sh 1 3. Alternatively, you can add the below alias and whenever you open a shell, run the command ? Preferred Lab timings for Group "enable_xilinx": 2 alias enable xilinx="export XILINXD LICENSE FILE=2100@10.208.22.170:/extra/xilinxlic/Xilinx.lic; Preferred Lab source /opt/Xilinx/14.7/ISE_DS/settings64.sh; source /opt/Xilinx/Vivado/2016.4/settings64.sh" timings for Group 3 3. After making changes to .bashrc, run "source .bashrc" in terminal. ? Preferred Lab timings for Group 4 😉 Lec01 Introduction 25 Submission Instructions: Jul 2017 Only one of the partner needs to submit. Also, you need to mention the partners name, entry ID (while doing 🖲 Lec01 the submission). Introduction 25 Jul 2017 print 1. Go to the submission tab. Setup and **DIGITAL LOGIC & SYSTEM DESIGN** Submission My home IN My cour s ⊫ So 1701 ► 1701-COL215 ► 24 July - 30 July ► Lab1_Sub Instructions NAVIGATION 🧏 Lab Exercise 1 Submission SchematicTutoria Т 🗐 Lab1 - Testbench and constraints file Lab1_Submis sion_Link_Gro on_Link_Grou up3_Thursday S Lab1 S on Link Group2 Friday - Car Light C ... 2. Upload the lab1_car_light.vhf by clicking on "choose a file" and then "browse..." Lab1_Submis sion_Link_Gro up4_Friday -Car Light Con... Lab1_Submis sion_Link_Gro up1_Monday -Car Light Con... 1 of 4 Monday 31 July 2017 01:27 PM Lab1_Submis sion_Link_Gro



1701-COL¹²19: Setup²⁴nd Submission Instructions https://moodle.iitd.ac.in/mod/page/view.php?id... December 6. Automatic evaluation will start. 25 December - 31 **DIGITAL LOGIC & SYSTEM DESIGN** December My home > My courses > Managed Courses > Semester 1701 > 1701-COL215 > 24 July - 30 July > Lab1_Submission_Link_Group2_Friday - Car Light Con... 1 January - 7 NAVIGATION - < **Evaluating** My home Site home Site pages January Lokesh Siddhu 8 January - 14 My profile Current course 1701-COL215 Participants January My courses General 24 July - 30 July Preferred Lab timings for Group 1 Preferred Lab timings for Group 2 Preferred Lab timings for Group 3 Sevaluate × Preferred Lab timings for Group 5 Preferred Lab timings for Group 4 Lec01 Introduction 25 Jul 2017 Lec01 Introduction 25 Jul 2017 print ¢ connecting - < **ADMINISTRATION** instructions to setup your envir Lab Exercise 1 Page module Schematic Lutonal Labl - Testbench and constraints file SLabl_Submission_Link_Group1_Thursday administration Edit settings 7. Accept certificates for all the servers. **DIGITAL LOGIC & SYSTEM DESIGN** Permissions My home 🕨 My courses 🕨 Managed Courses 🕨 Semester 1701 🕨 1701-COL215 🕨 24 July - 30 July 🕨 Lab1_Submission_Link_Group2_Friday - Car Light Con... Check permissions Filters NAVIGATION - < Evaluating My home Site home Site pages My profile Lokesh Siddhu Logs Backup Current course 1701-COL215 Participants General Accept self signed certificates × Restore You are using an encrypted connection. To use an encrypted connection with the execution servers it is required you accept its certificates. 24 July - 30 July Course administration If you have problems with this process, you can try to use a http (unencrypted) connection or other bro Preferred Lab timings for Group 1 Preferred Lab timings for Group 1 Preferred Lab timings for Group 2 Preferred Lab timings for Group 3 Preferred Lab timings for Group 4 Lec01 Introduction 25 Jul 2017 Please, click on the following links (server #) and accept the offered certificate. 1. <u>Server 1</u> 2. Server 2 3. Server 3 4. Server 4 5. Server 5 6. Server 6 Switch role to ... Lec01 Introduction 25 Jul 2017 print instructions to setup your e My profile settings Lab Exercise 1 SchematicTutorial OK Cancel Lab1 - Testbench and constraints file Lab1_Submission_Link_Group1_Thursday - Car Light C... Lab1_Submission_Link_Group2_Friday -Car Light Cost Lab1_Submiss Car Light Con..

8. After accepting all certificates, please click OK. DIGITAL LOGIC & SYSTEM DESIGN

IAVIGATION	Evaluating
ly home	Erataating
Site home	Lokesh Siddhu
Site pages	
My profile	
Current course	
T1701-COL215	Accept self signed certificates ×
Participants	You are using an encrypted connection.
General	To use an encrypted connection with the execution servers it is required you accept its certificates.
24 July - 30 July	If you have problems with this process, you can try to use a http://www.mtod).connection or other brouger
Preferred Lab timings for Group 1	If you have problems with this process, you can if y to use a http (unencrypted) connection of other provisel.
Preferred Lab timings for Group 2	Please, click on the following links (server #) and accept the offered certificate.
Preferred Lab timings for Group 3	
Preferred Lab timings for Group 4	OK Cancel
Lecol Introduction 25 Jul 2017	
Eccul Introduction 25 Jul 2017 print	
submiss	
Lab Exercise 1	
SchematicTutorial	
Lab1 - Testbench and constraints file	
Submission Link Group1 Thursday	

9. If no progress bar is shown, please press F5 button or re-submit the assignment.

Moodle execution server should start evaluating your assignment (In case it does not, re-submit.).
 Finally you would see the grade and the assessment report. Make sure you see your grade and

go through the assessment report to understand the errors

1701-COL215: Setup and Submission Tinstructions submission view https://moodle.iitd.ac.in/mod/page/view.php?id...

Grade

Reviewed on Wednesday, 26 July 2017, 8:51 PM by Automatic grade Grade 100 / 100 Assessment report

****** Vivado v2016.4 (64-bit) **** SW Build 1733598 on Wed Dec 14 22:35:42 MST 2016 **** IP Build 1731160 on Wed Dec 14 23:47:21 MST 2016 ** Copyright 1986-2016 Xilinx, Inc. All Rights Reserved.

source lab1_car_light.tcl # create_project lab1_car_light./vivado_tcl/lab1_car_light-part xc7a35tcpg236-1 # import_files -norecurse {./lab1_car_light.thf) # import_files -norecurse {./lab1_car_light_tb [get_fileset sim_1] # launch_simulation INFC: [SIM-utils-51] Simulation object is 'sim_1' INFC: [SIM-utils-51] Simulation depicts are files for 'lab1_car_light_tb in fileset INFO: [SIM-uils-51] Simulation object is 'sim_1'
INFO: [USF-XSim-37] Finding global include files...
INFO: [USF-XSim-37] Finding global include files...
INFO: [USF-XSim-37] Finding global include files...
INFO: [USF-XSim-37] Kinding global include files...
INFO: [USF-XSim-31] Kinding COMPLE and ANALYZE' step in /home/p16072/vivado_tcl/lab1_car_light/lab1_car_light/lab1_car_light.phav'
xvhdi-m64 -relax -pi lab1_car_light tb_ vhdl.pri
INFO: [VSF-XSim-31] Xacutary of the pinote p16072/vivado_tcl/lab1_car_light.sm/sim_orts/p16072/lab1_car_light.ph" into library xil_defaultib
INFO: [VRFC 10-163] Analyzing vHDL file '/home/p16072/vivado_tcl/lab1_car_light.sm/sources_1/imports/p16072/lab1_car_light.ets.vhd' into library xil_defaultib
INFO: [VRFC 10-163] Analyzing netty lab1_car_light
INFO: [USF-XSim-61] Xacutary [ELABORATE' step in /home/p16072/vivado_tol/lab1_car_light/lab1_car_light.sim/sim_1/behav'
Vivado Simulator 2016 4
Copyright 1986-1999, 2001-2016 Xilinx, Inc. All Rights Reserved.
Running: failoptXilinx1/Vivado/2016 A.bininumvapped/inck6.oxala -who 8cb2c37e82934103b404b12d0d5e329d --debug typical --relax --mt 8 -L xil_ Copyram Foor Foor Copyram Copyram Copyram Copyram Copyram Foor Foor Copyram Foor Foor Copyram Copyram

Last modified: Friday, 28 July 2017, 5:24 PM

(i) Moodle Docs for this page

You are logged in as Rajesh Kedia (Log out) 1701-COL215