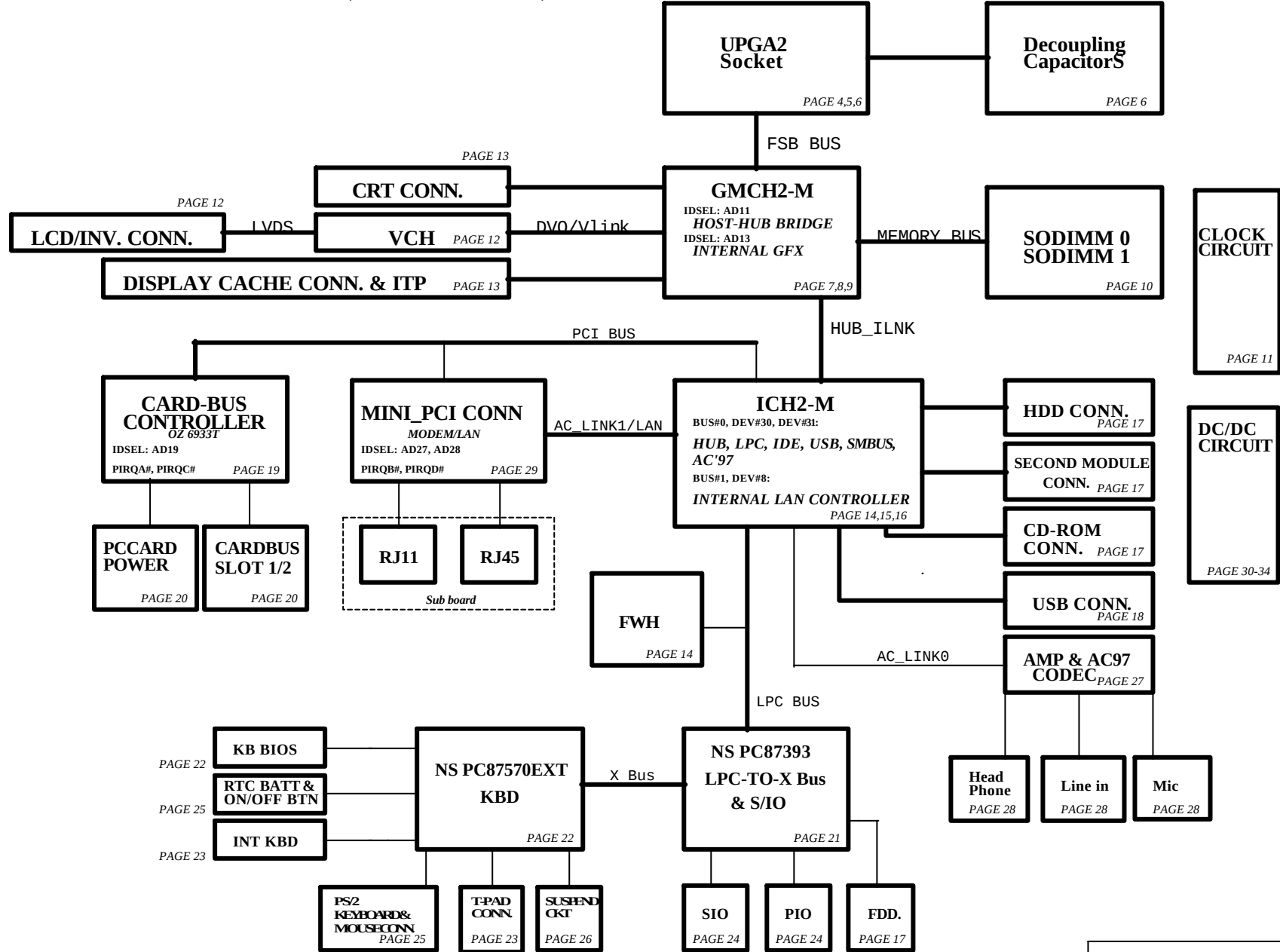


COMPAL CONFIDENTIAL
MODEL NAME : 888L2(SOLANO2-M)
Date: 01/11/01
Version: 2.0

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Compal Electronics, Inc.			
Title Cover Sheet			
Size	Document Number 888L2 Main Board		Rev 2.0
Date:	Thursday, January 11, 2001	Sheet	1 of 42

MODEL NAME : 888L2 (SOLANO2-M)



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Compal Electronics, Inc.			
Title			
System Block Diagram			
Size			
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Revision History			
#	Date	Description	Version
1	2000/7/21	First Release <i>(EVT-SST)</i>	0.1
2	2000/9/20	Second Release <i>(DVT1-PT1)</i>	0.2A
3	2000/11/3	Third Release <i>(DVT2-PT2)</i>	0.2C1
4	2000/12/27	Fouth Release <i>(ST)</i>	1B
5	2001/01/11	fifth release <i>(QT)</i>	2.0

Chip information

GMCH2-M FW82815EM

Version	S-spec.	Q-spec.
A0		QA38
A1	SL4MP	QA75

ICH2-M FW82801BAM

Version	S-spec.	Q-spec.
A0		Q908, Q909, Q910, Q911, Q912
B0	SL45HQ	Q967, Q968
B1	SL4HN	QA36, QA37
B2		QA56, QA57

VCH FW82807AA

Version	S-spec.	Q-spec.
A0		Q989
A1		QA76
A2		QB41ES

EVT *(SST)*

GMCH2-M	QA38ES (A0)
ICH2-M	Q967ES (B0)
VCH	Q989ES (A0)

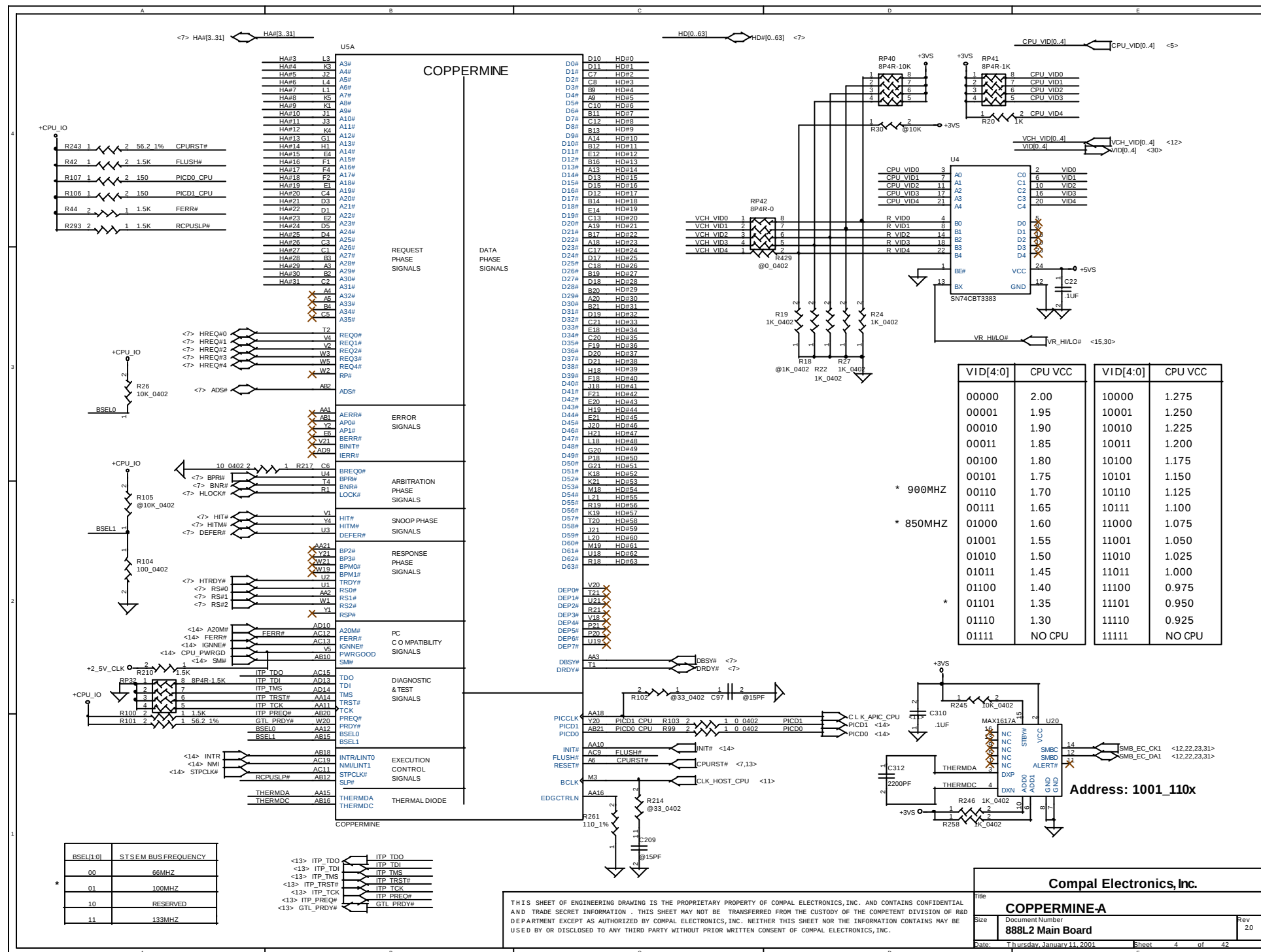
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GMCH2-M	QA75ES (A1)
ICH2-M	QA57ES (B2)
VCH	Q076ES (A1)

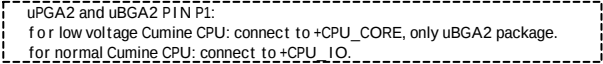
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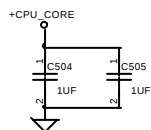
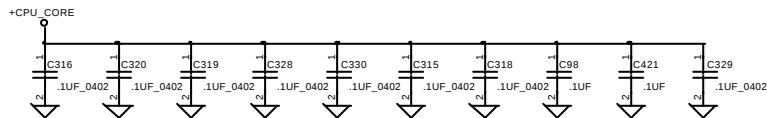
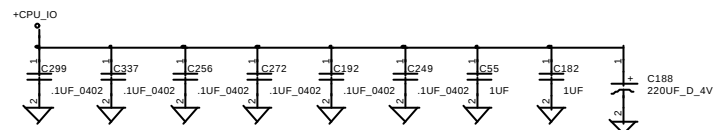
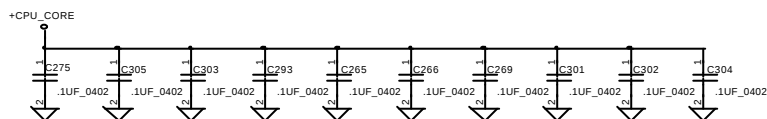
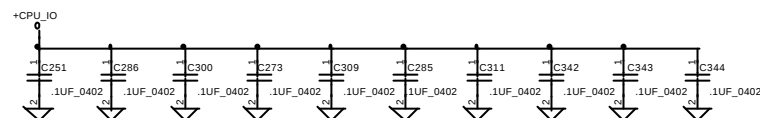
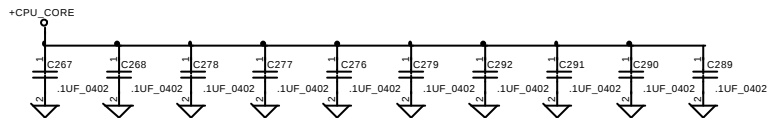
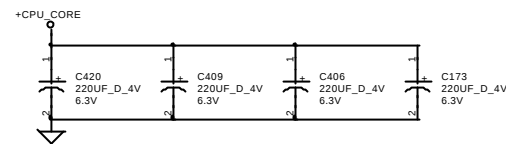
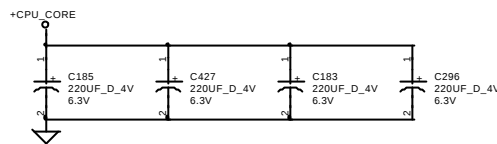
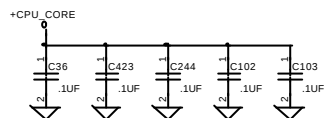
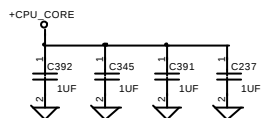
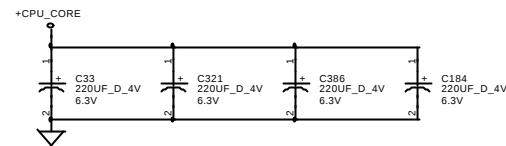
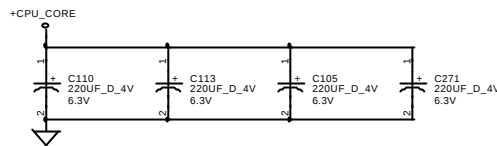
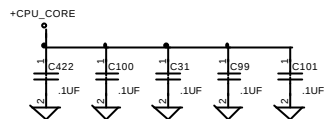
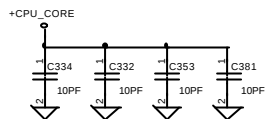
GMCH2-M	A1
ICH2-M	B2
VCH	A2

QT same as ST

Compal Electronics, Inc.			
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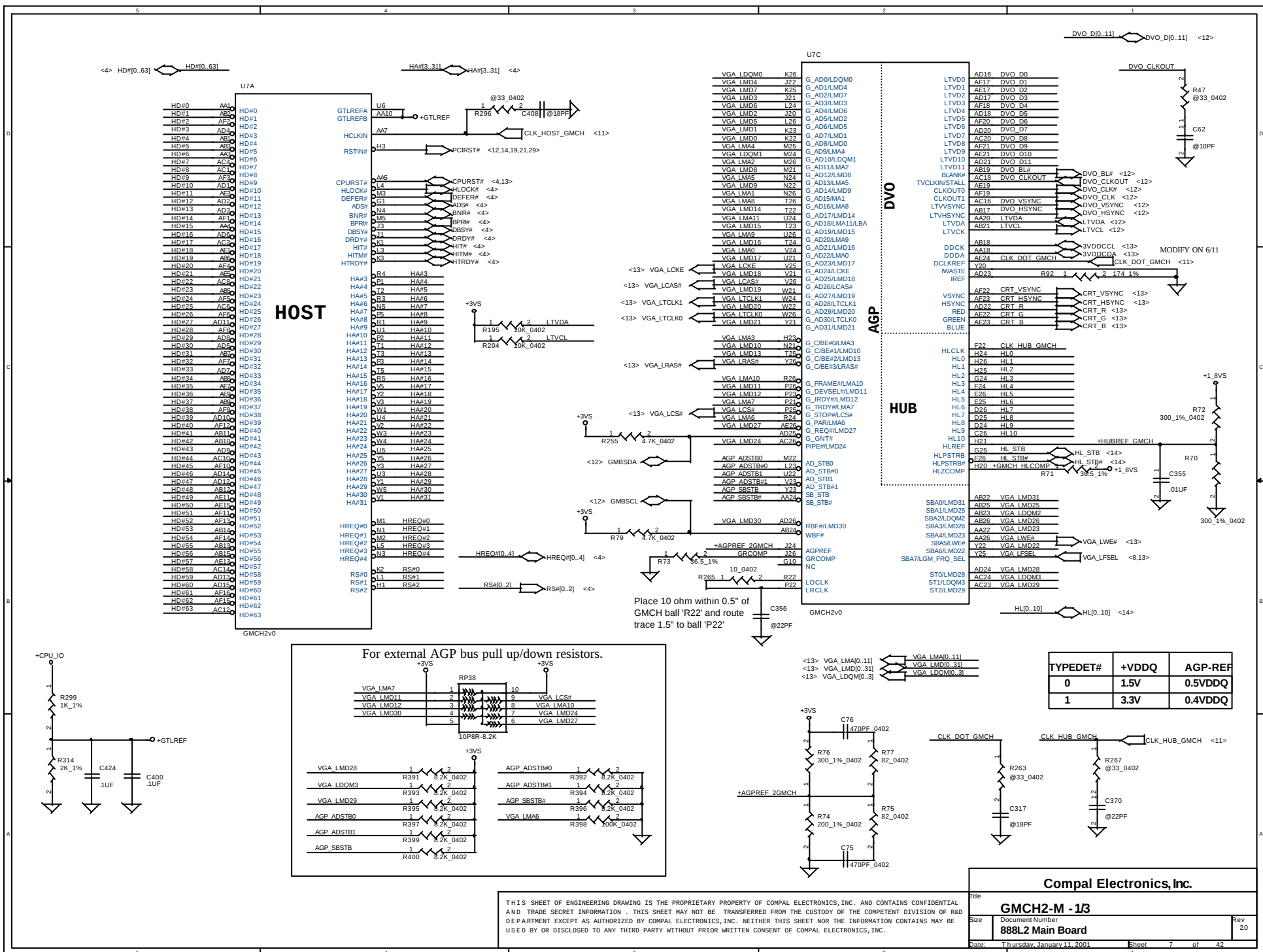




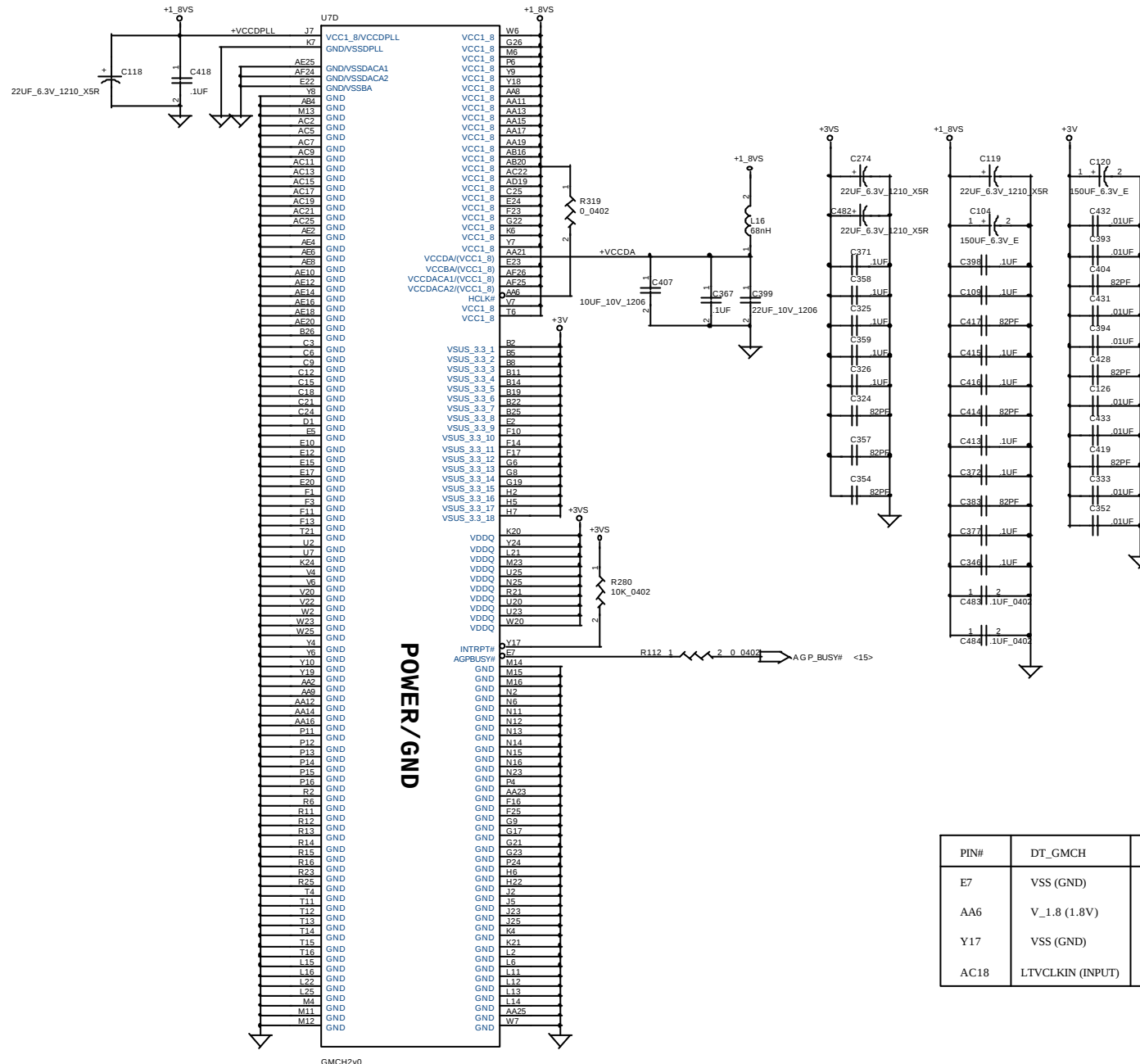


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File CPU BYPASS			
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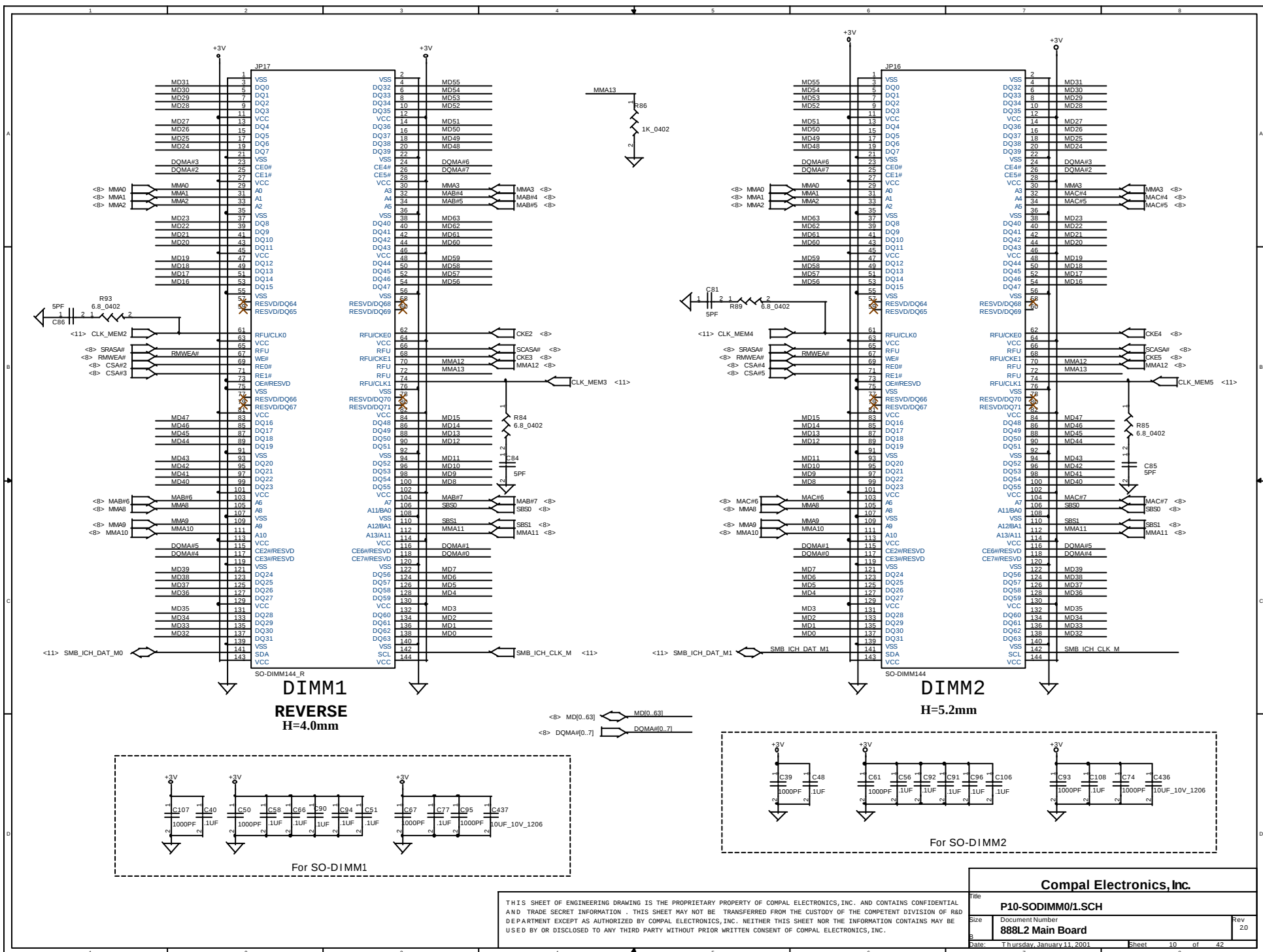
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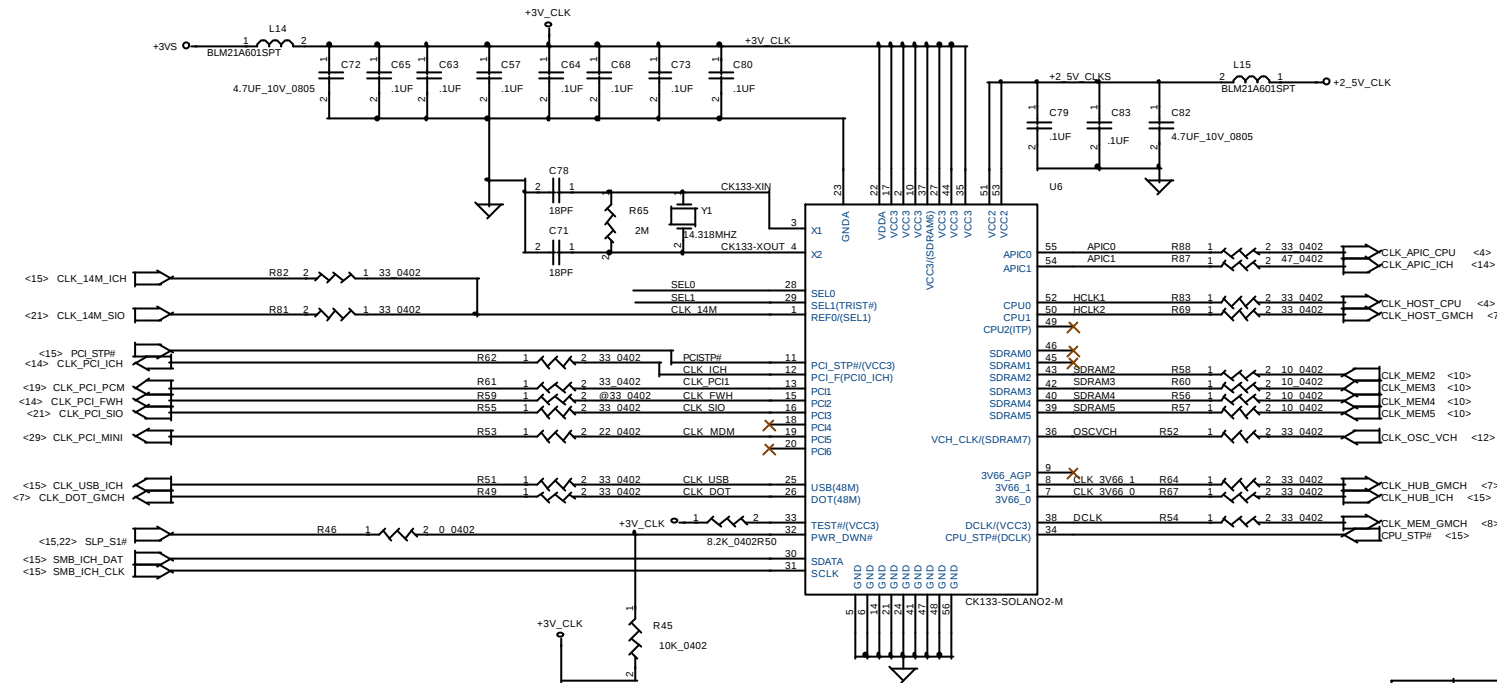


PIN#	DT_GMCH	GMCH2-M
E7	VSS (GND)	AGPBUSY# (OUTPUT)
AA6	V_1.8 (1.8V)	RESERVED (1.8V)
Y17	VSS (GND)	INTRPT# (INPUT)
AC18	LTVCLKIN (INPUT)	LTVCLKIN/STALL (INPUT)

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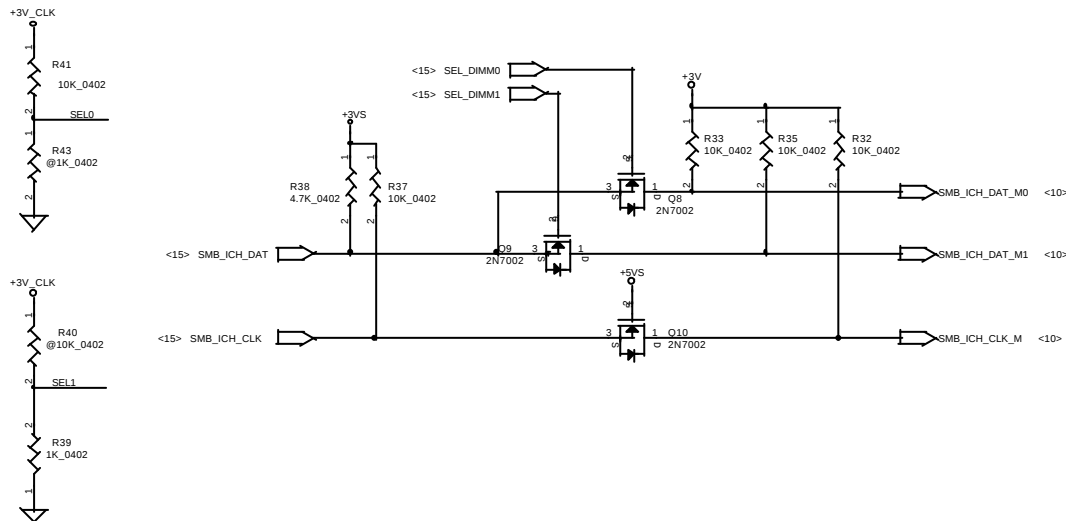
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File	GMCH2-M - 3/3	
Size	Document Number	Rev
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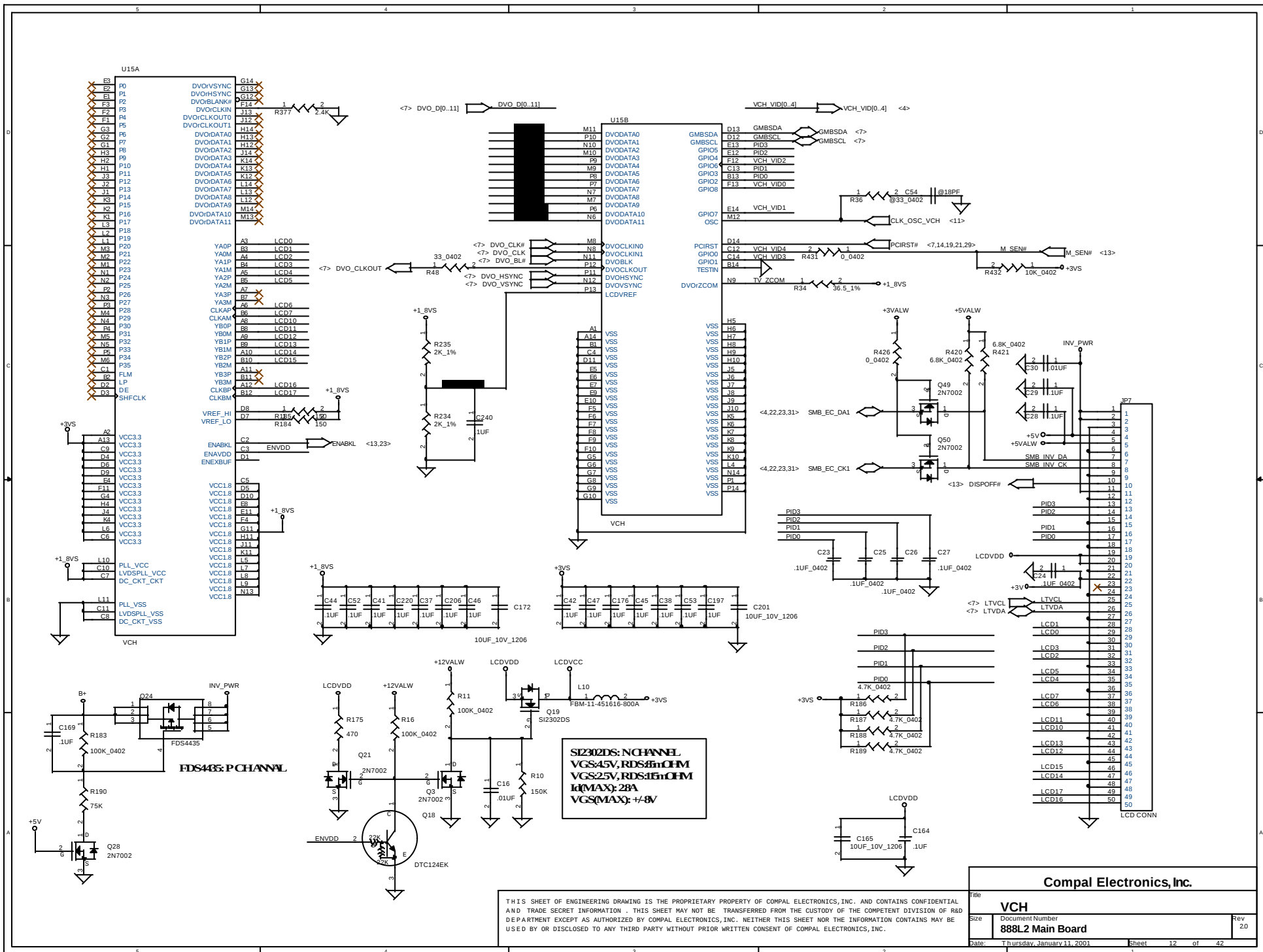
SEL1	SEL0	PSB	SDRAM
0	0	66	100
0	1	100	100
1	0	133	133
1	1	133	100

Pin #	CK-Solano		CK-Solano2-m	
	W218	C9815	W224	C9835
1	REF0/SEL1	REF/SEL1	REF	REF
11	VCC3	VCC3	PCI_STP#	PCI_STP#
29	Tristate#	Tristate#	SEL1	SEL1
33	VCC3	VCC3	TEST#	TEST#
34	DCLK	DCLK	CPU_STP#	CPU_STP#
36	SDRAM7	SDRAM7	VCH_CLK	VCH_CLK
37	SDRAM6	SDRAM6	VCC3	VCC3
38	VCC3	VCC3	DCLK	DCLK



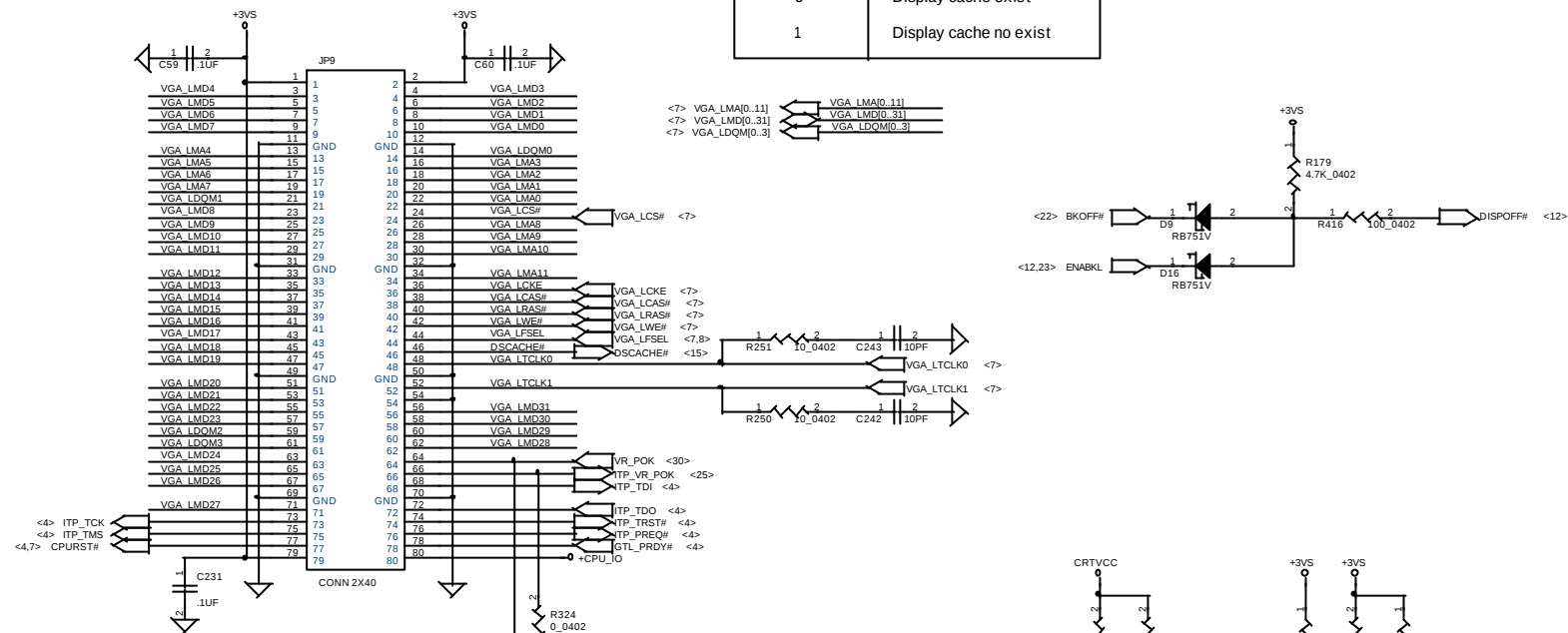
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Compal Electronics, Inc.			
File	Clock Synthesizer		
Size	Document Number		
	888L2 Main Board		
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			Rev 2.0

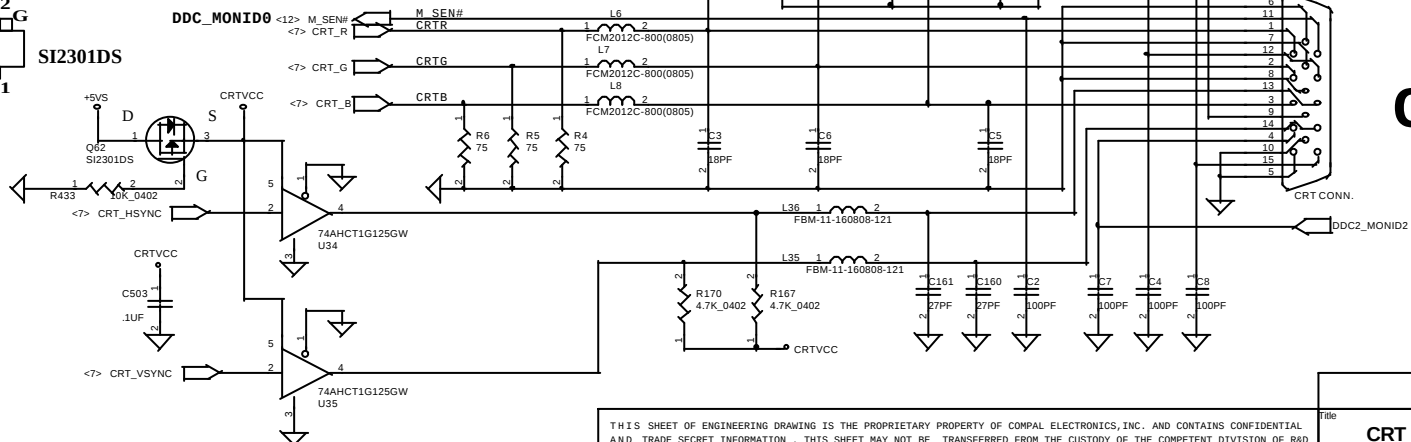
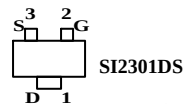


DISPLAY CACHE INTERFACE

DSCACHE#	Status
0	Display cache exist
1	Display cache no exist



SI2301DS: P CHANNEL
VGS: -4.5V, RDS: 130mOHM
VGS: -2.5V, RDS: 190mOHM
Id(MAX): 23A
VGS(MAX): +8V



CRT CONN.

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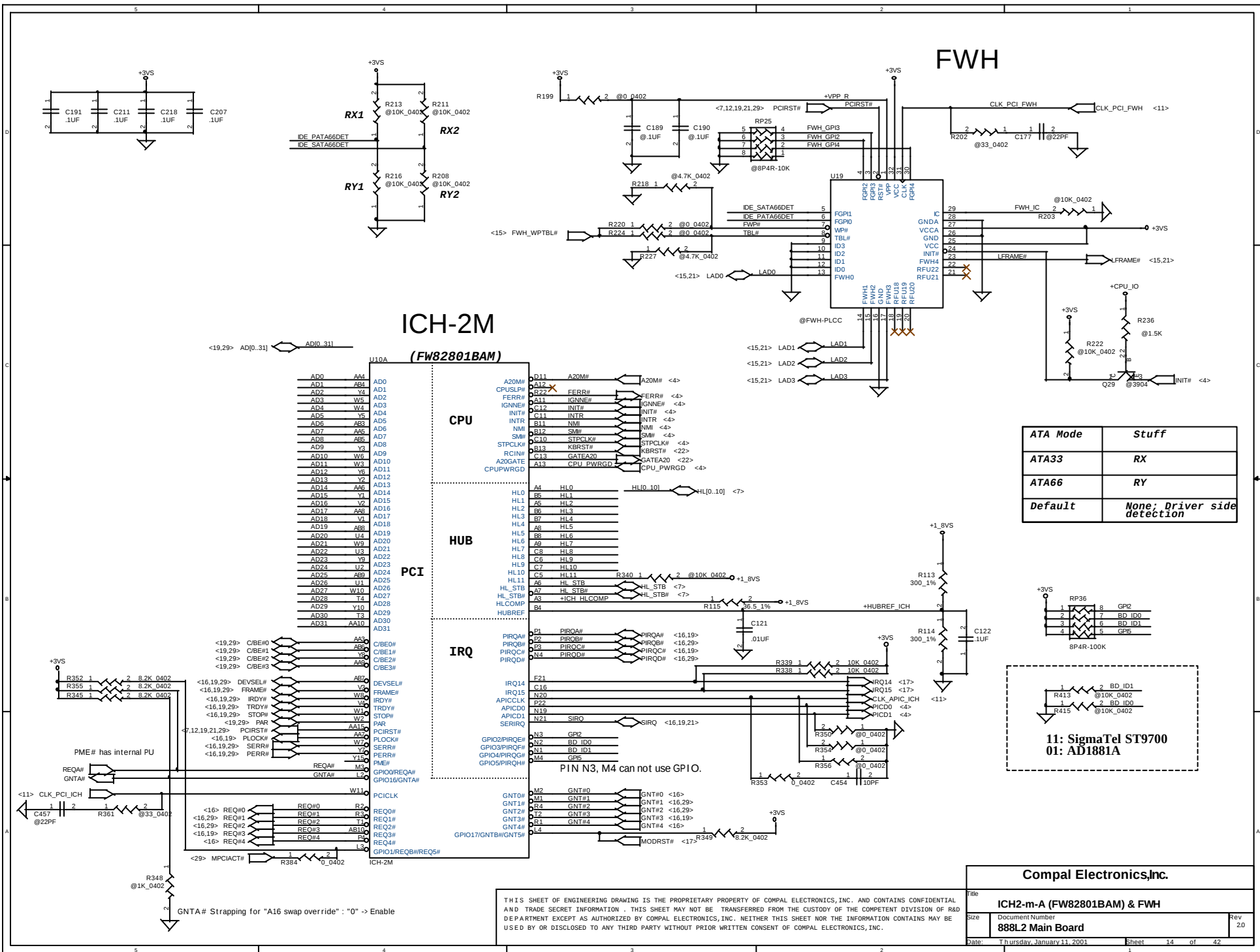
Compal Electronics, Inc.

CRT CONN

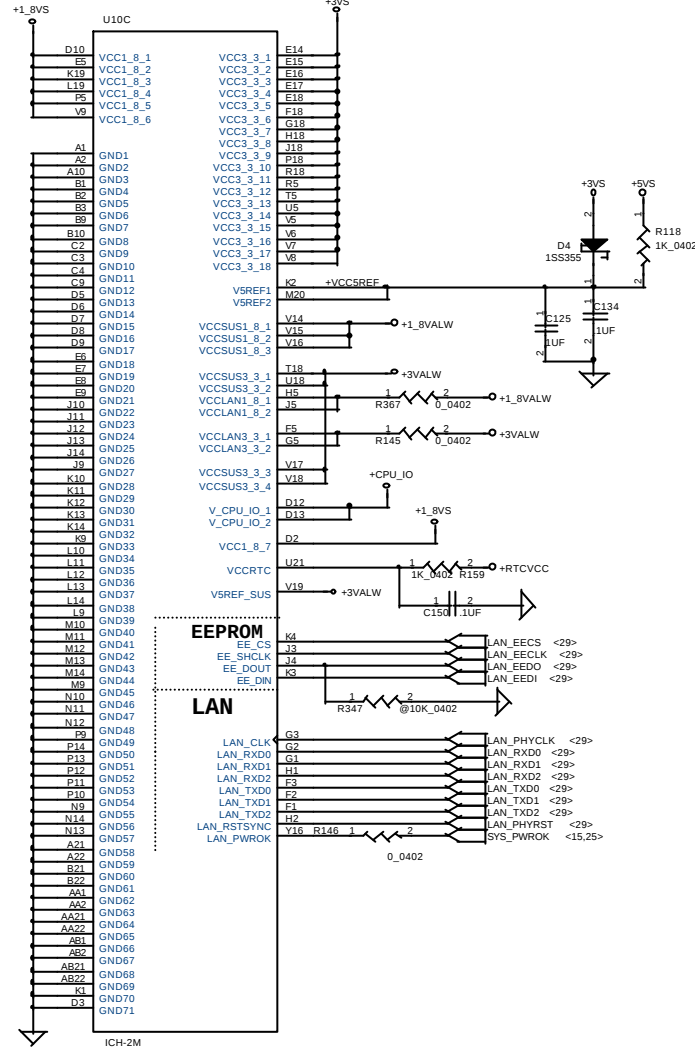
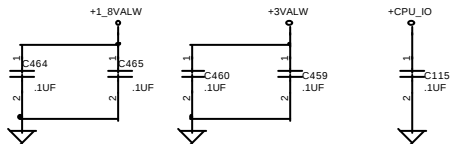
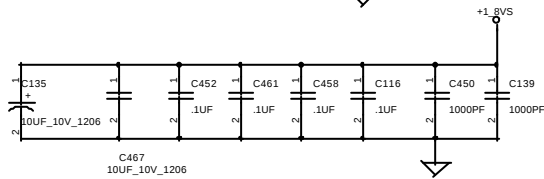
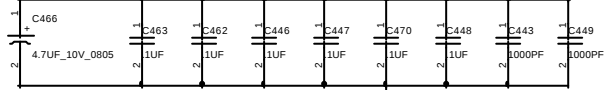
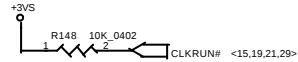
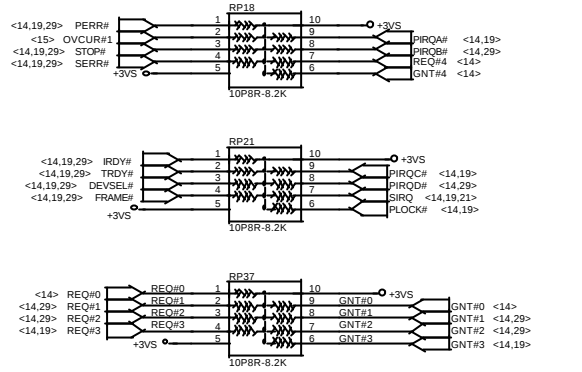
Document Number
888L2 Main Board

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PCI



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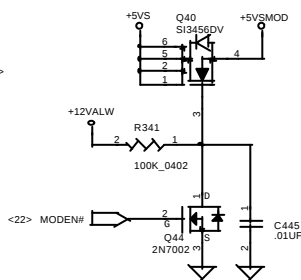
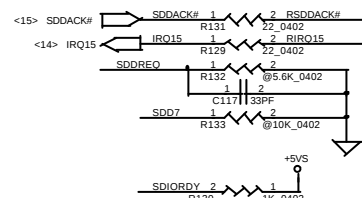
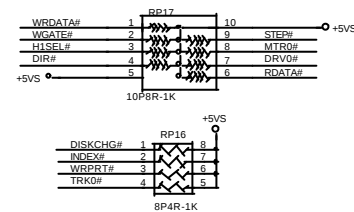
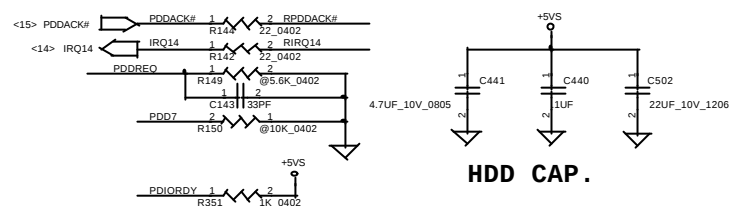
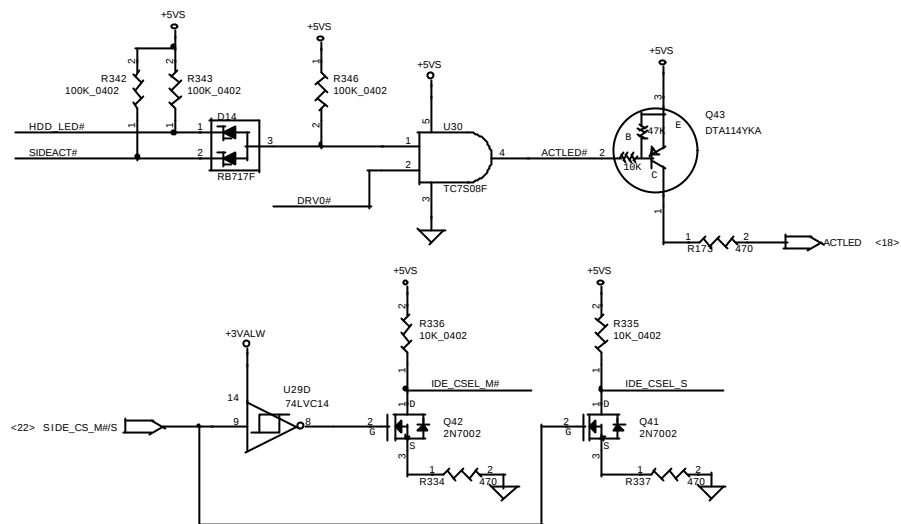
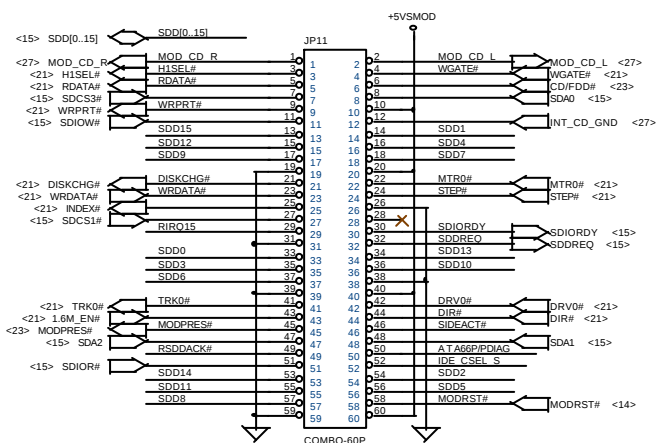
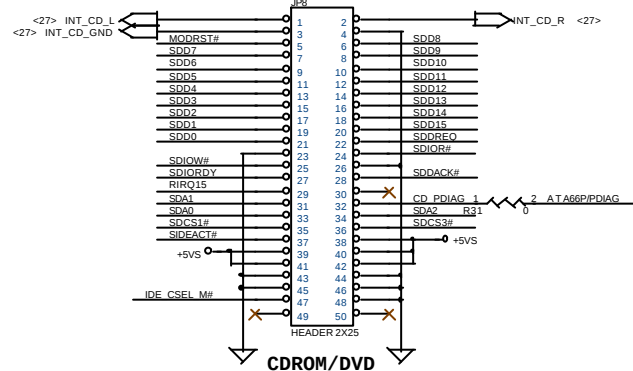
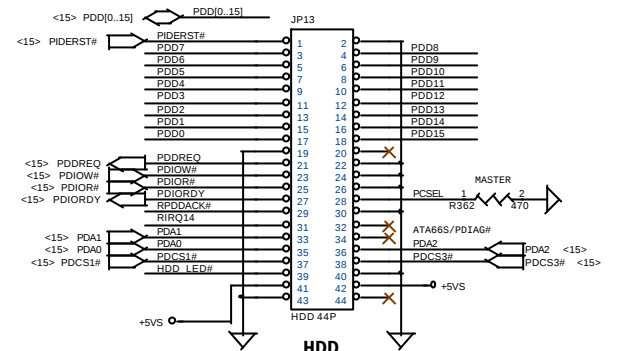
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Size Document Number 888L2 Main Board Rev 2.0

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IDE,CD-ROM & FDD Module CONN.



SI3456DV: NCHANNEL
VGS: 4.5V, RDS: 65mOHM
Id(MAX): 5.1A

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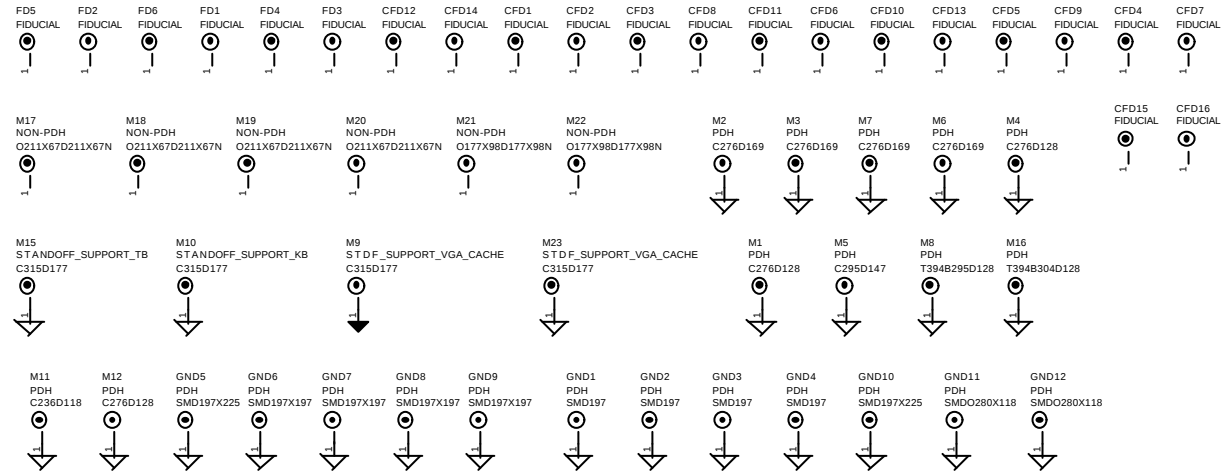
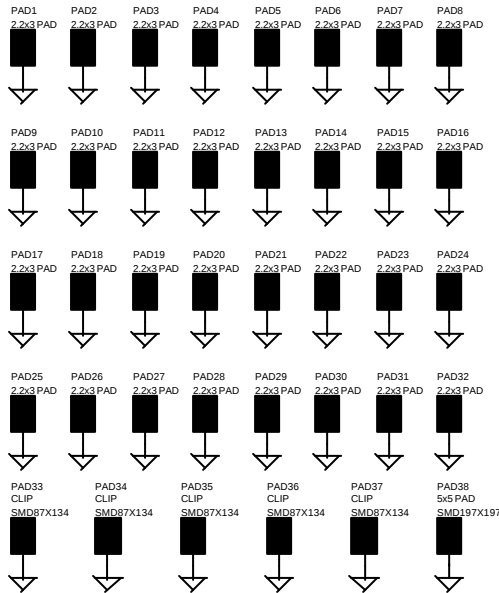
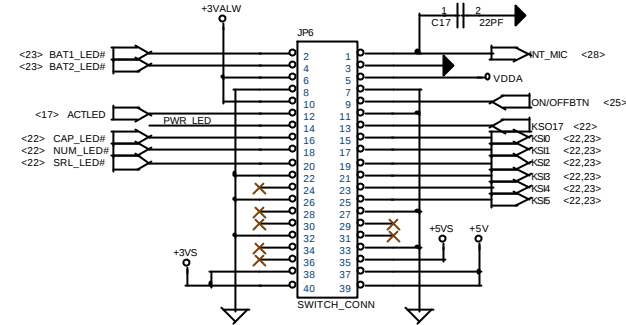
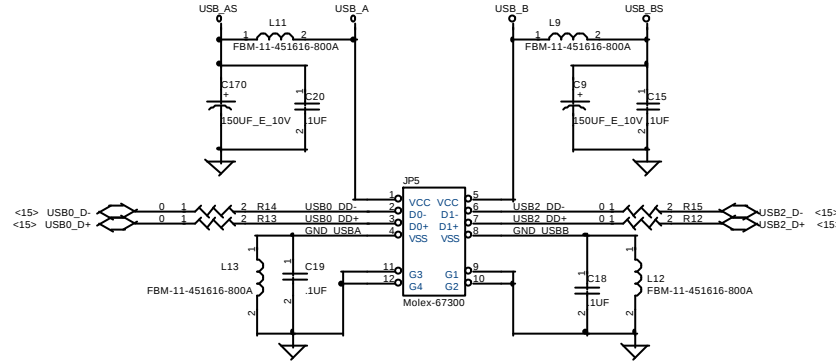
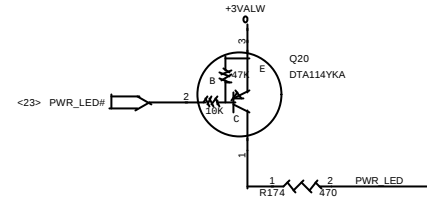
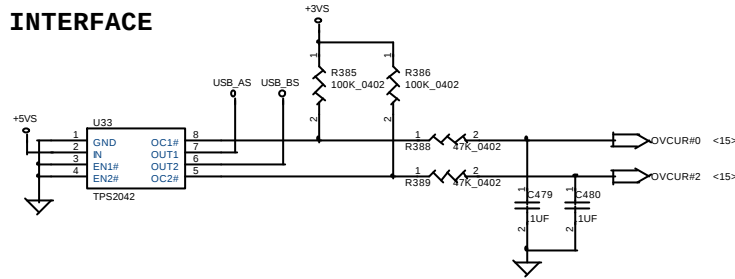
Compal Electronics, Inc.

IDE/FDD MODULE

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	888L2 Main Board

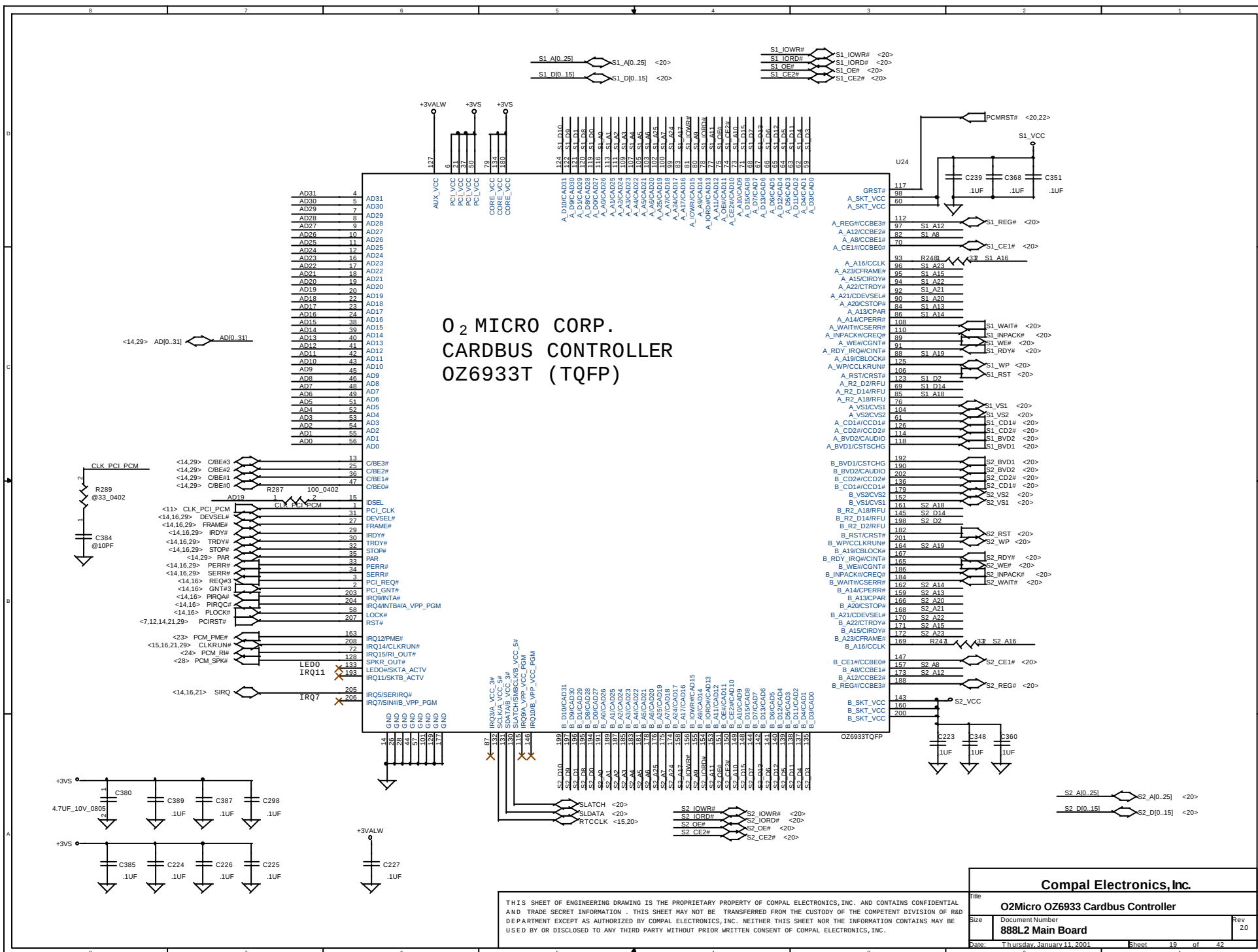
Rev	2.0
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USB&LED INTERFACE

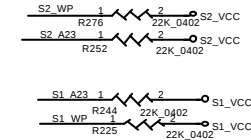
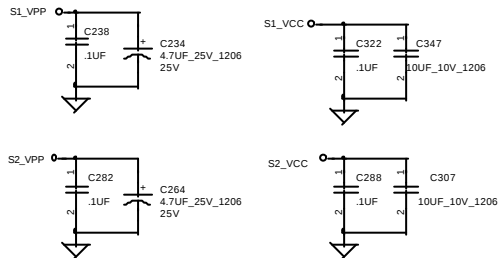
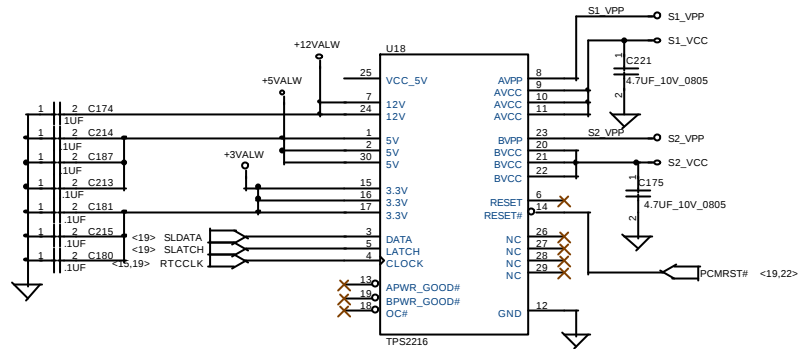


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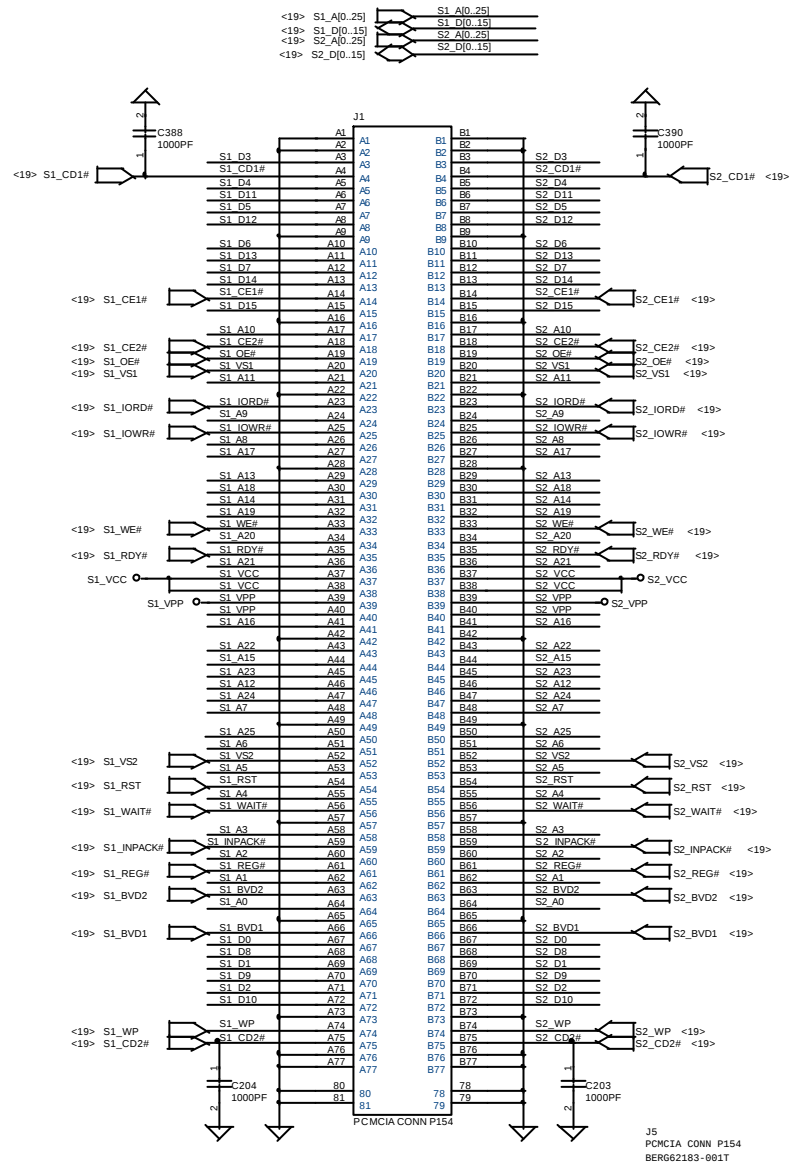
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USB & FRONT PANEL I/F		
Size		
Document Number		
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PCMCIA POWER CTRL.

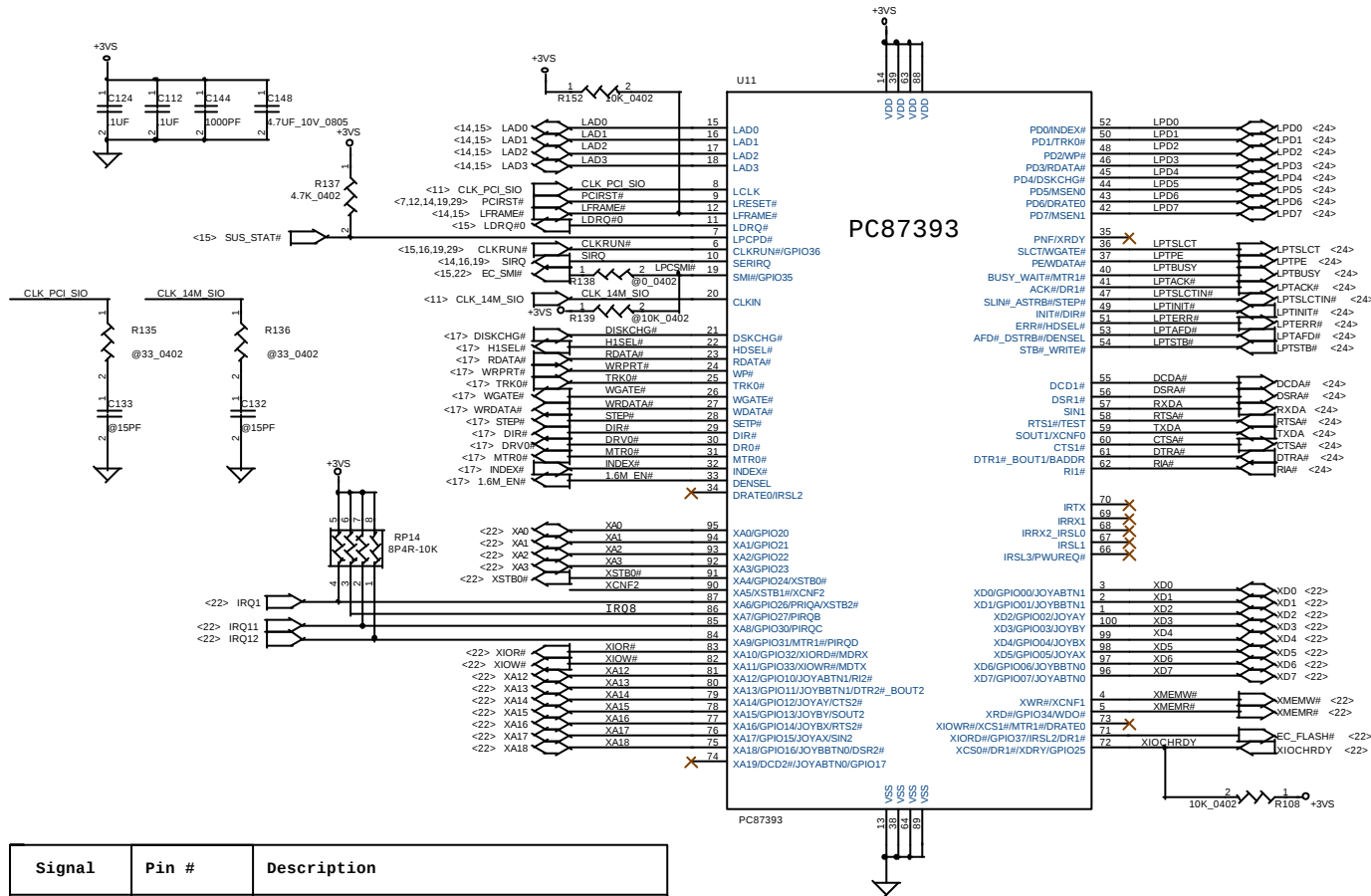


CARDBUS SOCKET



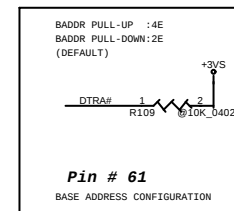
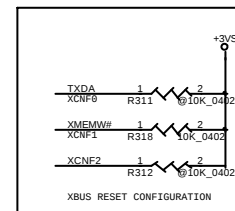
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Compal Electronics, Inc.		
Title FOXCONN Cardbus CONN .154PIN		
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Signal	Pin #	Description
BADDR	61	BASE Address Selection "0": 2E~2F (Default) "1": 4E~4F
TEST	58	"0": Normal (Default) "1": Test Mode
XCNF[2:0]	90, 4, 59	2 1 0 Function
		x 0 0 No BIOS
		x 0 1 Normal Mode. XRDY disabled
		(default) 0 1 0 Latch Mode. XA12-19, XRDY enabled
		1 1 0 Latch Mode. GPI010-17, XRDY enabled
		0 1 1 Latch Mode. XA12-19, XRDY disabled
		1 1 1 Latch Mode. GPI010-17, XRDY disabled

* 1 ROM SOLUTION



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File	SUPER I/O PC87393 CHIP		
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Pin 130 PU for Zero Latch

PC87570

NC : 1, 2, 43, 44, 45, 46, 87, 88, 89, 90, 91, 131, 132, 133, 134, 175, 176

Compal Electronics, Inc.

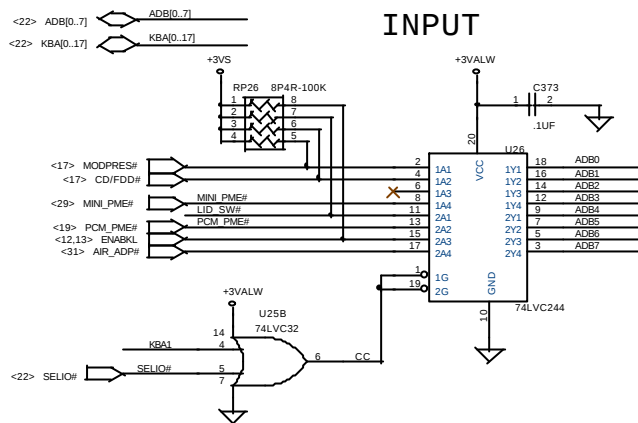
KBC/EC PC87570EXT

Document Number
888L2 Main Board

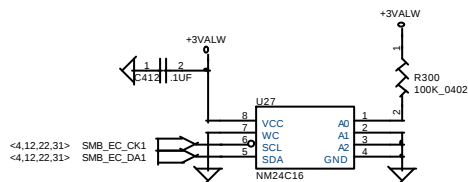
Date: Thursday, January 11, 2001

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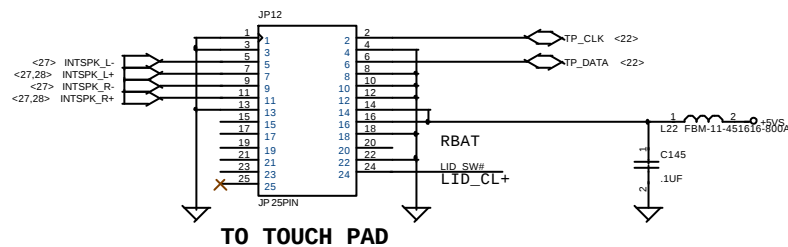
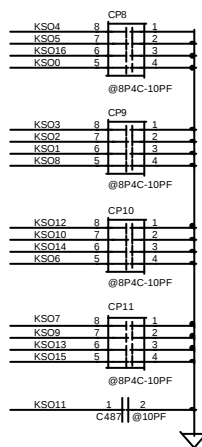
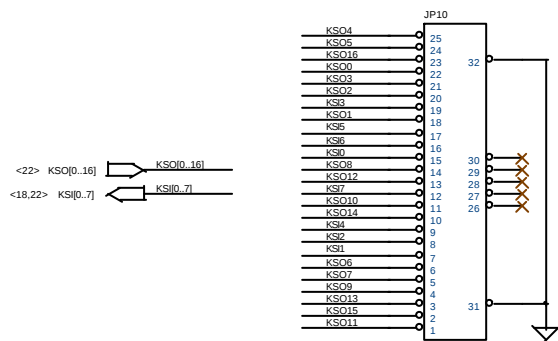
NM24C164 Address definition: 1 A2 A1# A0 B2 B1 B0 R/W#



EC I2C Bus Address:

24C164: 1011xxx R/W#

24C16: 1010xxx R/W#



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EC Extended I/O Port

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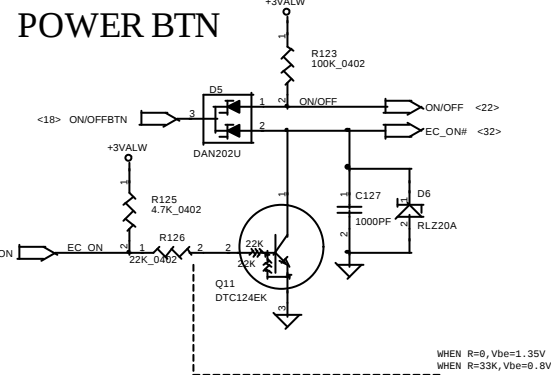
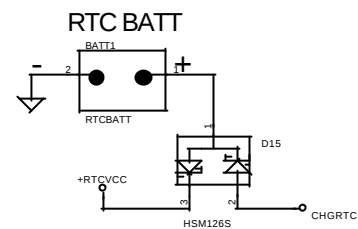
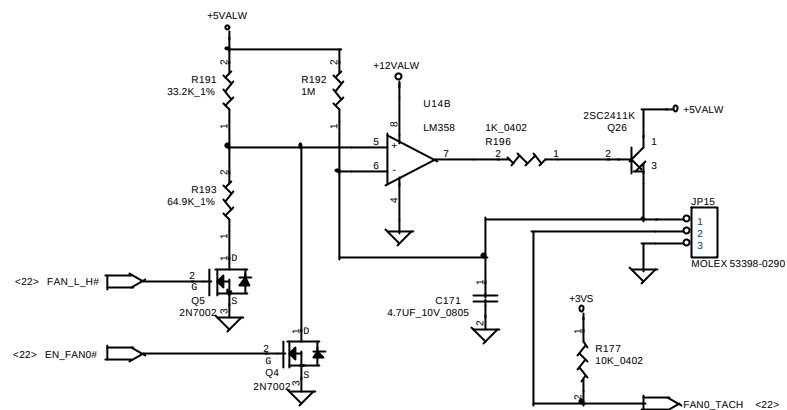
SERIAL & PARALLEL



<21> LPD[0..7] LPD[0..7]



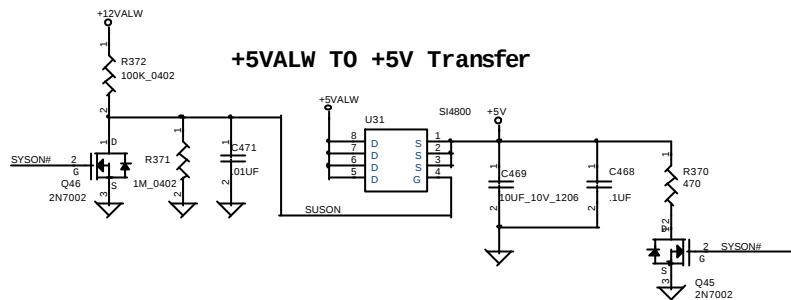
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Size	Document Number	Rev	
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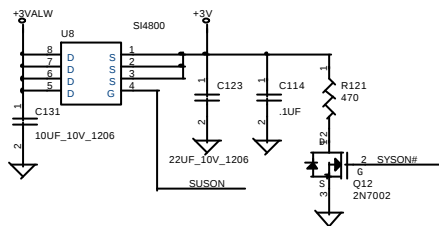
Compal Electronics, Inc.			
Title PS/2/RTC/RST CKT			
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RESET & SUSPEND CKT

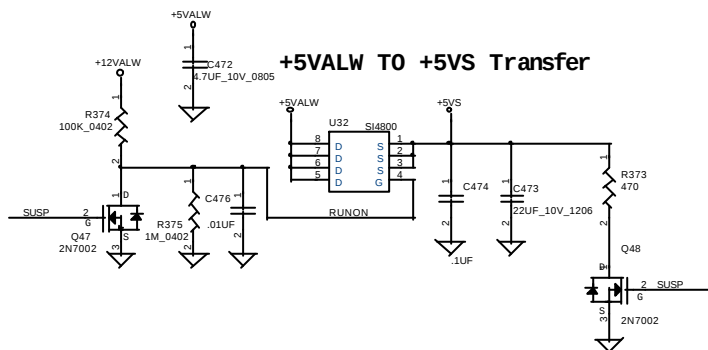
+5VALW TO +5V Transfer



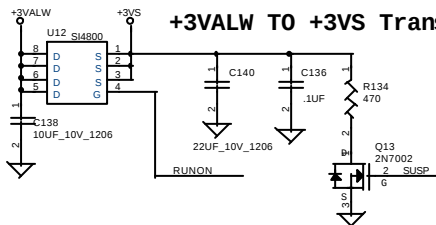
+3VALW TO +3V Transfer



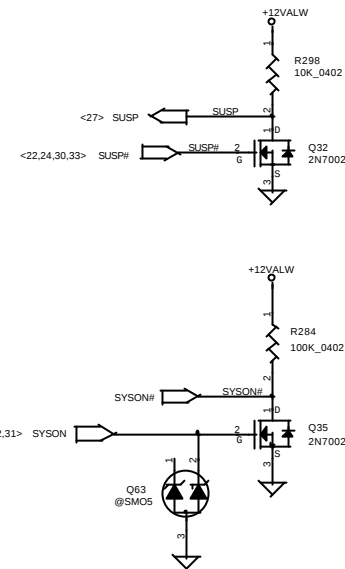
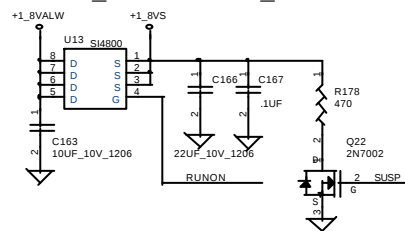
+5VALW TO +5VS Transfer



+3VALW TO +3VS Transfer



+1_8VALW TO +1_8VS Transfer



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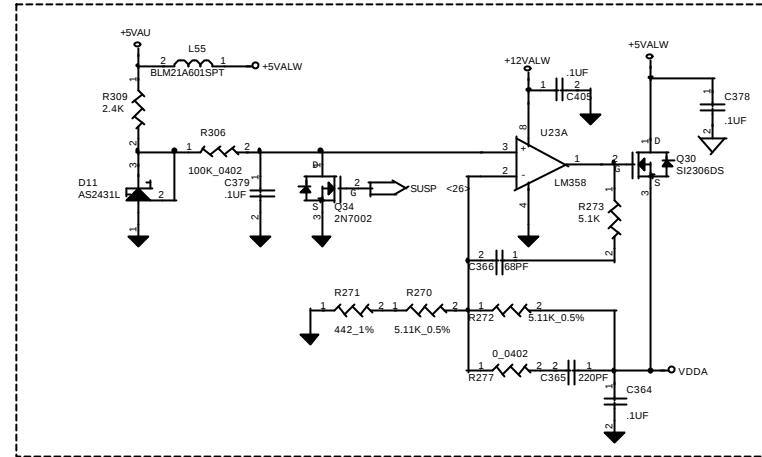
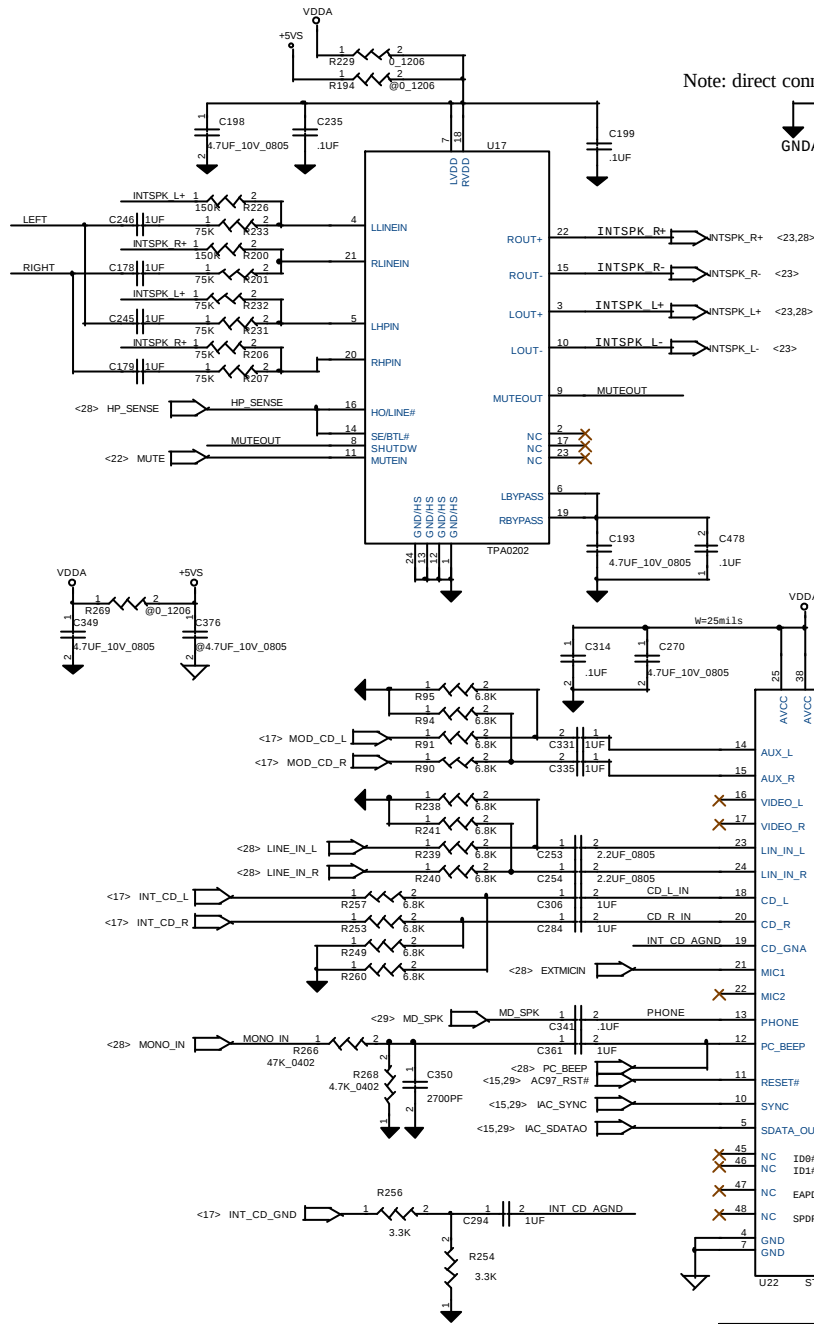
DC/DC INTERFACE

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Note: direct connect GNDA to GND.

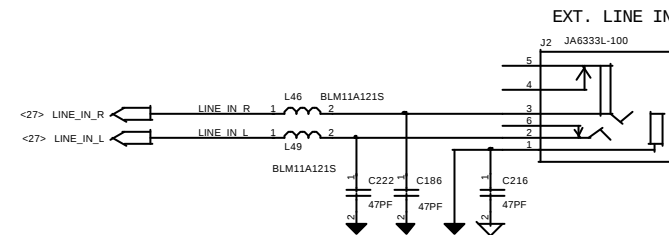
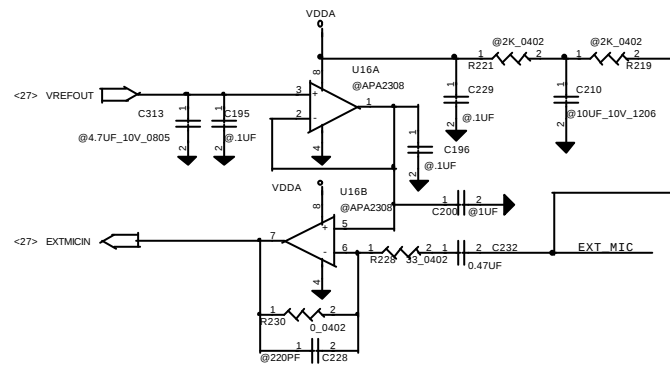
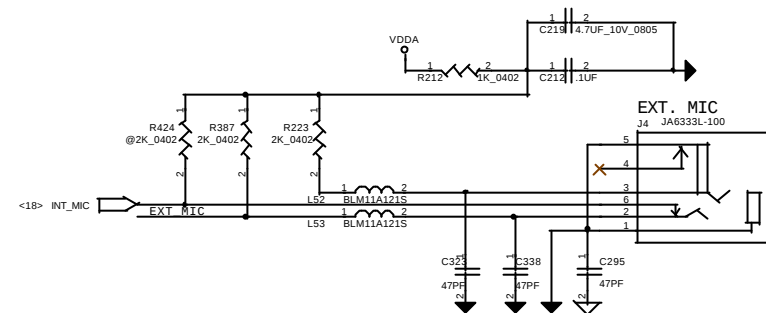
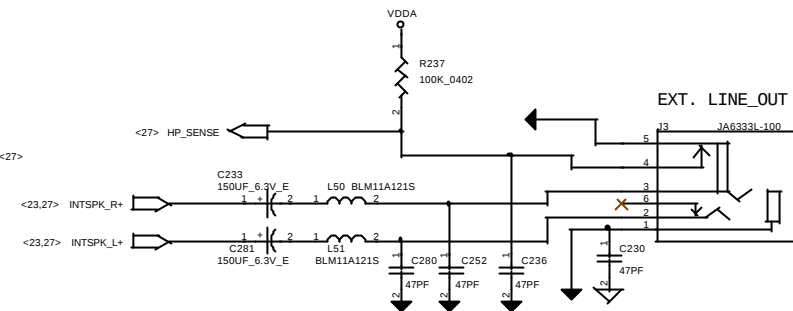
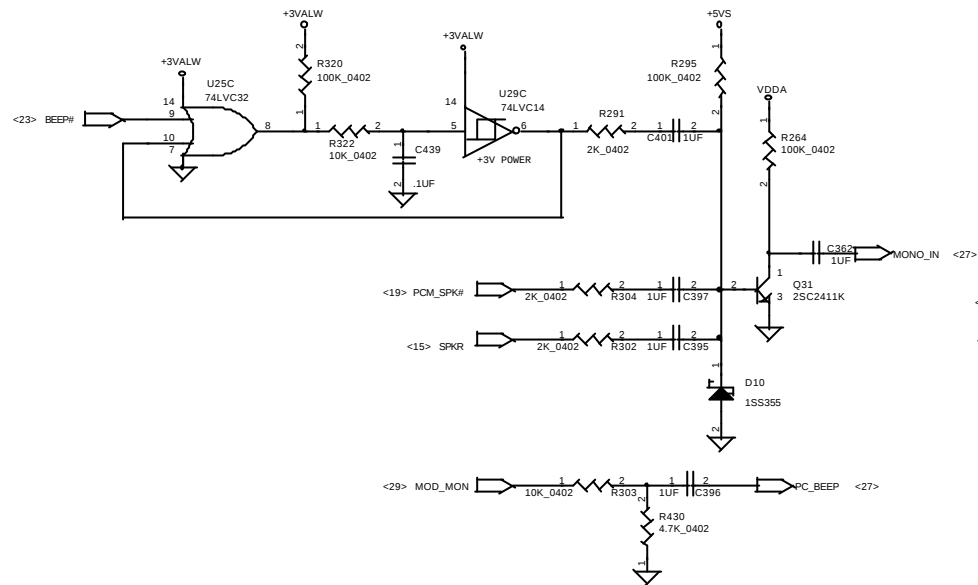


	STAC9700	AD1881A
C248	1UF	4.7UF 10V 0805
C260	1000PF	270PF
C259	1000PF	270PF
C501	Depop	1UF

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AUDIO JACK



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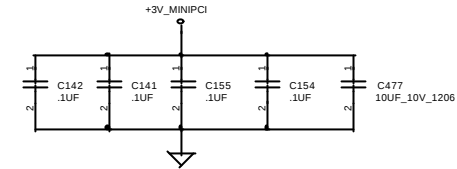
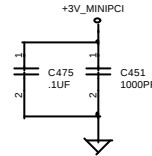
MONO_IN

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888L2 Main Board

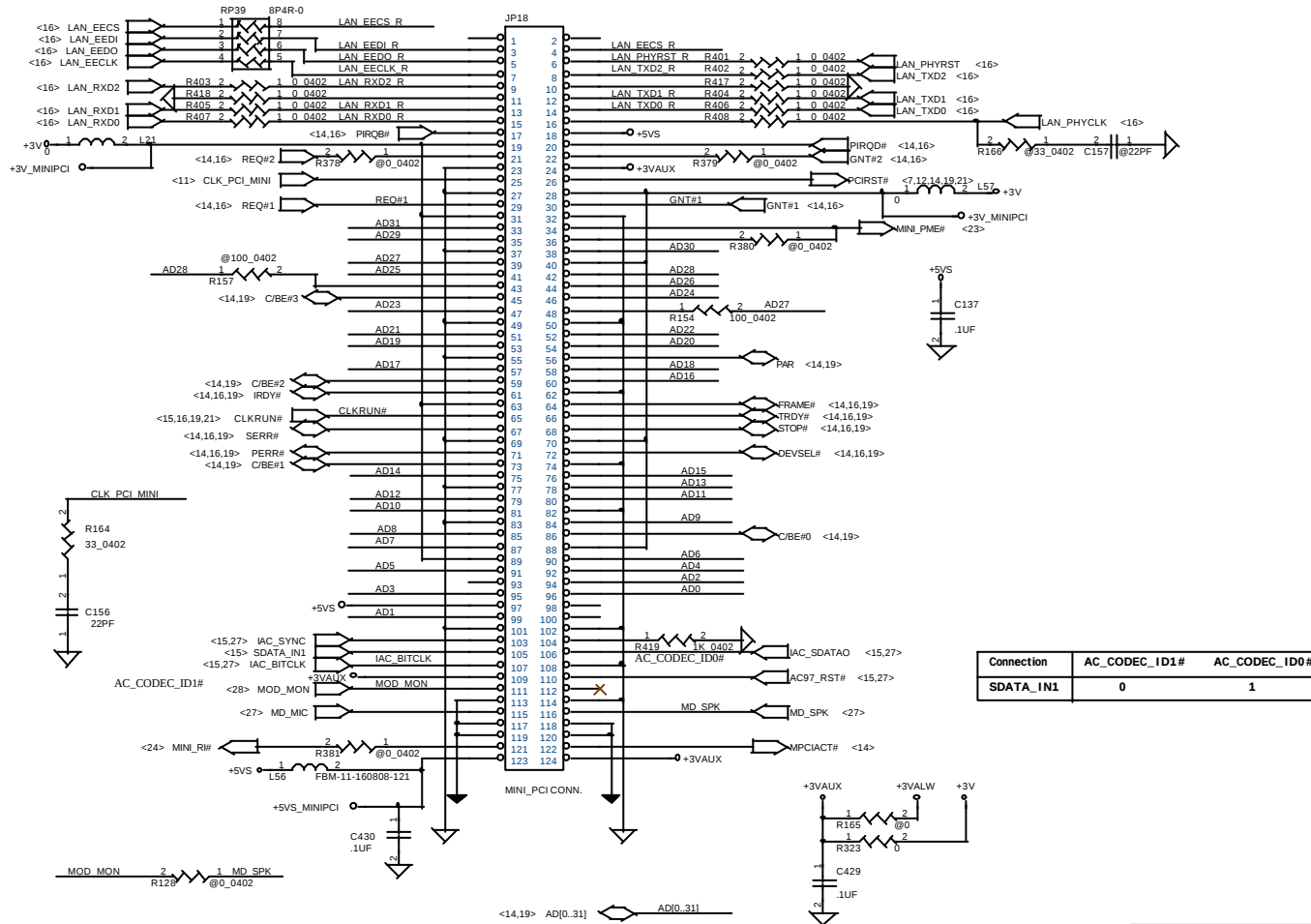
Rev	2.0
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For Compal private Mini-PCI combo card - Mini-PCI type IIIc
pin 21. LAN REQ#
pin 22. LAN GNT#
pin 36. LAN PME#
pin 43. LAN IDSEL



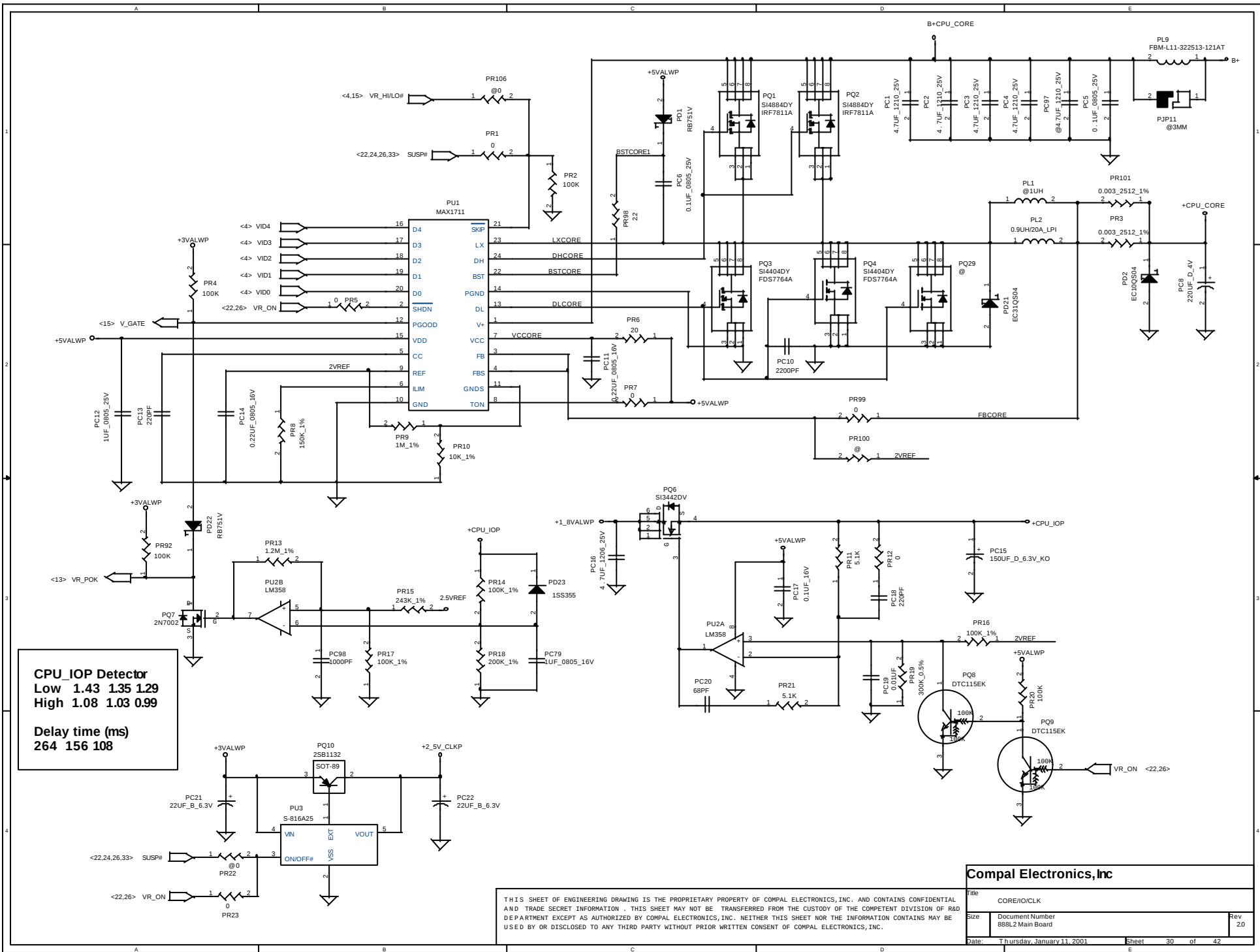
MINI_PCI CONNECTOR

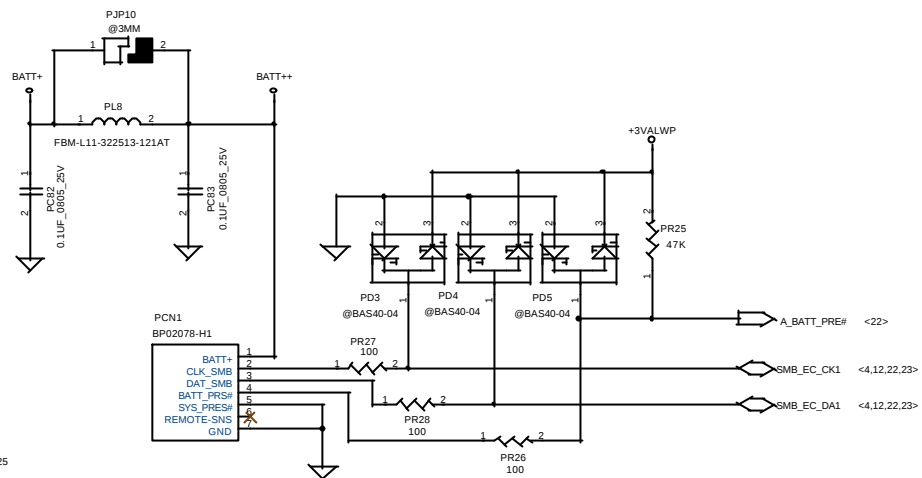
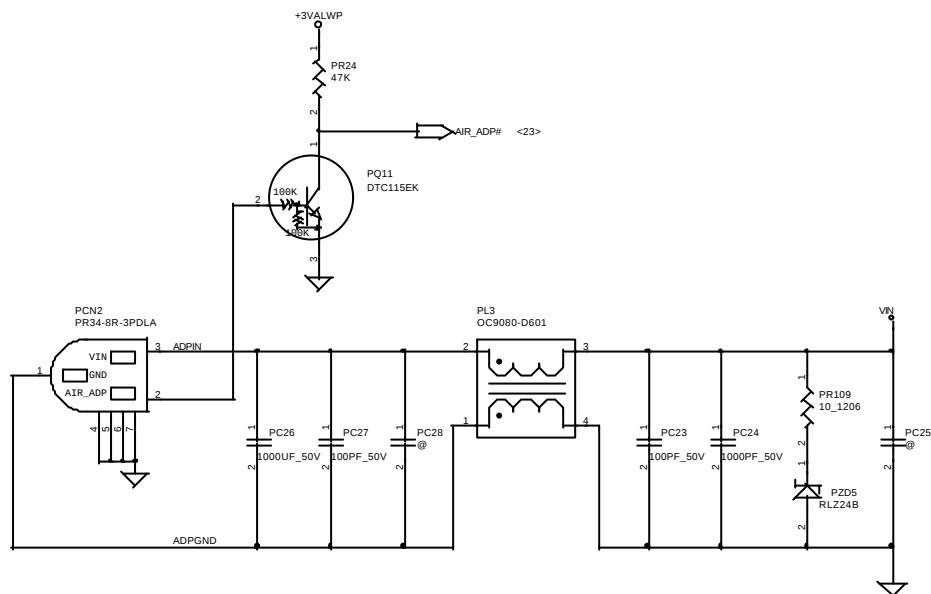


Connection	AC_CODEC_ID1#	AC_CODEC_ID0#
SDATA_IN1	0	1

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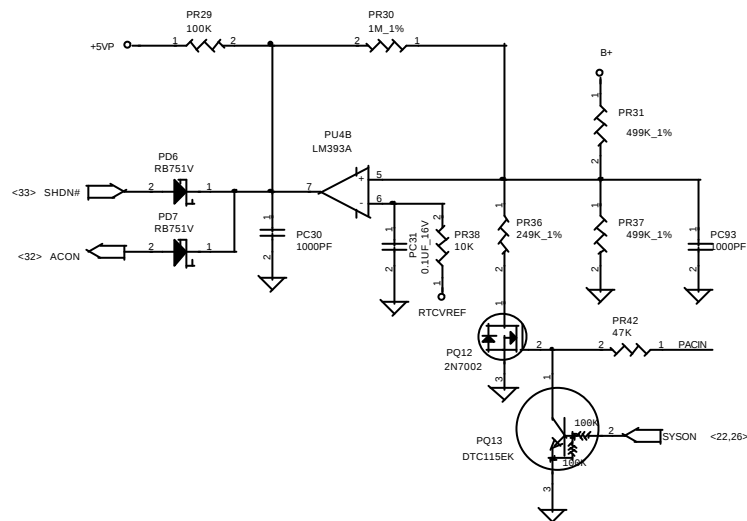
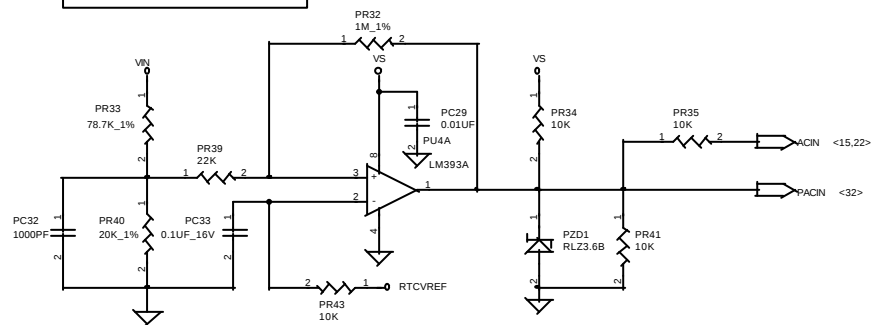
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Mini-PCI Connector			
File	888L2 Main Board		
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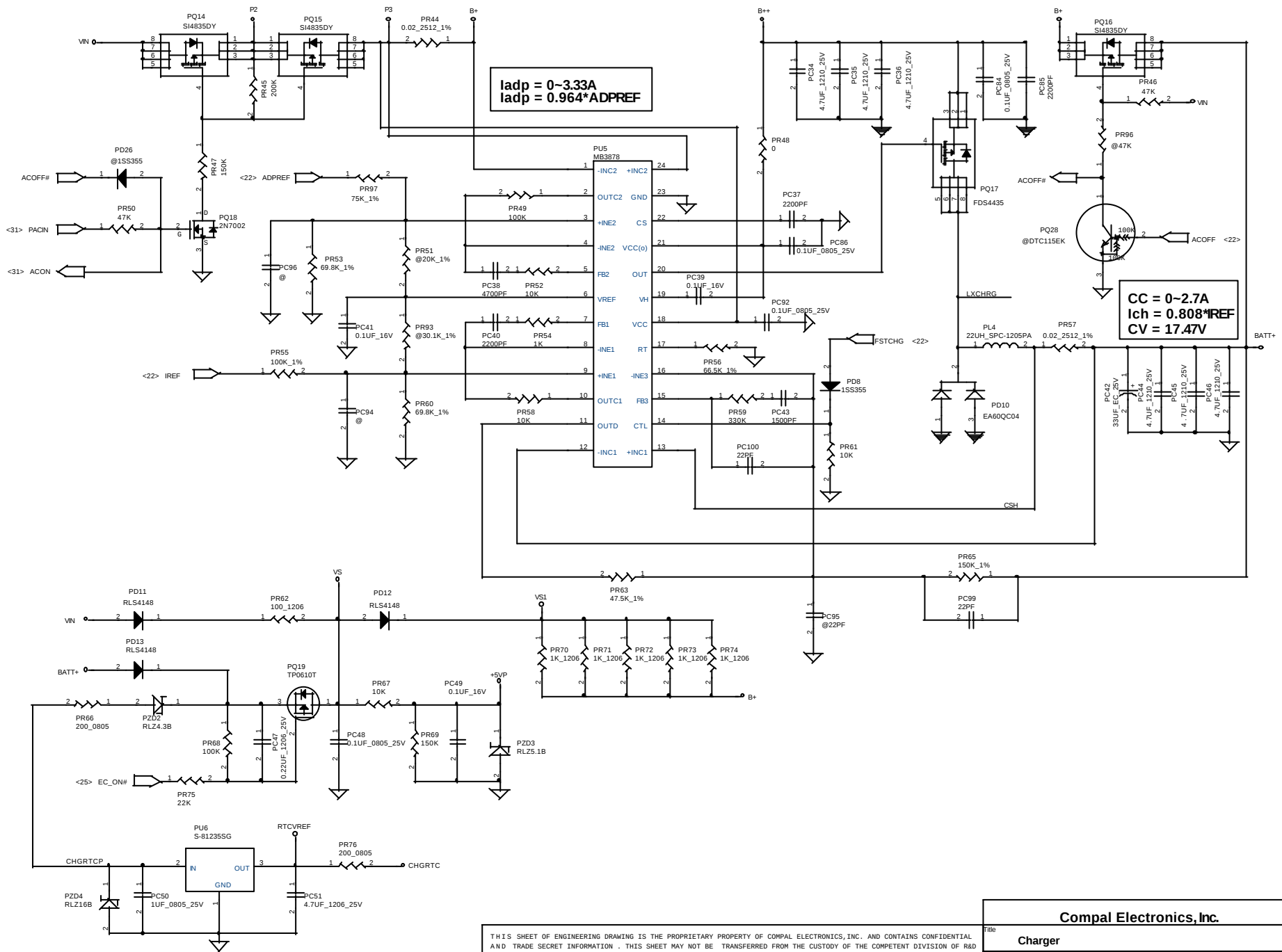
Precharge detector while AC adaptor
High 16.5 15.8 15.1
Low 13.9 13.2 12.5

Vin Detector
High 18.7 17.9 17.1
Low 18.0 17.3 16.5



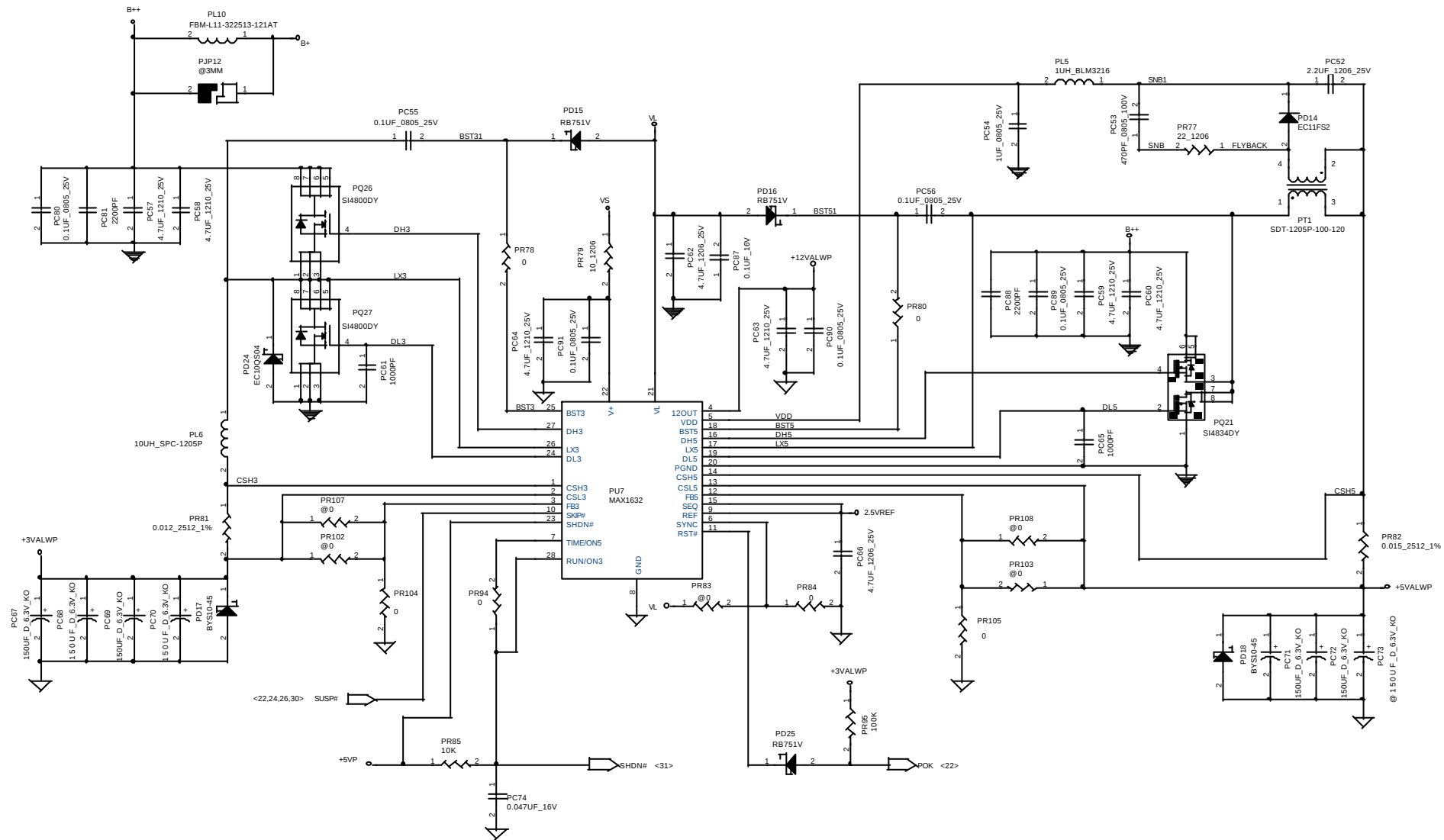
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Title	Detector		
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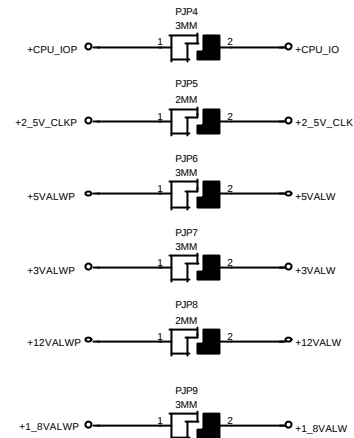
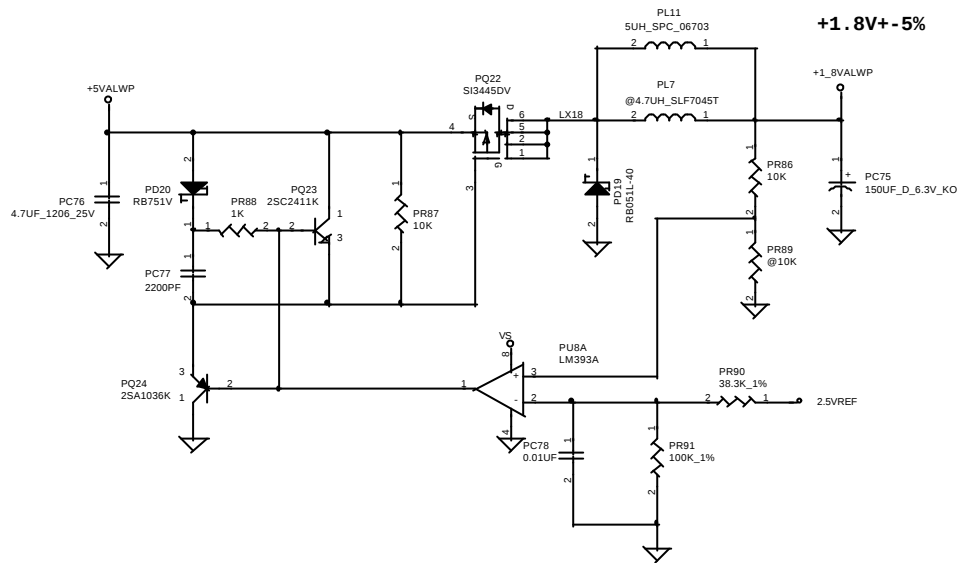
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Title			
Charger			
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Compal Electronics, Inc.			
File	+1.8VS		
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