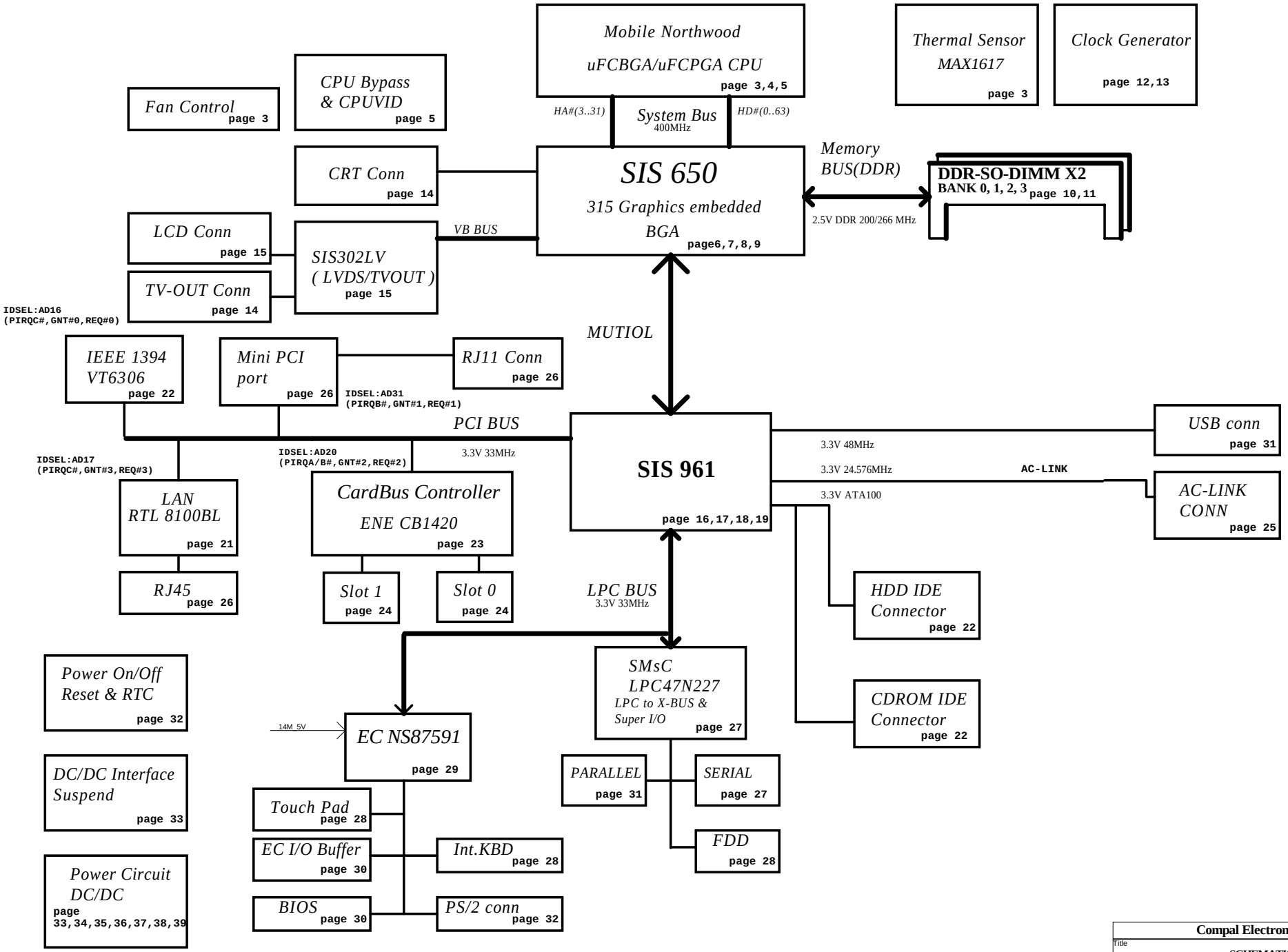
File Name : LA-1341



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U1A

6 HA[3..31]

HA3 K2 A#3
HA4 K4 A#4
HA5 L6 A#5
HA6 K1 A#6
HA7 L3 A#7
HA8 M6 A#8
HA9 L2 A#9
HA10 M3 A#10
HA11 M4 A#11
HA12 N1 A#12
HA13 M1 A#13
HA14 N2 A#14
HA15 N4 A#15
HA16 N5 A#16
HA17 T1 A#17
HA18 R2 A#18
HA19 P3 A#19
HA20 P4 A#20
HA21 R3 A#21
HA22 T2 A#22
HA23 U1 A#23
HA24 P6 A#24
HA25 U3 A#25
HA26 T4 A#26
HA27 V2 A#27
HA28 R6 A#28
HA29 W1 A#29
HA30 T5 A#30
HA31 U4 A#31
V3 A#32
W2 A#33
Y1 A#34
AB1 A#35

6 HD[0..63]

HD0 B21 D#0
HD1 B22 D#1
HD2 A23 D#2
HD3 A25 D#3
HD4 C21 D#4
HD5 D22 D#5
HD6 B24 D#6
HD7 C23 D#7
HD8 C24 D#8
HD9 B25 D#9
HD10 G22 D#10
HD11 H21 D#11
HD12 C26 D#12
HD13 D23 D#13
HD14 J21 D#14
HD15 D25 D#15
HD16 H22 D#16
HD17 E24 D#17
HD18 G23 D#18
HD19 F23 D#19
HD20 F24 D#20
HD21 E25 D#21
HD22 F26 D#22
HD23 D26 D#23
HD24 L21 D#24
HD25 G26 D#25
HD26 H24 D#26
HD27 M21 D#27
HD28 L22 D#28
HD29 J24 D#29
HD30 K23 D#30
HD31 H25 D#31
HD32 M23 D#32
HD33 N22 D#33
HD34 P21 D#34
HD35 M24 D#35
HD36 N23 D#36
HD37 M26 D#37
HD38 N26 D#38
HD39 N25 D#39
HD40 R21 D#40
HD41 P24 D#41
HD42 R25 D#42
HD43 R24 D#43
HD44 T26 D#44
HD45 T25 D#45
HD46 T22 D#46
HD47 T23 D#47
HD48 U26 D#48
HD49 U24 D#49
HD50 U23 D#50
HD51 V25 D#51
HD52 U21 D#52
HD53 V22 D#53
HD54 V24 D#54
HD55 W26 D#55
HD56 Y26 D#56
HD57 W25 D#57
HD58 Y23 D#58
HD59 Y24 D#59
HD60 Y21 D#60
HD61 AA25 D#61
HD62 AA22 D#62
HD63 AA24 D#63

6 DBI#[0..3]

DBI#0 E21 DBI0
DBI#1 G25 DBI1
DBI#2 P26 DBI2
DBI#3 V21 DBI3

ZIF_SOCKET478_478P

A20M C6 A20M# → A20M# 17
FERR B6 FERR# → FERR# 17
INIT W5 INIT# → INIT# 17
LINT0 D1 INTR → INTR 17
LINT1 E5 NMI → NMI 17
IGNNE B2 IGNNE# → IGNNE# 17
SMI B5 SMI# → SMI# 17
SLP AB26 CPUSLP# → CPUSLP# 17
STPCLK Y4 STPCLK# → STPCLK# 17

ADS G1 ADS# → ADS# 6
ADSTB0 L5 HASTB0# → HASTB0# 6
ADSTB1 R5 HASTB1# → HASTB1# 6
BNR G2 BNR# → BNR# 6
DBSY H5 DBSY# → DBSY# 6
DEFER E2 DEFER# → DEFER# 6
DRDY H2 DRDY# → DRDY# 6
HIT F3 HIT# → HIT# 6
HITM E3 HITM# → HITM# 6
TRDY J6 HTRDY# → HTRDY# 6
BPRI D2 BPRI# → BPRI# 6
BR0 H6 BREQ0# → BREQ0# 6
LOCK G4 HLOCK# → HLOCK# 6

REQ0 J1 HREQ0# → HREQ0# 6
REQ1 K5 HREQ1# → HREQ1# 6
REQ2 J4 HREQ2# → HREQ2# 6
REQ3 J3 HREQ3# → HREQ3# 6
REQ4 H3 HREQ4# → HREQ4# 6

RS0 F1 RS0# → RS0# 6
RS1 G5 RS1# → RS1# 6
RS2 F4 RS2# → RS2# 6

DSTBP0 F21 HDSTBP#0
DSTBN0 E22 HDSTBN#0
DSTBP1 J23 HDSTBP#1
DSTBN1 K22 HDSTBN#1
DSTBP2 P23 HDSTBP#2
DSTBN2 R22 HDSTBN#2
DSTBP3 W23 HDSTBP#3
DSTBN3 W22 HDSTBN#3

BCLK0 AF22 CPU_CLK → CPUCLK0 12
BCLK1 AF23 CPU_CLK# → CPUCLK0# 12

ITP_CLK1 AD26 ITP_CLK#
ITP_CLK0 AC26 ITP_CLK

IERR AC3 IERR#
MCERR V6

DBRESET AF25 DBRESET → ITP_DBR#

RESET AB25 CPURST# → CPURST# 6

PWRGOOD AB23 CPUPWRGD → CPUPWRGD 6

VSS75 AF26

COMP0 L24 CPU_COMP0
COMP1 P1 CPU_COMP1

DEP0 J26
DEP1 K25
DEP2 K26
DEP3 L25

THERMDA B3 H_THERMDA
THERMDC C4 H_THERMDC
THERMTRIP A2 TRIP# → H_THERMTRIP#
PROCHOT C3 PROCHOT# → H_PROCHOT# 4

TESTHI0 AD24 HTEST0
TESTHI1 AA2 HTEST1
TESTHI2 AC21 HTEST2
TESTHI3 AC20 HTEST3
TESTHI4 AC24 HTEST4
TESTHI5 AC23 HTEST5
TESTHI6 AA20 HTEST6
TESTHI7 AB22 HTEST7
TESTHI8 U6 HTEST8
TESTHI9 W4 HTEST9
TESTHI10 Y3 HTEST10
TESTHI11 A6 HTEST11 — R34 0 → PM_CPUPERF# 17
TESTHI12 AD25 HTEST12 → H_DPSLP# 17

BSEL0 AD6 BSEL0
BSEL1 AD5 BSEL1

TCK D4 TCK → TCK
TDI C1 TDI → TDI
TDO D5 TDO → TDO
TMS F7 TMS → TMS
TRST E6 TRST# → TRST#

VID0 AE5 CPU_VID0
VID1 AE4 CPU_VID1
VID2 AE3 CPU_VID2
VID3 AE2 CPU_VID3
VID4 AE1 CPU_VID4

HDSTBP#[0..3] → HDSTBP#[0..3] 6
HDSTBN#[0..3] → HDSTBN#[0..3] 6

+CPU_CORE

HTEST0 R512 56
HTEST1
HTEST2 R513 56
HTEST3
HTEST4
HTEST5
HTEST6
HTEST7
HTEST8 R514 56
HTEST9
HTEST10
HTEST12 R515 56
HTEST11 R525 56

+CPU_CORE

PROCHOT# R3 62_0603
ITP_CLK# R4 1K
ITP_CLK R5 1K
TDI R6 150
TDO R7 75
TMS R9 39

+CPU_CORE

STPCLK# R12 56 C464 0.1UF
SMI# R13 56 C465 0.1UF
CPUSLP# R14 56 C466 0.1UF
INIT# R15 56 C467 0.1UF
CPUPWRGD R16 51
FERR# R17 62_0603 C468 0.1UF
IERR# R18 1K C469 0.1UF
TRIP# R20 62_0603
A20M# R21 56 C470 0.1UF
IGNNE# R22 56 C471 0.1UF
INTR R23 56 C472 0.1UF
NMI R24 56 C473 0.1UF
BREQ0# R25 200
CPURST# R27 51

TCK R28 27
TRST# R29 680
CPU_COMP0 R30 51.1_1%_0603
CPU_COMP1 R32 51.1_1%_0603

+CPU_CORE

DBRESET R538 150

+3VS

R61 10K
RP5 8P4R_10K_0804

CPU_VID0
CPU_VID1
CPU_VID2
CPU_VID3
CPU_VID4

Thermal Sensor MAX1617

W=15mil

+5VS

C1 0.1UF
R1 10K

U2 MAX1617

NC 1 | 16 NC
VCC 2 | 15 STBY
DXP 3 | 14 SMBCLK → EC_SMC2 29
DXN 4 | 13 NC
NC 5 | 12 SMBDATA → EC_SMD2 29
ADD1 6 | 11 ALERT
GND 7 | 10 ADD0
GND 8 | 9 NC

C2 2200PF
H_THERMDA
H_THERMDC

R8 1K
+5VS

R11 1K
R10 10K
+5VS

Fan1 Control circuit

+12VS
R19 3.48K_1%_0603
Q1 FMMT619
D1 1SS355
D2 1N4148
CB1 2.2UF_16V_0805
D58 @1SS355
D3 1N4148
C3 1000PF
Q2 2SA1036K
FAN1
+5VFAN1
JP1 53398-0310
+3VS
R26 10K
FANSPEED1 29
C480 1000PF

+3VS
R36 @1K
R37 @1K
R40 @1K
R41 @1K
BSEL0
BSEL1

C483 .1UF
C484 .1UF
+5VS
29 EN_FAN1
U42 LMV321_SOT23-5
R561 13K_1%_0603
R562 7.32K_1%_0603
FAN1

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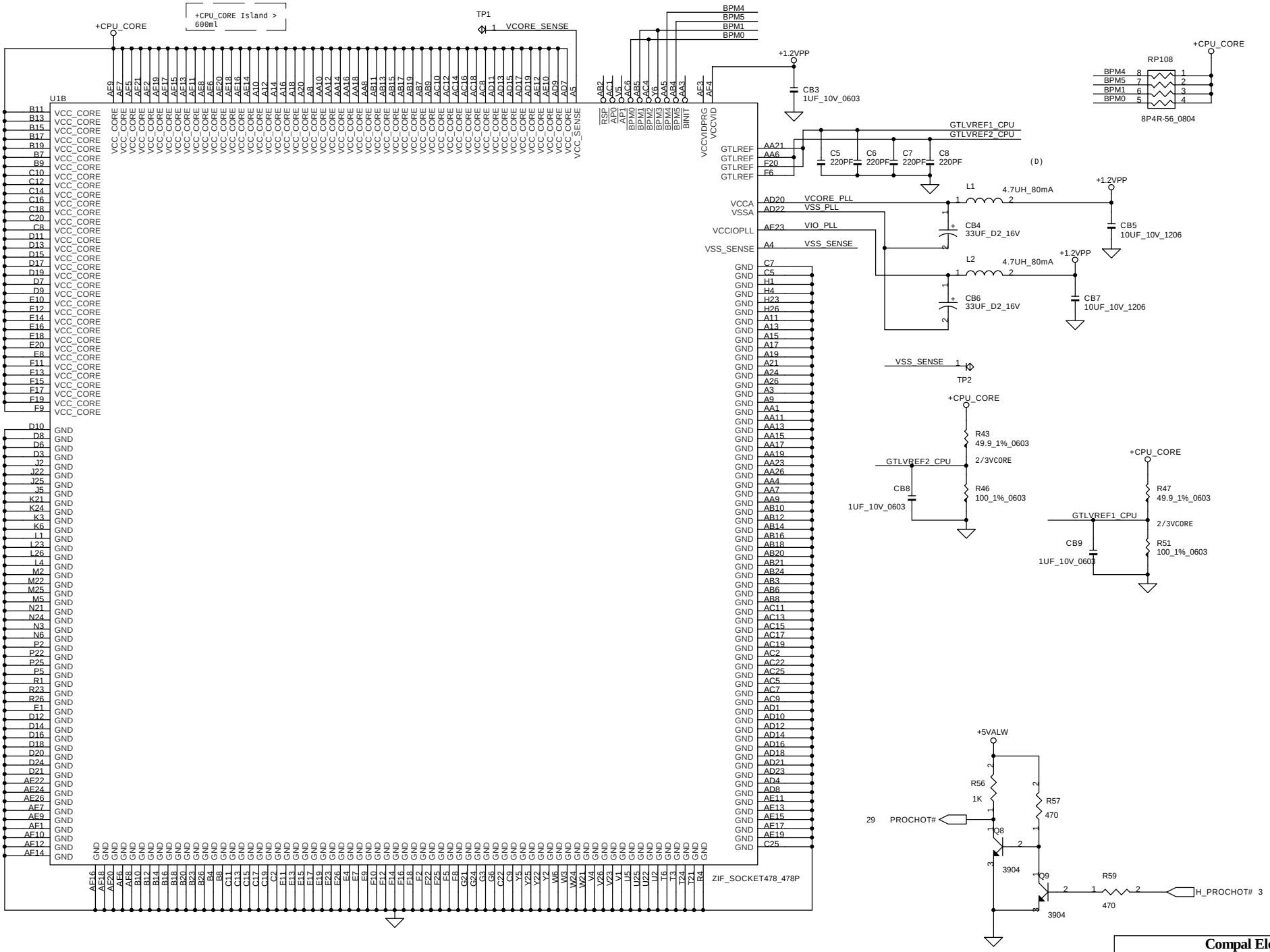

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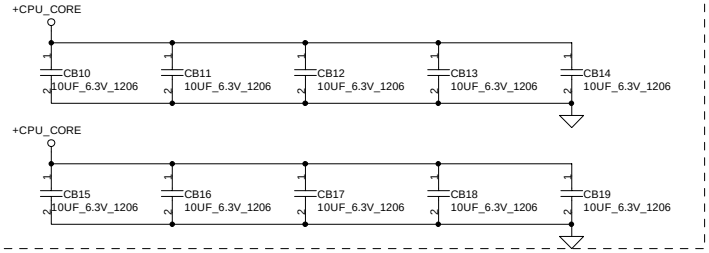
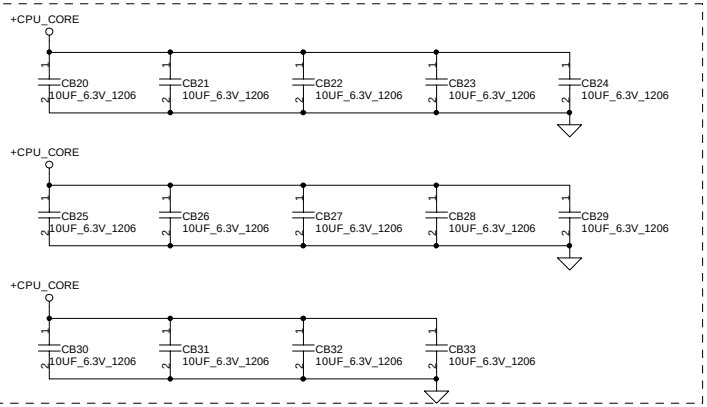
Layout note :

Place close to CPU, Use 2~3 vias per PAD.
Place .22uF caps underneath balls on solder side.
Place 10uF caps on the peripheral near balls.
Use 2~3 vias per PAD.

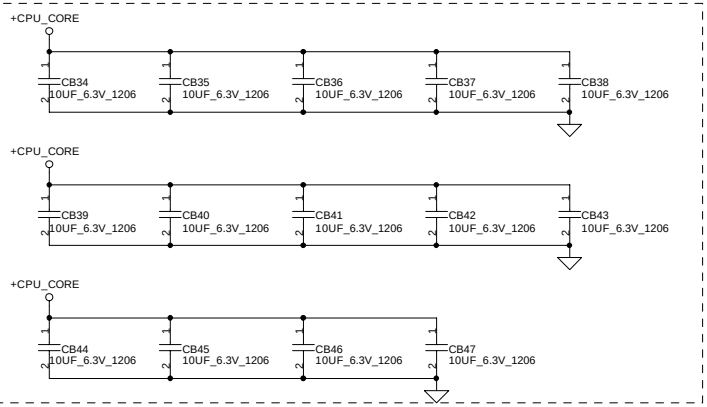
Please place these cap in the socket cavity area



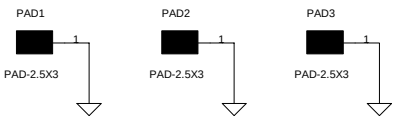
Please place these cap on the socket north side



Please place these cap on the socket south side



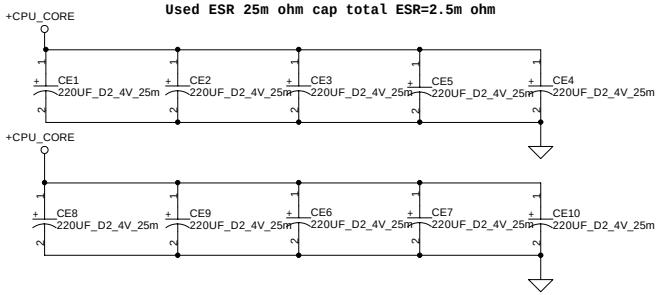
EMI Clip PAD for CPU

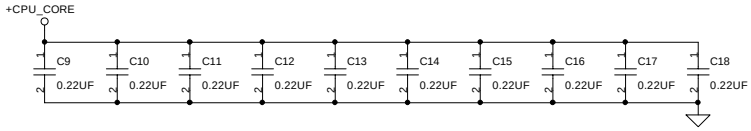


Layout note :

Place close to CPU power and ground pin as possible (<1inch)

Used ESR 25m ohm cap total ESR=2.5m ohm





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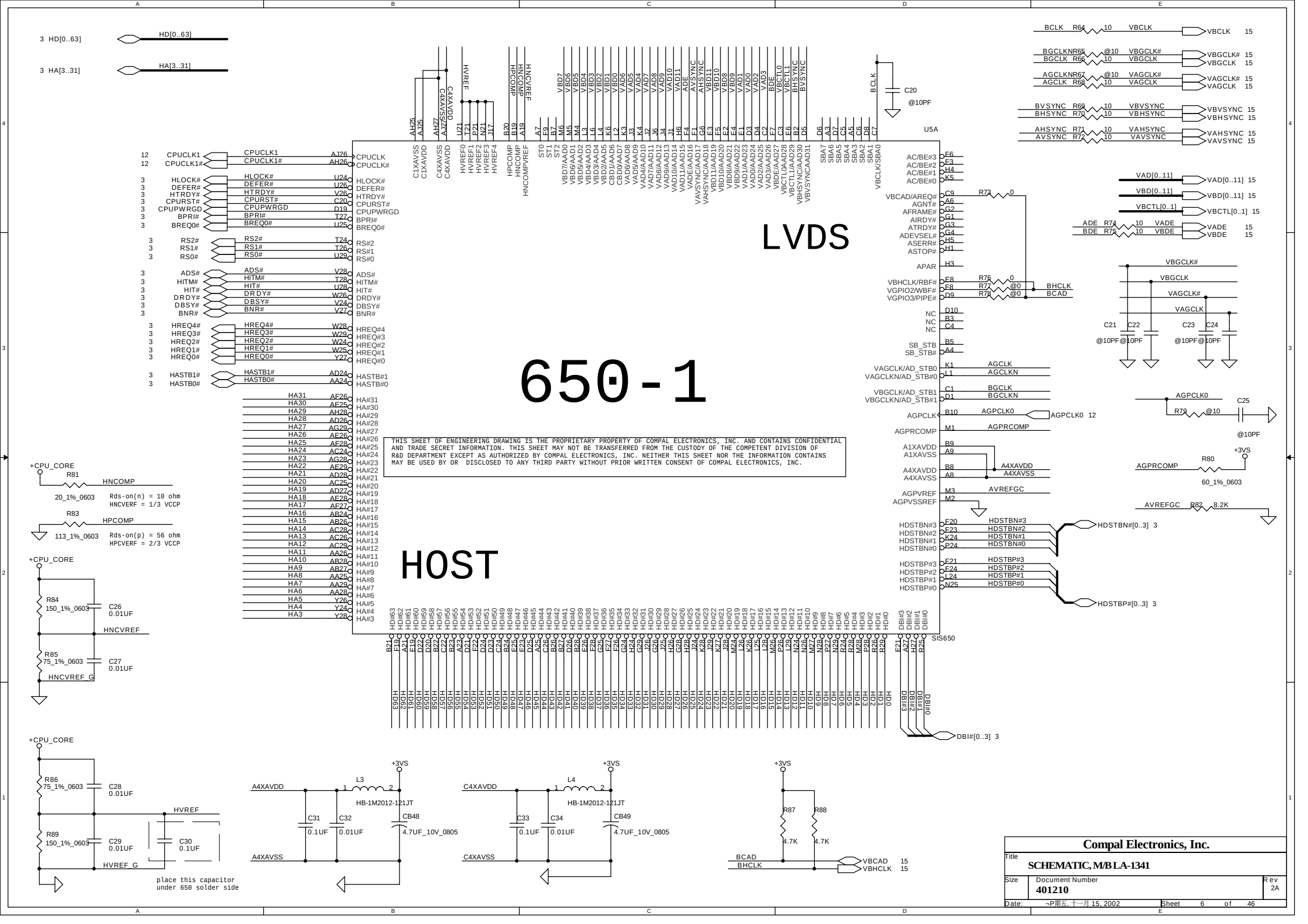
LVDS
650-1
HOST
U5A
SIS650
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place this capacitor under 650 solder side

NOTE:This page is for pure DDR SDRAM PCB design

MD[0..63] → MD[0..63] 10,11
RDQM[0..7] → RDQM[0..7] 10,11
RDQS[0..7] → RDQS[0..7] 10,11
MAA[0..14] → MAA[0..14] 10,11

RP* PLACE CLOSE TO DIMM

MD4	RP10 8P4R_10_0804	1	8	MMD4
MD5		2	7	MMD5
MD0		3	6	MMD0
MD1		4	5	MMD1
MD6	RP11 8P4R_10_0804	1	8	MMD6
RDQS0		2	7	DQS0
MD2		3	6	MMD2
MD3		4	5	MMD3
MD12	RP12 8P4R_10_0804	1	8	MMD12
MD8		2	7	MMD8
MD7		3	6	MMD7
MD9		4	5	MMD9
MD13	RP13 8P4R_10_0804	1	8	MMD13
MD14		2	7	MMD14
MD10		3	6	MMD10
RDQS1		4	5	DQS1
MD11	RP14 8P4R_10_0804	1	8	MMD11
MD20		2	7	MMD20
MD16		3	6	MMD16
MD15		4	5	MMD15
MD21	RP15 8P4R_10_0804	1	8	MMD21
MD17		2	7	MMD17
RDQS2		3	6	DQS2
MD18		4	5	MMD18
MD22	RP16 8P4R_10_0804	1	8	MMD22
MD19		2	7	MMD19
MD23		3	6	MMD23
MD24		4	5	MMD24
MD28	RP18 8P4R_10_0804	1	8	MMD28
MD29		2	7	MMD29
MD25		3	6	MMD25
RDQS3		4	5	DQS3
MD30	RP20 8P4R_10_0804	1	8	MMD30
MD27		2	7	MMD27
MD31		3	6	MMD31
MD26		4	5	MMD26
MD32	RP23 8P4R_10_0804	1	8	MMD32
MD37		2	7	MMD37
MD36		3	6	MMD36
MD33		4	5	MMD33
MD34	RP24 8P4R_10_0804	1	8	MMD34
MD39		2	7	MMD39
MD38		3	6	MMD38
RDQS4		4	5	DQS4
MD35	RP25 8P4R_10_0804	1	8	MMD35
MD45		2	7	MMD45
MD44		3	6	MMD44
MD40		4	5	MMD40
MD42	RP26 8P4R_10_0804	1	8	MMD42
MD46		2	7	MMD46
MD41		3	6	MMD41
RDQS5		4	5	DQS5
MD48	RP27 8P4R_10_0804	1	8	MMD48
MD47		2	7	MMD47
MD43		3	6	MMD43
MD52		4	5	MMD52
MD49	RP28 8P4R_10_0804	1	8	MMD49
MD54		2	7	MMD54
RDQS6		3	6	DQS6
MD53		4	5	MMD53
MD55	RP29 8P4R_10_0804	1	8	MMD55
MD60		2	7	MMD60
MD50		3	6	MMD50
MD51		4	5	MMD51
MD56	RP30 8P4R_10_0804	1	8	MMD56
MD61		2	7	MMD61
MD57		3	6	MMD57
RDQS7		4	5	DQS7
MD58	RP31 8P4R_10_0804	1	8	MMD58
MD59		2	7	MMD59
MD62		3	6	MMD62
MD63		4	5	MMD63

RDQM0	R101	10	DQM0
RDQM1	R102	10	DQM1
RDQM2	R103	10	DQM2
RDQM3	R104	10	DQM3
RDQM4	R105	10	DQM4
RDQM5	R106	10	DQM5
RDQM6	R107	10	DQM6
RDQM7	R108	10	DQM7

U5B

MMD0	AJ23	MD0
MMD1	AG22	MD1
MMD2	AH21	MD2
MMD3	AJ21	MD3
MMD4	AD23	MD4
MMD5	AE23	MD5
MMD6	AE22	MD6
MMD7	AE21	MD7
DQM0	AD22	DQM0
DQS0	AH22	DQS0/CSB#0
MMD8	AD21	MD8
MMD9	AG20	MD9
MMD10	AE19	MD10
MMD11	AF19	MD11
MMD12	AE21	MD12
MMD13	AD20	MD13
MMD14	AD19	MD14
MMD15	AH19	MD15
DQM1	AE20	DQM1
DQS1	AH20	DQS1/CSB#1
MMD16	AF18	MD16
MMD17	AG18	MD17
MMD18	AH17	MD18
MMD19	AD16	MD19
MMD20	AD18	MD20
MMD21	AD17	MD21
MMD22	AF17	MD22
MMD23	AJ17	MD23
DQM2	AE17	DQM2
DQS2	AH18	DQS2/CSB#2
MMD24	AD14	MD24
MMD25	AG14	MD25
MMD26	AJ13	MD26
MMD27	AE13	MD27
MMD28	AJ15	MD28
MMD29	AF14	MD29
MMD30	AD13	MD30
MMD31	AF13	MD31
DQM3	AH13	DQM3
DQS3	AH14	DQS3/CSB#3
MMD32	AD10	MD32
MMD33	AH10	MD33
MMD34	AE9	MD34
MMD35	AD8	MD35
MMD36	AG10	MD36
MMD37	AF10	MD37
MMD38	AH9	MD38
MMD39	AF9	MD39
DQM4	AD9	DQM4
DQS4	AJ9	DQS4/CSB#4
MMD40	AH5	MD40
MMD41	AG4	MD41
MMD42	AE5	MD42
MMD43	AH3	MD43
MMD44	AG6	MD44
MMD45	AF6	MD45
MMD46	AF5	MD46
MMD47	AF4	MD47
DQM5	AH4	DQM5
DQS5	AJ3	DQS5/CSB#5
MMD48	AE4	MD48
MMD49	AD6	MD49
MMD50	AE2	MD50
MMD51	AC5	MD51
MMD52	AG2	MD52
MMD53	AG1	MD53
MMD54	AF3	MD54
MMD55	AC6	MD55
DQM6	AD4	DQM6
DQS6	AF2	DQS6/CSB#6
MMD56	AB6	MD56
MMD57	AD3	MD57
MMD58	AA6	MD58
MMD59	AB3	MD59
MMD60	AC4	MD60
MMD61	AE1	MD61
MMD62	AD2	MD62
MMD63	AC1	MD63
DQM7	AB4	MD7
DQS7	AC2	DQS7/CSB#7

SIS650

650-2

MA1	RP6 8P4R_10_0804	8	1	MAA1
MA0		7	2	MAA0
MA3		6	3	MAA3
MA2		5	4	MAA2
MA5	RP7 8P4R_10_0804	8	1	MAA5
MA4		7	2	MAA4
MA7		6	3	MAA7
MA6		5	4	MAA6
MA9	RP8 8P4R_10_0804	8	1	MAA9
MA8		7	2	MAA8
MA14		6	3	MAA14
MA13		5	4	MAA13
MA12	RP9 4P2R_10	4	1	MAA12
MA11		3	2	MAA11

MA10 — R93 10 — MAA10

RP* place close to DIMM1

MA0	AH11	MA0	
MA1	AF12	MA1	
MA2	AH12	MA2	
MA3	AG12	MA3	
MA4	AD12	MA4	
MA5	AH15	MA5	
MA6	AF15	MA6	
MA7	AH16	MA7	
MA8	AE15	MA8	
MA9	AD15	MA9	
MA10	AF11	MA10	
MA11	AG8	MA11	
MA12	AJ11	MA12	
MA13	AG16	MA13	
MA14	AF16	MA14	

SRAS#	AH8	RAS-	R516 10	SRAS#	SRAS#	10,11
SCAS#	AJ7	CAS-	R517 10	SCAS#	SCAS#	10,11
SWE#	AH7	WE-	R97 10	SWE#	SWE#	10,11

CS#0	AF7	CS-0	4P2R_10 RP19	RCS0#	RCS0#	10,11
CS#1	AF7	CS-1		RCS1#	RCS1#	10,11
CS#2	AH6	CS-2	4P2R_10 RP21	RCS2#	RCS2#	10,11
CS#3	AJ5	CS-3		RCS3#	RCS3#	10,11
CS#4	AF8					
CS#5	AD7					

CKE[0..3] 10

CKE0	AB2	CKE0	
CKE1	AA4	CKE1	
CKE2	AB1	CKE2	
CKE3	Y6	CKE3	
CKE4	AA5		
CKE5	Y5		
S3AUXSW#	Y4	S3AUXSW#	S3AUXSW# 29,32

SDCLK — AA3 SDCLK — R98 @10 — C41 @10PF — SDCLK 12

FWDSDCLKO — AD11 SDCLK — R99 10 — FWDSDCLKO — C42 @10PF — FWDSDCLKO 13

SDRCLKI — AE11

SDAVDD	Y1	SDAVDD
SDAVSS	Y2	SDAVSS
DDRAVDD	AA1	DDRAVDD
DDRAVSS	AA2	DDRAVSS

DDRVREFA	AJ19	DDRVREFA
DDRVREFB	AH2	DDRVREFB

+3VALW

DRAM_SEL — W3 — R100 4.7K

+2.5V

C35 0.01UF — R90 150_1%_0603 — R91 — DDRVREFA — SDREF 10,35 — C36 0.01UF — R92 @0_0603 150_1%_0603 — DDRVREFA_G

+2.5V

C37 0.01UF — R94 150_1%_0603 — R95 — DDRVREFB — SDREF 10,35 — C38 0.01UF — R96 @0_0603 150_1%_0603 — DDRVREFB_G

+2.5V

RP22 8P4R_470_0804

CKE0	1	8
CKE1	2	7
CKE2	3	6
CKE3	4	5

+3VS

SDAVDD — L5 HB-1M2012-121JT — C39 0.1UF — C40 0.01UF — CB50 4.7UF_10V_0805 — SDAVSS

+3VS

DDRAVDD — L6 HB-1M2012-121JT — C43 0.1UF — C44 0.01UF — CB51 4.7UF_10V_0805 — DDRAVSS

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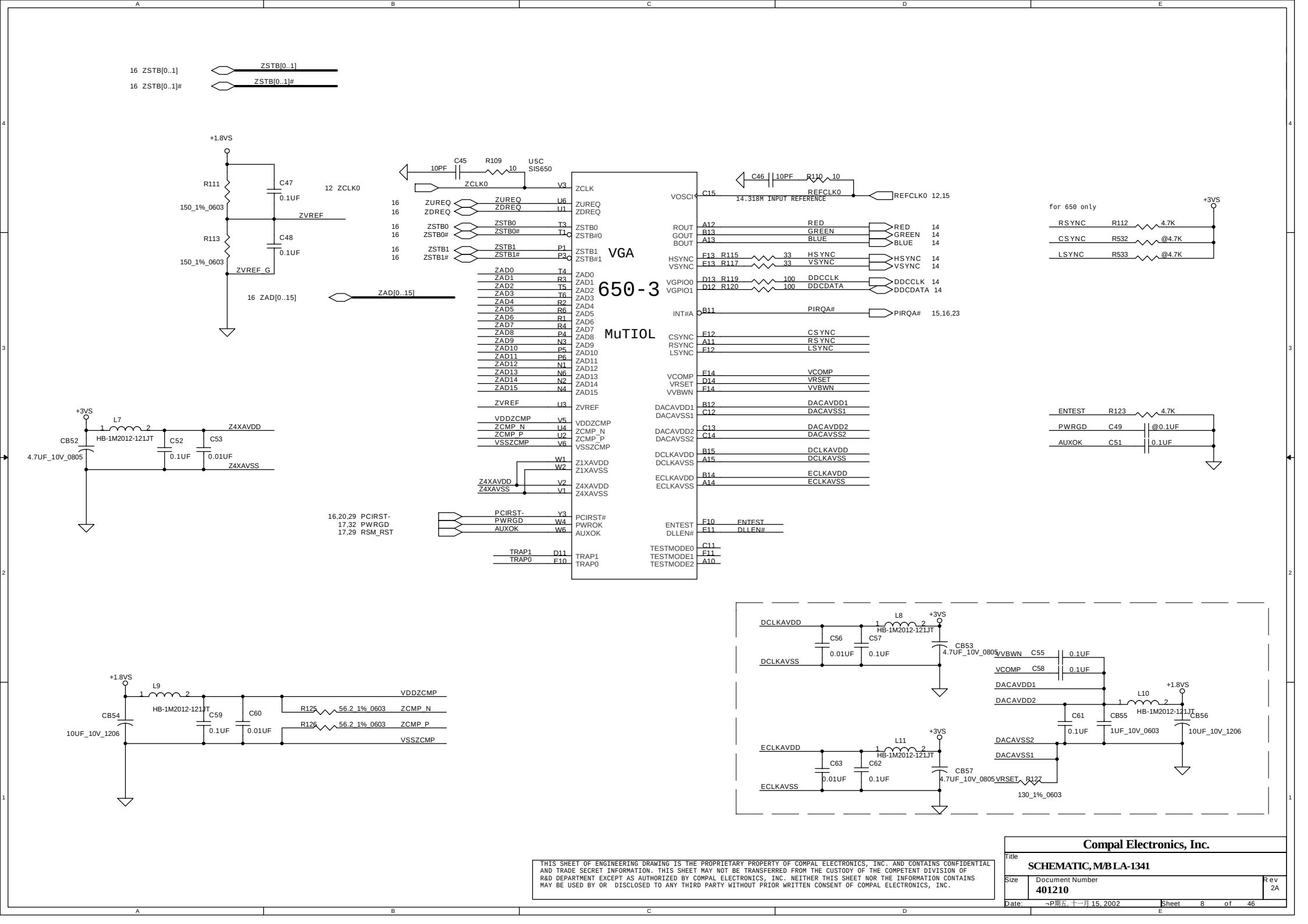
Title: SCHEMATIC, M/B LA-1341
Size: Document Number: 401210 Rev: 2A
Date: ¬P期五, 十一月 15, 2002 Sheet 7 of 46



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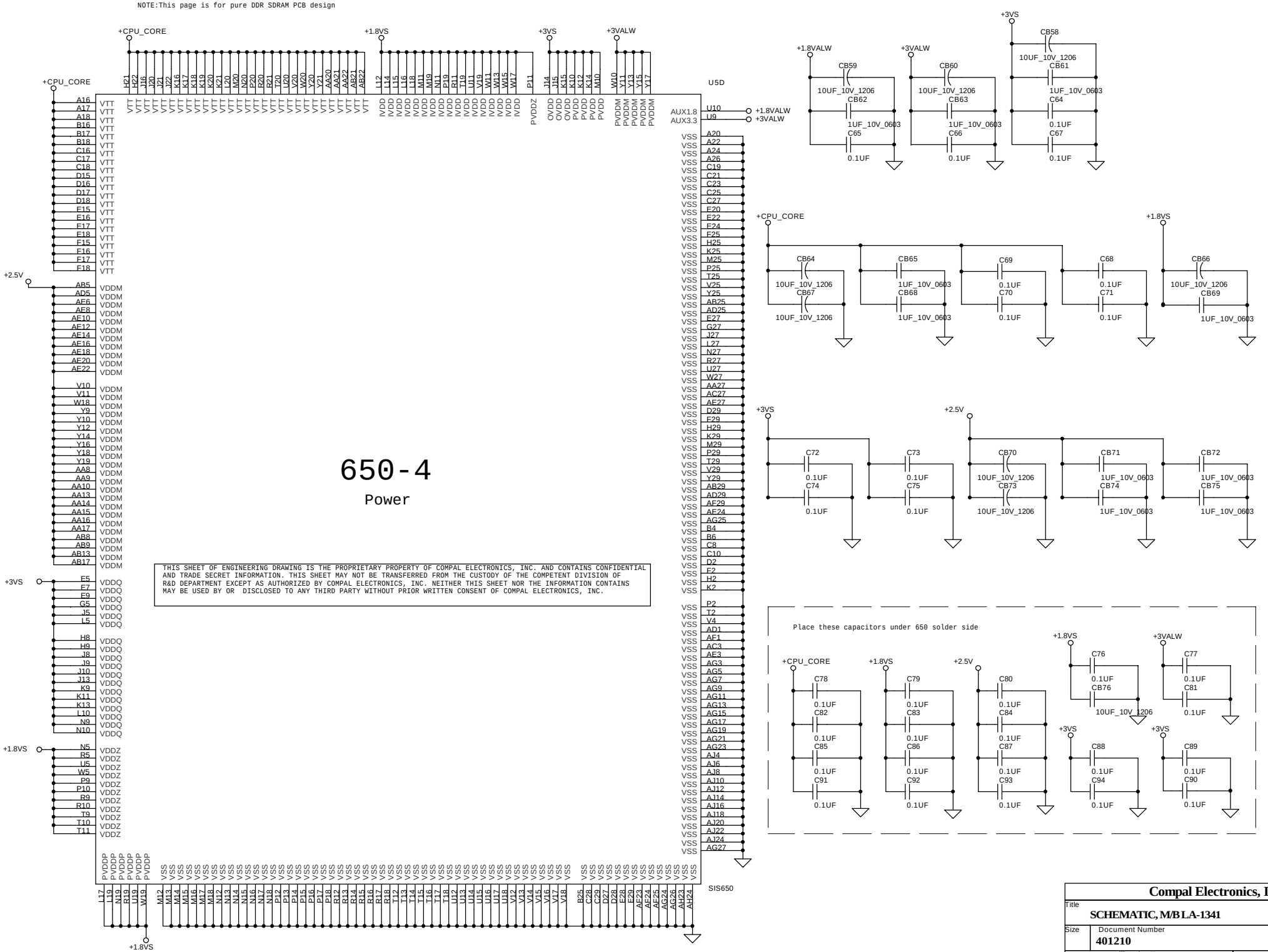
NOTE:This page is for pure DDR SDRAM PCB design



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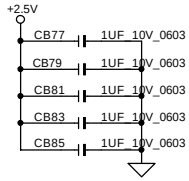
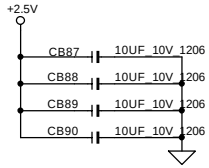
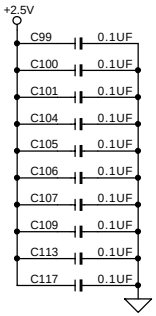
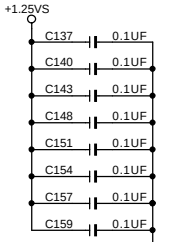
Decoupling capacitors
(Place near DDR SODIMM)

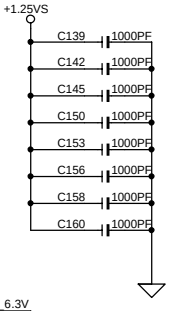


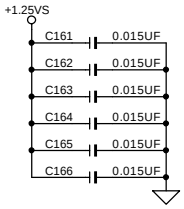


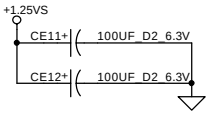


Place these decoupling capacitors close to VTT_MEM termination resistors.



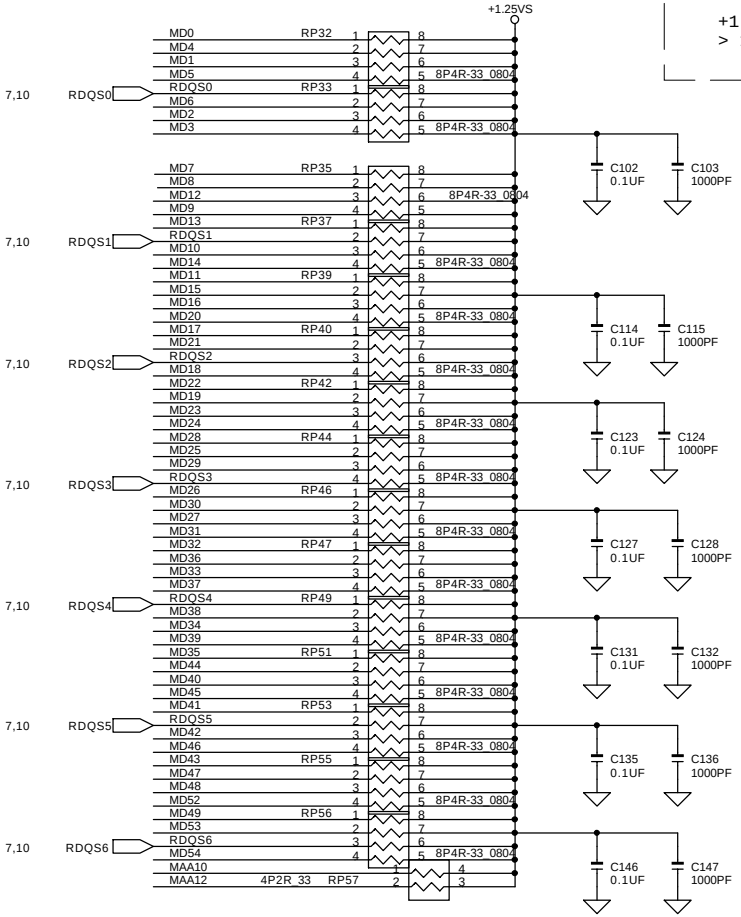


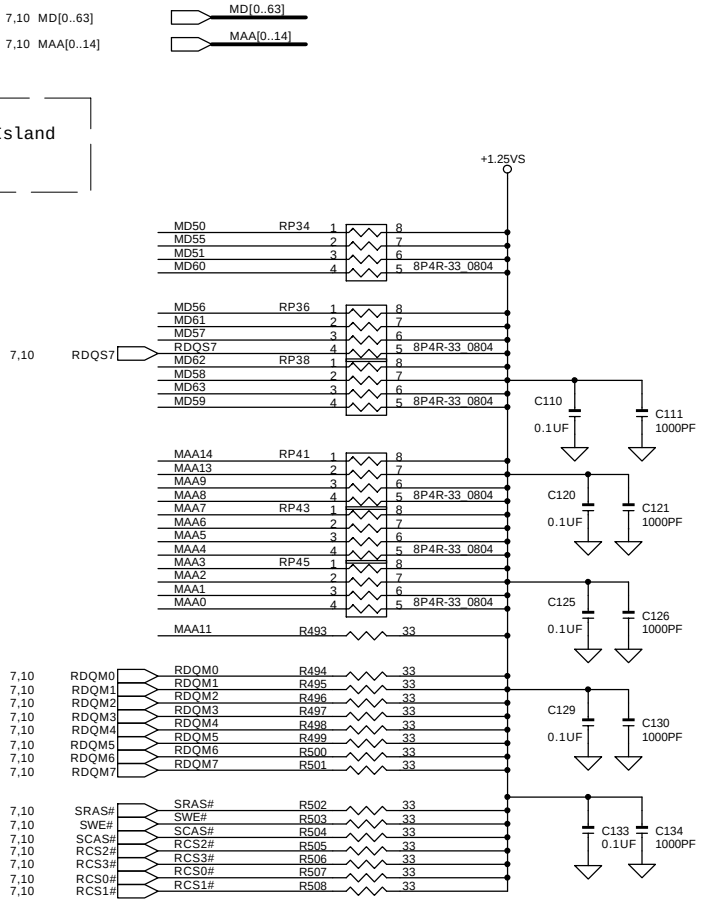




Termianation Network place closely to DIMM

+1.25V Island
> 150ml





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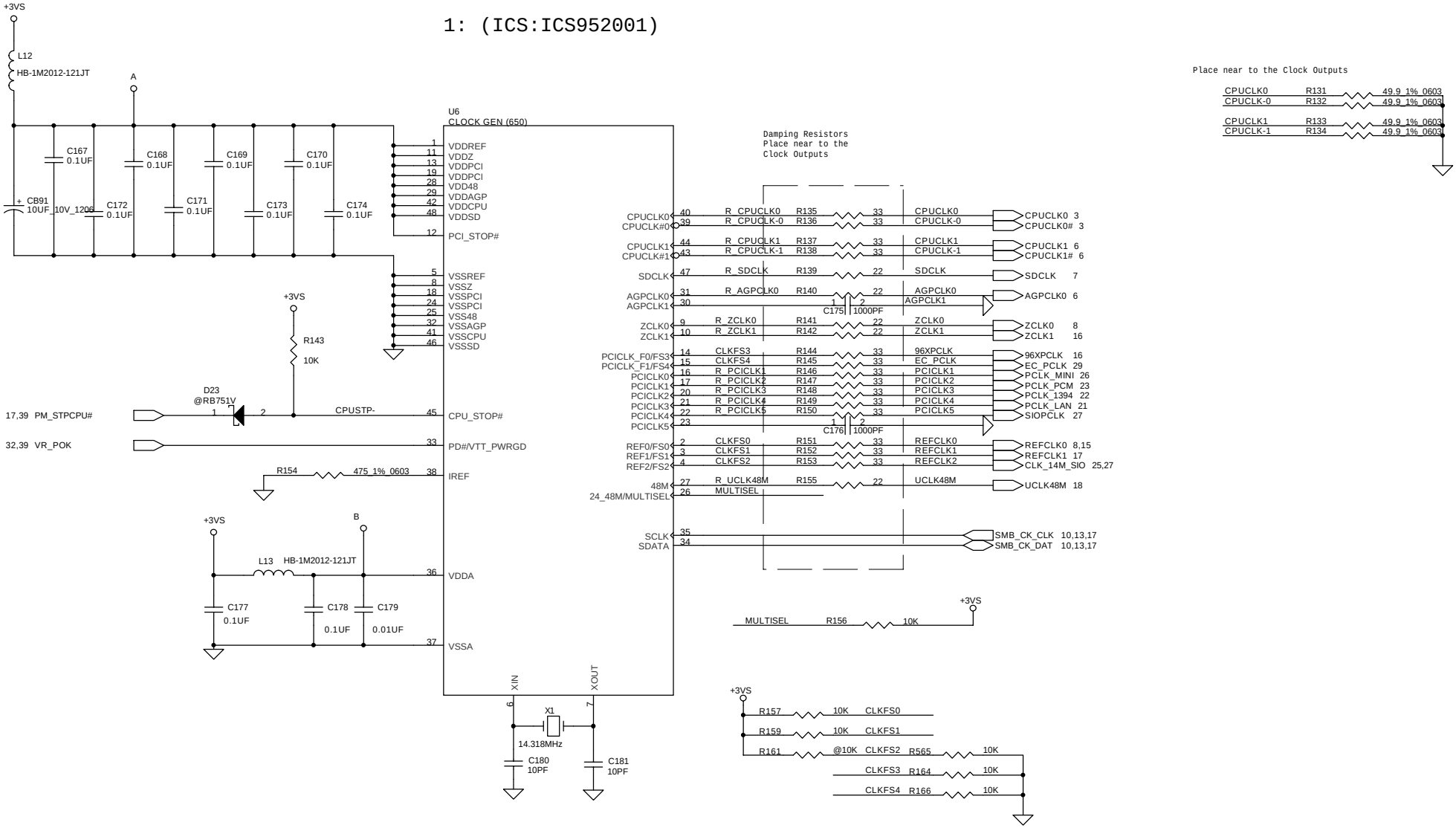
Main Clock Generator

1: (ICS:ICS952001)



	FS4	FS3	FS2	FS1	FS0	CPU	DDR	ZCLK	AGP	PCI
	1	1	0	0	0	100.0	133.0	80.0	66.7	33.3
	1	1	0	0	1	100.0	100.0	80.0	66.7	33.3
*	0	0	1	1	1	100.0	133.3	80.0	66.7	33.3

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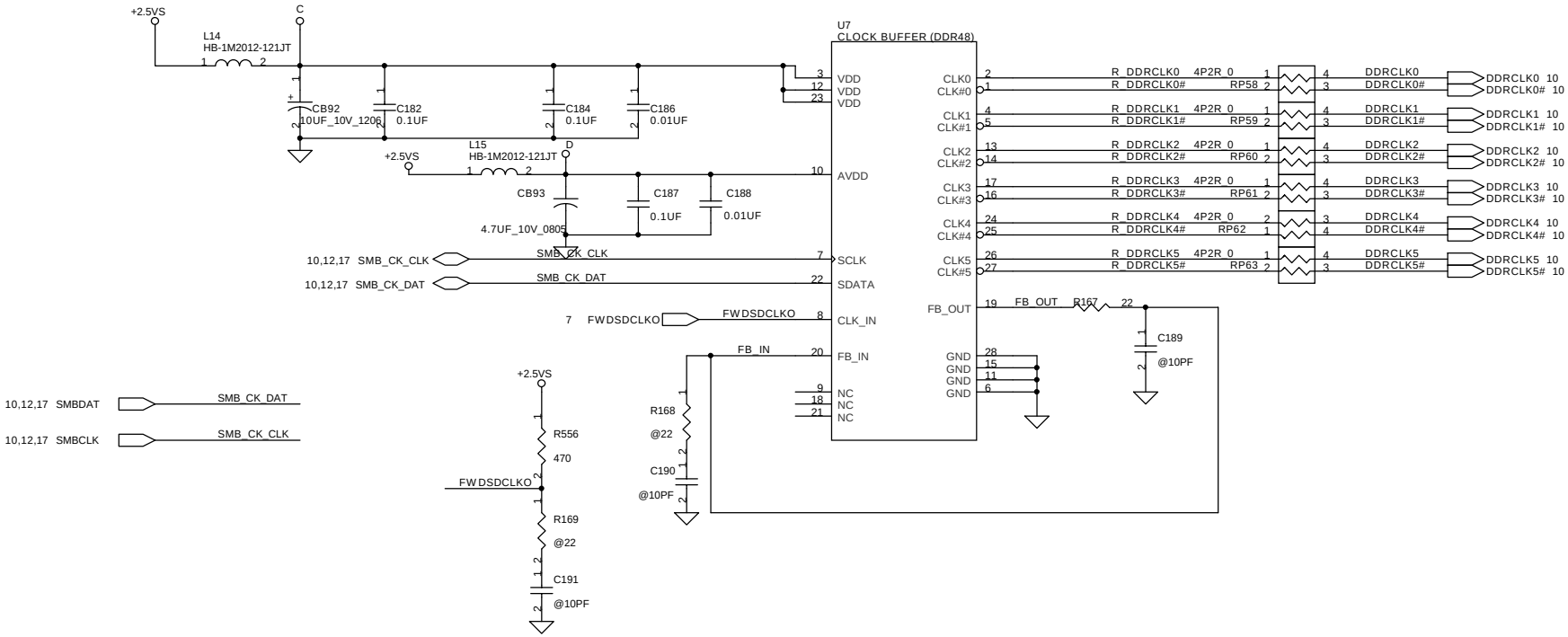
Clock Buffer (DDR)

1:(ICS:ICS93722)



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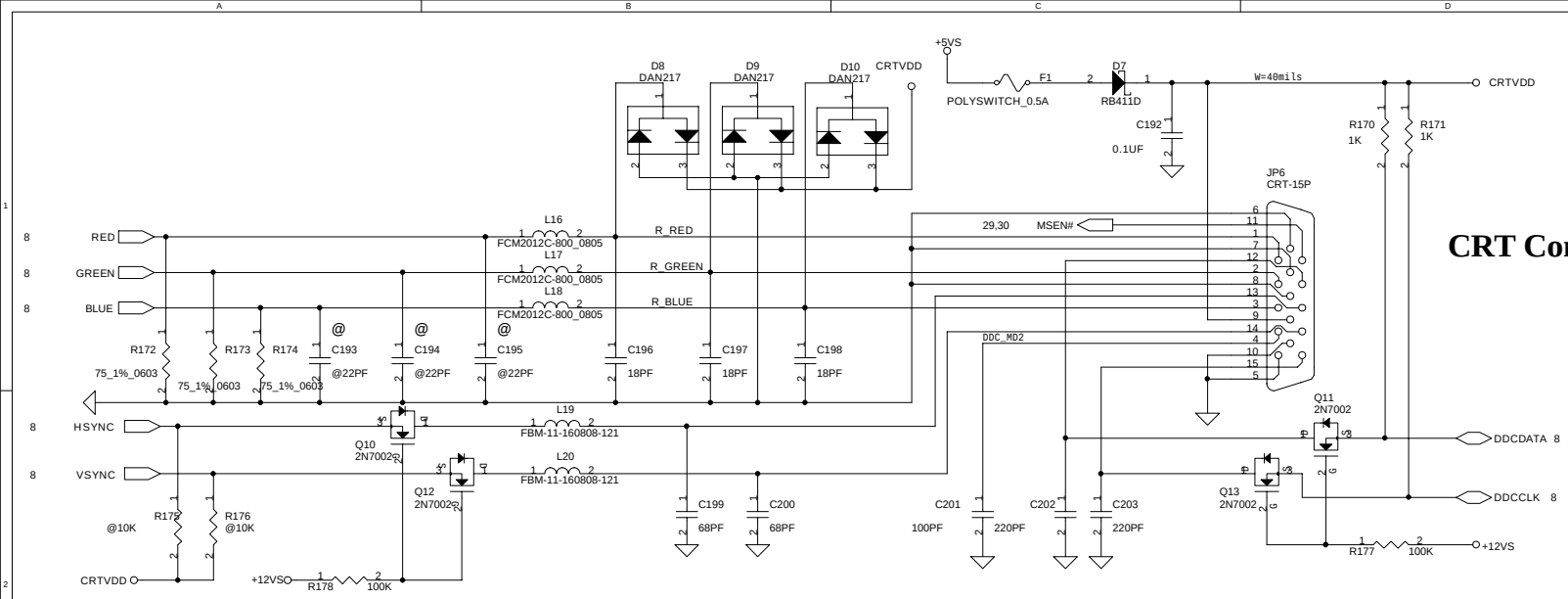

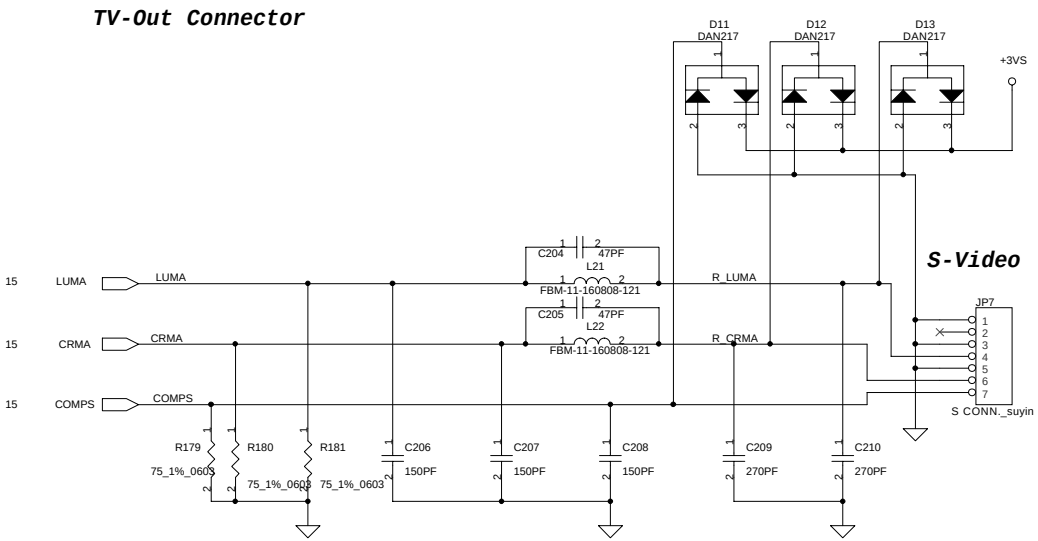

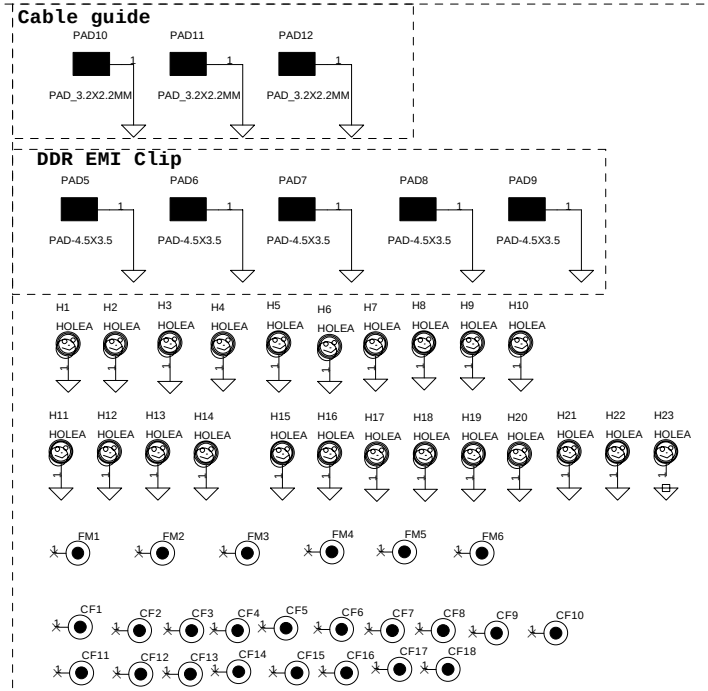

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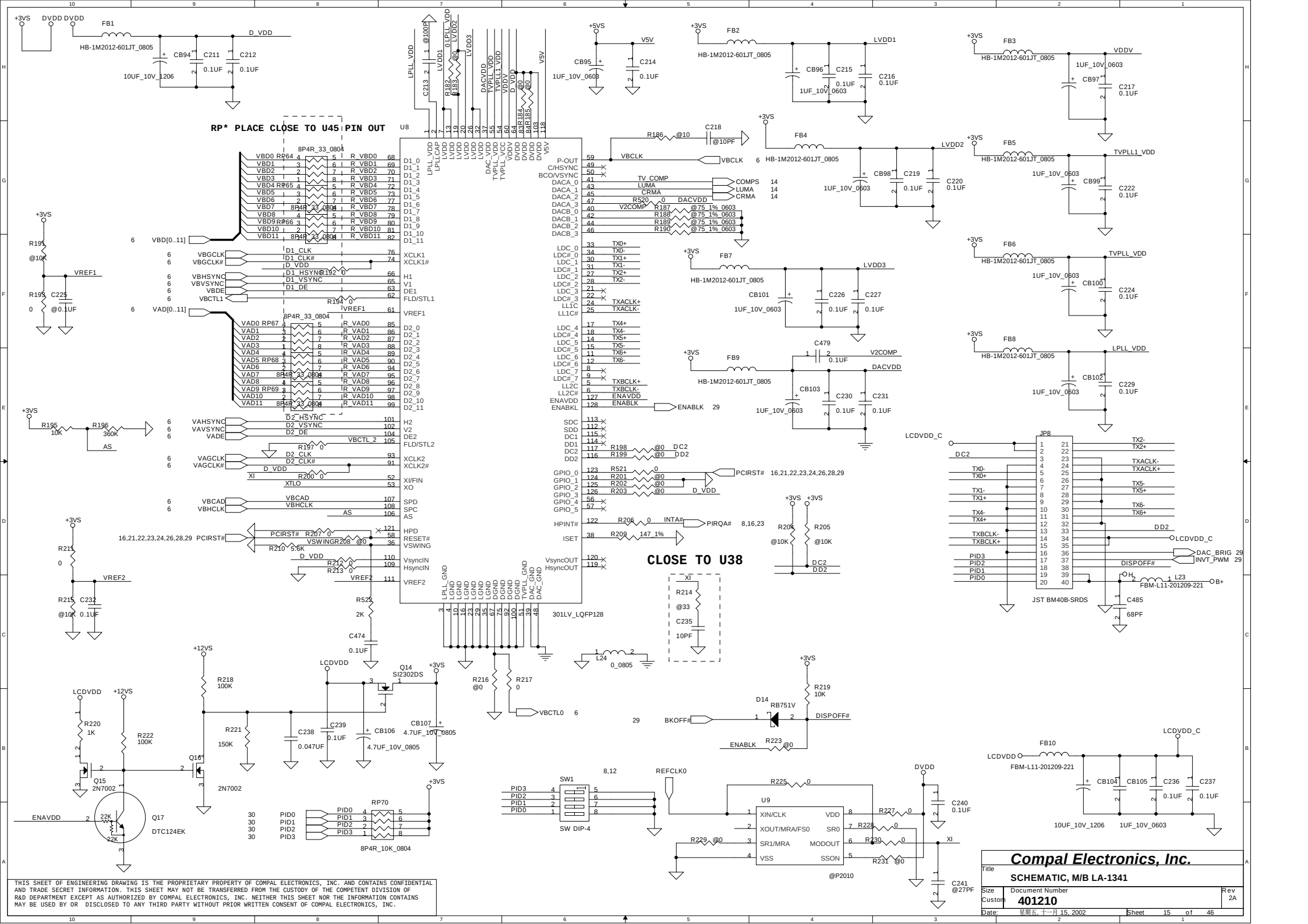
RP* PLACE CLOSE TO U45 PIN OUT
CLOSE TO U38
U8
301LV_LQFP128
8P4R_33_0804
8P4R_10K_0804
SW DIP-4
U9
@P2010
JST BM40B-SRDS
Q14 SI2302DS
Q15 2N7002
Q16 2N7002
Q17 DTC124EK
D14 RB751V
FBM-L11-201209-221
HB-1M2012-601JT_0805
10UF_10V_1206
1UF_10V_0603
0.1UF
68PF
0_0805
@33
10PF
@10PF
@75_1%_0603
147_1%
@0
@10
@27PF
0.047UF
4.7UF_10V_0805
360K
150K
100K
1K
2K
5.6K
22K
10K
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SCHEMATIC, M/B LA-1341
401210
2A
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2002
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961-1

PCI

MULTIO

IDE

SIS961

RP71 8P4R_8.2K_0804 — PIRQB# PIRQA# PIRQC# PIRQD#

RP73 8P4R_8.2K_0804 — REQ#0 REQ#1 REQ#2 REQ#3 REQ#4 R233 8.2K

RP74 8P4R_8.2K_0804 — GNT#0 GNT#1 GNT#2 GNT#3 GNT#4 R234 8.2K

RP75 8P4R_4.7K_0804 — IRDY# TRDY# DEVSEL# STOP# PLOCK# PERR# SERR# FRAME#

RP76 8P4R_4.7K_0804

21,22,23,26 AD[0..31] — AD[0..31]

21,22,23,26 REQ#[0..4] — REQ#4 REQ#3 REQ#2 REQ#1 REQ#0 — PREQ#4 PREQ#3 PREQ#2 PREQ#1 PREQ#0

21,22,23,26 GNT#[0..4] — GNT#4 GNT#3 GNT#2 GNT#1 GNT#0 — PGNT#4 PGNT#3 PGNT#2 PGNT#1 PGNT#0

21,22,23,26 CBE#[0..3] — CBE#3 CBE#2 CBE#1 CBE#0 — C/BE#3 C/BE#2 C/BE#1 C/BE#0

8,15,23 PIRQA# — 21,23,26 PIRQB# — 22 PIRQC# — 26 PIRQD# — INT#A INT#B INT#C INT#D

21,22,23,26 FRAME# — 21,22,23,26 IRDY# — 21,22,23,26 TRDY# — 21,22,23,26 STOP# — FRAME# IRDY# TRDY# STOP#

21,23,26 SERR# — 21,22,23,26 PAR — 21,22,23,26 DEVSEL# — SERR# PAR DEVSEL# PLOCK#

12 96XPCLK — 8,20,29 PCIRST- — PCICLK PCIRST#

21,22,23,26 PERR# — PERR#

R235 @10 — C245 @10PF — 12 ZCLK1 — ZCLK

8 ZSTB0 — 8 ZSTB0# — ZSTB0 ZSTB0#

8 ZSTB1 — 8 ZSTB1# — ZSTB1 ZSTB1#

8 ZUREQ — 8 ZDREQ — ZUREQ ZDREQ

PAR R236 2.2K

96XPCLK R237 10 C246 10PF

SVDDZCMP R19 — SZCMP_N N18 — VDDZCMP ZCMP_N

SZCMP_P R18 — SVSSZCMP P18 — ZCMP_P VSSZCMP

U20 Z1XAVDD — U19 Z1XAVSS

SZ4XAVDD — T20 Z4XAVDD — SZ4XAVSS T19 Z4XAVSS

SZVREF R20 — P20 VZREF VZSSREF

+1.8VS — R238 C247 0.1UF 150_1%_0603 — SZVREF — R241 C248 0.1UF 150_1%_0603 — SZVREF_G

+1.8VS — R239 @4.7K R240 @4.7K — ZSTB0 ZSTB1 — ZSTB0# ZSTB1# — R242 @4.7K R243 @4.7K

8 ZAD[0..15] — ZAD0 ZAD1 ZAD2 ZAD3 ZAD4 ZAD5 ZAD6 ZAD7 ZAD8 ZAD9 ZAD10 ZAD11 ZAD12 ZAD13 ZAD14 ZAD15

U10A

IDEAVDD IDEAVSS — C242 0.01UF C243 0.1UF

ICHRDYA IDREQA IIRQA CBLIDA — ICHRDYA IDEREQA IDEIRQA CBLIDA — ICHRDYA 20 IDEREQA 20 IDEIRQA 20 CBLIDA 20

IIOR#A IIOW#A IDACK#A — IDEIOR-A IDEIOW-A IDACK-A — IDEIOR#A 20 IDEIOW#A 20 IDACK#A 20

IDSAA2 IDSAA1 IDSAA0 — IDESAA2 IDESAA1 IDESAA0 — IDESAA[0..2] — IDESAA[0..2] 20

IDECSA#1 IDECSA#0 — IDECS#A1 IDECS#A0 — IDECS#A[0..1] — IDECS#A[0..1] 20

ICHRDYB IDREQB IIRQB CBLIDB — ICHRDYB IDEREQB IDEIRQB CBLIDB — ICHRDYB 20 IDEREQB 20 IDEIRQB 20 CBLIDB 20

IIOR#B IIOW#B IDACK#B — IDEIOR-B IDEIOW-B IDACK-B — IDEIOR#B 20 IDEIOW#B 20 IDACK#B 20

IDSAB2 IDSAB1 IDSAB0 — IDESAB2 IDESAB1 IDESAB0 — IDESAB[0..2] — IDESAB[0..2] 20

IDECSB#1 IDECSB#0 — IDECS#B1 IDECS#B0 — IDECS#B[0..1] — IDECS#B[0..1] 20

IDA0 IDA1 IDA2 IDA3 IDA4 IDA5 IDA6 IDA7 IDA8 IDA9 IDA10 IDA11 IDA12 IDA13 IDA14 IDA15 — IDEDA0 IDEDA1 IDEDA2 IDEDA3 IDEDA4 IDEDA5 IDEDA6 IDEDA7 IDEDA8 IDEDA9 IDEDA10 IDEDA11 IDEDA12 IDEDA13 IDEDA14 IDEDA15 — IDEDA[0..15] 20

IDB0 IDB1 IDB2 IDB3 IDB4 IDB5 IDB6 IDB7 IDB8 IDB9 IDB10 IDB11 IDB12 IDB13 IDB14 IDB15 — IDEDB0 IDEDB1 IDEDB2 IDEDB3 IDEDB4 IDEDB5 IDEDB6 IDEDB7 IDEDB8 IDEDB9 IDEDB10 IDEDB11 IDEDB12 IDEDB13 IDEDB14 IDEDB15 — IDEDB[0..15] 20

+3VALW — Put near 961A Chip. — U11A 74LVC125 — PCIRST- — PCIRST# 15,21,22,23,24,26,28,29

Analog Power supplies of Transzip function for 961A Chip.

+3VS — L26 HB-1M2012-121JT — SZ4XAVDD — CB109 4.7UF_10V_0805 — C251 0.1UF — C252 0.01UF — SZ4XAVSS

+1.8VS — L25 HB-1M2012-121JT — CB108 4.7UF_10V_0805 — C249 0.1UF — C250 0.01UF — R244 56.2_1%_0603 SZCMP_N — R245 56.2_1%_0603 SZCMP_P — SVDDZCMP — SVSSZCMP

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Programable on-die pull-high strength for CPU_S:
(Infinite, 150, 110, 56 Ohm)

U10B

CPU_S
LPC
RTC
GPIO
AC97
ACPI
MII
/Geyserville
961-2
SIS961

INIT# A20M# SMI# INTR NMI IGNNE# FERR# STPCLK# CPUSLP#
APICCK APICD0 APTCD1
LAD0 LAD1 LAD2 LAD3
LFRAME# LDRQ# SIRQ
BATOK PWROK
OSC32KHI OSC32KHO
RTCVDD RTCVSS
SDATI0 SDATI1 AC_SDIN0 AC_SDIN1 AC_SDOUT AC_SYNC AC_RESET# AC_BIT_CLK
OSCI ENTEST SPK PWRBTN# PME# PSON# AUXOK ACPILED
MIICLK25M MIITXCLK MIITXEN MIITXD0 MIITXD1 MIITXD2 MIITXD3 MIIRXCLK MIIRXDV MIIRXER MIIRXD0 MIIRXD1 MIIRXD2 MIIRXD3 MIICOL MIICRS MIIMDC MIIMDIO MIIAVDD MIIAVSS
GPIO0 GPIO1/LDRQ1# GPIO2/THERM# GPIO3/EXTSMI# GPIO4/CLKRUN# GPIO5/PREQ5# GPIO6/PGNT5# *GPIO7 GPI8/RING GPI9/AC_SDIN2 GPI10/AC_SDIN3 *GPIO11 *GPIO12/CPUSTP# *GPIO13/DPRSLPVR *GPIO14 *GPO15/VR_HILO# *GPO16/LO_HI# *GPO17/VGATEM# *GPO18/PMCLK *GPIO19 *GPIO20

PWRGD C256 @0.1UF
C257 10PF R256 33 BIT_CLK
C258 @10PF R259 @33 REFCLK1
C259 0.1UF
SPKR
PSON#
RSM_RST

R542 4.7K +3VALW
AC_VID0 AC_VID1 AC_VID2 AC_VID3 AC_VID4
D55 RB751V EC_THERM# 29
D19 RB751V EXTSMI# 29
D20 RB751V PM_BATLOW# 29
SCI# 29
SWI# 29
AC_IN 29
LID_OUT# 29
PM_STPCPU# 12,39
DPRSLPVR
FLASH# 30
VR_HILO#
D26 RB751V LO_HI# PM_CPUPERF# 3
VGATMBM
IDERST# 20
SMBCLK 10,12,13
SMBDAT 10,12,13
Q20 2N7002
Q21 2N7002
+5VS

CHGRTC W=30mils +RTCVCC
R247 200_0603
D15 RB751V
D17 RB751V
R250 470
BATT1 RTCBATT
C253 0.1UF
CB110 22UF_10V_1206
Q18 MMBT3906
Q19 3904
R246 10K
R248 47K
R251 15K
SIS AP note

+RTCVCC D16 1N4148 R249 10K BATOK J6
C254 1UF_10V_0603
C255 0.01UF
CB111 10UF_10V_1206
Decoupling Capacitor
Place close to 961A

U41 7SH08 AC_VID3 DPRSLPVR PM_DPRSLPVR 39 R554 @0
D18 @1N4148 R254 @1K AUXOK R255 100K
U4 7SH08 AC_VID4 VR_HILO# PM_GMUXSEL 39 R540 0

Put closed to 961A CHIP
OSC32KHO OSC32KHI R260 10M_0603 32.768KHZ X3 C260 15PF C261 20PF
GPIO pins pull HIGH
NEED NOT to place
close to 961A
AC_VID3 R288 10K
AC_VID2 R287 10K
GPIO5 R262 10K
EXT_SMI# R263 10K
GPIO10 R258 4.7K
GPIO14 R257 10K
GPIO18 R264 10K
GPIO11 R266 10K
RING R268 10K
PWRBTN# R270 10K
PME# R271 10K
VGATMBM R272 @10K
PM_STPCPU# R541 10K
GPIO9 R279 10K
NEED NOT to place
close to 961A
RP77 8P4R_10K_0804
LAD3 LAD2 LAD1 LAD0
LDRQ# R273 10K
SIRQ R275 10K
SENTEST R278 47
ATE request

SDATI0 R277 100K
SDATI1 R280 100K
AC'97 Pull-Down:

R551 10K H_DPSLP# 3 Q49 3904 PM_STPCPU#

R282 10K LFRAME#
SMBDAT R274 10K
SMBCLK R276 10K
VR_HILO# R283 10K
GPIO19 R566 10K
GPIO20 R567 10K
DPRSLPVR R284 4.7K
AC_VID0 R285 10K
AC_VID1 R286 10K
AC_VID4 R289 10K

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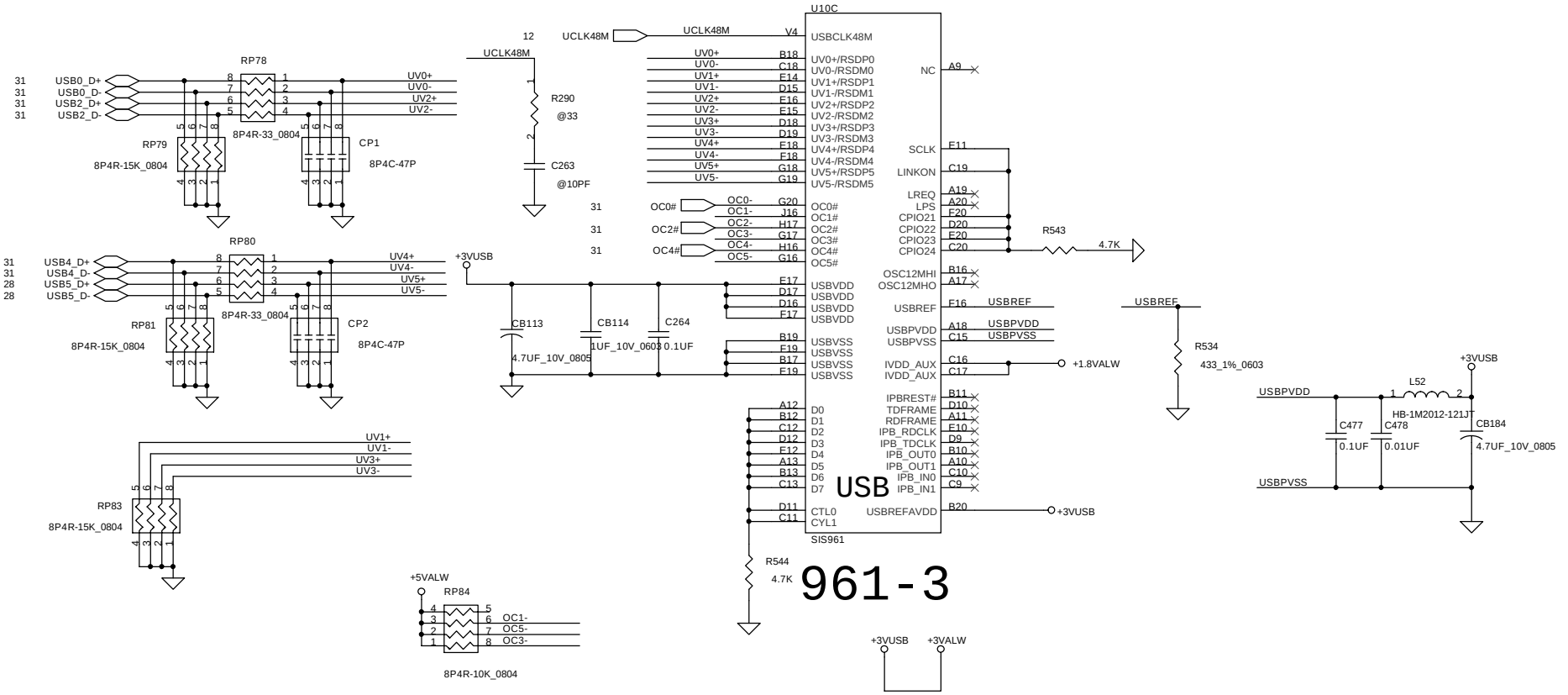

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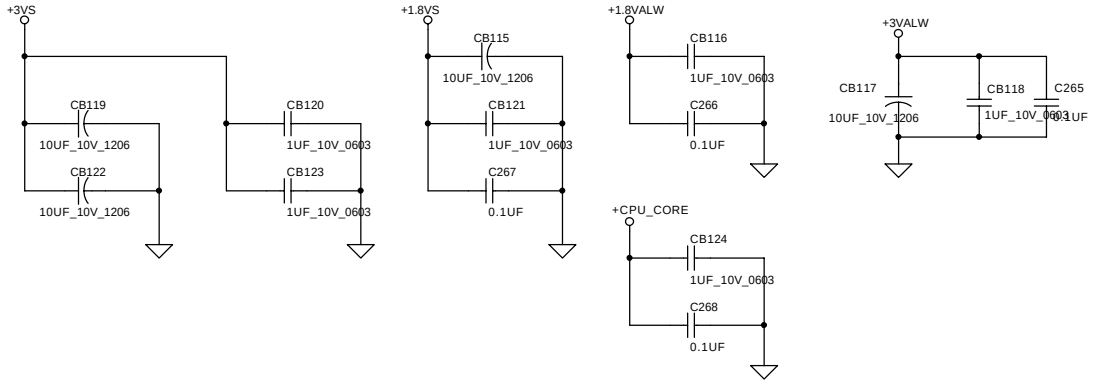

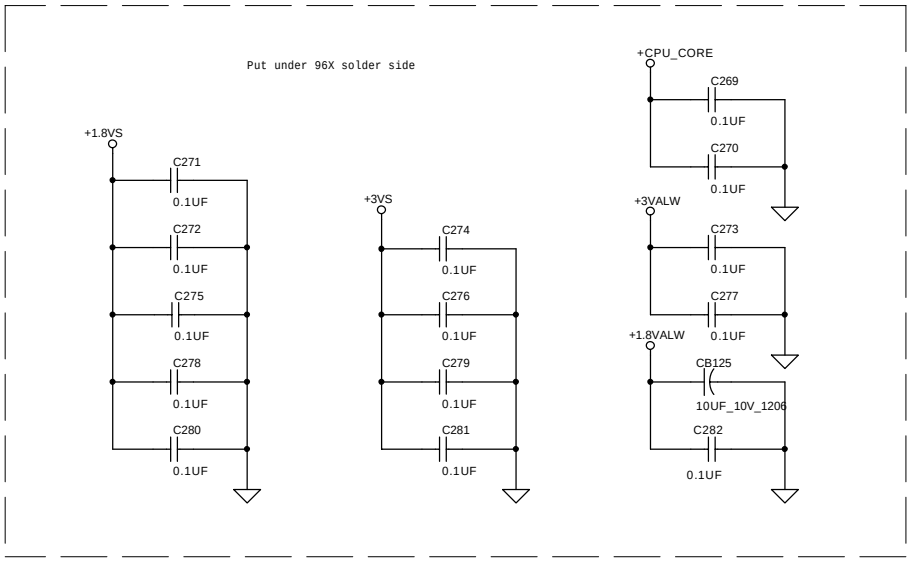

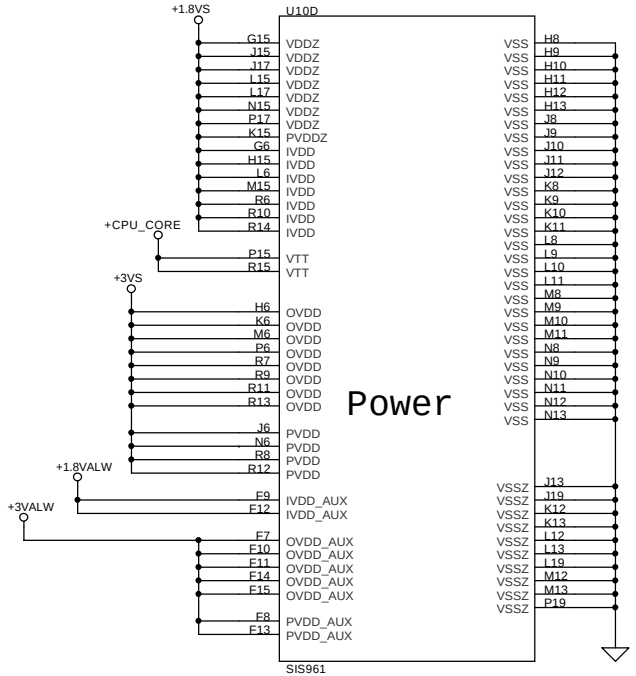

961-4

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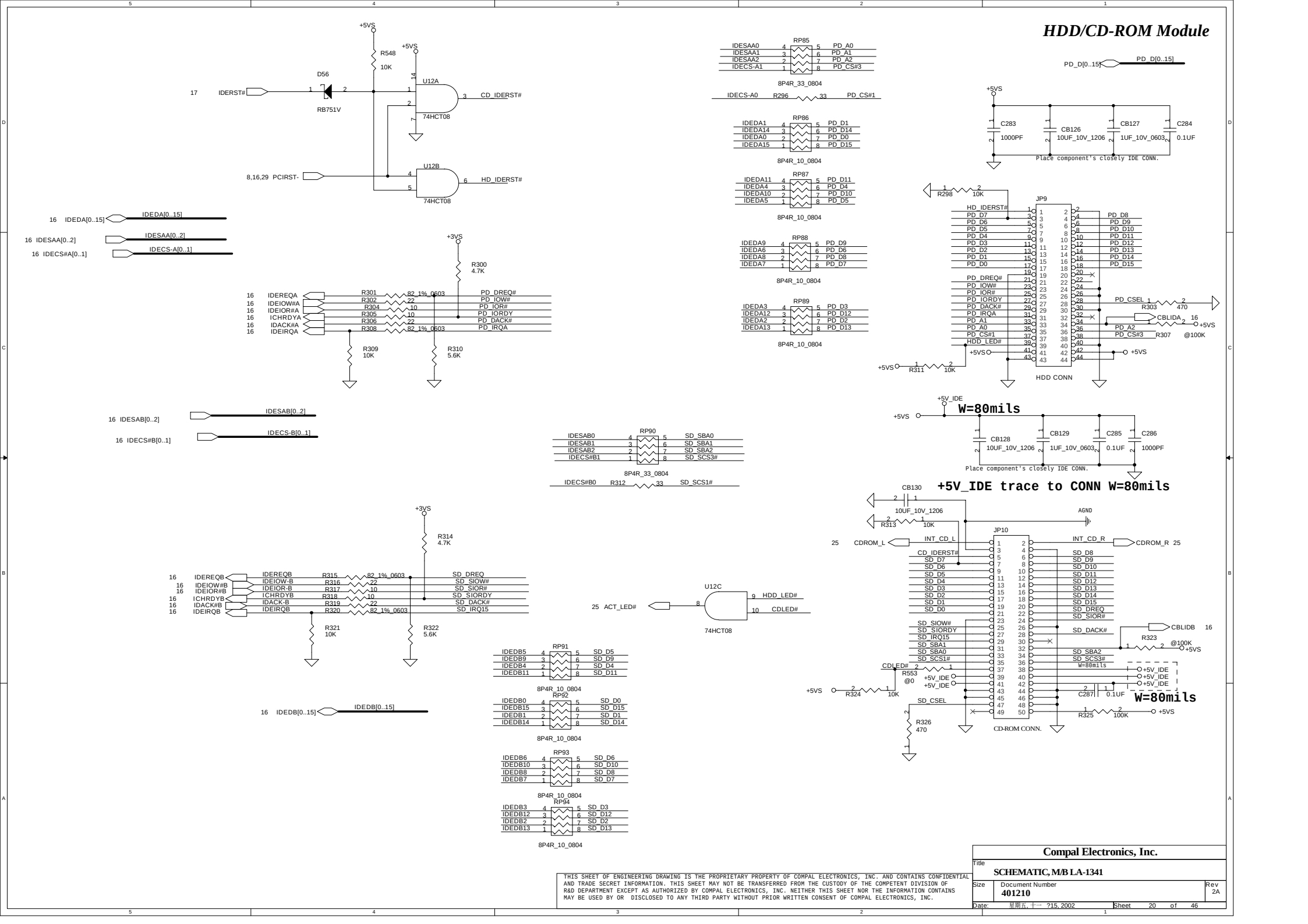
HDD/CD-ROM Module



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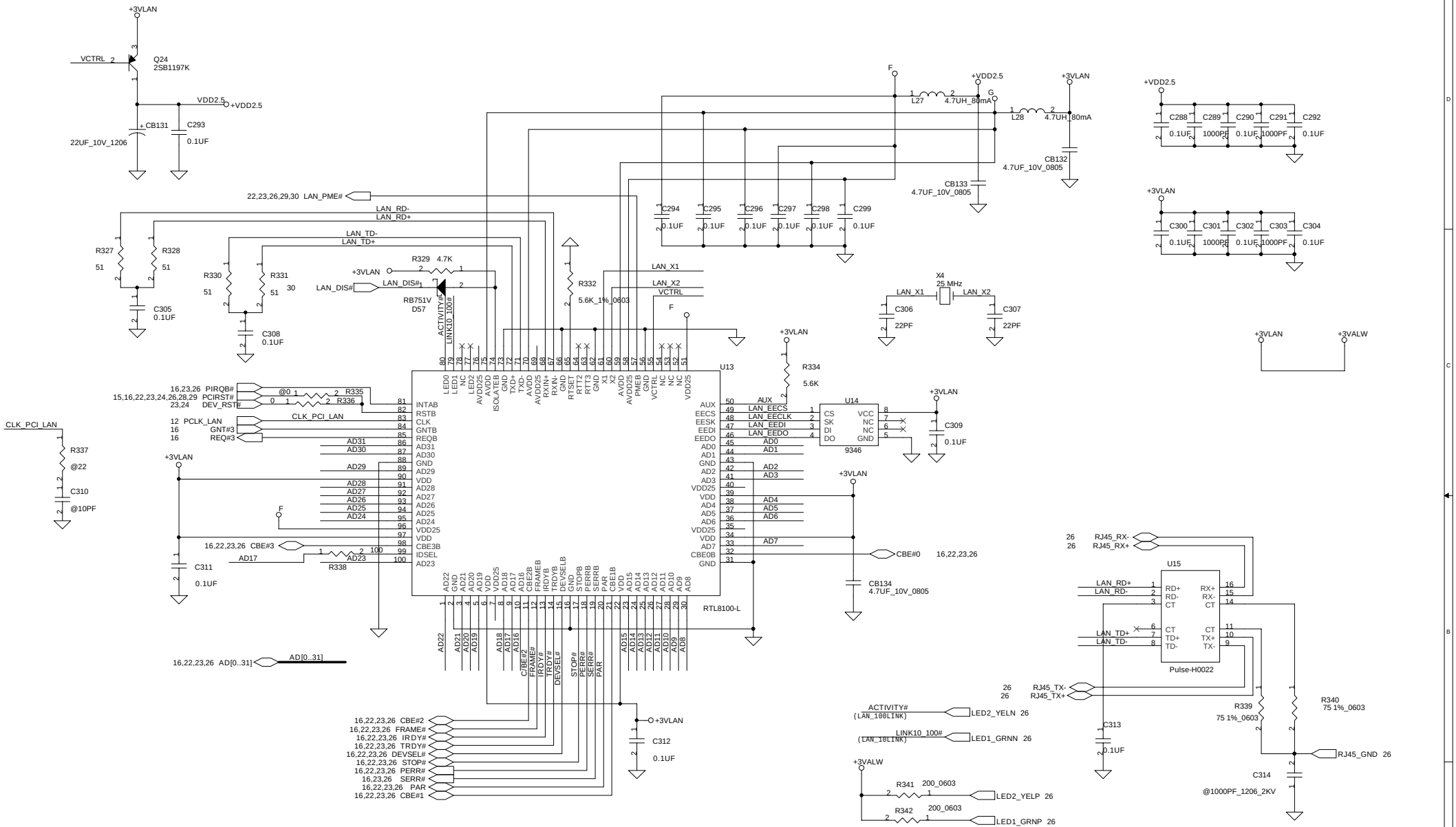

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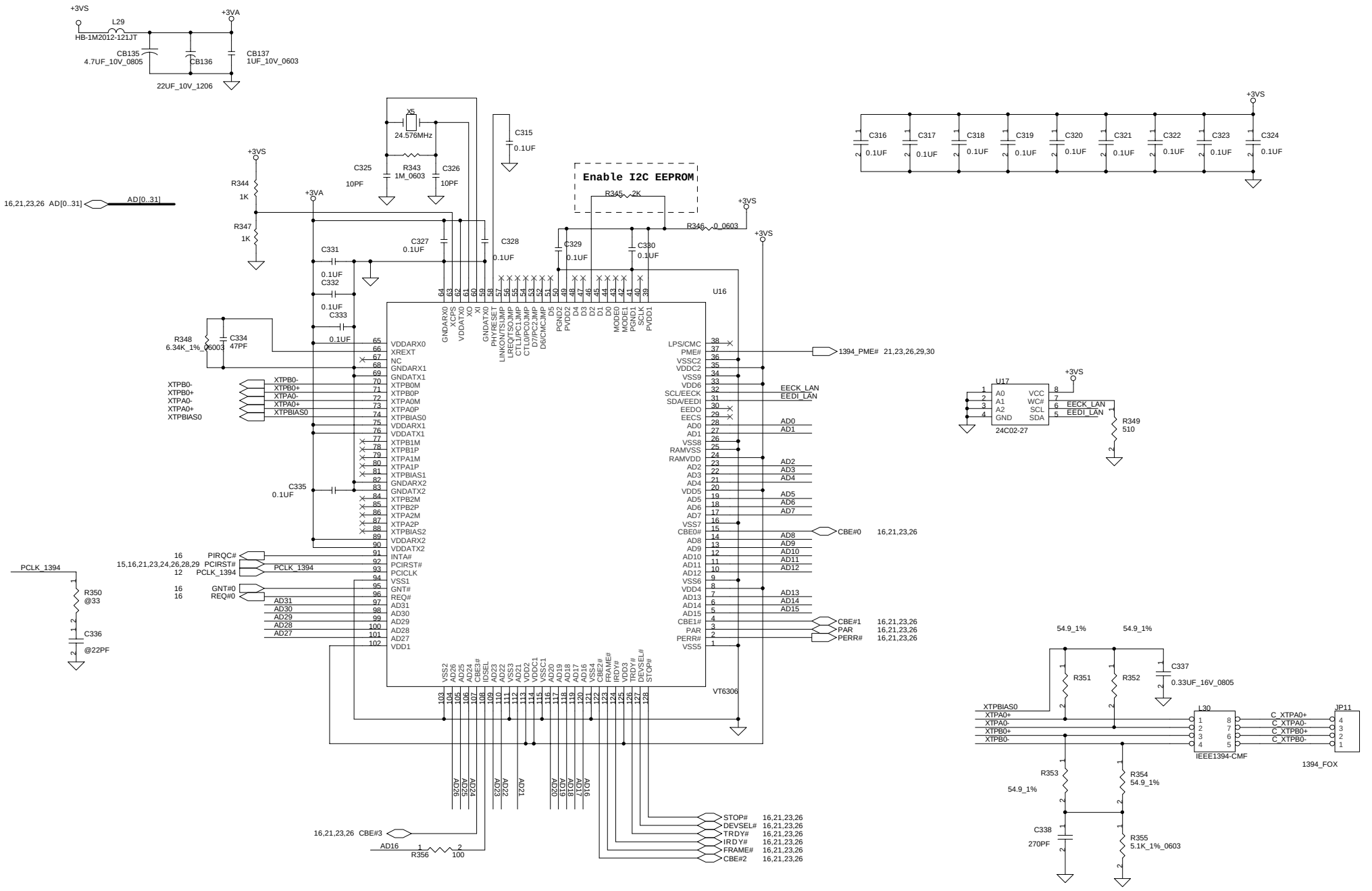
IEEE1394 Controller/PHY



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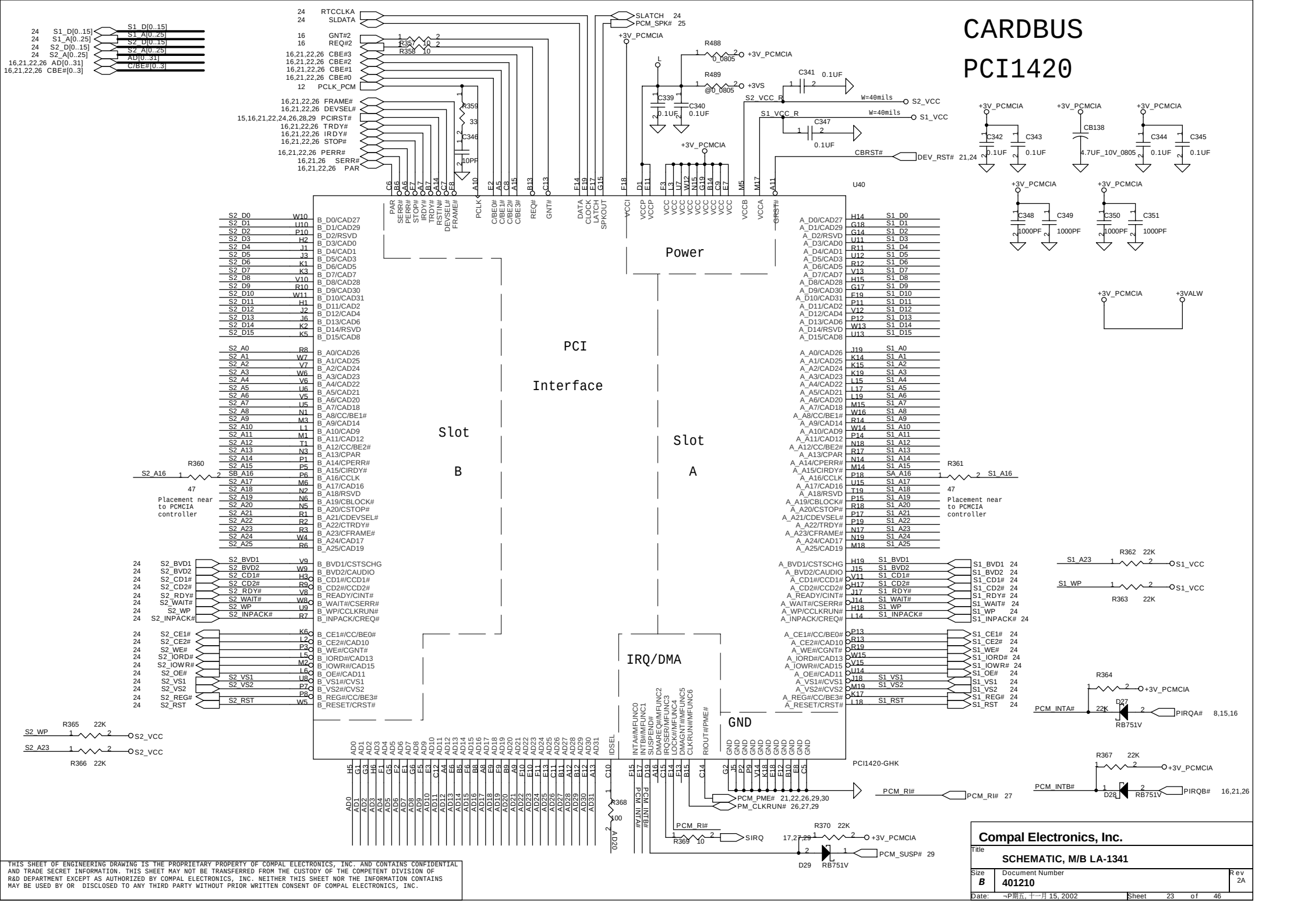
CARDBUS PCI1420



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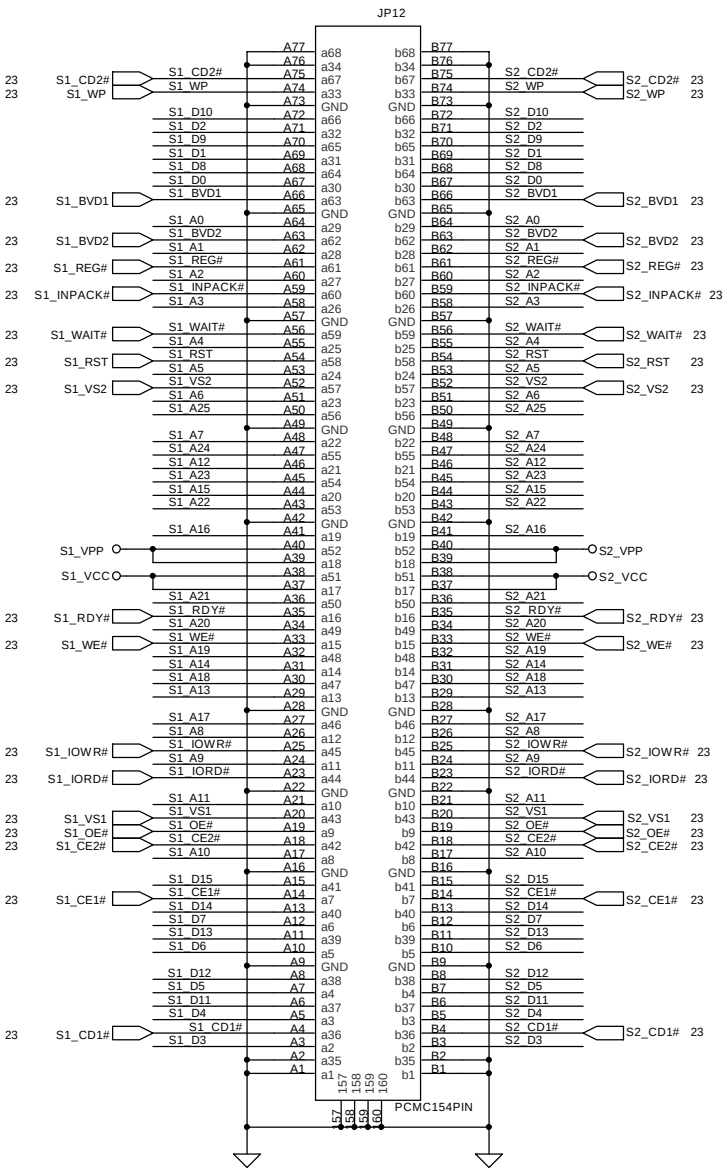
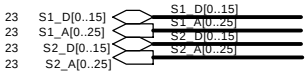
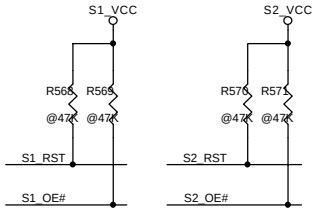
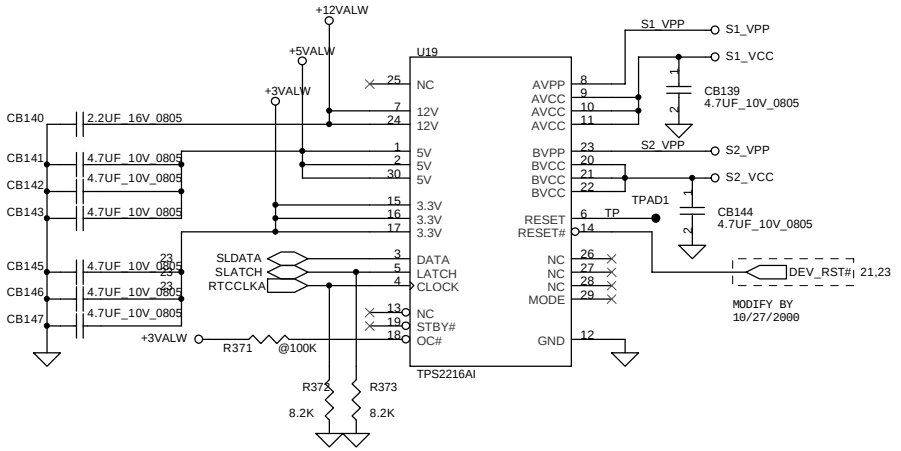
CARDBUS SOCKET

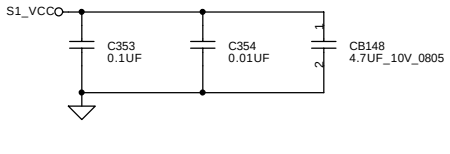


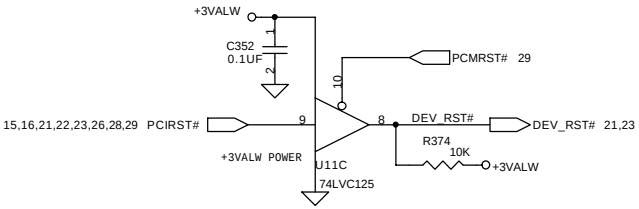


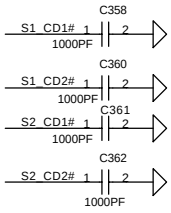
PCMCIA POWER CTRL.

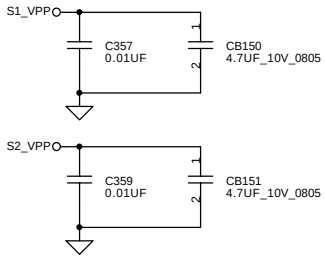












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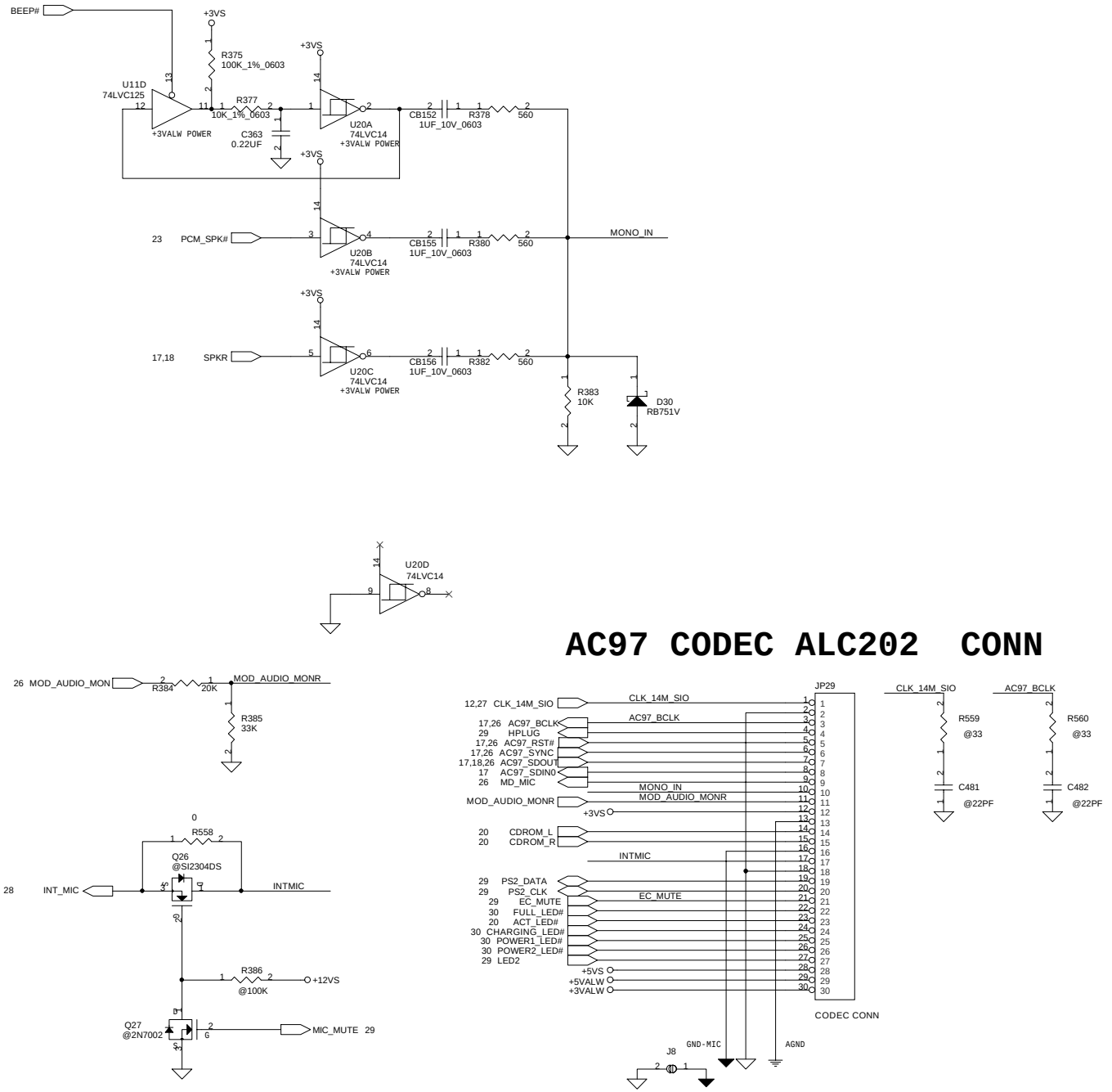

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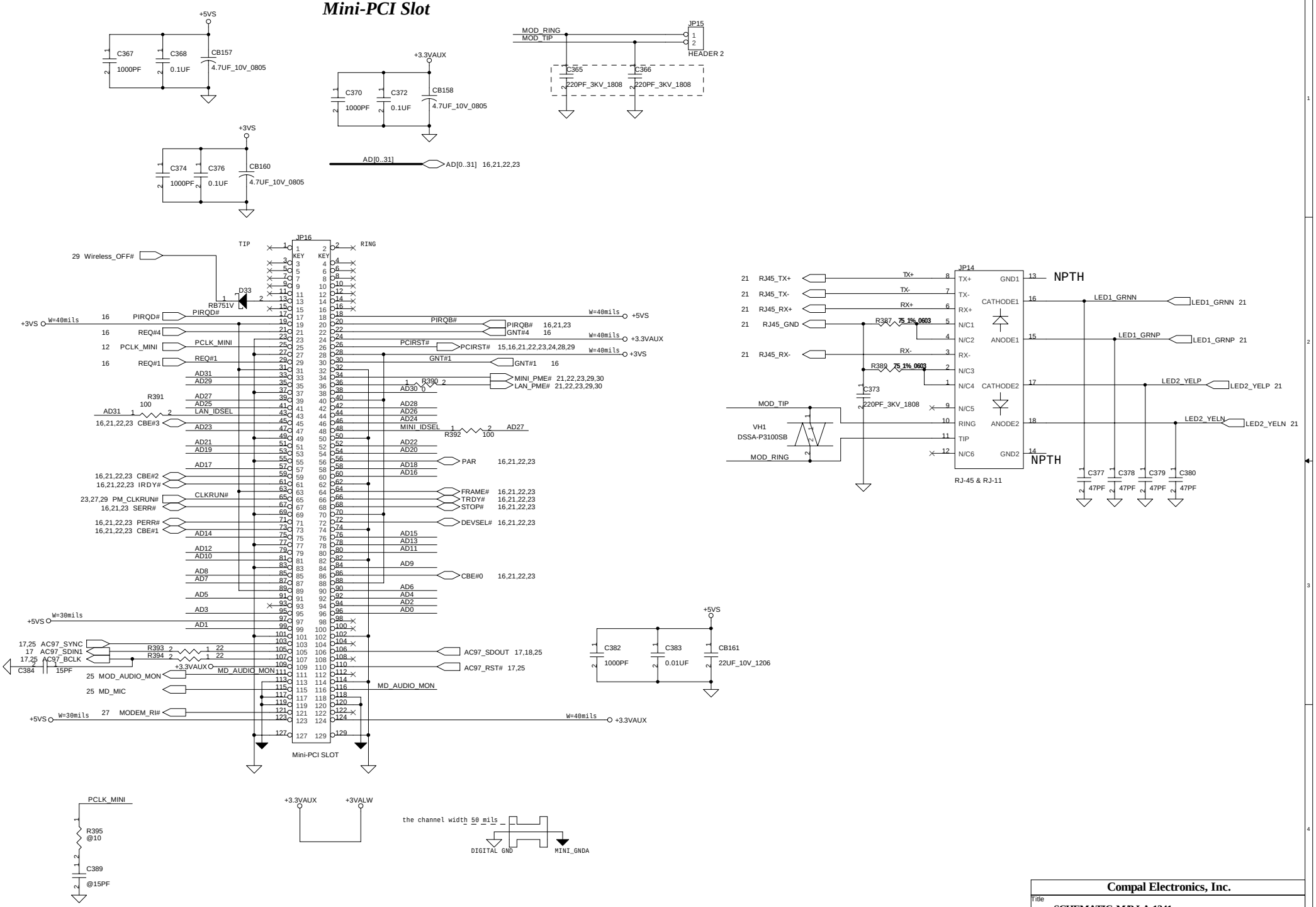
Mini-PCI Slot



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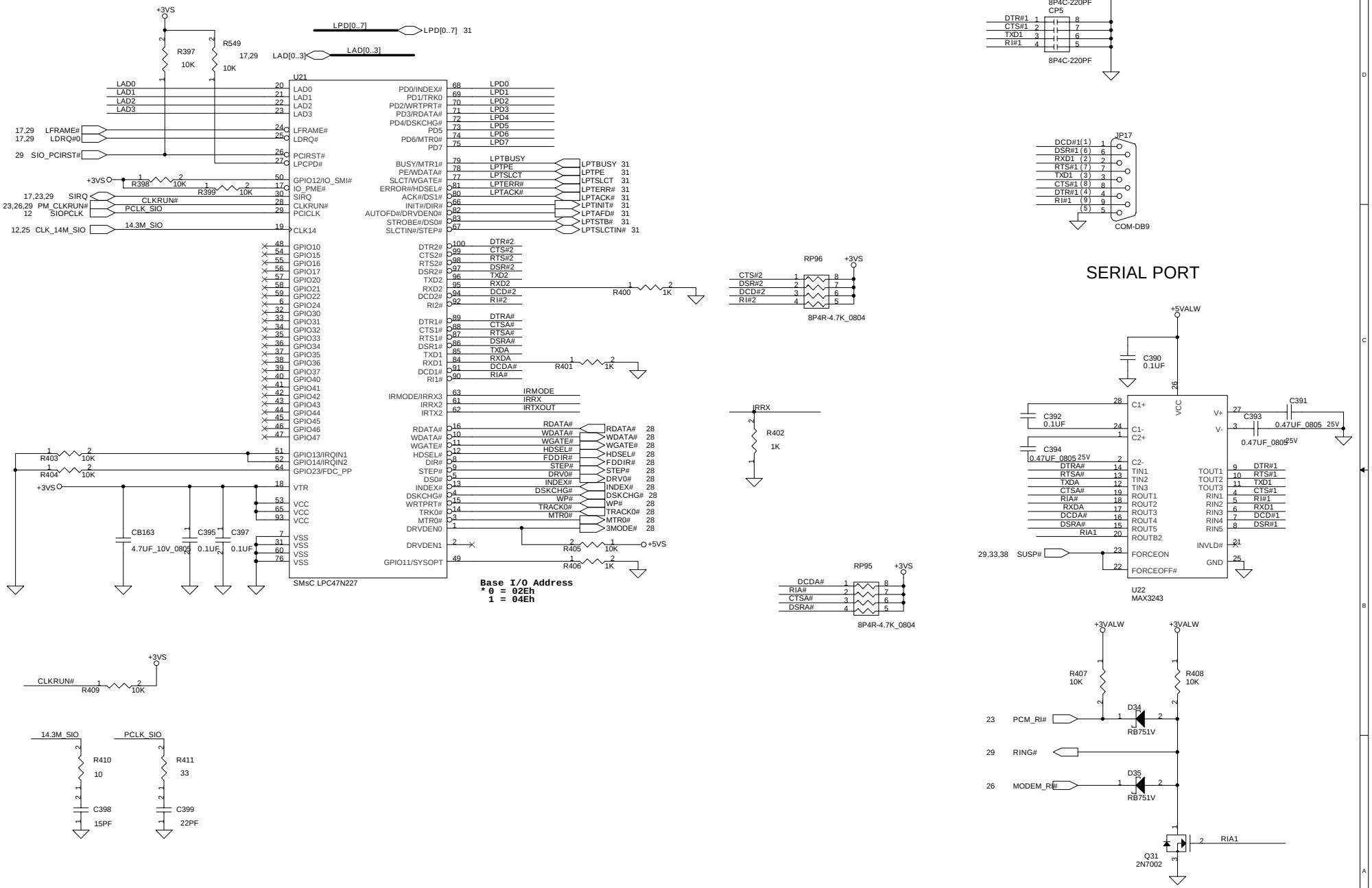
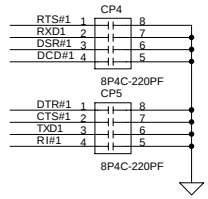
SUPER I/O SMsC LPC47N227





SERIAL PORT

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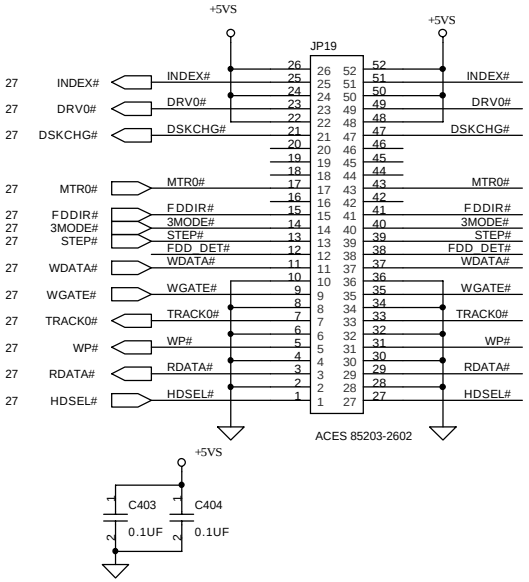
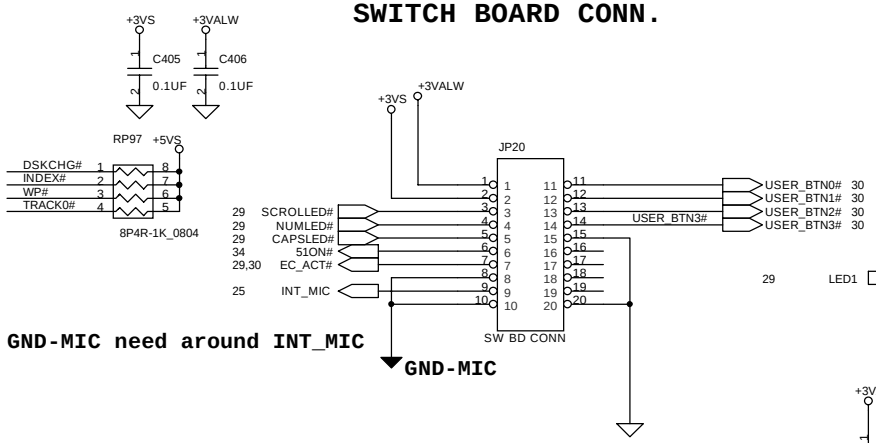
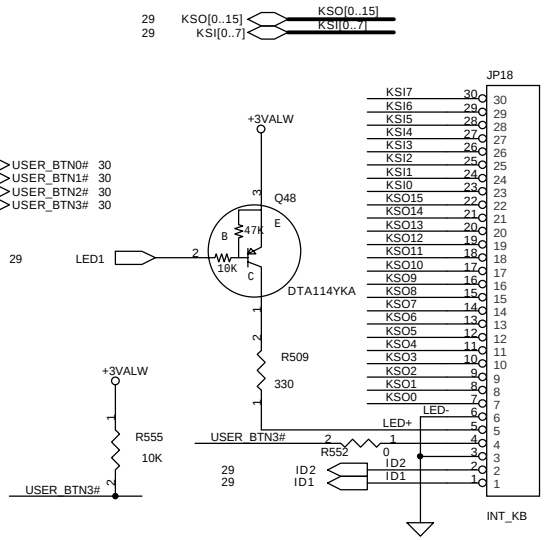
FDD CONN.

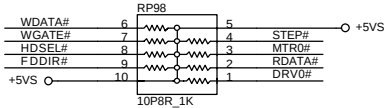


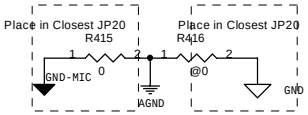
SWITCH BOARD CONN.

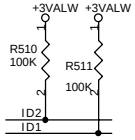


INT_KBD CONN.

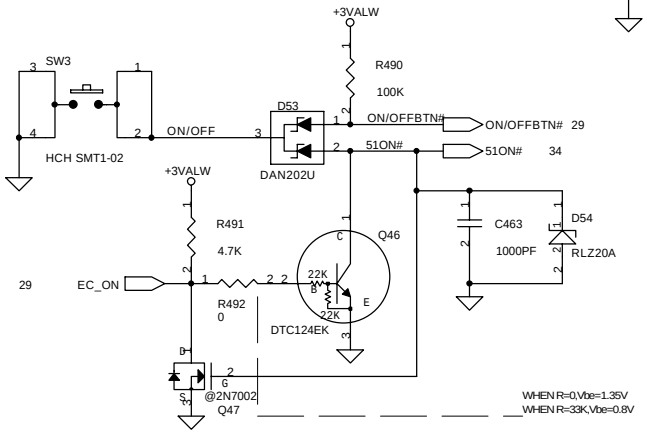


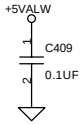


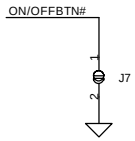




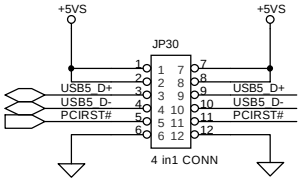
ON/OFF BUTTON

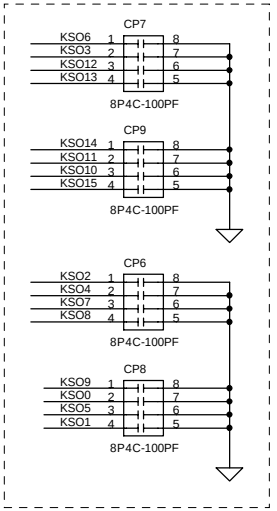






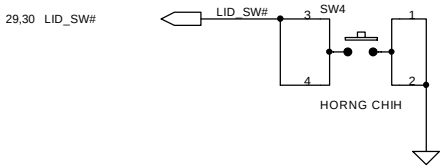
4 IN 1 CONN





NEED CLOSEST JP18 ADD BY EMI REQUEST

LID SW



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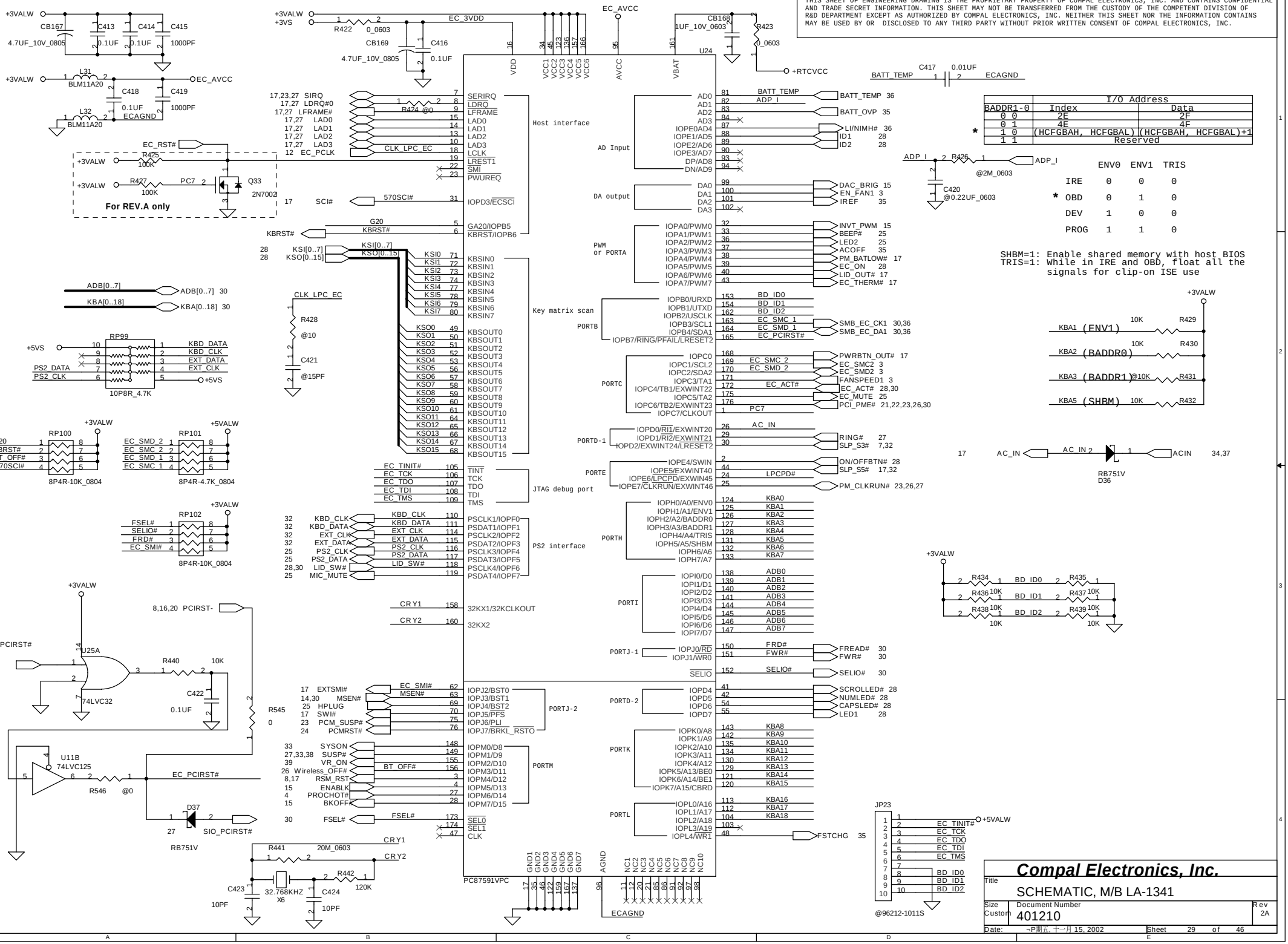
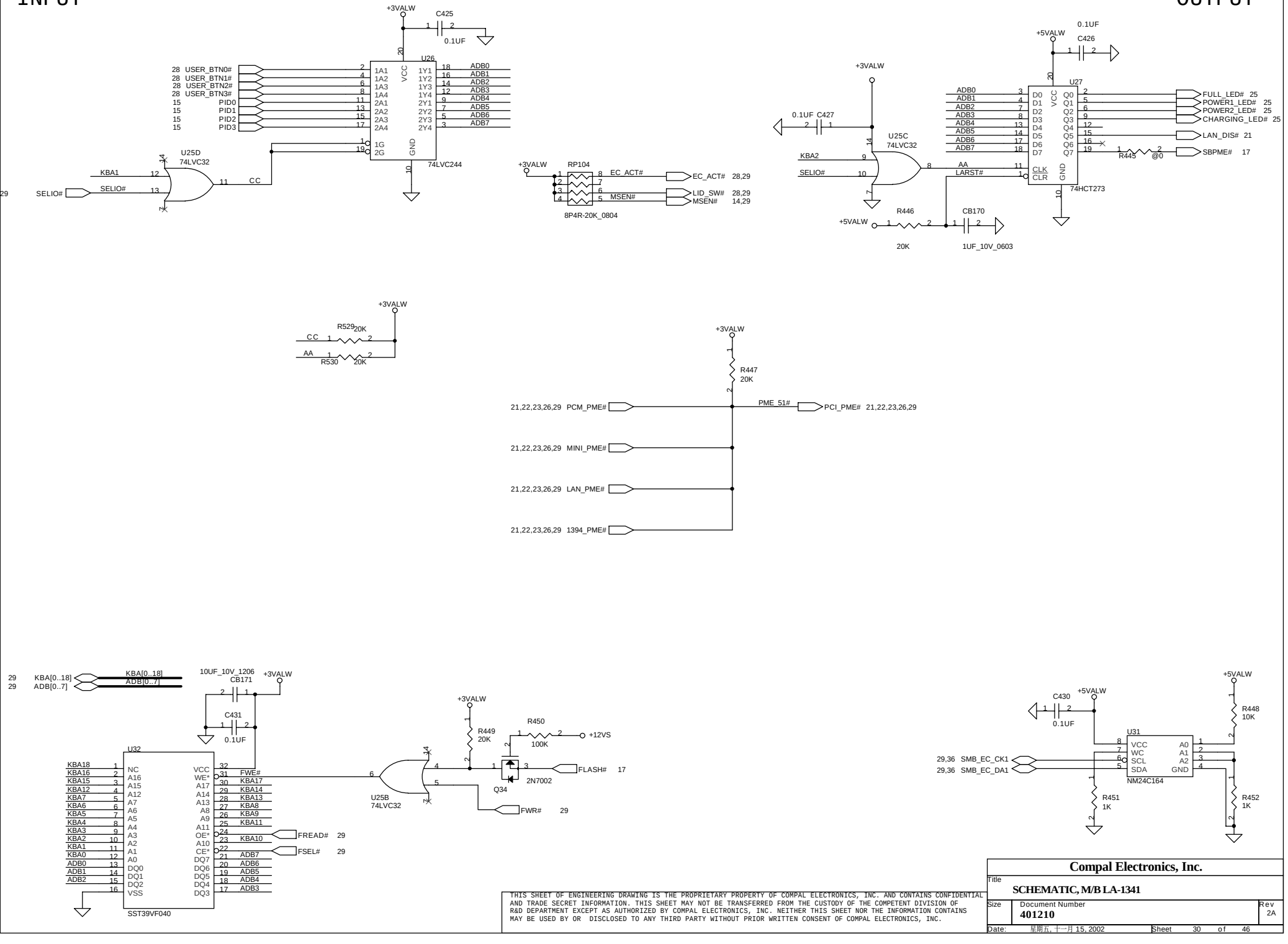


INPUT

OUTPUT



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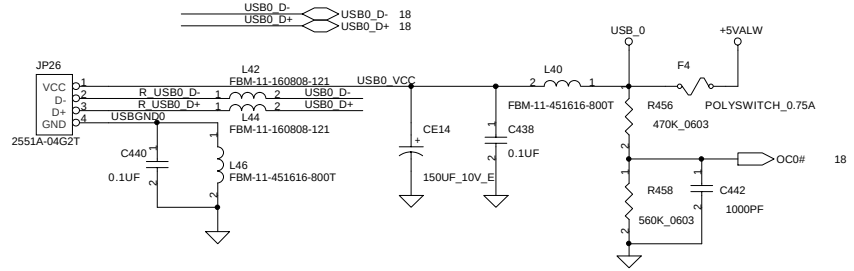
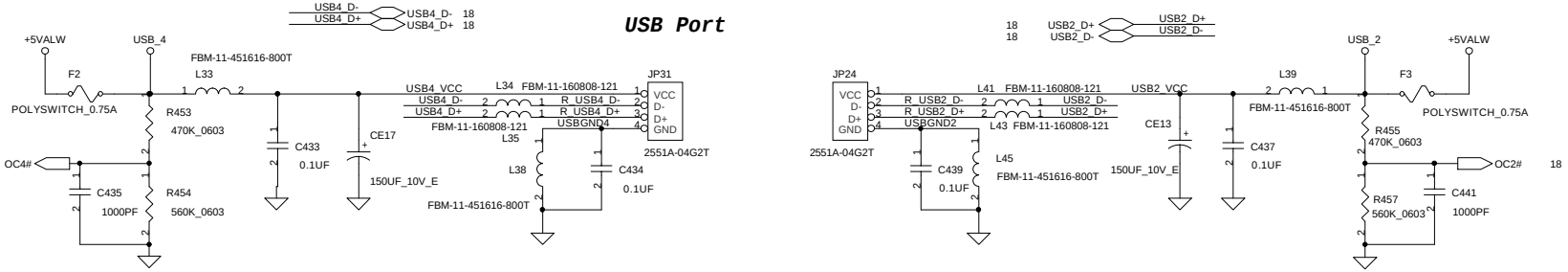
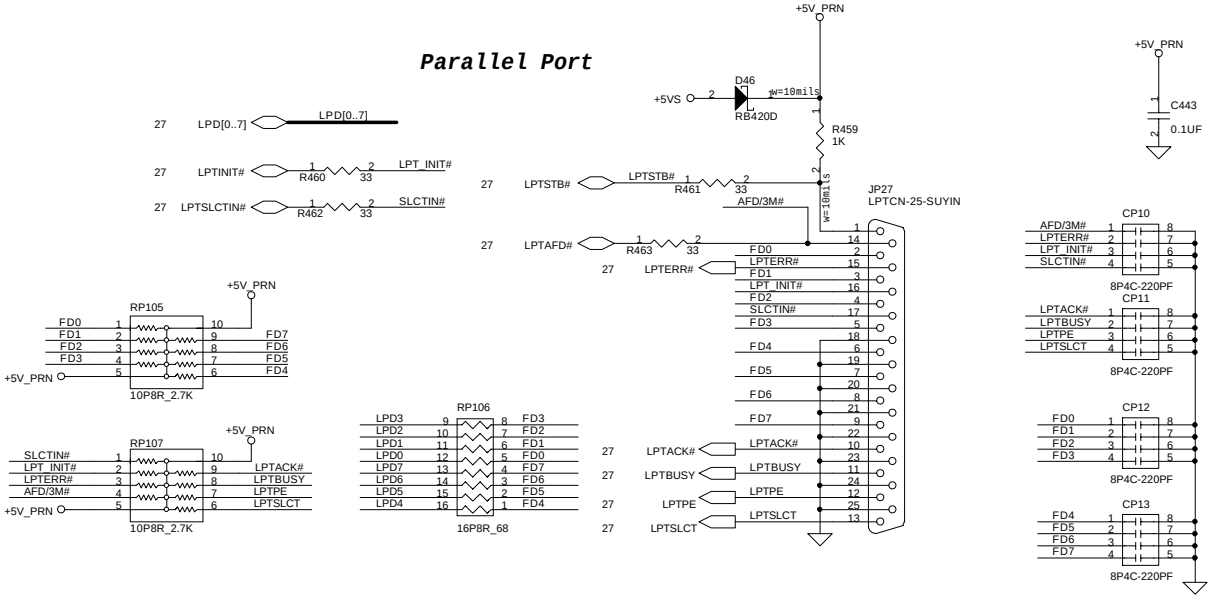
Printer Port/USB



USB Port



Parallel Port



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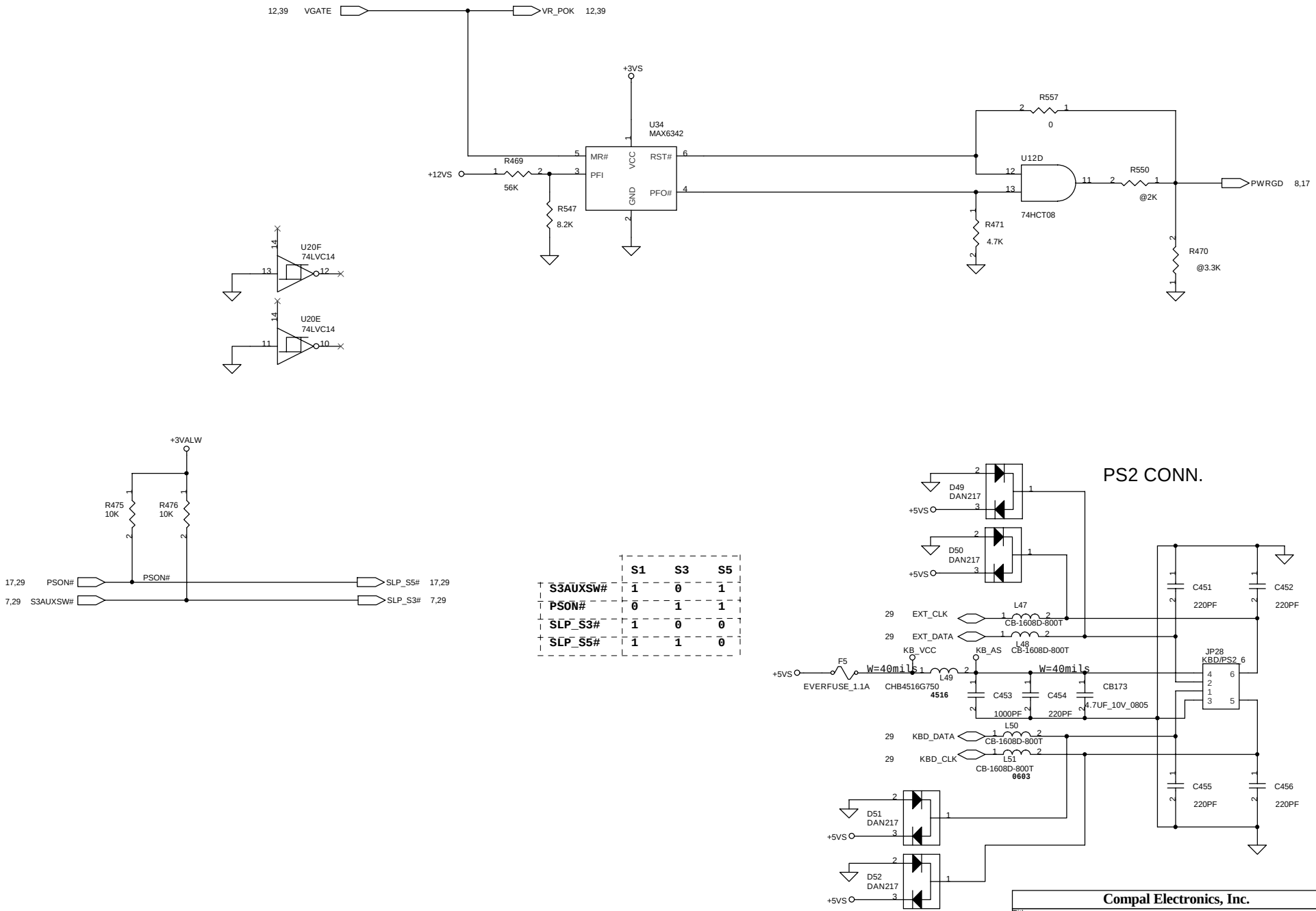

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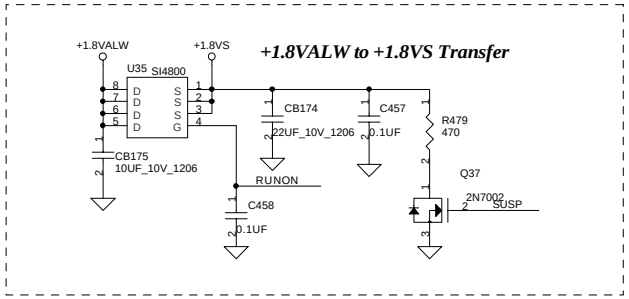
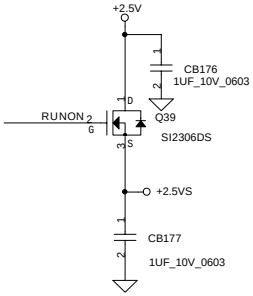
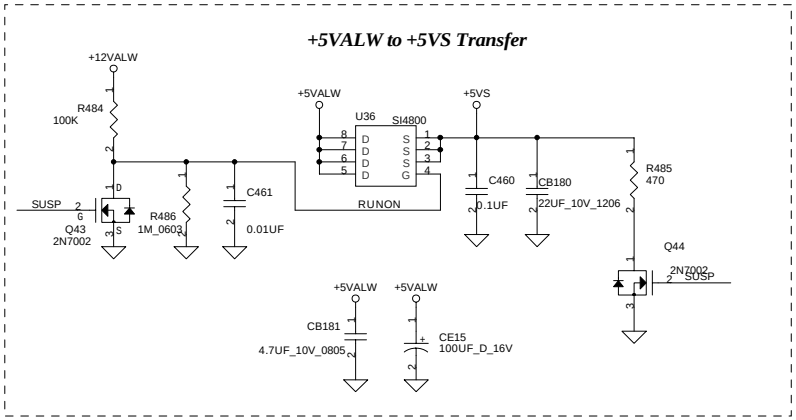
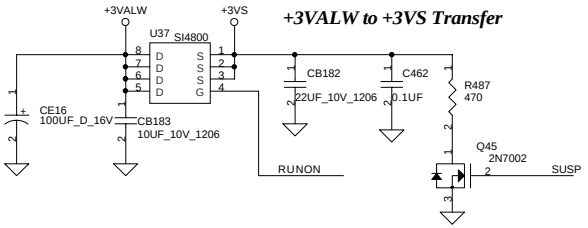
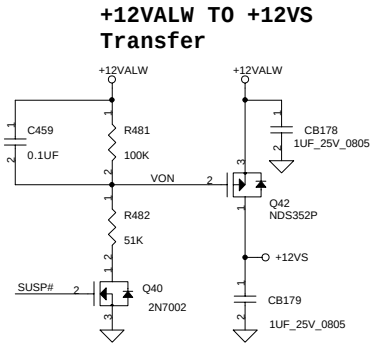


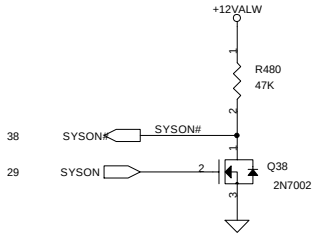


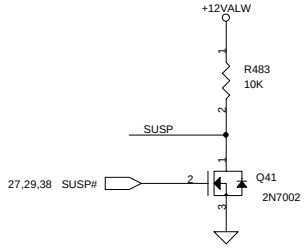




1.8VALW/+1.5VS Power direct provide







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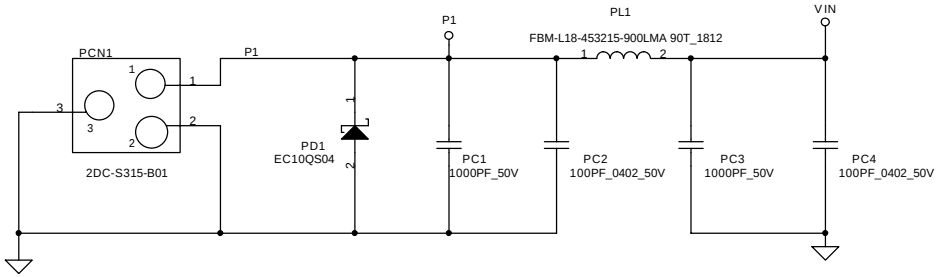
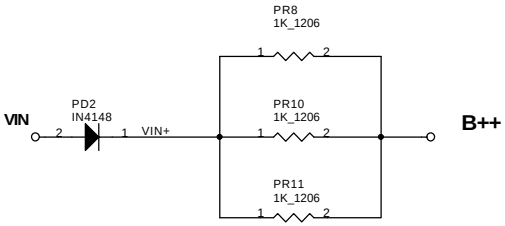
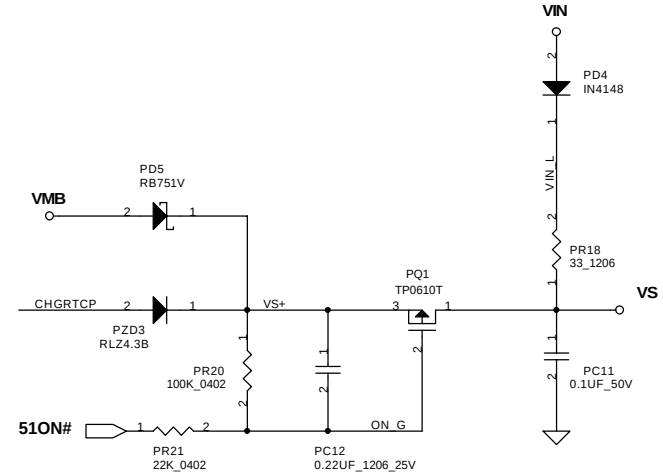
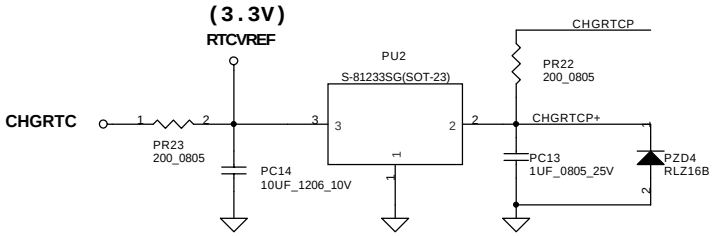
Vin Detector

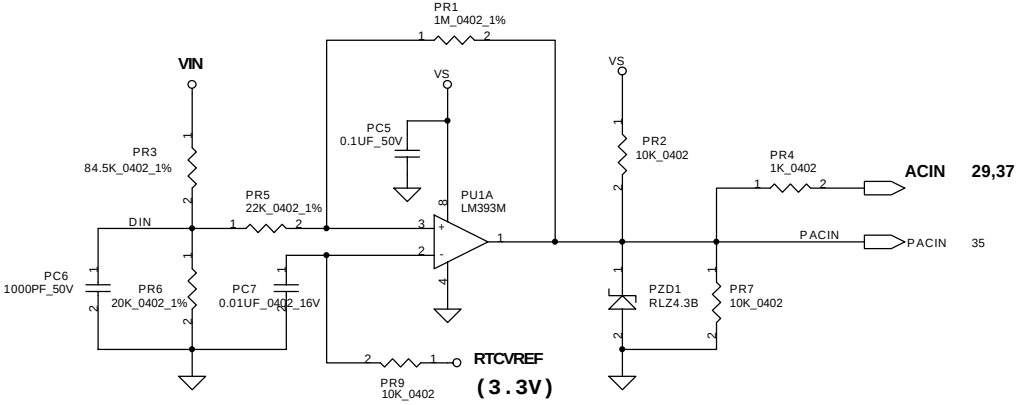
High	**18.784**	**17.901**	**17.077** V
Low	**17.877**	**17.043**	**16.195** V

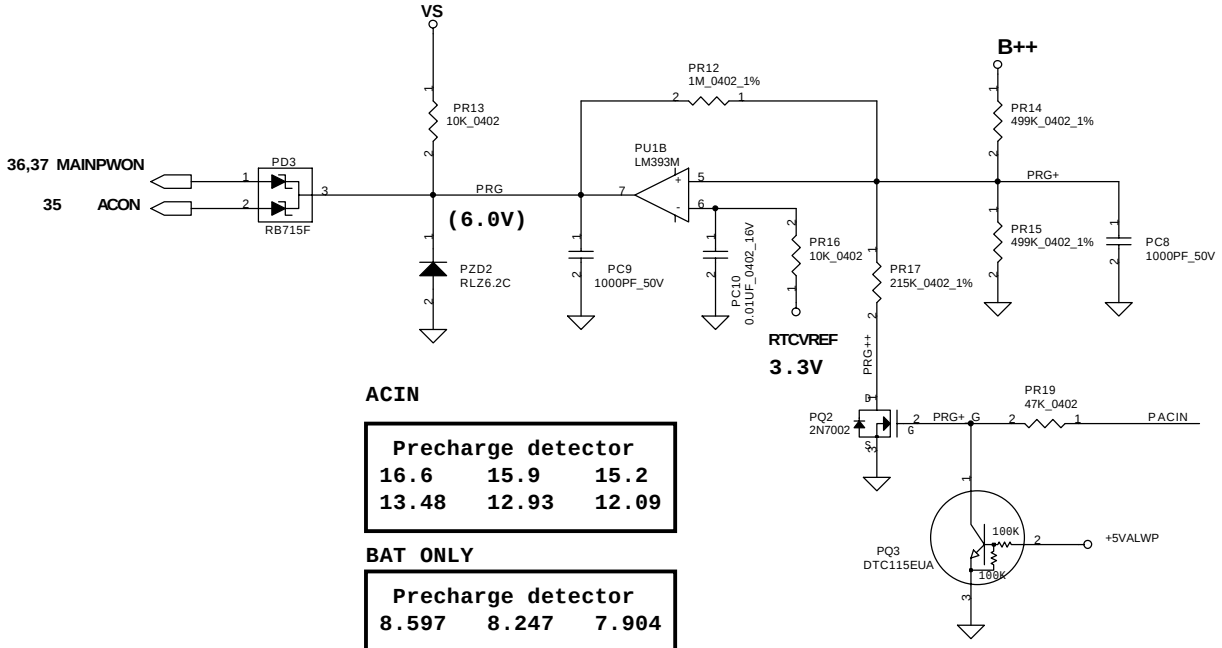












ACIN

Precharge detector		
16.6	15.9	15.2
13.48	12.93	12.09

BAT ONLY

Precharge detector		
8.597	8.247	7.904
6.310	6.101	5.683

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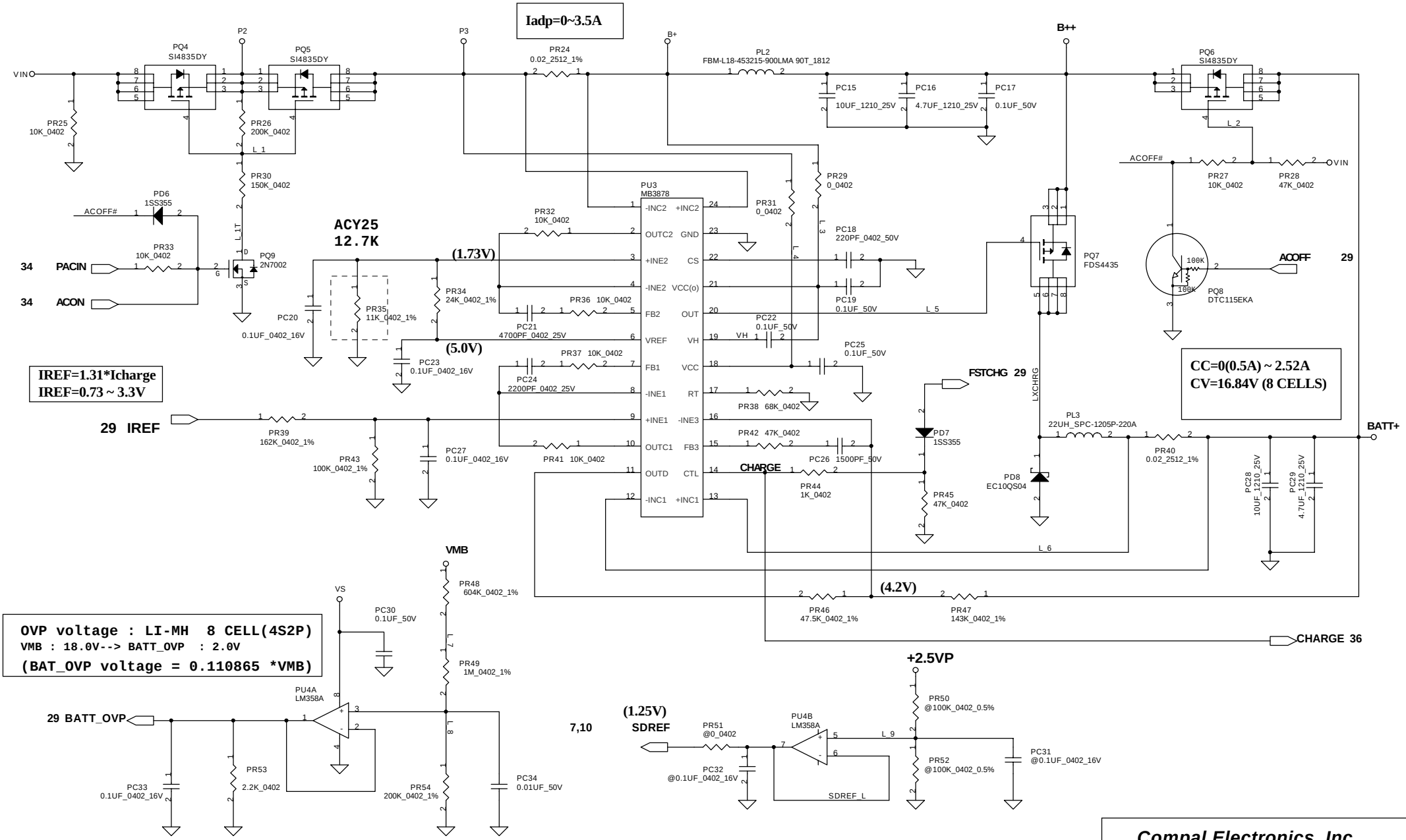

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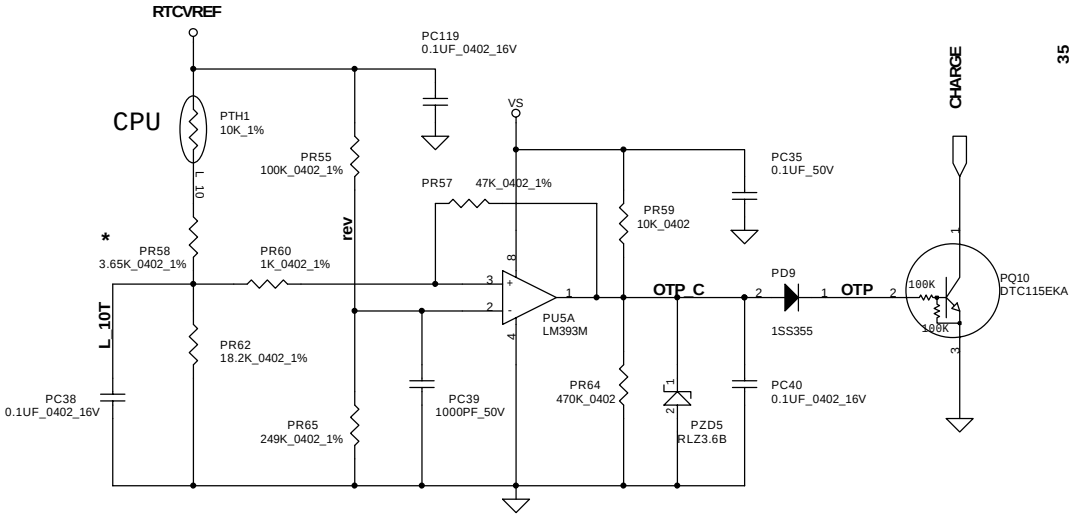
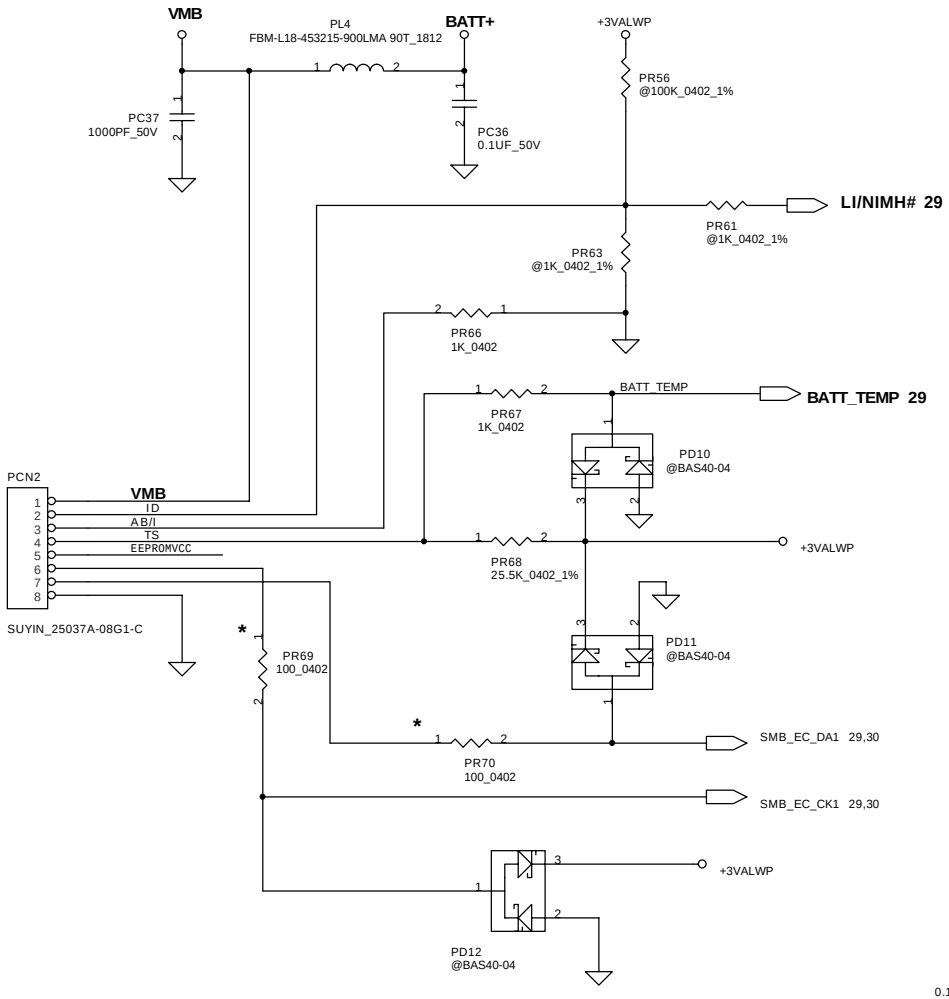
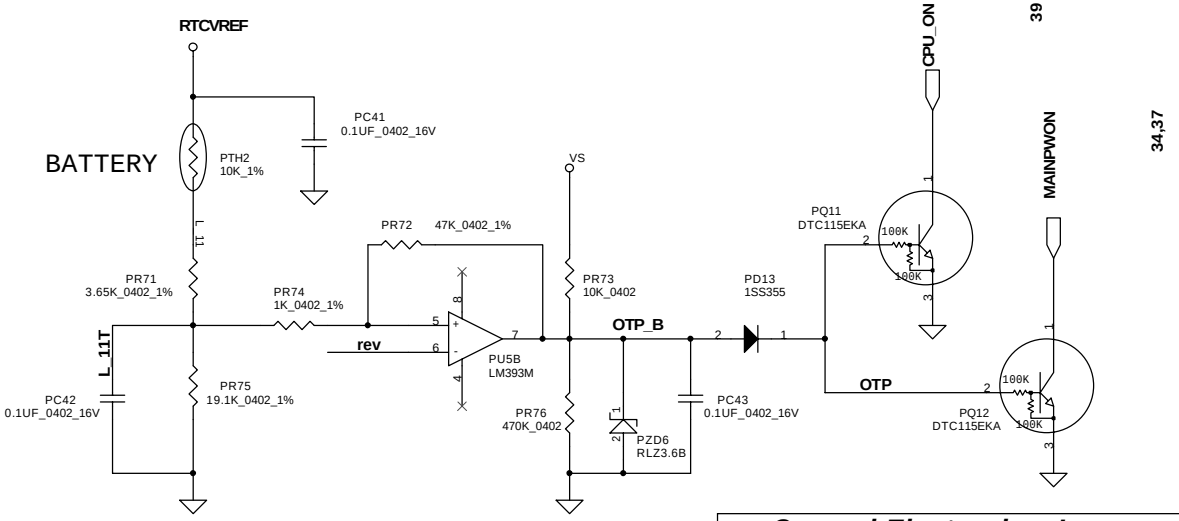
PJP1 3MM: +5VALWP — +5VALW
PJP2 2MM: +12VALWP — +12VALW
PJP3 4MM: +2.5VP — +2.5V
PJP4 3MM: +3VALWP — +3VALW
PJP5 2MM: +1.8VALWP — +1.8VALW
PJP6 3MM: +1.25VP — +1.25VS

PH1 under CPU botten side :
CPU thermal protection at 90(91)+-3 degree C
Recovery at 50(51)+-3 degree C





PH2 near main Battery CONN :
BAT. thermal protection at 85(86)+-3 degree C
Recovery at 50(51) degree C



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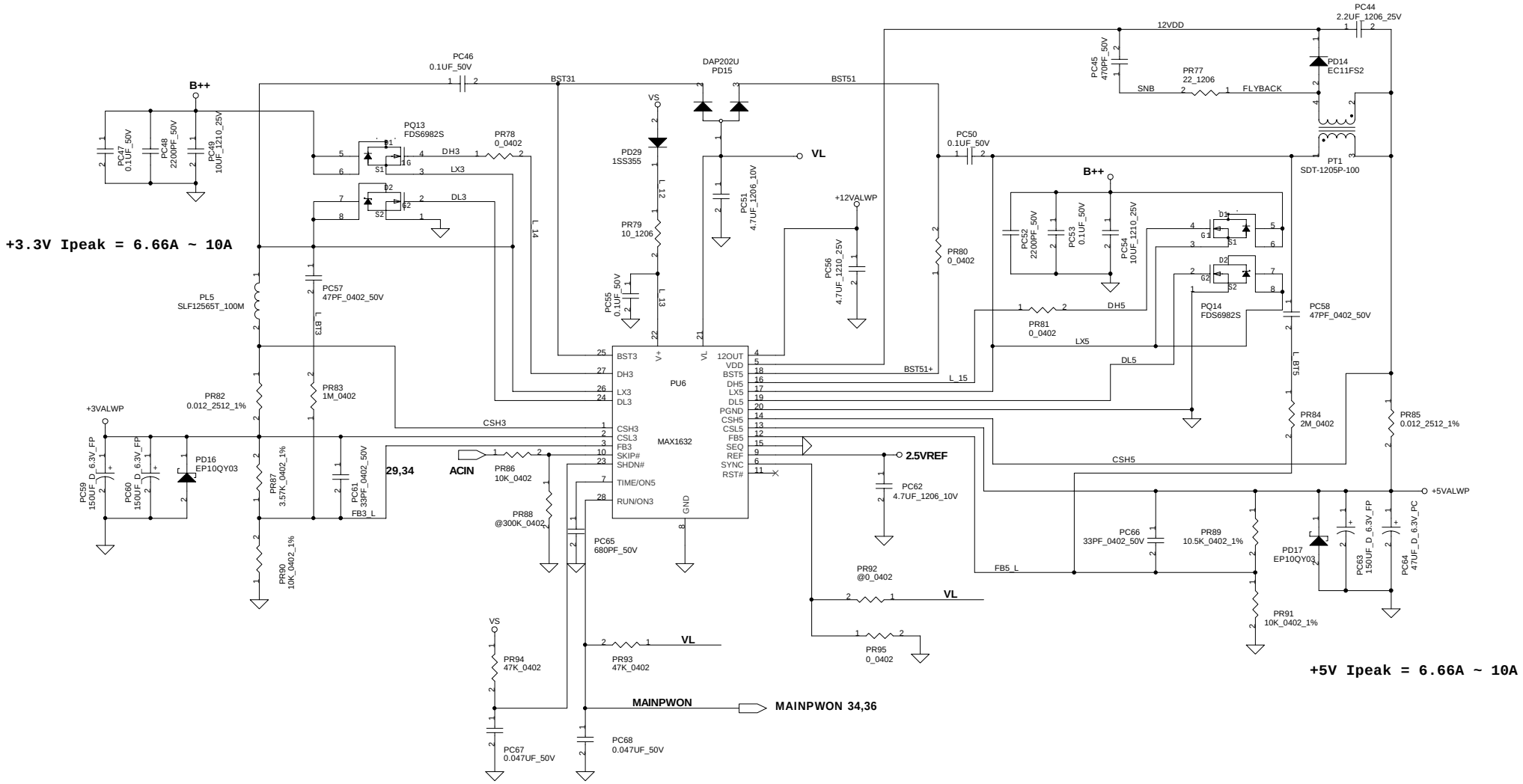

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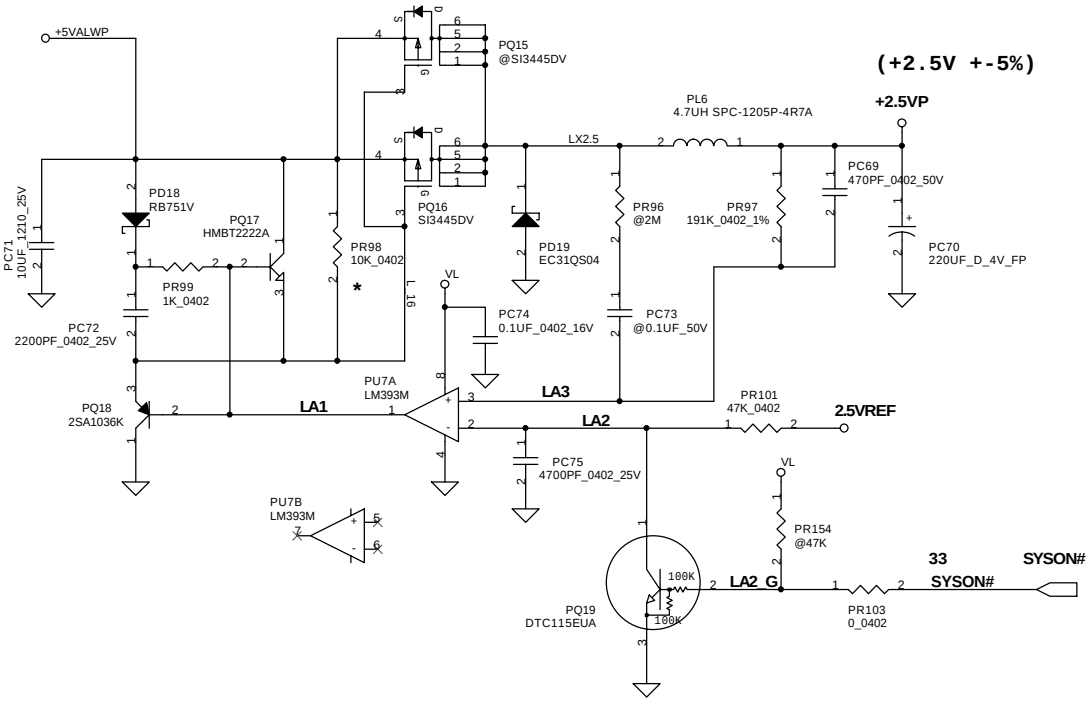
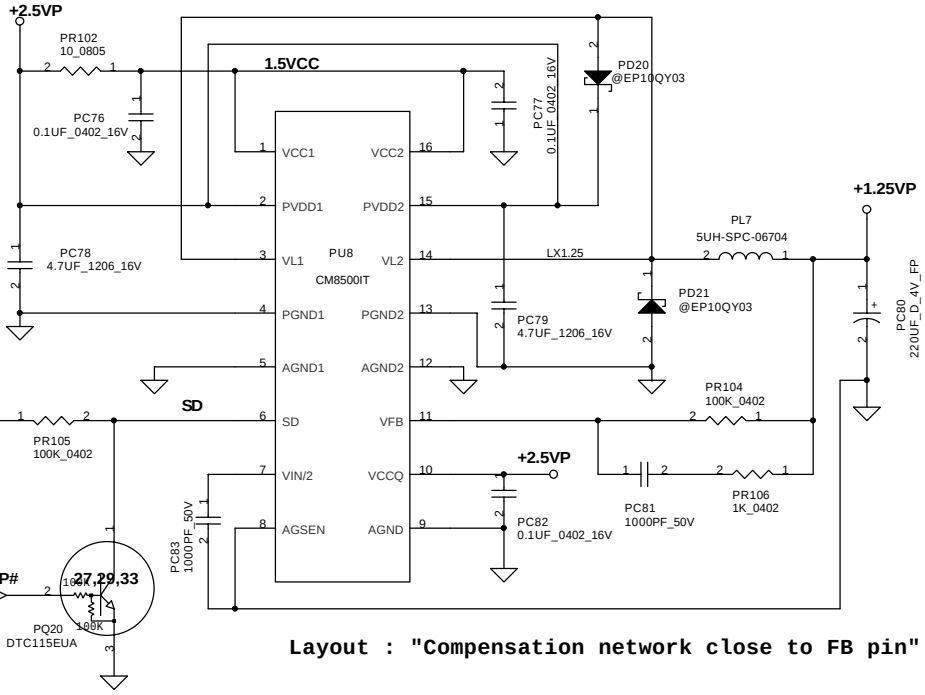
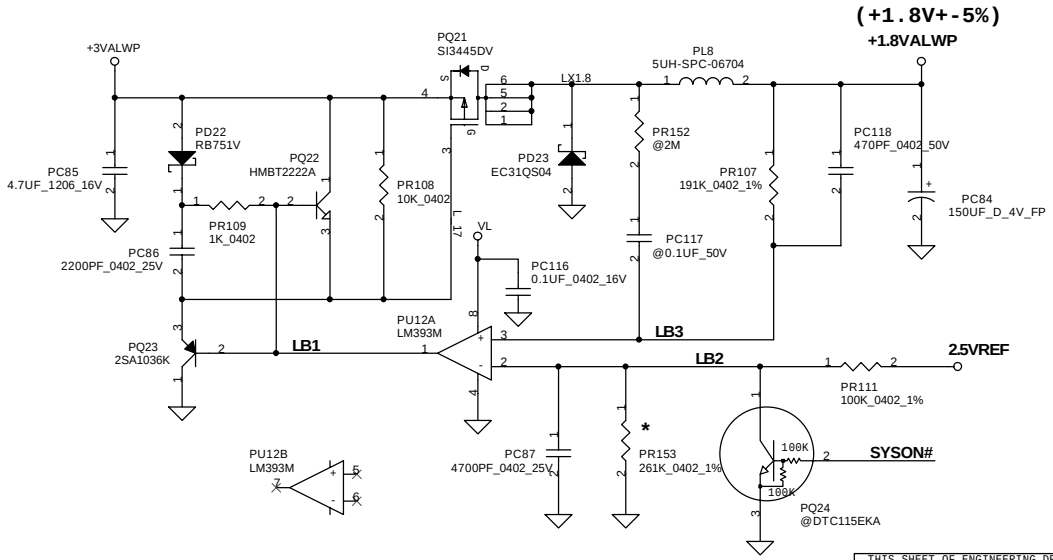




Layout : "Compensation network close to FB pin"



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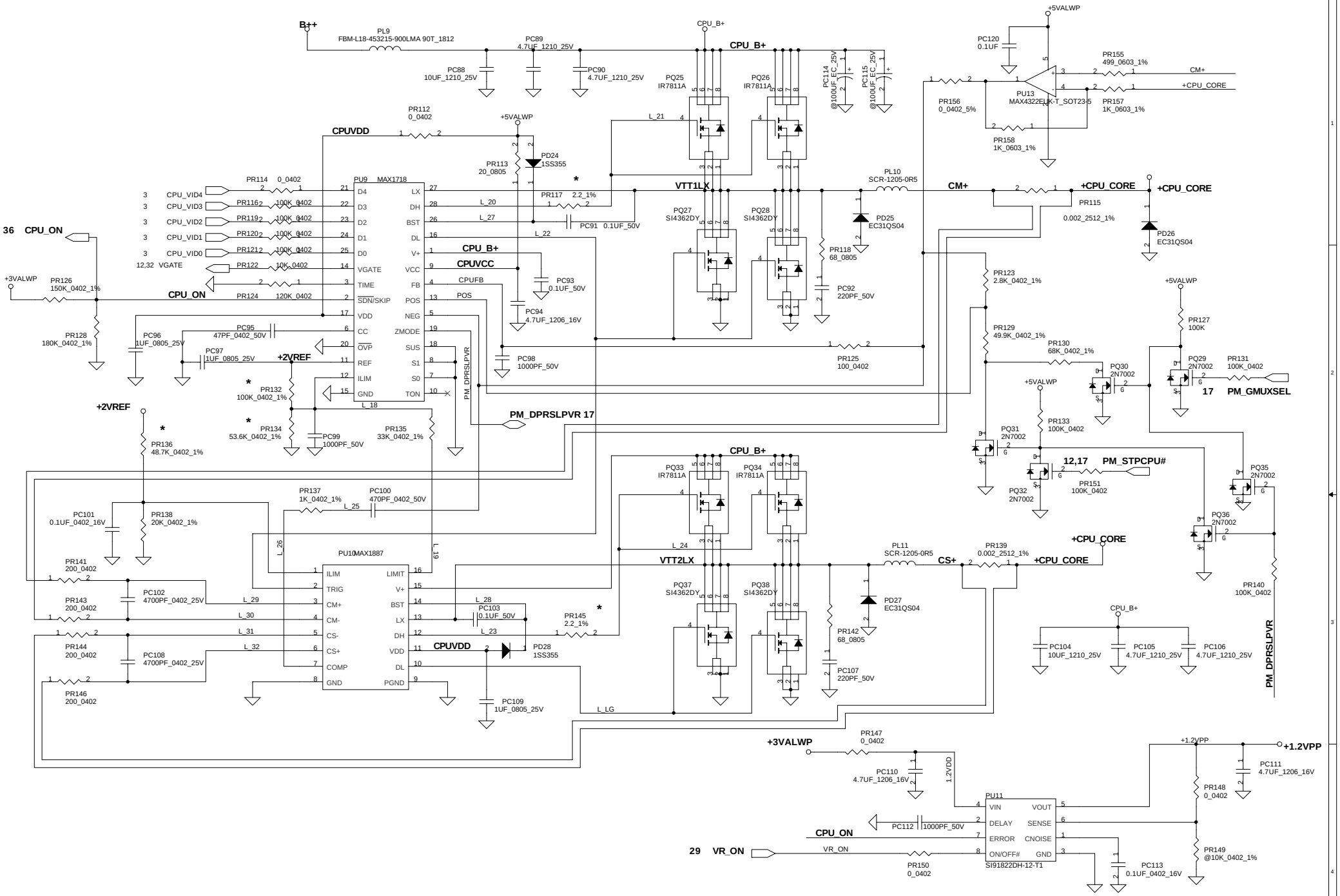

	GMUXSEL	STPCPU#	DPRSLPVR	VCORE'	Offset	VCORE
PM	1	1	0	1.30V	0%	1.30V
PM D-S	1	0	0		4.62%	1.239V
BM	0	1	0	1.20V	2.0%	1.176V
BM D-S	0	0	0		4.62%	1.144V
Deeper	X	0	1	1.0V	0%	1.0V

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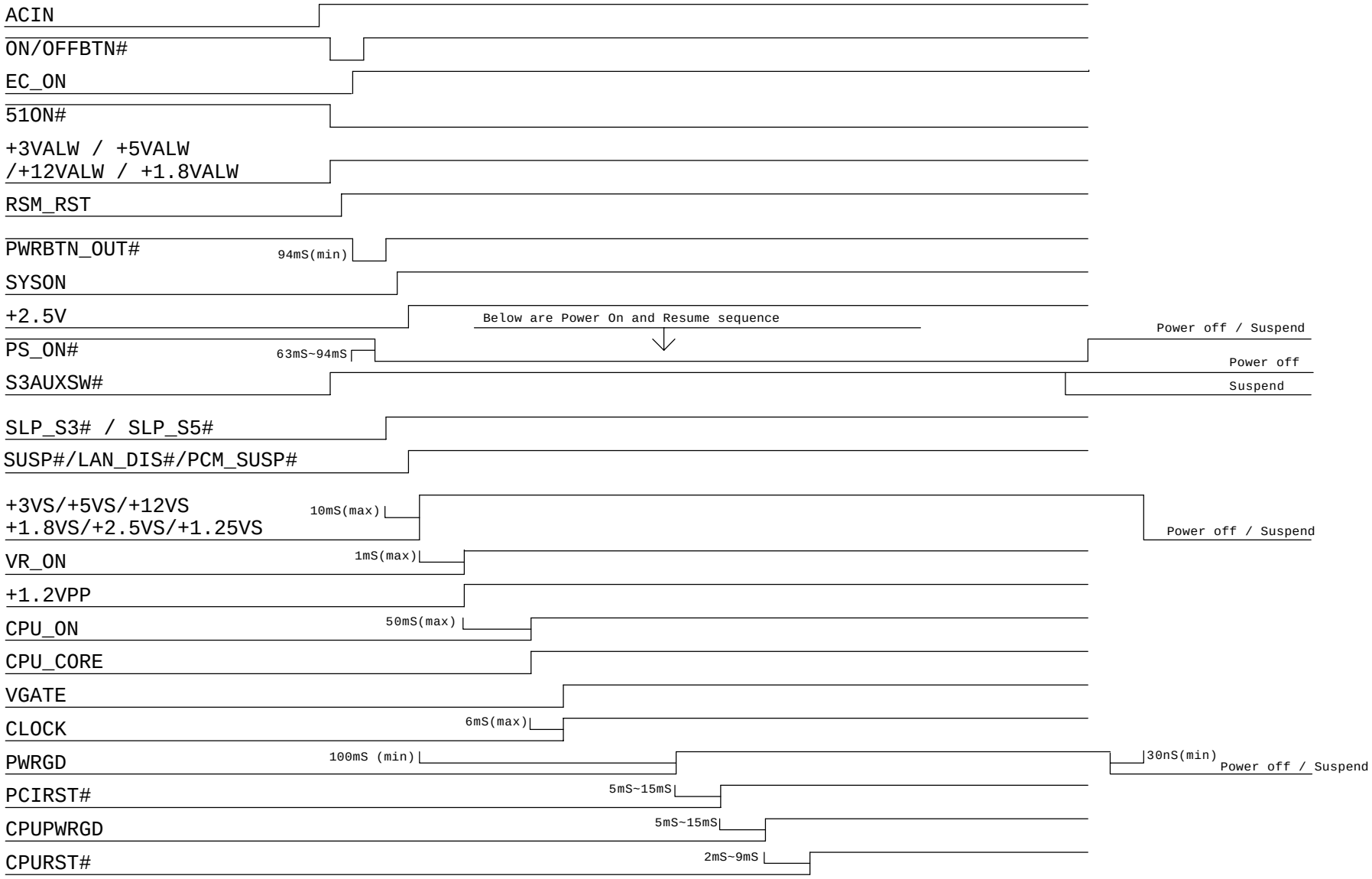
Power ON sequence

Power Off sequence



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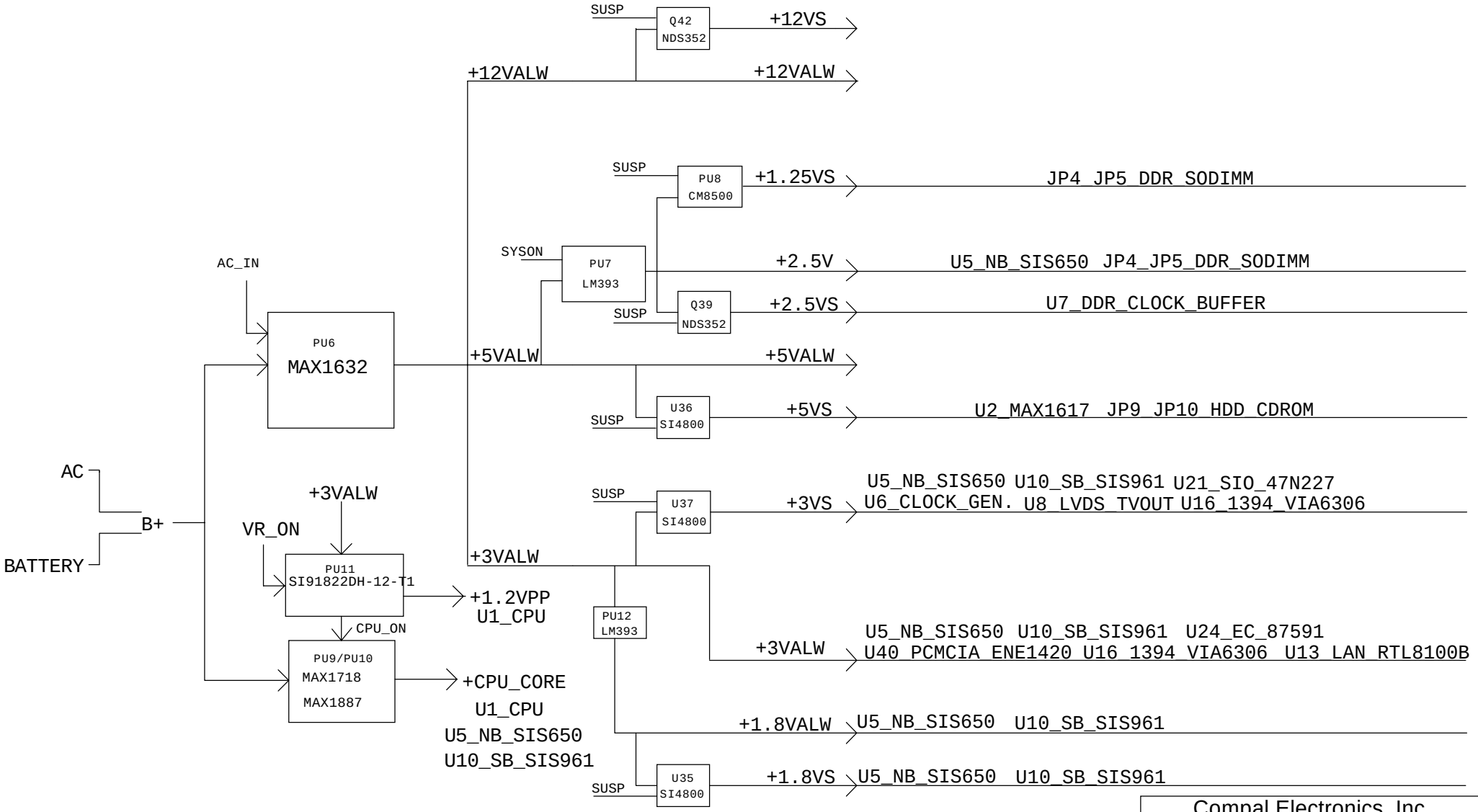
POWER SOURCE BLOCK DIAGRAM



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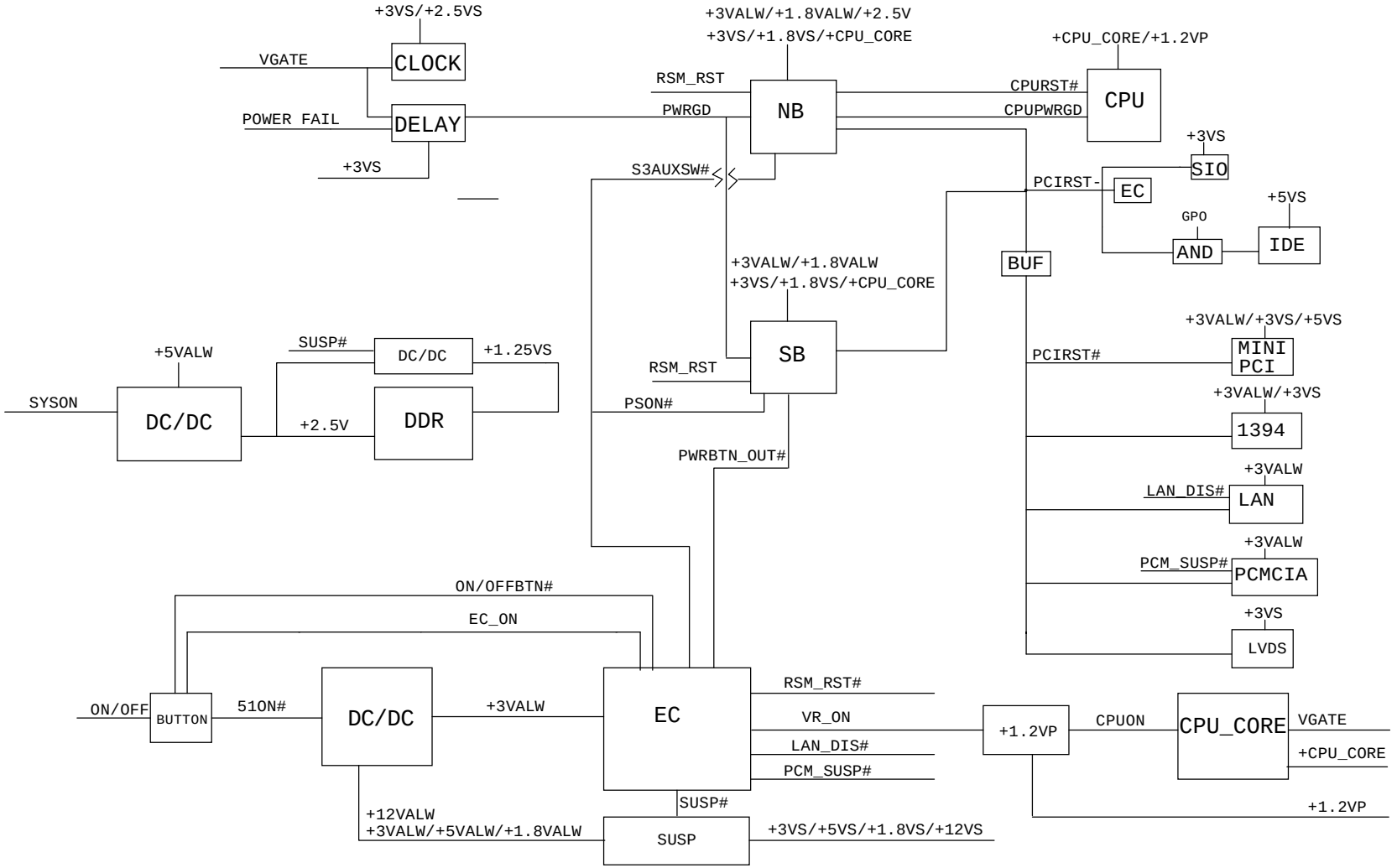
ACY25 RESET MAP DIAGRAM



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Power PIR

Item	Fixed Issue	Reason for change	Page	Modify item	MB_Ver.	Phase
1	NO	Improve CPU_CORE voltage regulator.	39		0.3	C test
2						
3						
4						
5						
6						
7						
8						
9						
10						
11						
12						
13						
14						
15						
16						
17						
18						

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UNIT	IDSEL	IRQ	GNT	REQ
1394 U16	AD16	PIRQC#	GNT#0	REQ#0
MINIPCI JP16	AD31	PIRQB#	GNT#1	REQ#1
LAN U13	AD17	PIRQB#	GNT#3	REQ#3
CARDBUS U40	AD20	PIRQA/B#	GNT#2	REQ#2

SMBUS from EC

EC_SMD2,EC_SMC2 : U2 (MAX1617) AD : 1001 110

SMB_EC_CK1,SMB_EC_DA1: U31(EEPROM) , PCN2 (BATT CONN)

SMBUS from SIS961

SMBCLK , SMBDAT: JP5 , JP4 , CLK GEN (U6) , CLK BUF(U7).

ACY25 A-TEST Modify

Change to 310LV : Add R64,R73,R76. Del R77,R78,Q49,Q50,R539

Add DPSLP# : Add Q49 ,R551

DPRSLPVR : Change to pull Low =Del R536 Add R284 ; Add U41 ,R554.

AUXOK : Del CB112

IDERST# : Add D56 , R548

LAN : VDD2.5 add L27,C294,C297,C299

1394 : +3VA change to +3VS power , R351,R352,R353,R3354 change to 54.9_1% , R355 change to 5.1K_1% , C337 change to .33UF , C338 change to 270PF

KB : Add R552 to disconnect ON/OFF

PSON# : Connect to SLP_S5# directly. Del U33

S3AUXSW# : Connect to SLP_S3# directly

PWRGD : Add R550 transfer 5V to 3V

+2.5V : Q39 using SI2306 to transfer +2.5VS

H_DPSLP# : Add Q49 , R551 to connect with PM_STPCPU#

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CDLED# : Add R553 to disconnect with JP10

USER_BTN3# : Add R552 to disconnect with JP18,R555 pull high to +3VALW

DDR termination : RP32~RP57 , R494~R508 chanage to 33 ohm

PWRGD : Del C42 , C256

SUSP# : R487 change to 75 ohm

FOR C TEST

LED1 : Q48 PIN3 CHANGE TO +3VALW

FWDSDCLKO : Add pull high resistor R556

PWRGD : Add pass through resistor R557

CRTVDD : R170,R171 change from 2.2K to 1K

GPIO19,20 : Add pull high resistor R566,R567 , del R269

EC_ON : Del Q47. R492 change to 0 ohm

EN_FAN1 : Add U42 , C483 , C484 , R561 ,R562

CLKFS2 :Del R161 , Add R565

AUXOK : Del D18 , R254

For MP

+5VFAN1 : Add D58 for reserve

S1_RST# ,S2_OE# , S2_RST# , S2_OE# :Add R568,R569,R570,R571 for reserve

LAN_DIS# : Del R333 , add D57 for reserve

MINI PCI : Change to REQ#4 & GNT#4

For MP 5/3

R248 change to 47K , R251 change to 15K

For MP 6/6

R278 change from 0 to 47 ohm

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