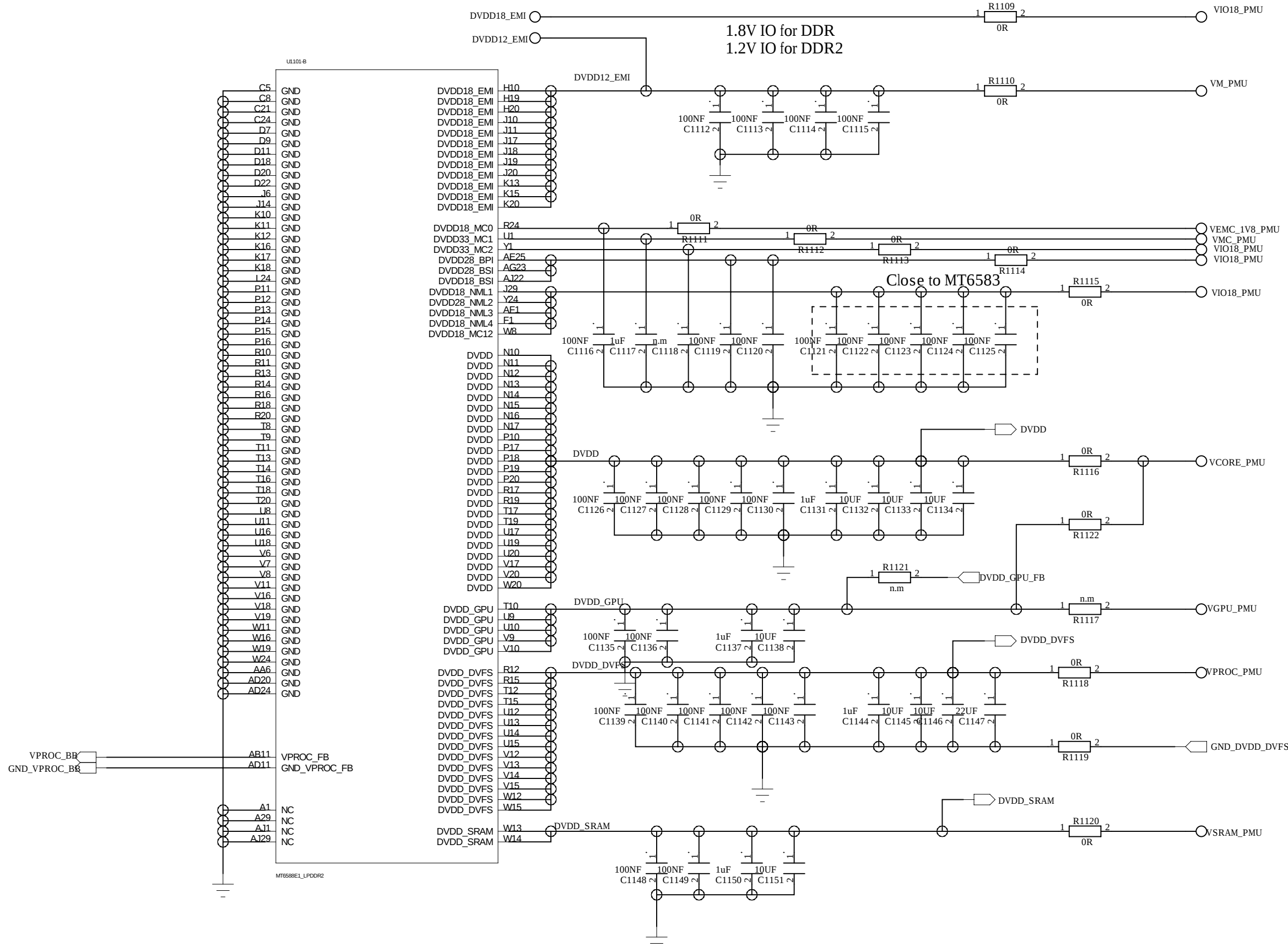
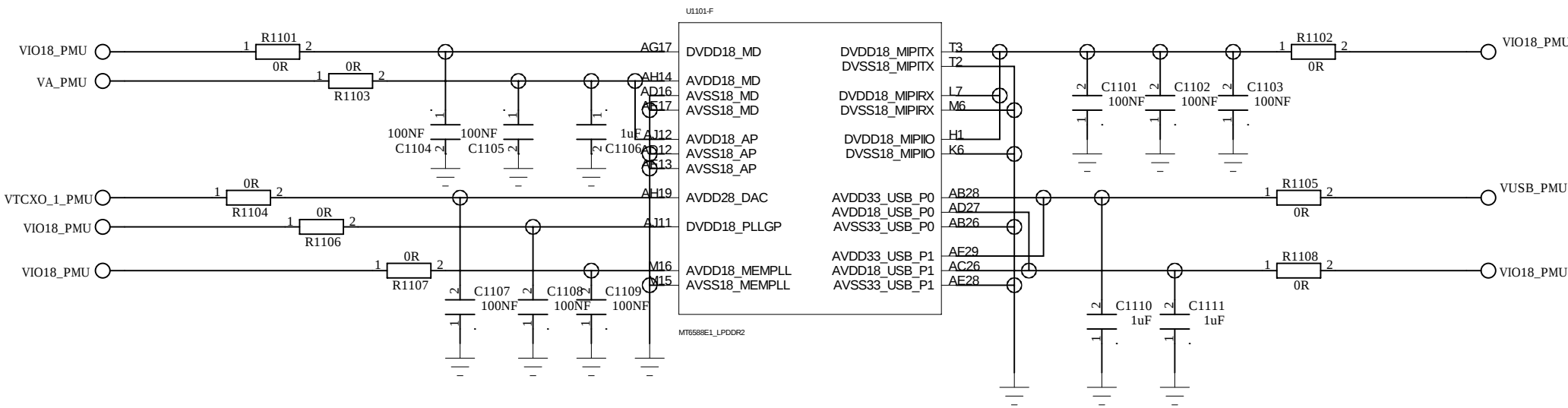
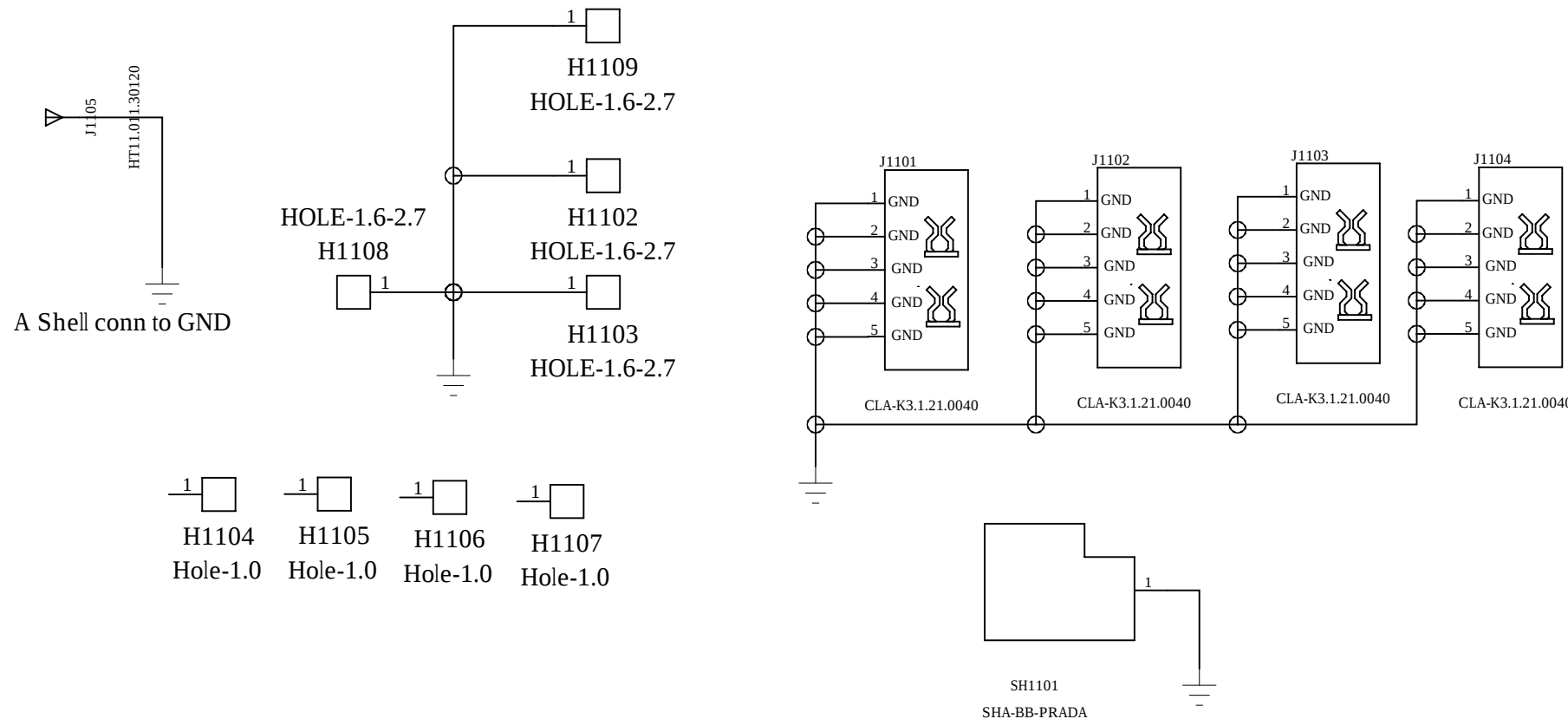


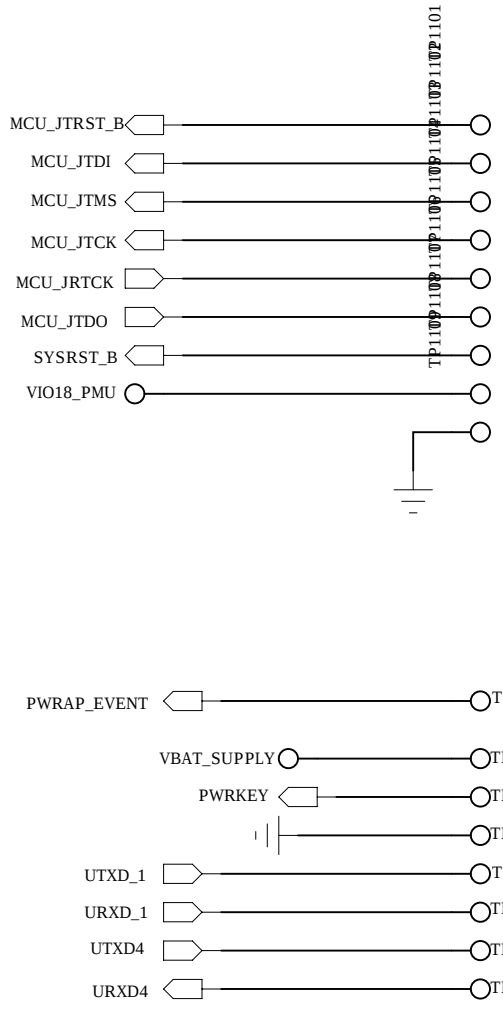
REVISION RECORD			
LTR	ECO NO:	APPROVE	DATE



MT6588 Power



* Spring & Hole *

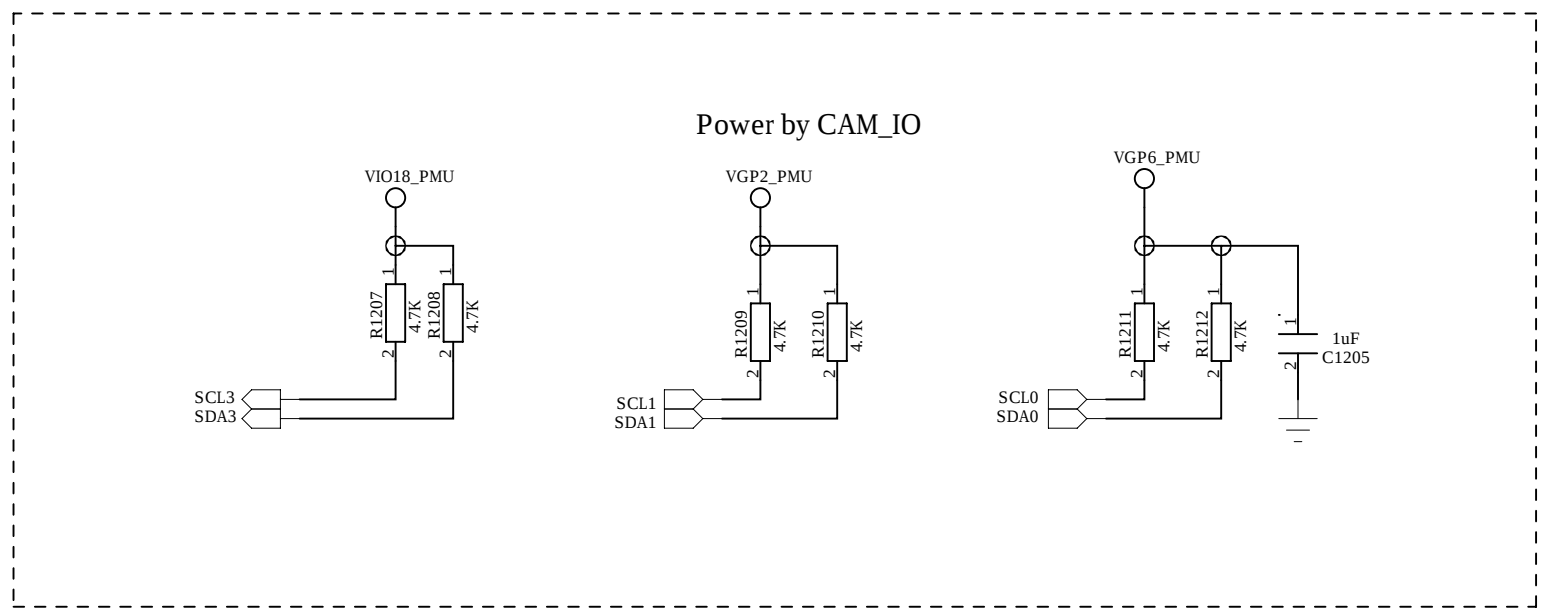
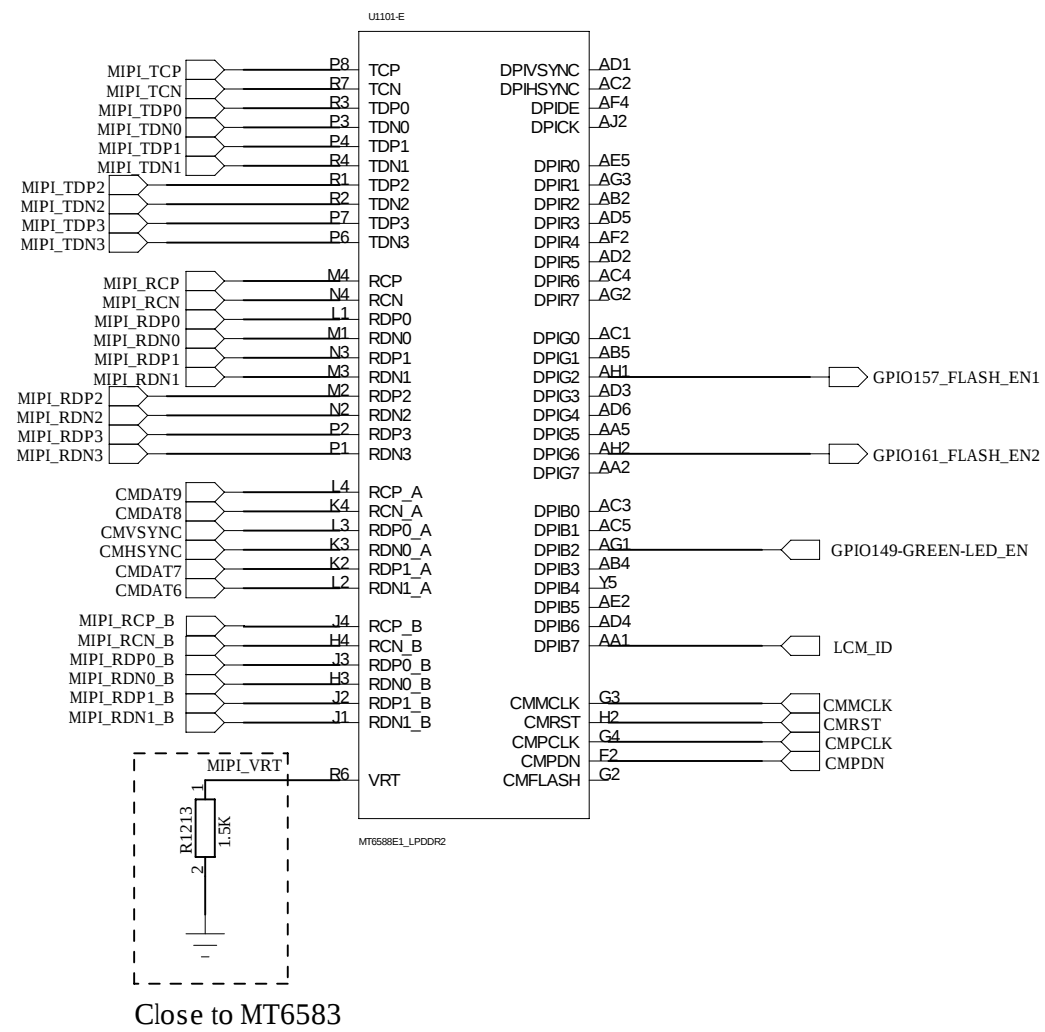
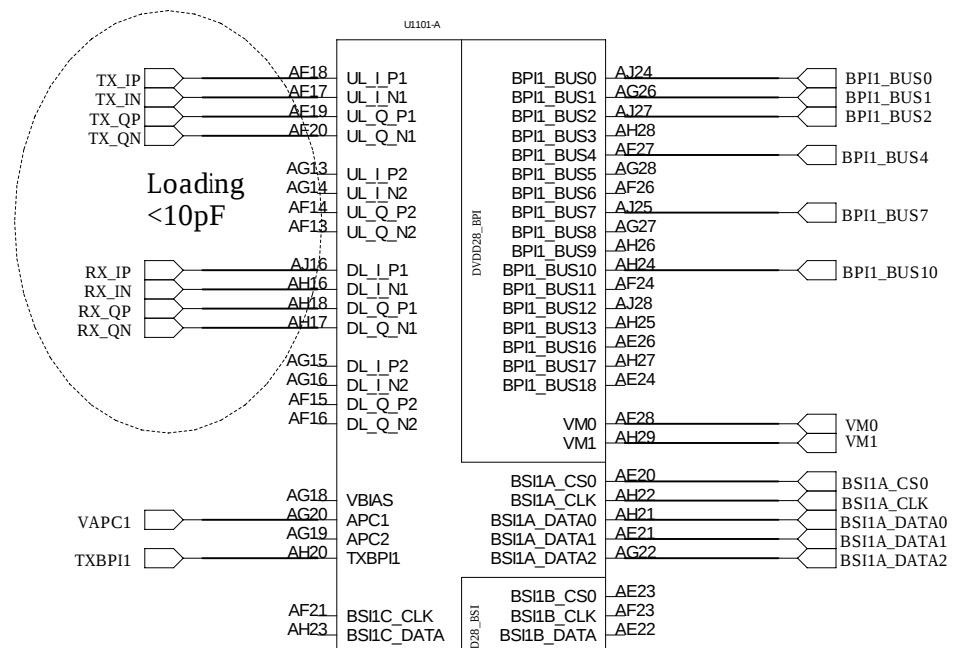
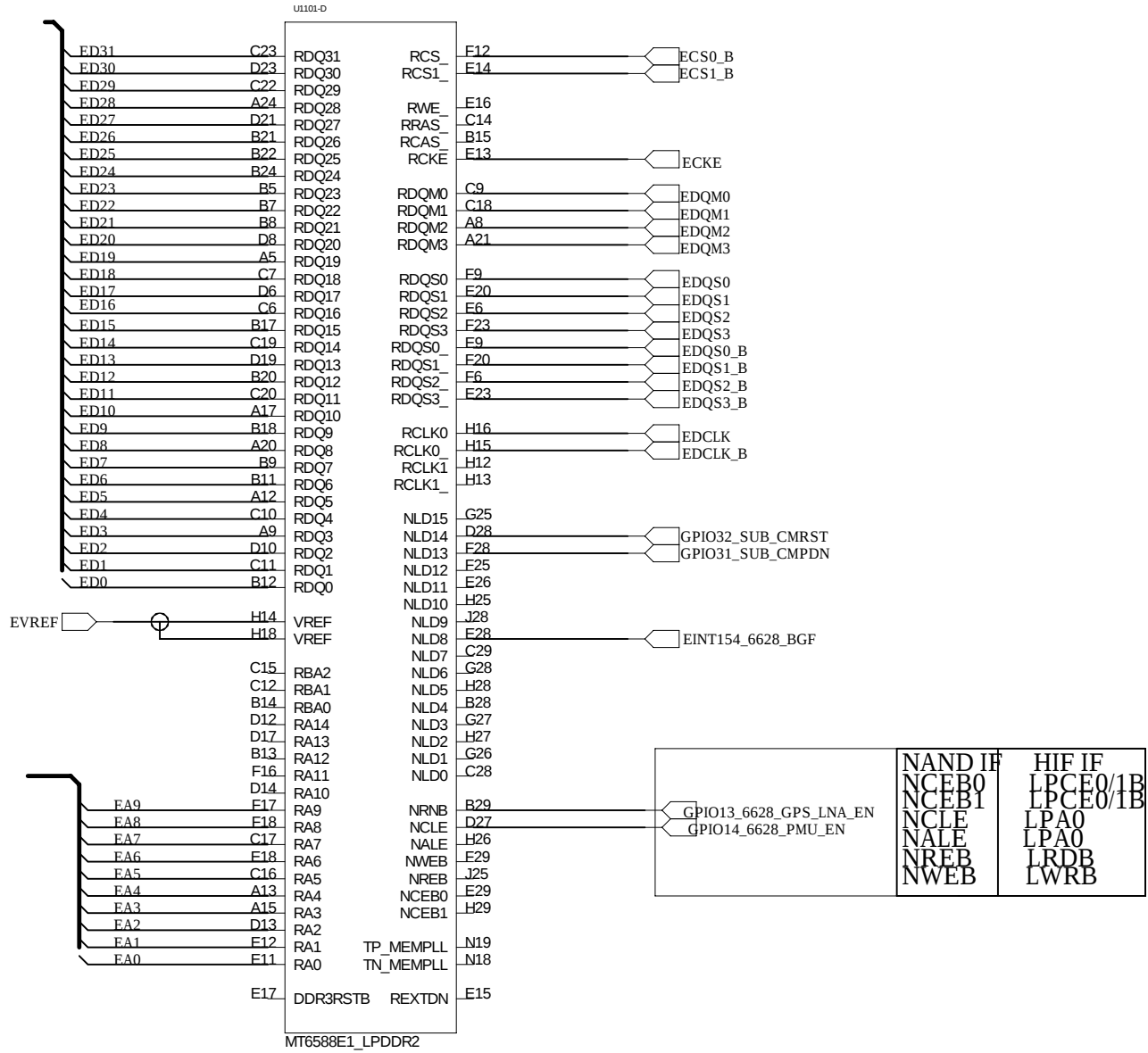
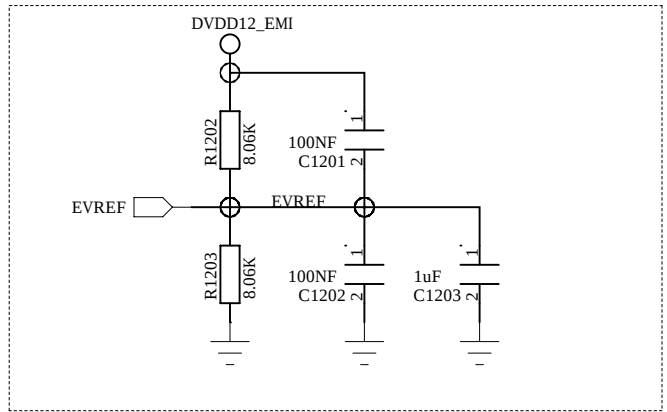
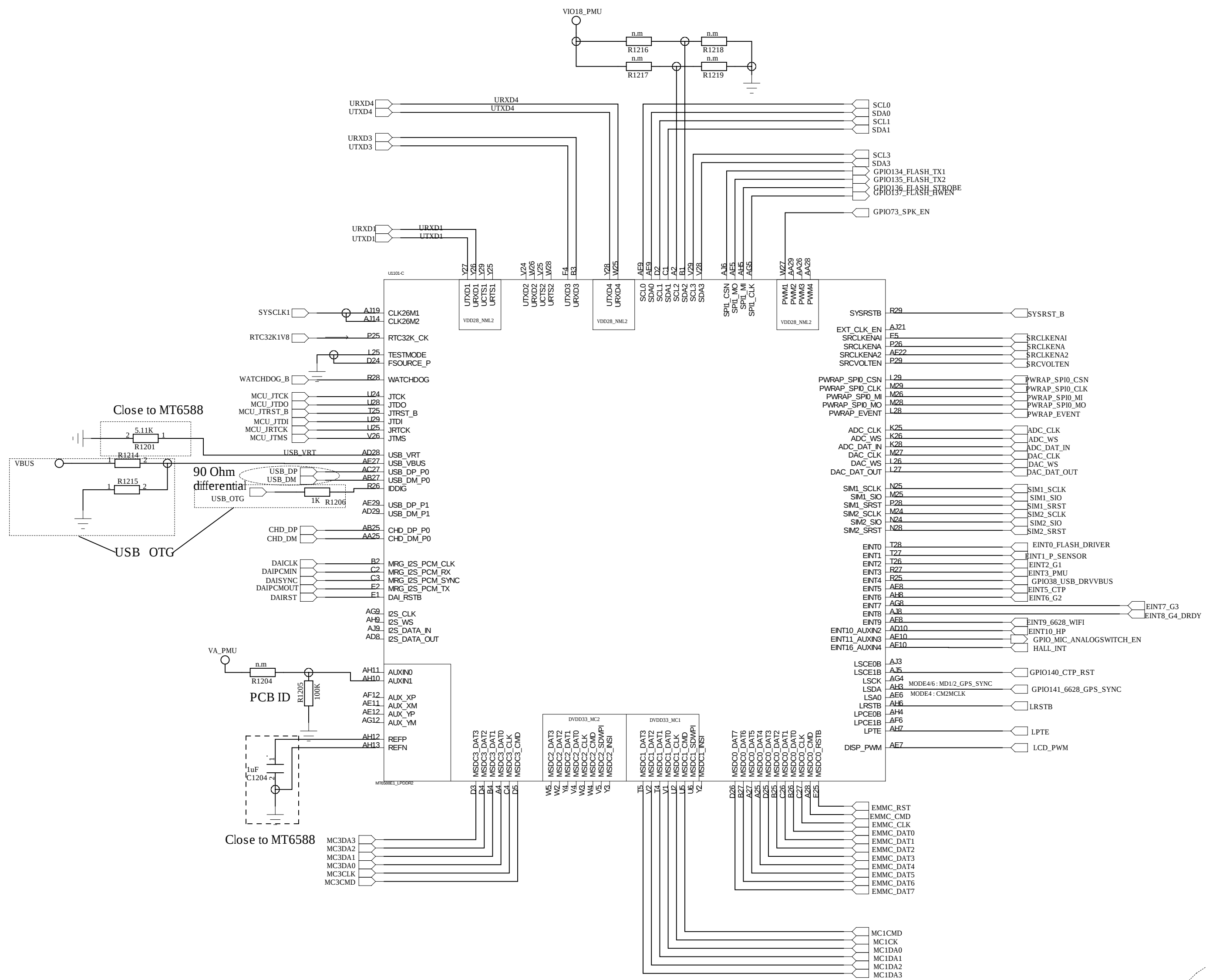


Trace32 Debug port

MP Test Points

COMPANY: Lenovo MIDH_Phone					
TITLE: Prada_MB_H101					
DRAWN: Chenqm2	DATED: 20120828	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED: <Checked By>	DATED: <Checked Date>	<Code>	D	11_MT6588_POWER	H101
QUALITY CONTROL: <QC Date>	DATED: <QC Date>	SCALES			SHEET: OB0
RELEASED: <Released By>	DATED: <Release Date>				

REVISION RECORD			
LTR	ECO NO:	APPROVED	DATE

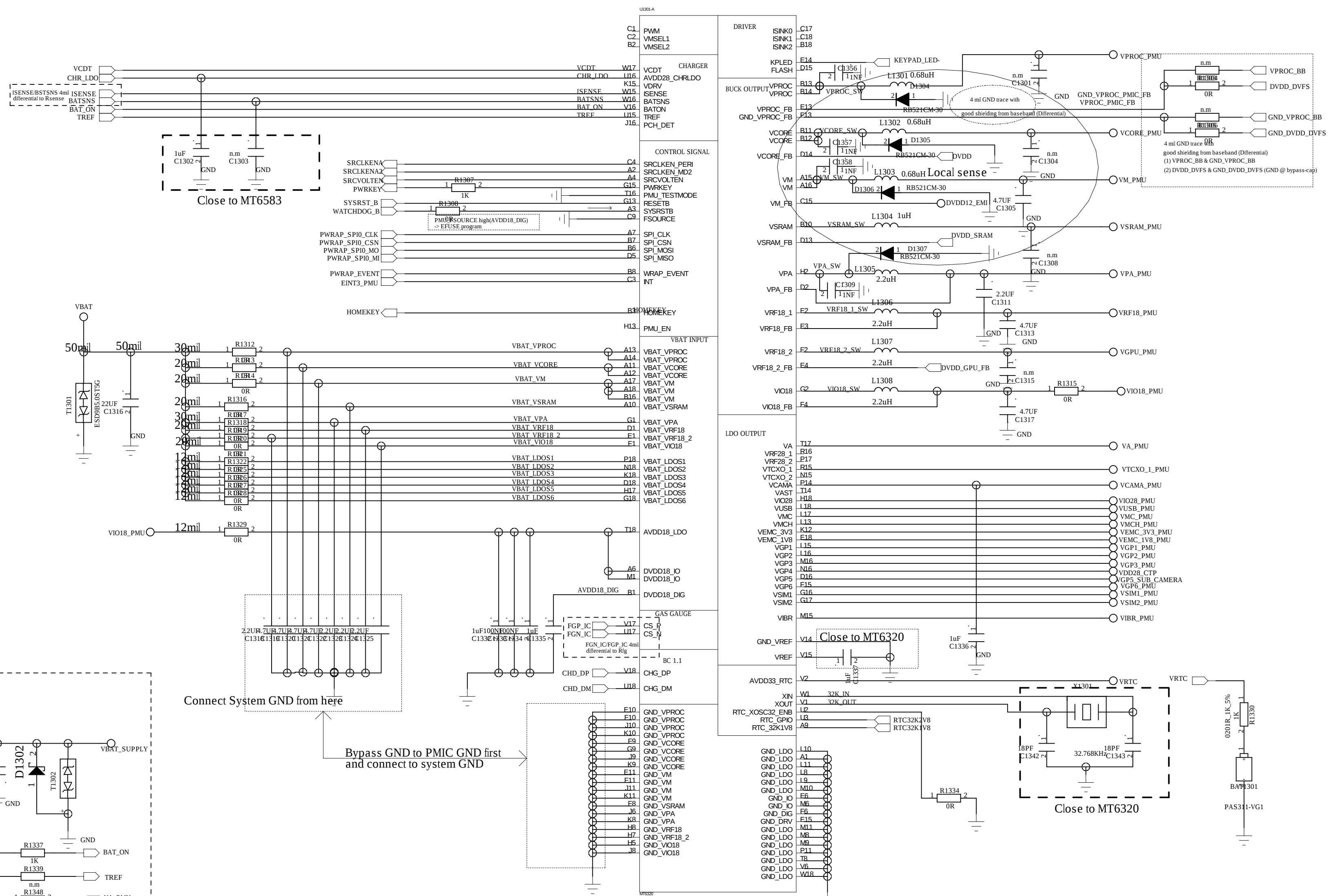


COMPANY:					Lenovo MIDH_Phone				
TITLE:									
Prada_MB_H101									
CODE:		SIZE:		DRAWING NO:				REV:	
<Code>		D		12_MT6588_BASEBAND				H101	
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DRAWN: Chenqm2	DATED: 20120828
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO:	APPROVE	DATE

Symbol	LPDDR2/1.2V	DDR3U/1.25V	DDR3U/1.35V	DDR3/1.5V
VM_SEL1	L	L	H	H
VM_SEL2	L	H	L	H



Digital

Audio

Charger

BAT. CONN.

Reserve layout for 4 pin BAT

	Symbol	Application	Vout (V)	Iout (mA)	Cap Value(μF)
Buck	VPROC	CPU	0.7~1.4 (DC/DC+PTP)	2000	42
	VCORE	MDSYS/Infra	0.7~1.4 (DC/DC)	1200	30
	VM		1.2/1.35/1.5	1100	10
	VSRAM	Memory	0.7~1.4 (DC/DC+PTP)	600	10
	VPA	3GPA	0.5~3.4 (50mV/step)	600	2.2+2.2
	VRF18_1	1st RF	1.825	450	4.7
	VRF18_2	2nd RF	1.825	450	10
Analog LDO	GPIO_OD	IO App.	1.05~1.25 (50mV/step)	600	4.7
	VA	ABB	1.8/2.5	60	1
	VA28	Audio Block	2.8	40	1
	VRF28_1	MDSYS	2.85	200	2.2
	VRF28_2	General	1.8/2.85	200	2.2
	VTXCO_1	MDSYS	2.8	40	1
	VTXCO_2	MDSYS	1.8/2.8	40	1
Digital LDO	VCAMA	VCAMA	1.5/1.8/2.5/2.8	200	2.2
	VIO28	AST3001	2.8	400	2.2
	VAST		0.9/1.0/1.1/1.2	300	2.2
	VUSB	T-Card	3.3	200	1
	VMC	T-Card	1.8/3.3	200	1
	VMCH	T-Card	3.0/3.3	800	4.7
	VEMC_3V3	eMMC (Core)	3.0/3.3	800	4.7
	VGP1	eMMC	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	2.2
	VGP2	VCAMD	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	400	2.2
	VGP3	VCAM_AF	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP4	CTP/CMMB	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP5	CTP/CMMB	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP6	CTP/CMMB	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VSIM1	VSIM1	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VSIM2	VSIM2	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	Vibrator	Vibrator	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	RTC	RTC Block	2.8	2	1-22

COMPANY: Lenovo MIDH_Phone

TITLE: Prada_MB_H101

DRAWN: Chenqm2

DATED: 20120828

CHECKED: <Checked By>

DATED: <Checked Date>

QUALITY CONTROL: <QC By>

DATED: <QC Date>

RELEASED: <Released By>

DATED: <Release Date>

CODE: <Code>

SIZE: D

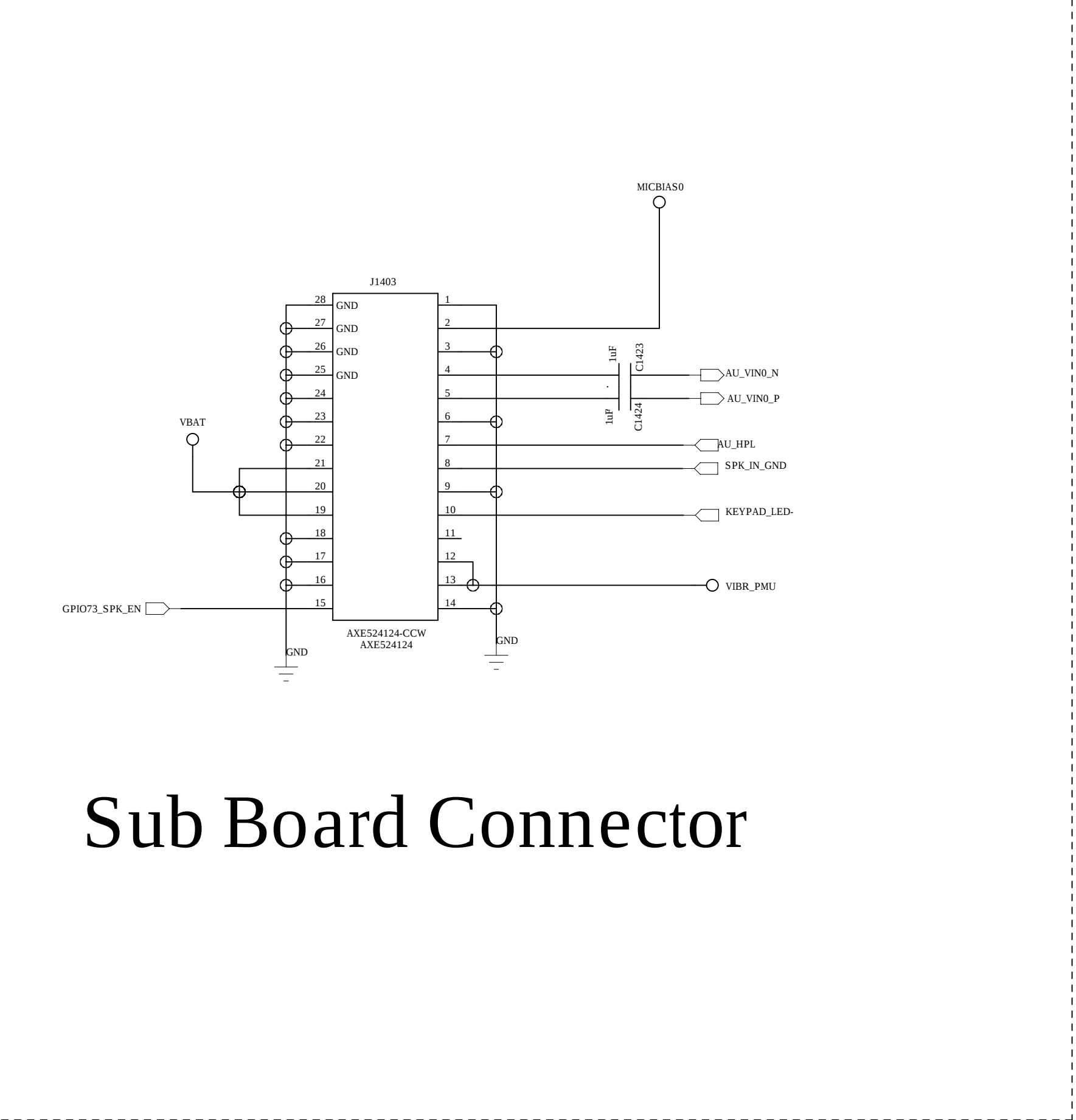
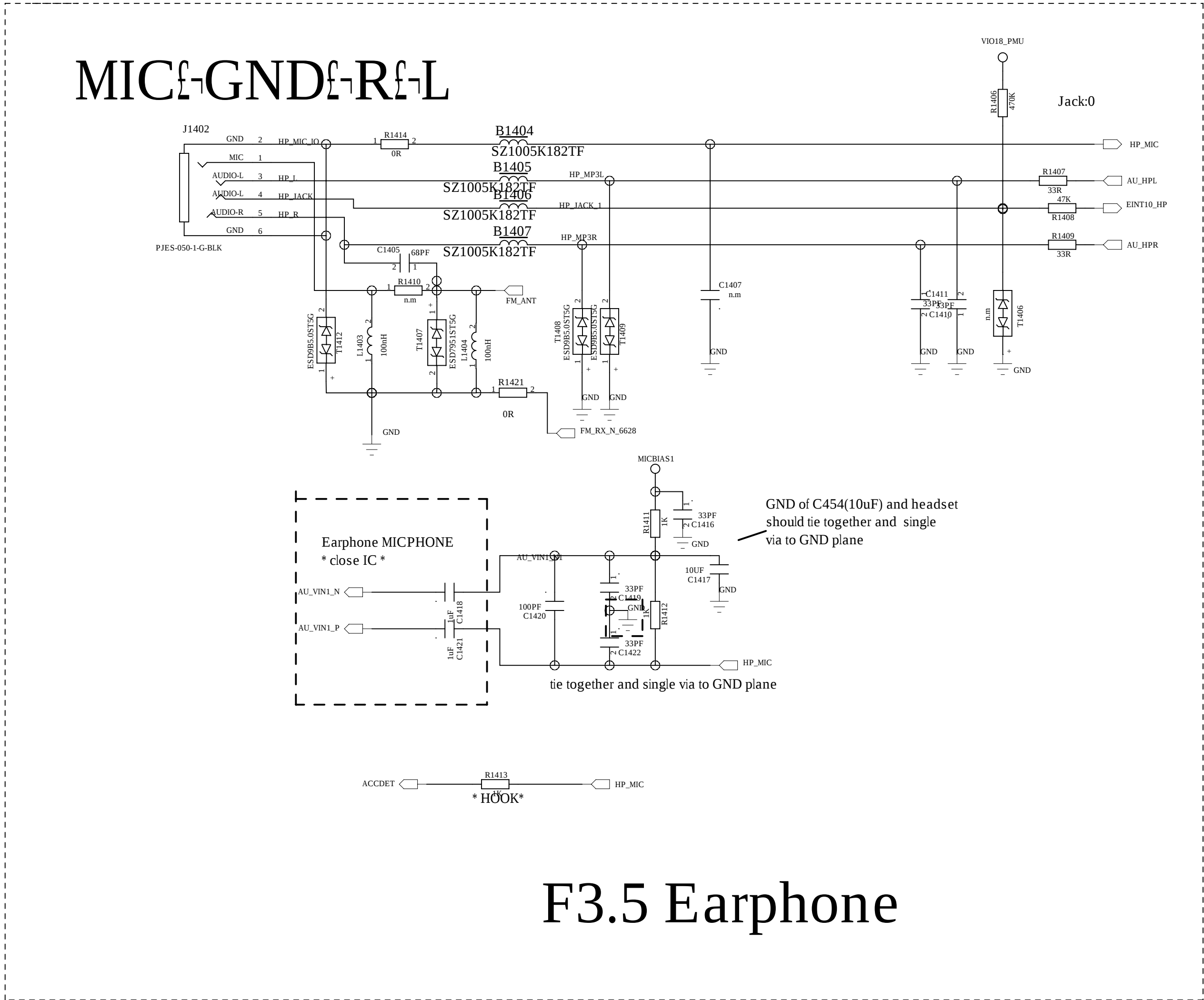
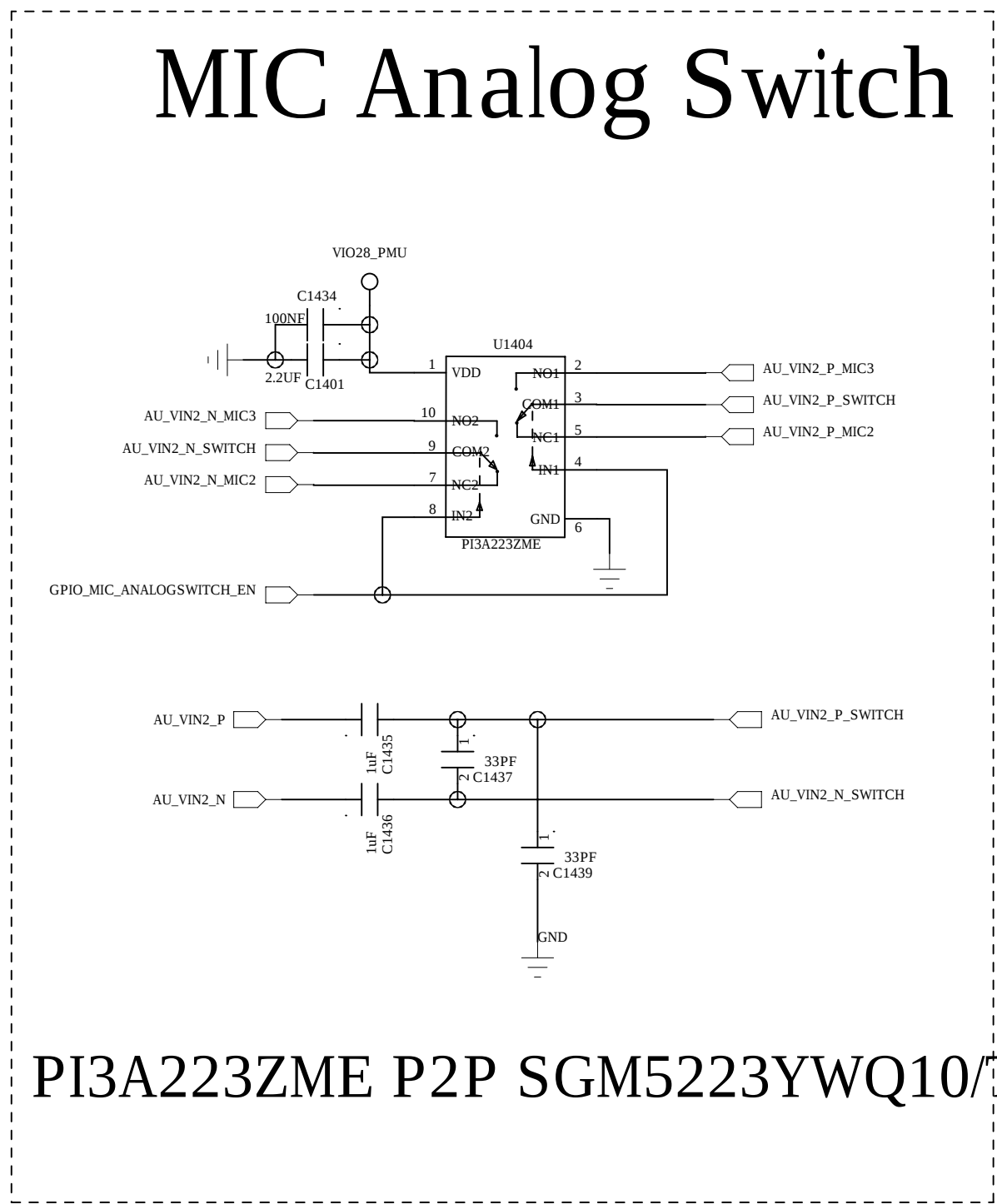
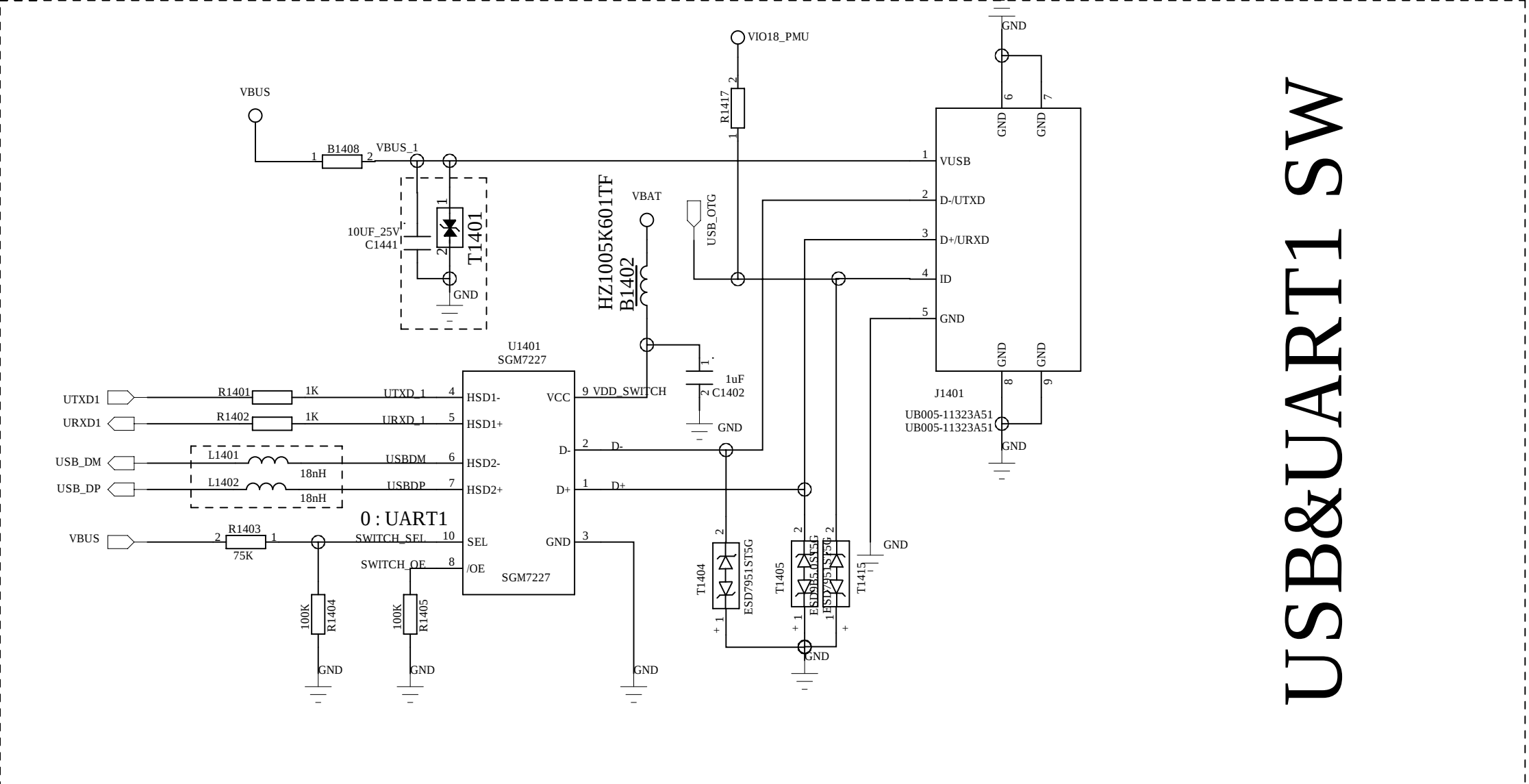
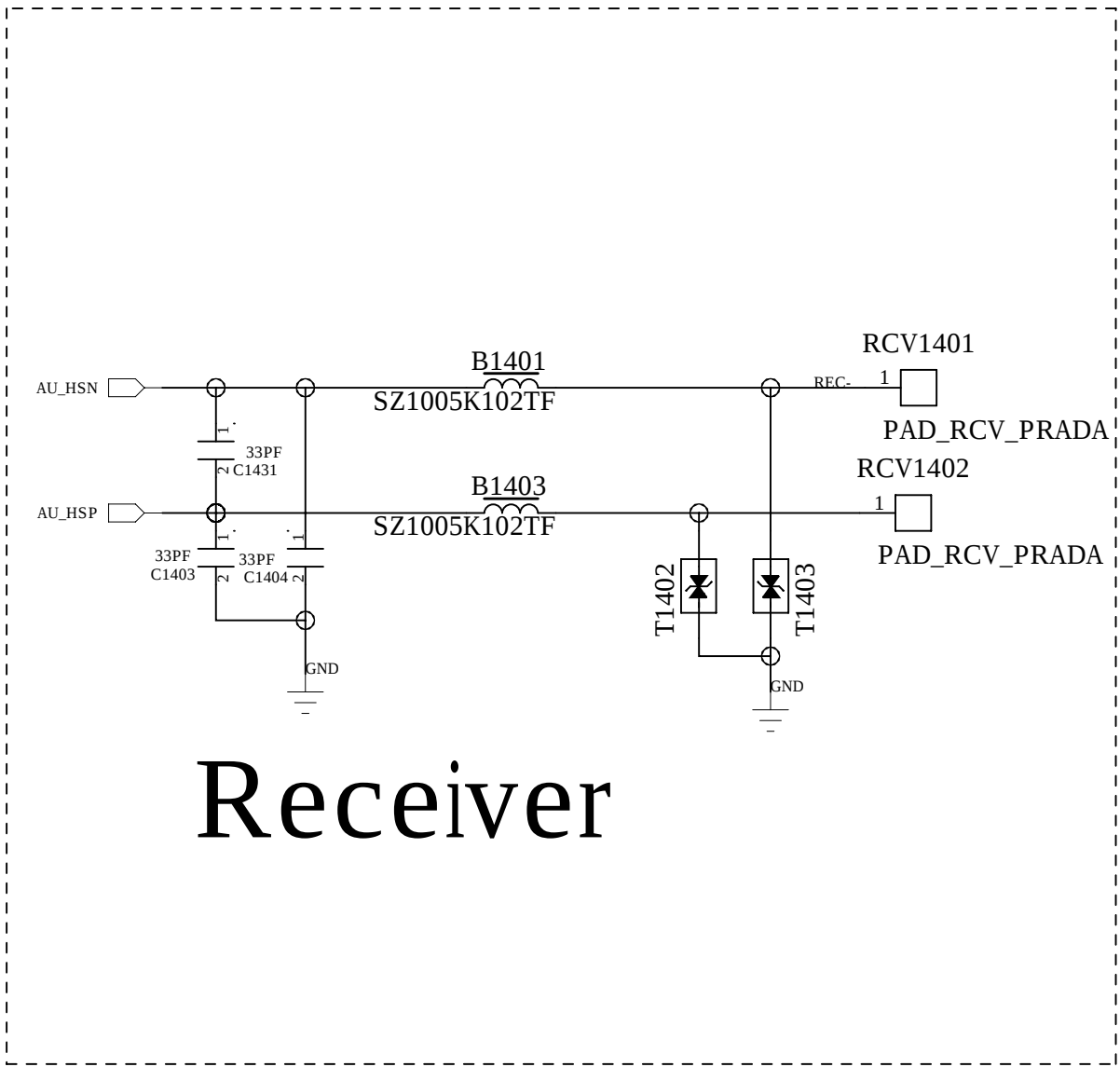
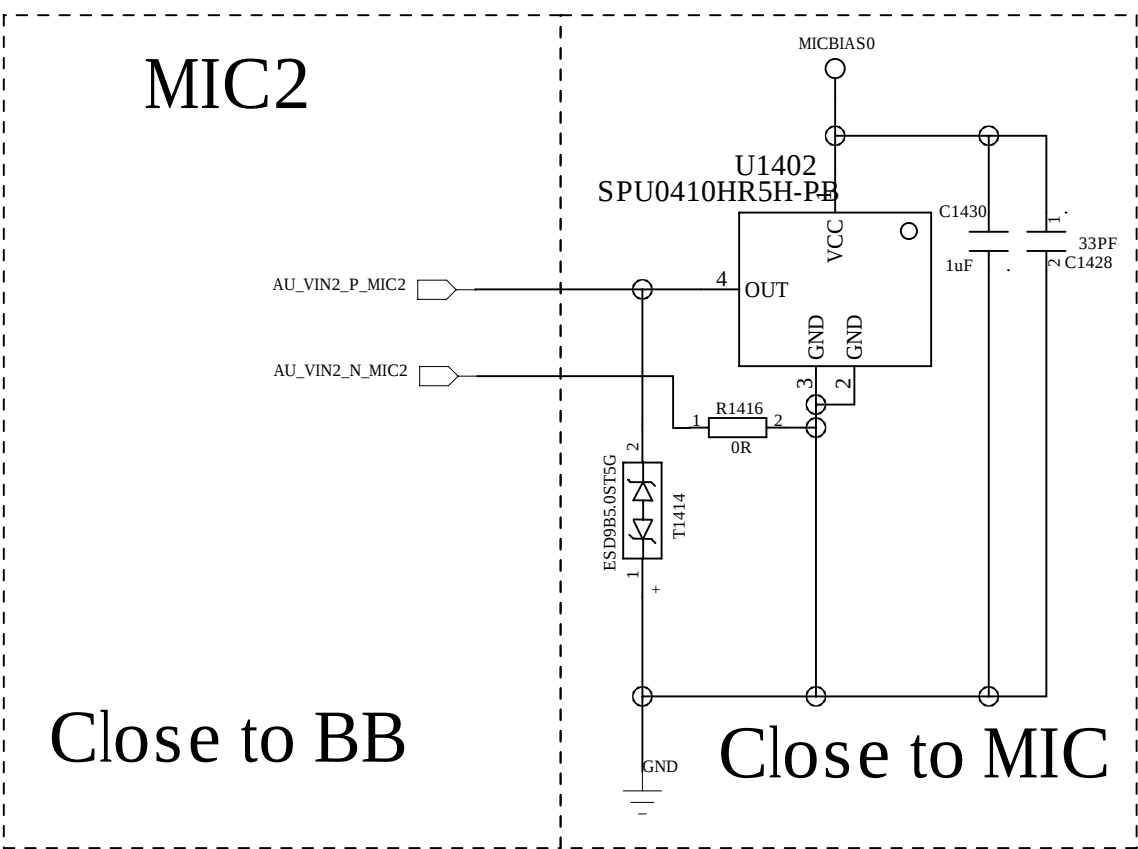
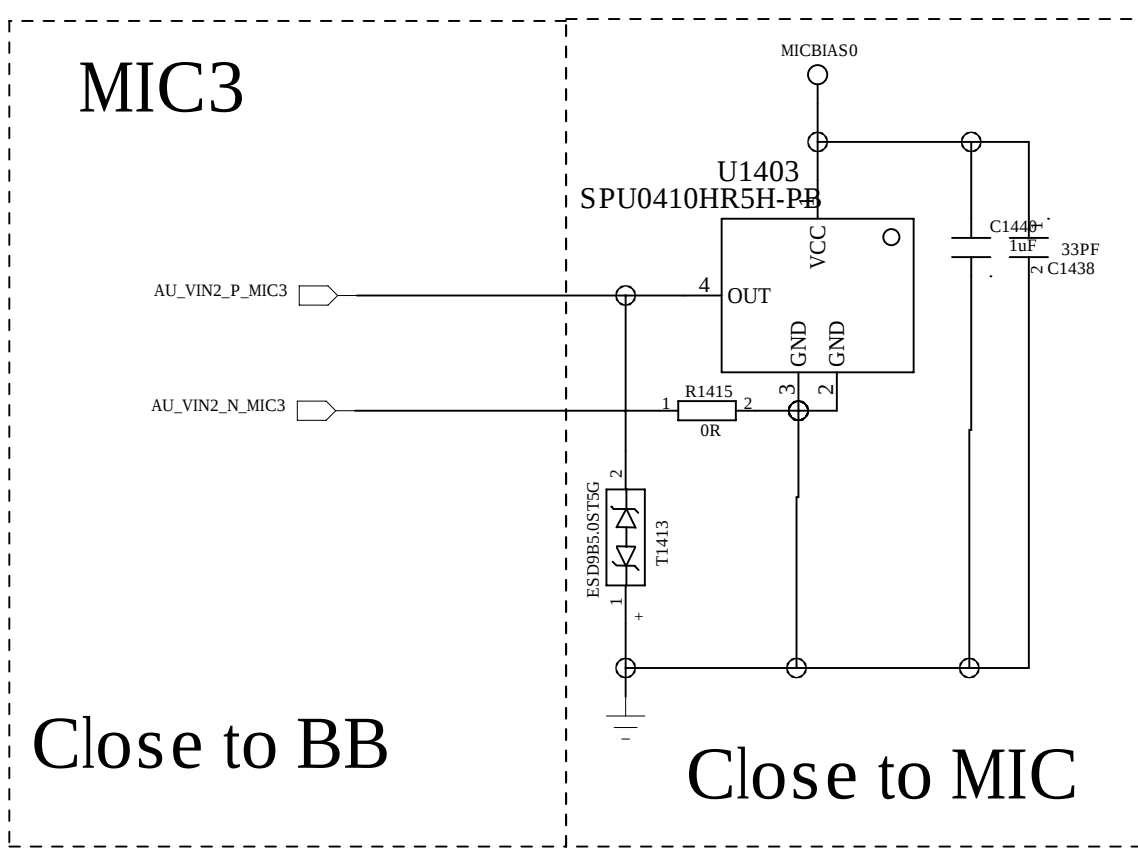
DRAWING NO: 13_MT6320

REV: H101

SCALE: Scale

SHEET: ORIO

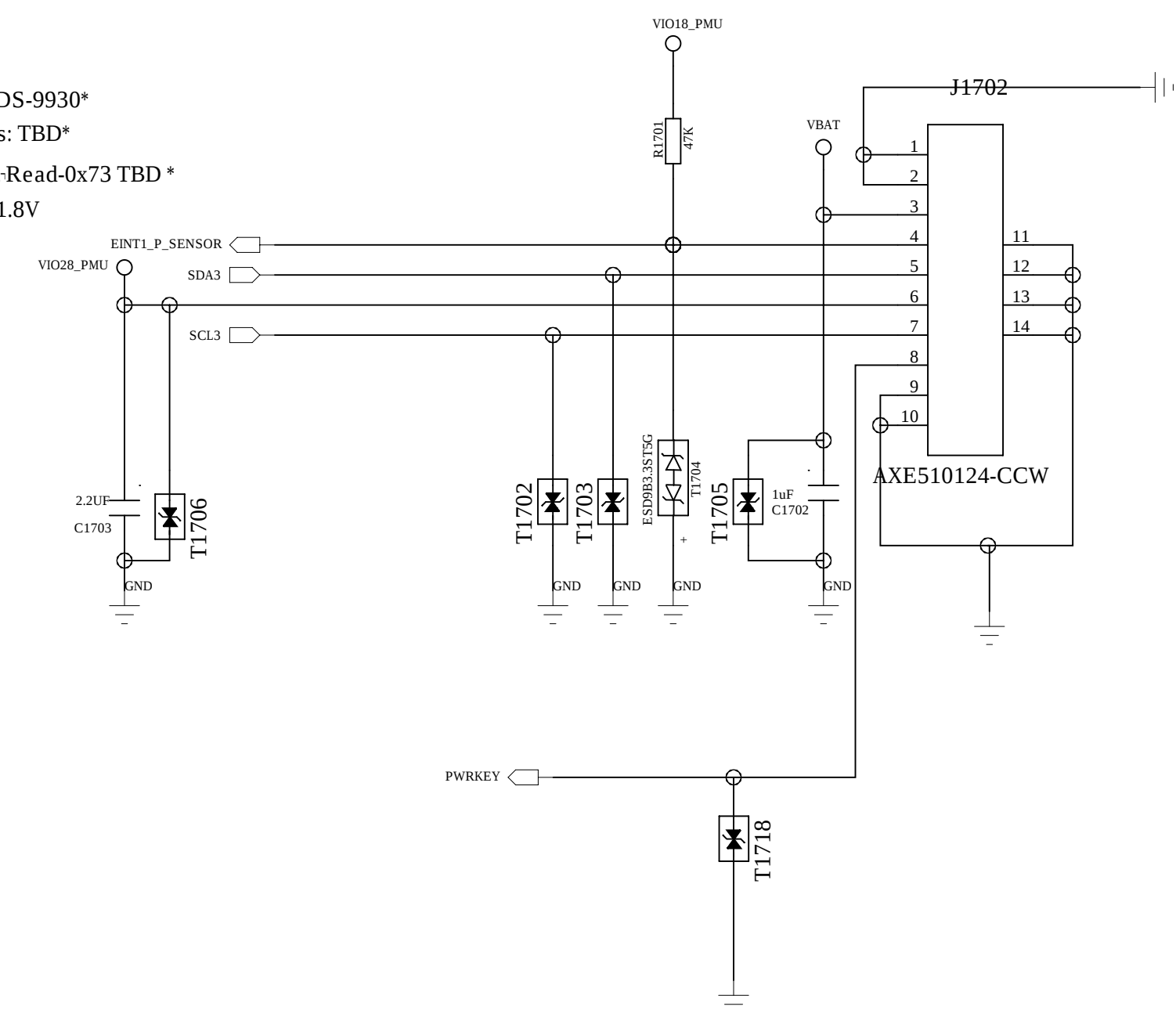
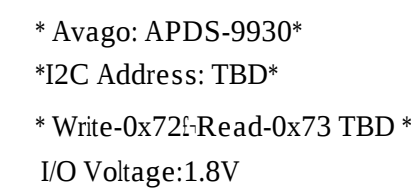
REVISION RECORD			
LTR	ECO NO:	APPROVE	DATE



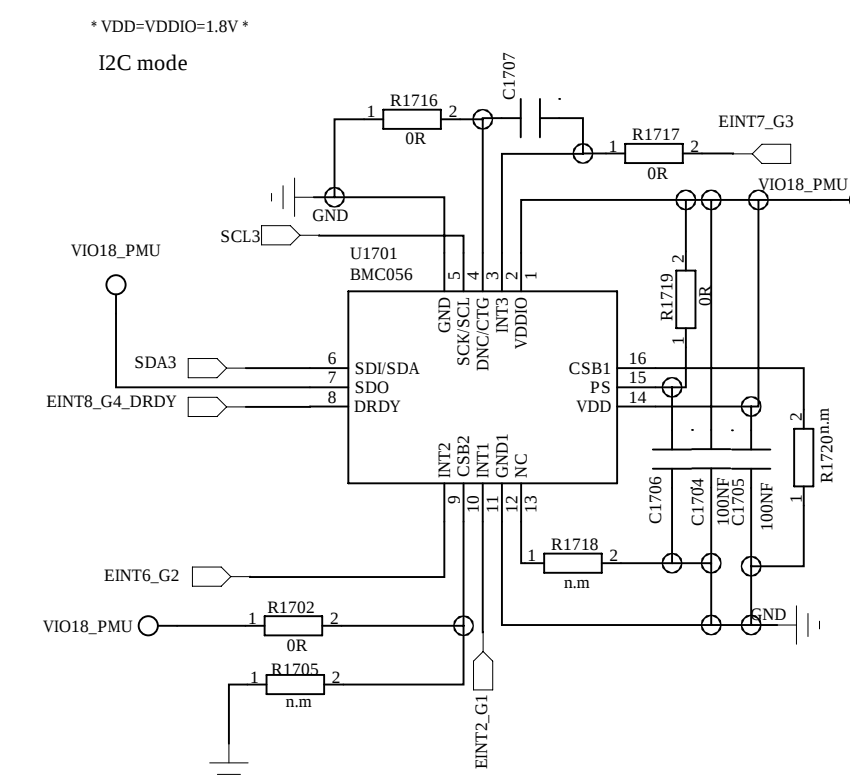
F3.5 Earphone

DRAWN: Chenqm2		DATED: 20120828		COMPANY: Lenovo MIDH_Phone	
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: Prada_MB_H101	
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE: <Code>	SIZE: D
RELEASED: <Released By>		DATED: <Release Date>		DRAWING NO: 14_AUDIO_USB	
				REV: H101	
				SCALE: 1:1	
				SHEET: 01/01	

REVISION RECORD			
LTR	ECO NO:	APPROVED	DATE:



ALS & PS (Avago P-sensor)



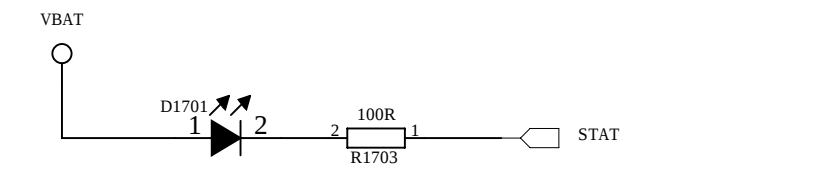
G+M -Sensor
Part number:BMC056

G+M Compatible With BMC056&LSM303D

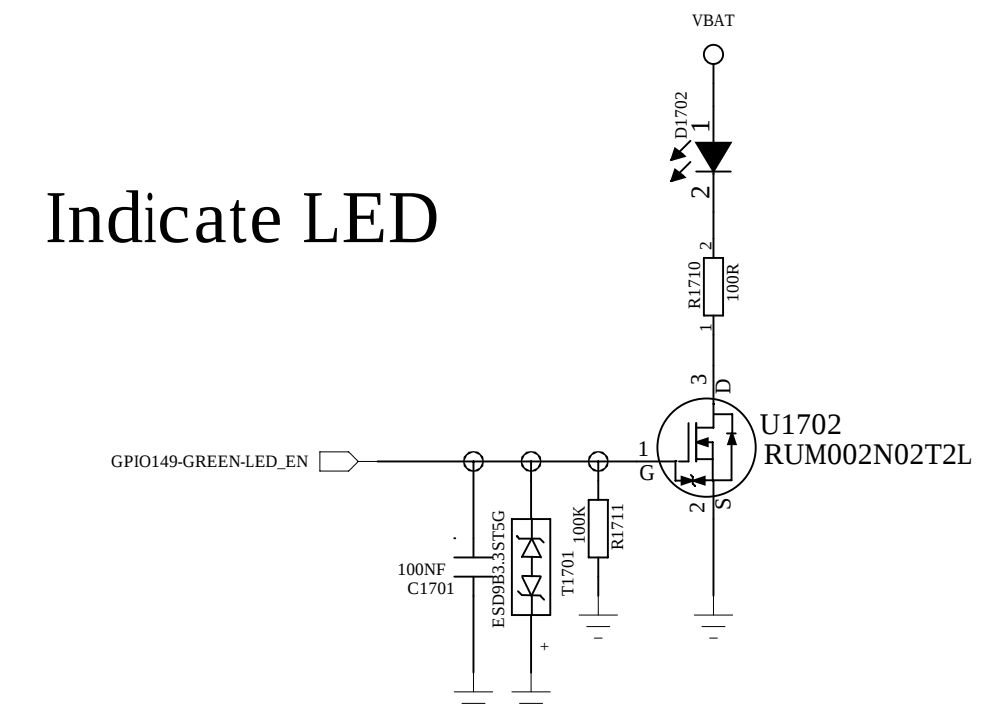
	BMC056	LSM303D
C1707	NM	220nF
R1706	0R	NM
R1707	0R	NM
R1708	NM	0R
C1706	NM	4.7uF
R1719	0R	NM
R1720	NM	0R

I2C Address Information

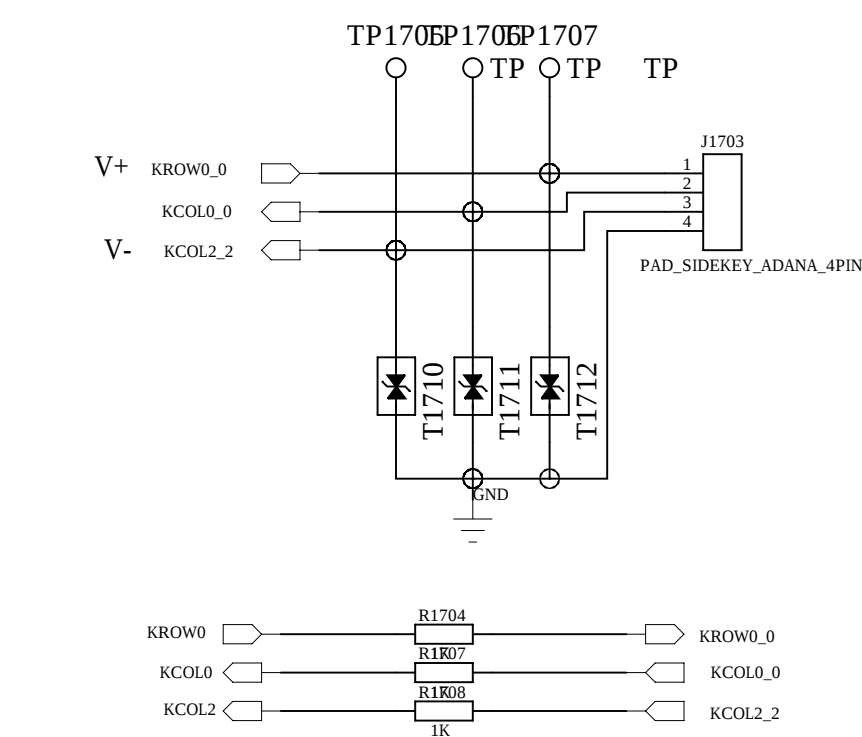
SDO CSB2	GND	VDDIO
GND	Shared I/O10 Not Recommended for Android	Shared I/O11
VDDIO	Accelerometer I2C Address I/O10 Magnetometer I2C Address I/O12	Accelerometer I2C Address I/O11 Magnetometer I2C Address I/O13



CHARGE LED

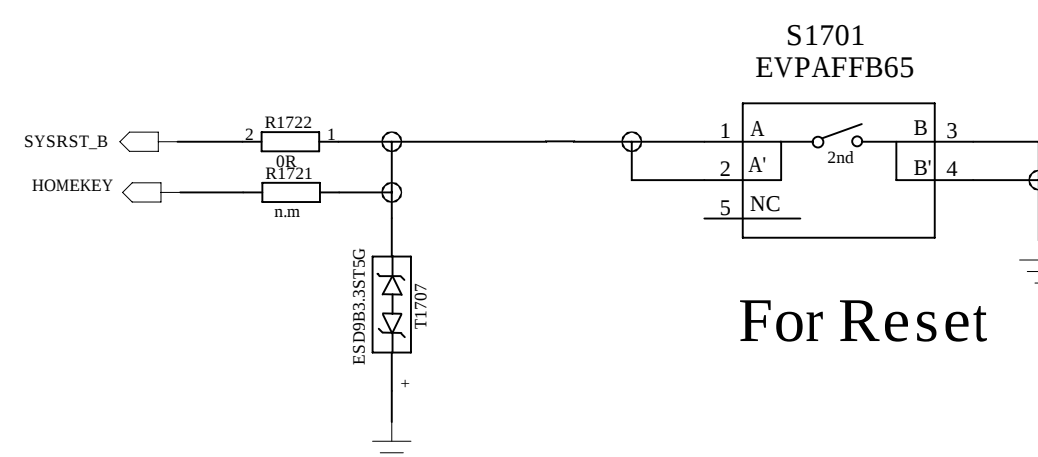


Indicate LED



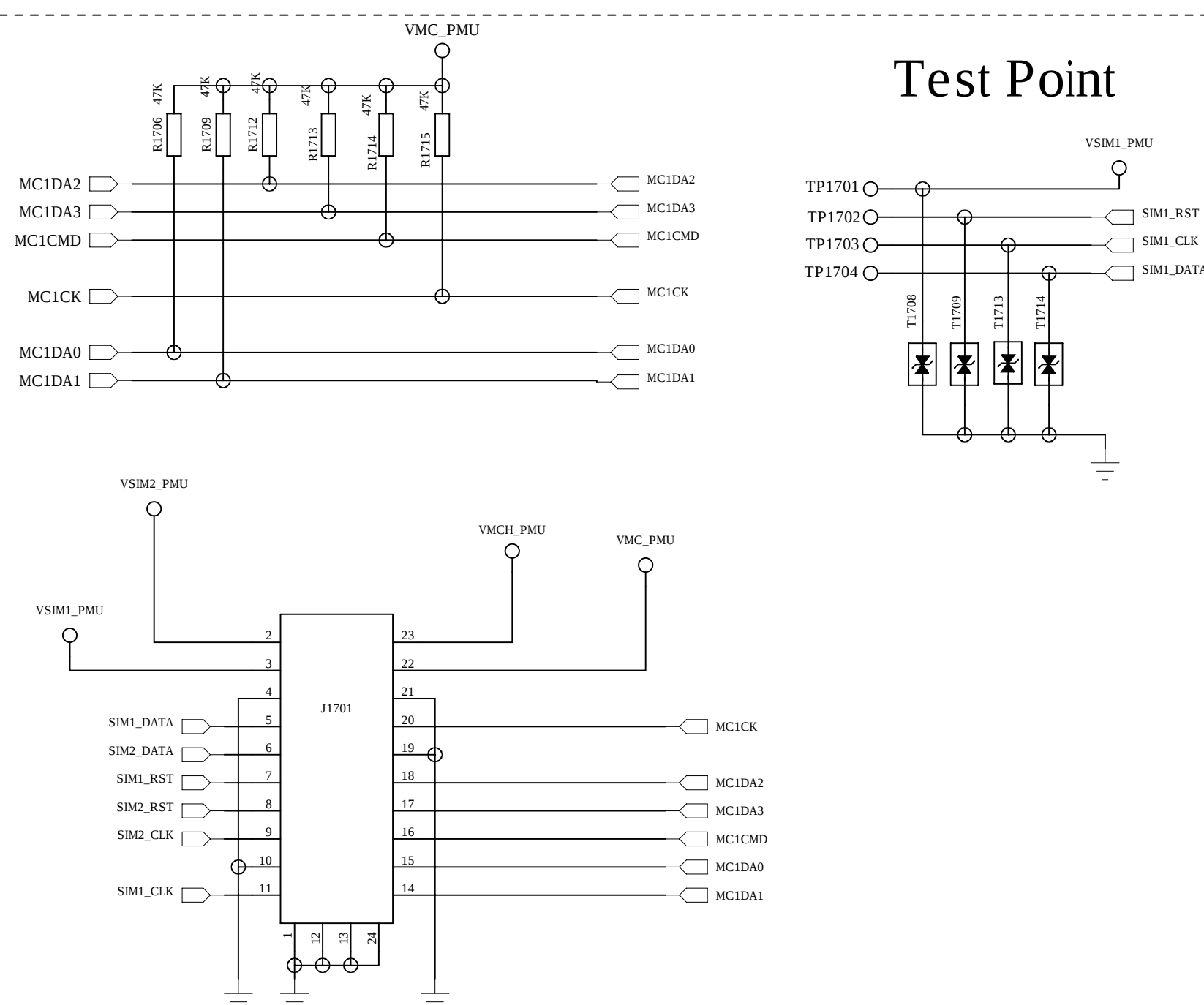
	KROW0
KCOL0	V+
KCOL2	V-

MODE	KEY
USB Download	V+
Meta	V- & Power
Factory	V+ & Power
Recovery	V+ & V- & Power



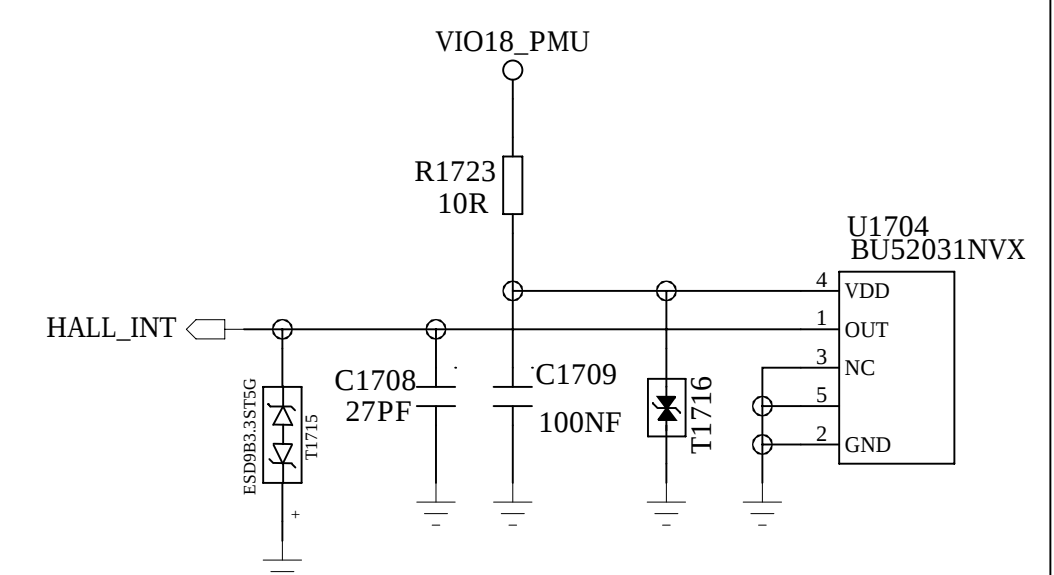
For Reset

KEYPAD



Test Point

SIM CARD& T Card Connector



Hall

		COMPANY: Lenovo MIDH_Phone					A
		TITLE: Prada_MB_H101					
DRAWN: Chenqm2	DATED: 20120828						
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE:	DRAWING NO:		REV:	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	<Code>	D	17_KEY_SIM_SENSOR		H101	
RELEASED: <Released By>	DATED: <Release Date>	SCALE>			SHEET: 080		

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

D

C

B

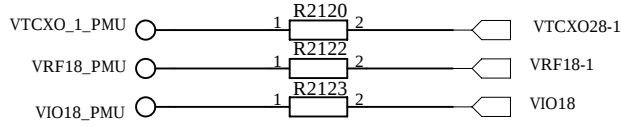
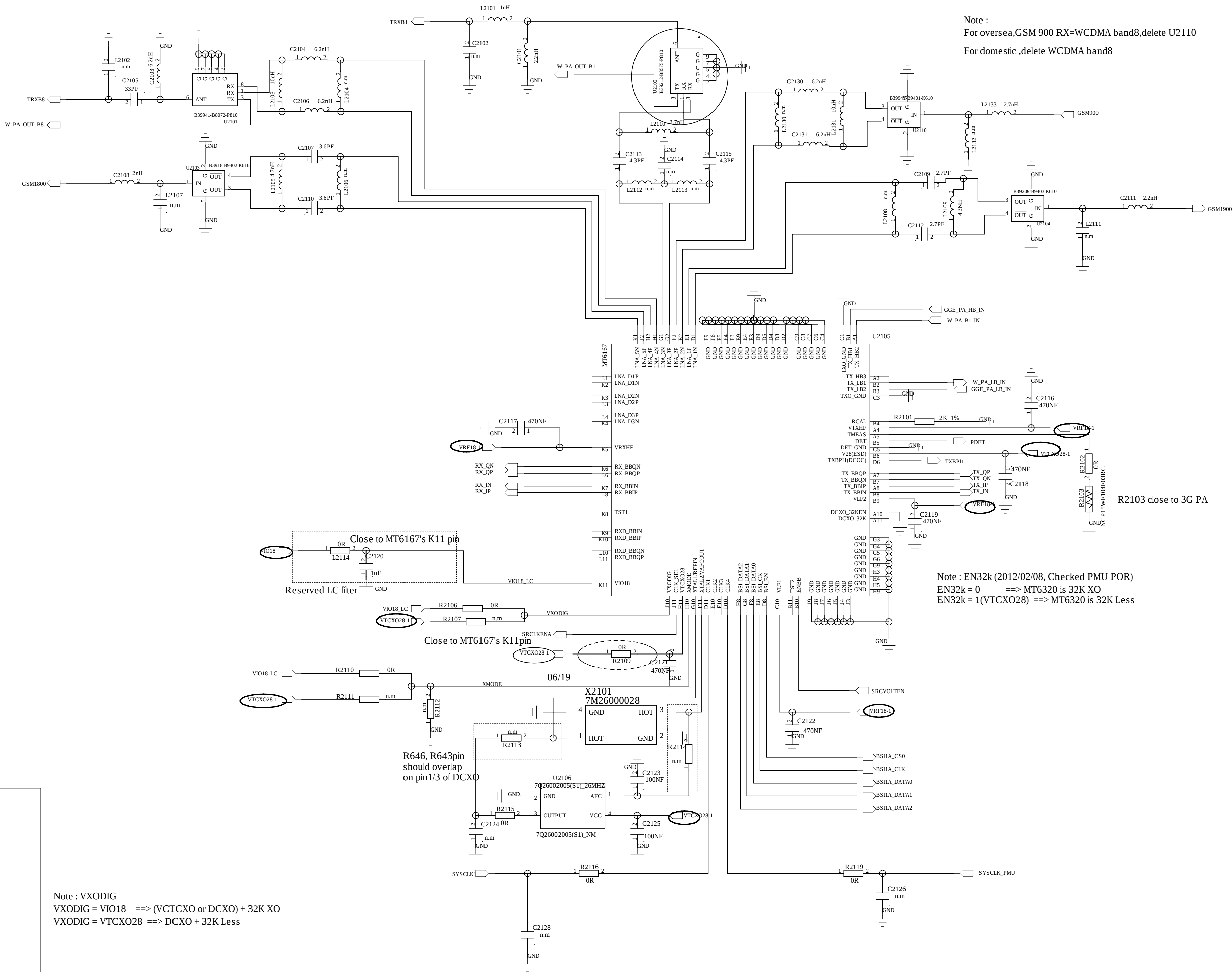
A

D

C

B

A



Note : VXODIG
VXODIG = VIO18 ==> (VTCX0 or DCXO) + 32K XO
VXODIG = VTCX028 ==> DCXO + 32K Less

Note : Xmode
Xmode = 0 ==> VTCX0 + 32K XO
Xmode = 1(VIO18) ==> DCXO + 32K XO
Xmode = 1(VTCX28) ==> DCXO + 32K Less

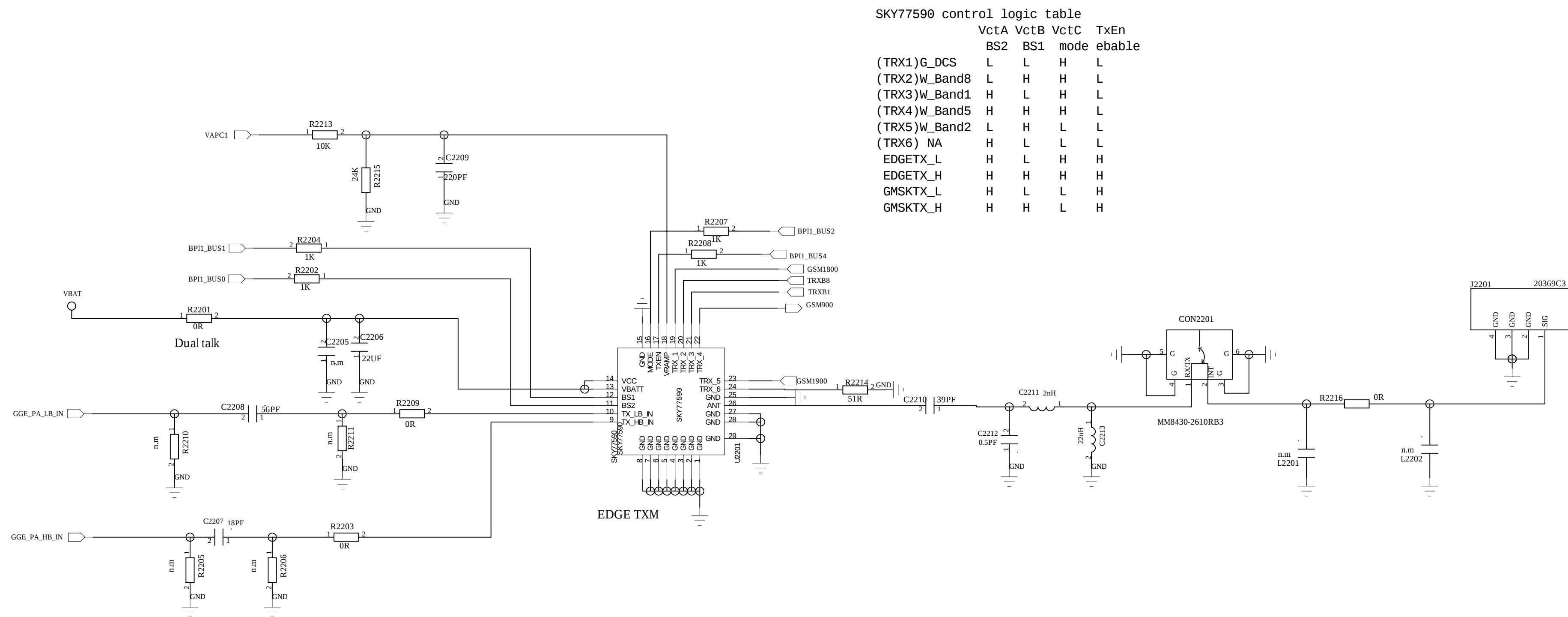
Note :
For oversea,GSM 900 RX=WCDMA band8,delete U2110
For domestic ,delete WCDMA band8

Note : EN32k (2012/02/08, Checked PMU POR)
EN32k = 0 ==> MT6320 is 32K XO
EN32k = 1(VTCX028) ==> MT6320 is 32K Less

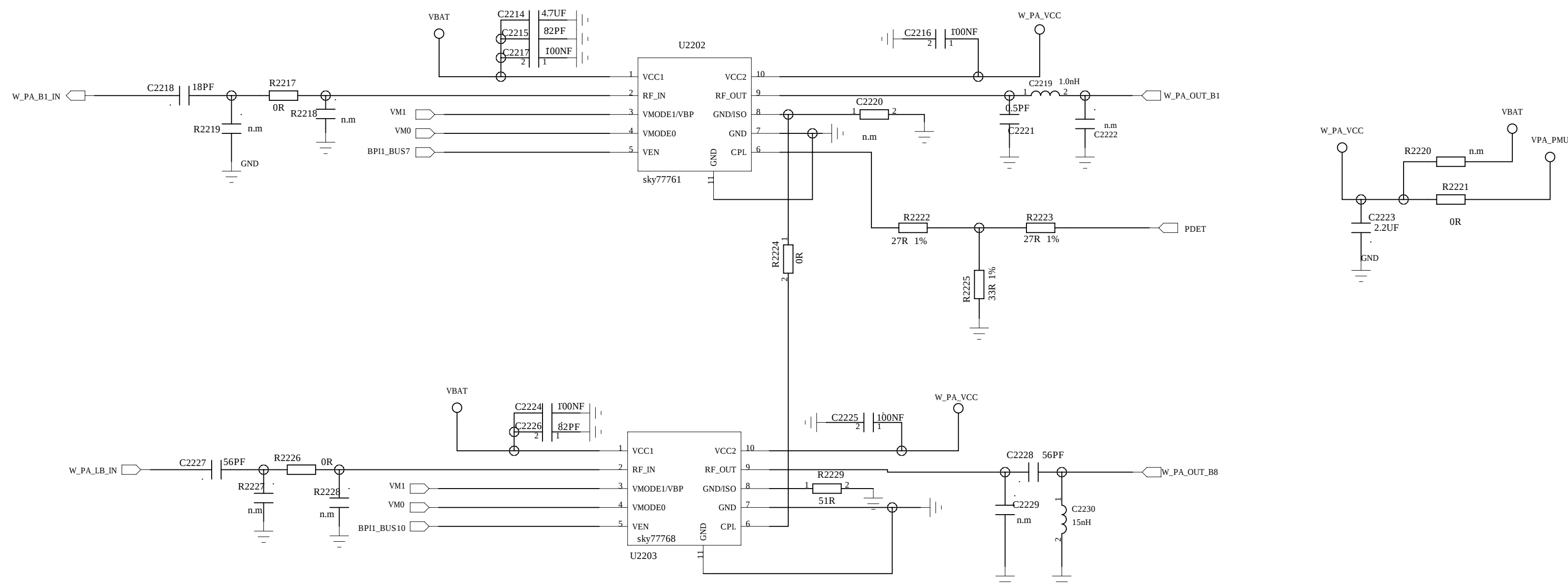
R2103 close to 3G PA

COMPANY: Lenovo MIDH_Phone			
TITLE: Prada_MB_H101			
DRAWN: Chenqm2	DATED: 20120828	CODE: <Code>	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: <Drawing Number>	REV: H101
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	SHEET: 8# 10
RELEASED: <Released By>	DATED: <Release Date>		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



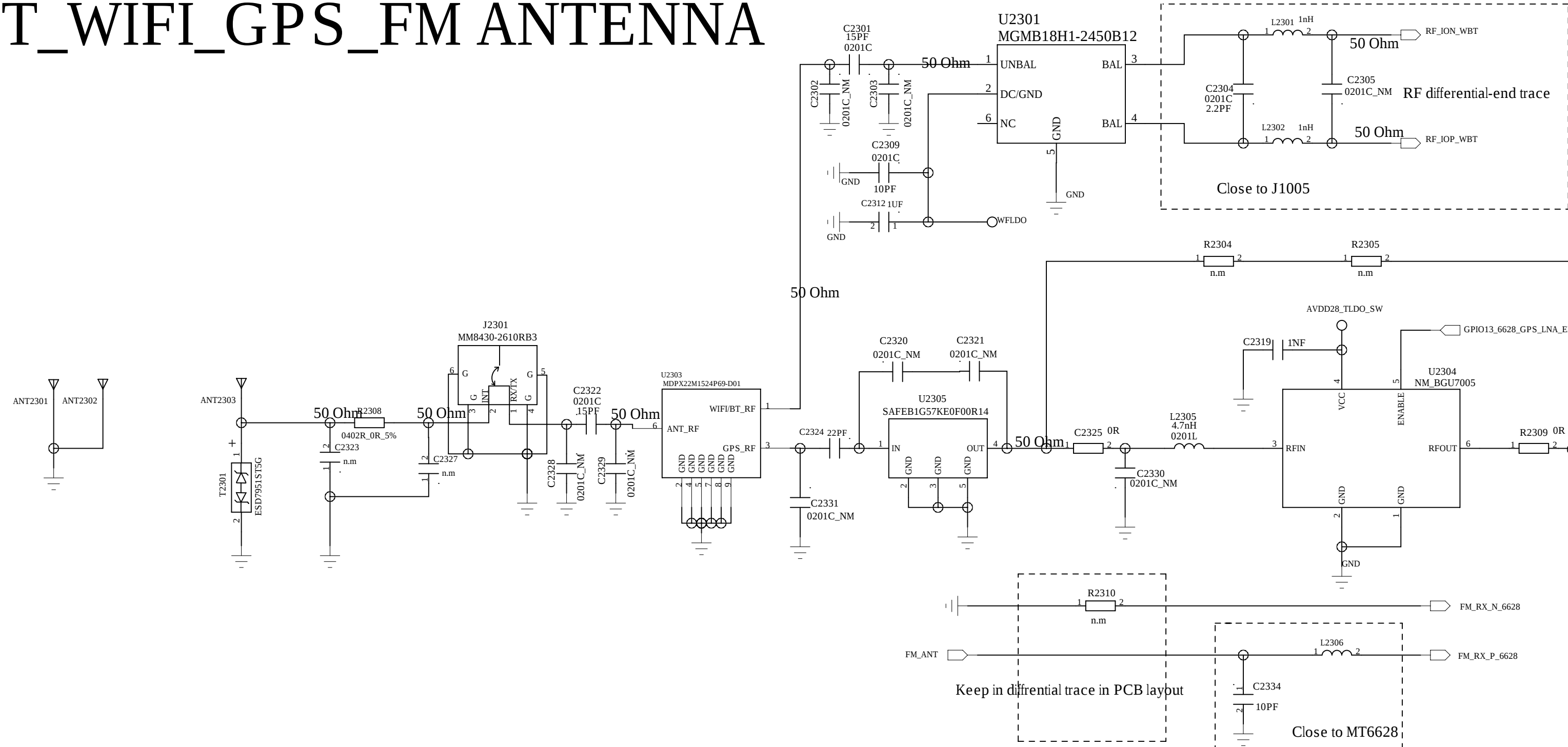
SKY77590 control logic table				
	VctA	VctB	VctC	TxEn
	BS2	BS1	mode	enable
(TRX1)G_DCS	L	L	H	L
(TRX2)W_Band8	L	H	H	L
(TRX3)W_Band1	H	L	H	L
(TRX4)W_Band5	H	H	H	L
(TRX5)W_Band2	L	H	L	L
(TRX6) NA	H	L	L	L
EDGETX_L	H	L	H	H
EDGETX_H	H	H	H	H
GMSKT_X_L	H	L	L	H
GMSKT_X_H	H	H	L	H



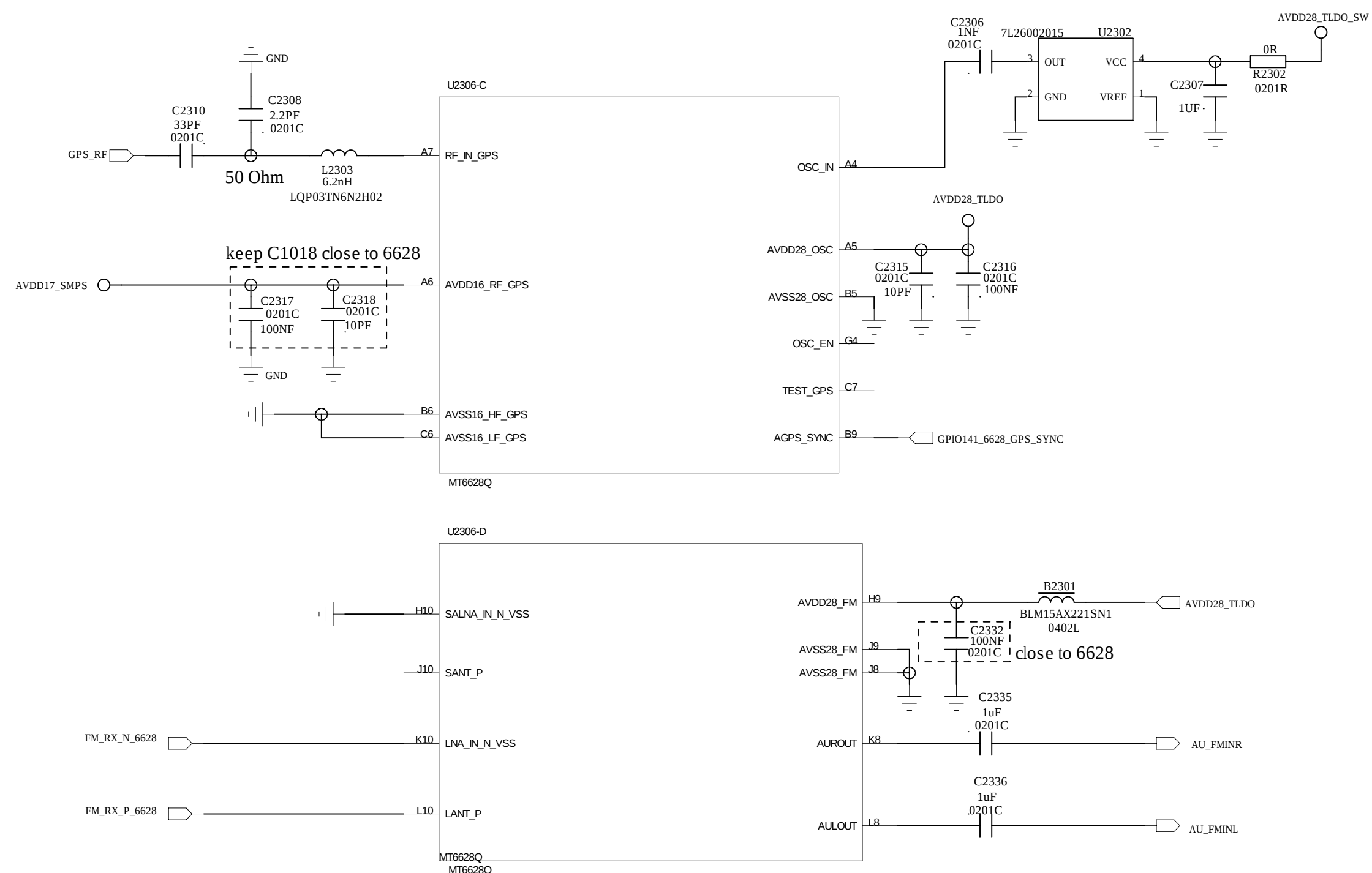
DRAWN: Chenqm2	DATED: 20120828
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: Lenovo MIDH_Phone				
TITLE: Prada_MB_H101				
>	CODE:	SIZE:	DRAWING NO:	REV:
	<Code>	D	<Drawing Number>	H101
SCALE: <Scale>			SHEET: 0#	10

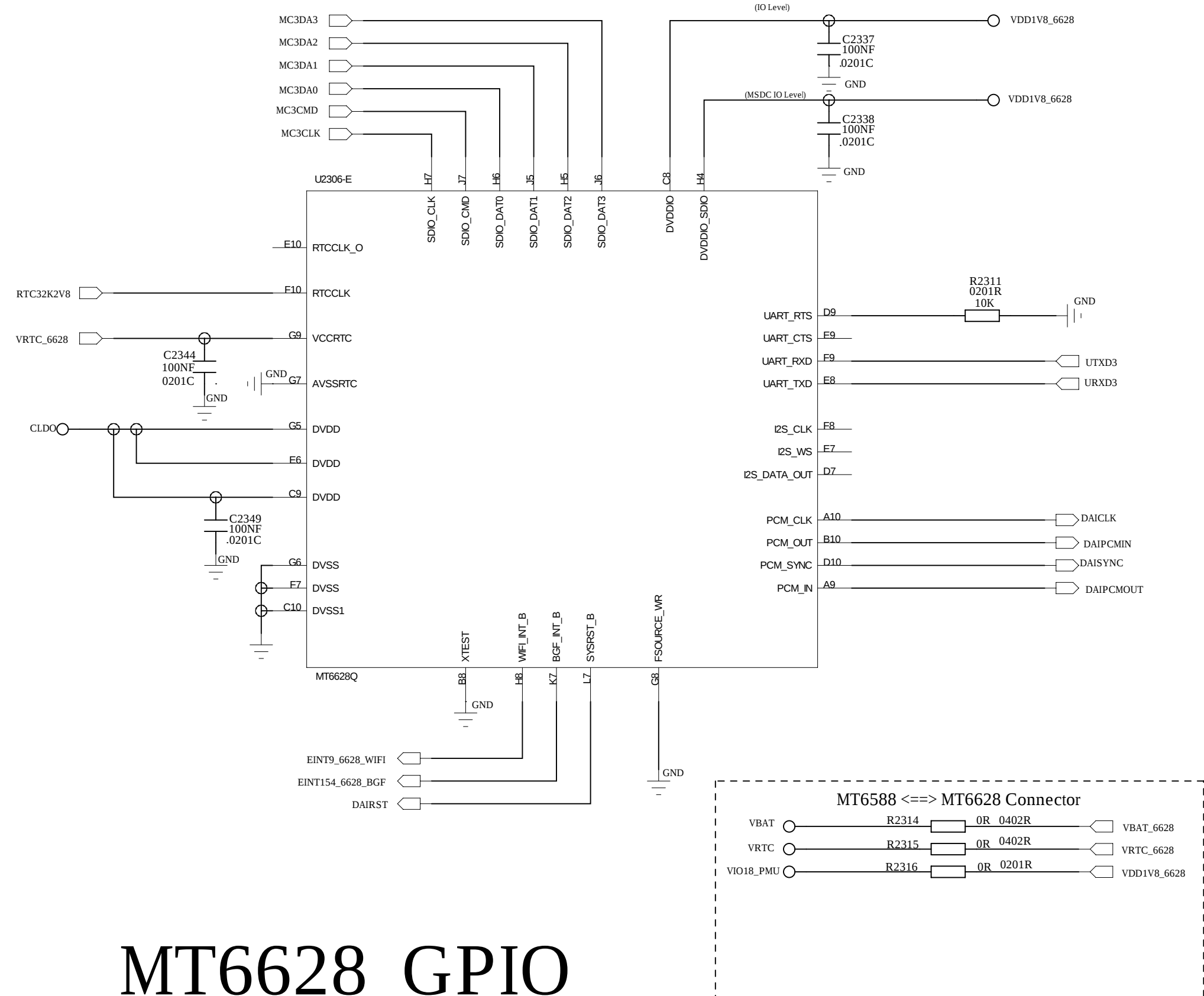
BT_WIFI_GPS_FM ANTENNA



MT6628_GPS/FM

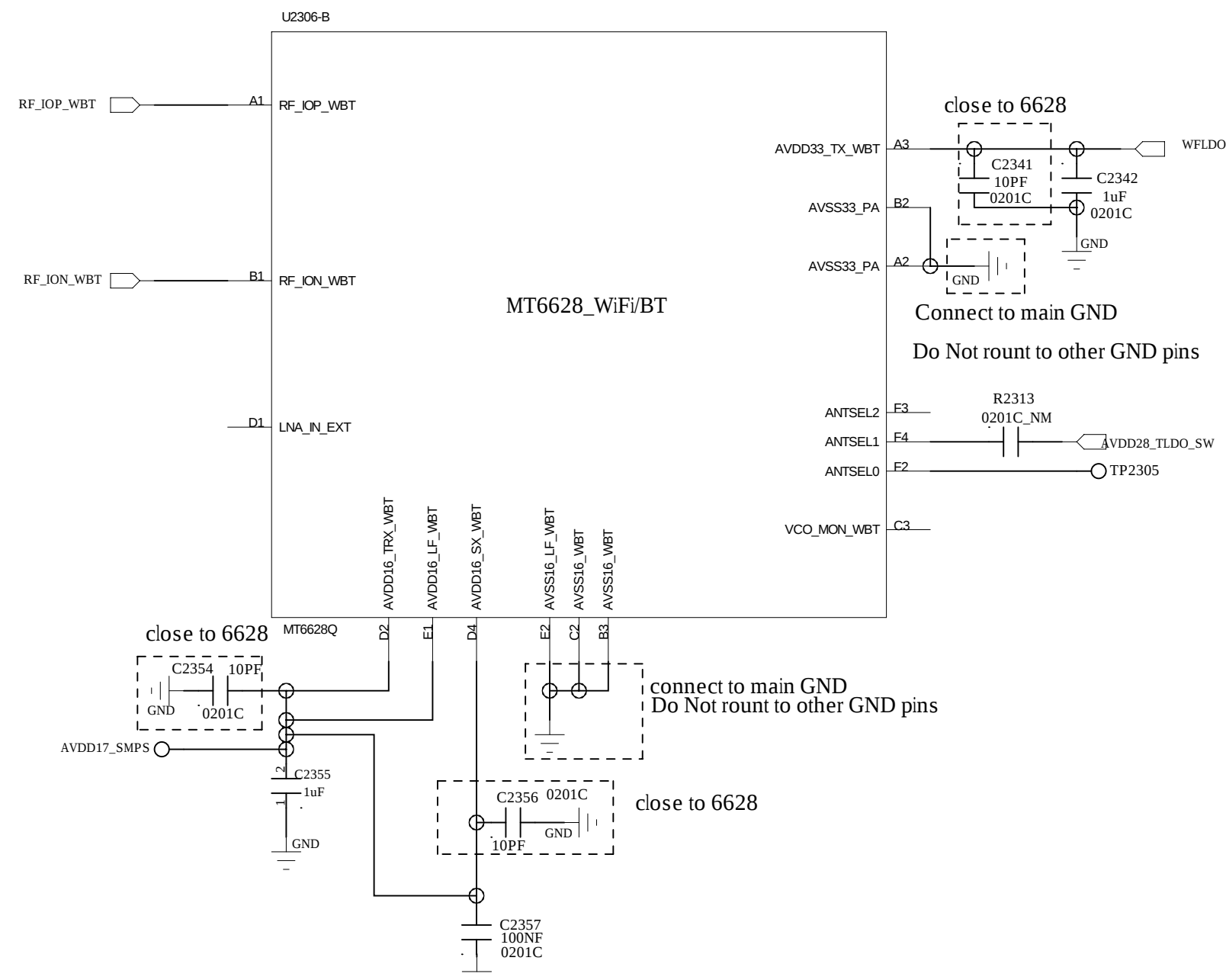


MT6628_GPIO



BT/COM Host UART_RTS	0	UART	MT6628E2
	1	SDIO	MT6628E2

BT/COM Host ANTSEL-0	0	SDIO	MT6628E1
	1	UART	MT6628E1



MT6628_BT/WIFI

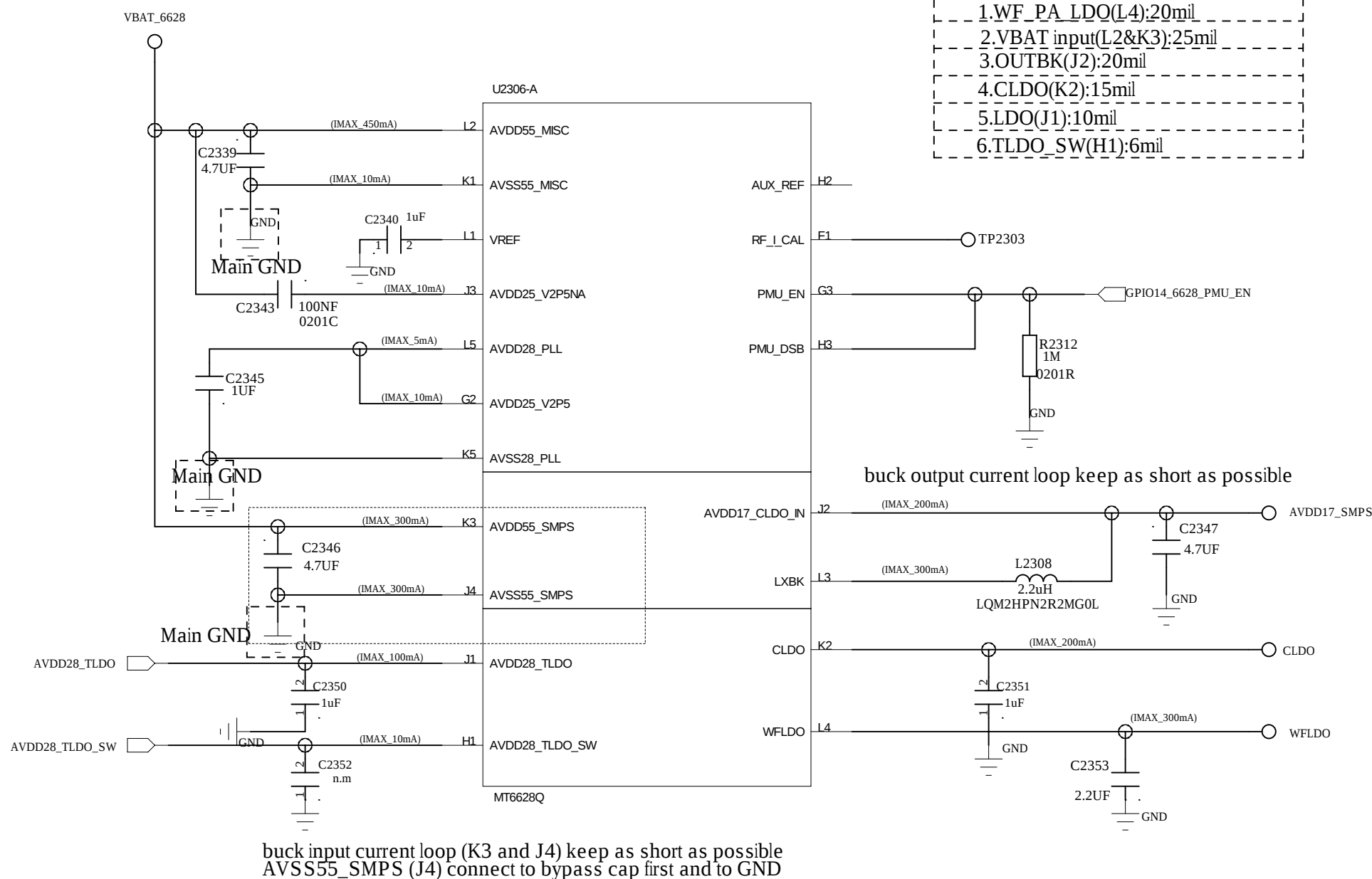
MT6628_PMU

PMU POWER Domain

- a.CLDO:1.2V
- b.WF_PA_LDO:3.3V
- c.TLDO&TLDO_SW:2.8V
- d.BUCK:1.7V

Keep Power trace rule as below

- 1.WF_PA_LDO(L4):20mil
- 2.VBAT input(L2&K3):25mil
- 3.OUTBK(J2):20mil
- 4.CLDO(K2):15mil
- 5.LDO(J1):10mil
- 6.TLDO SW(H1):6mil



DRAWN: <> Chenqm2	DATED: 20120828
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: Lenovo MIDH_Phone			
TITLE: Prada_MB_H101			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	D	<Drawing Number>	H101
SCALE: <Scale>		SHEET: 00 10	

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