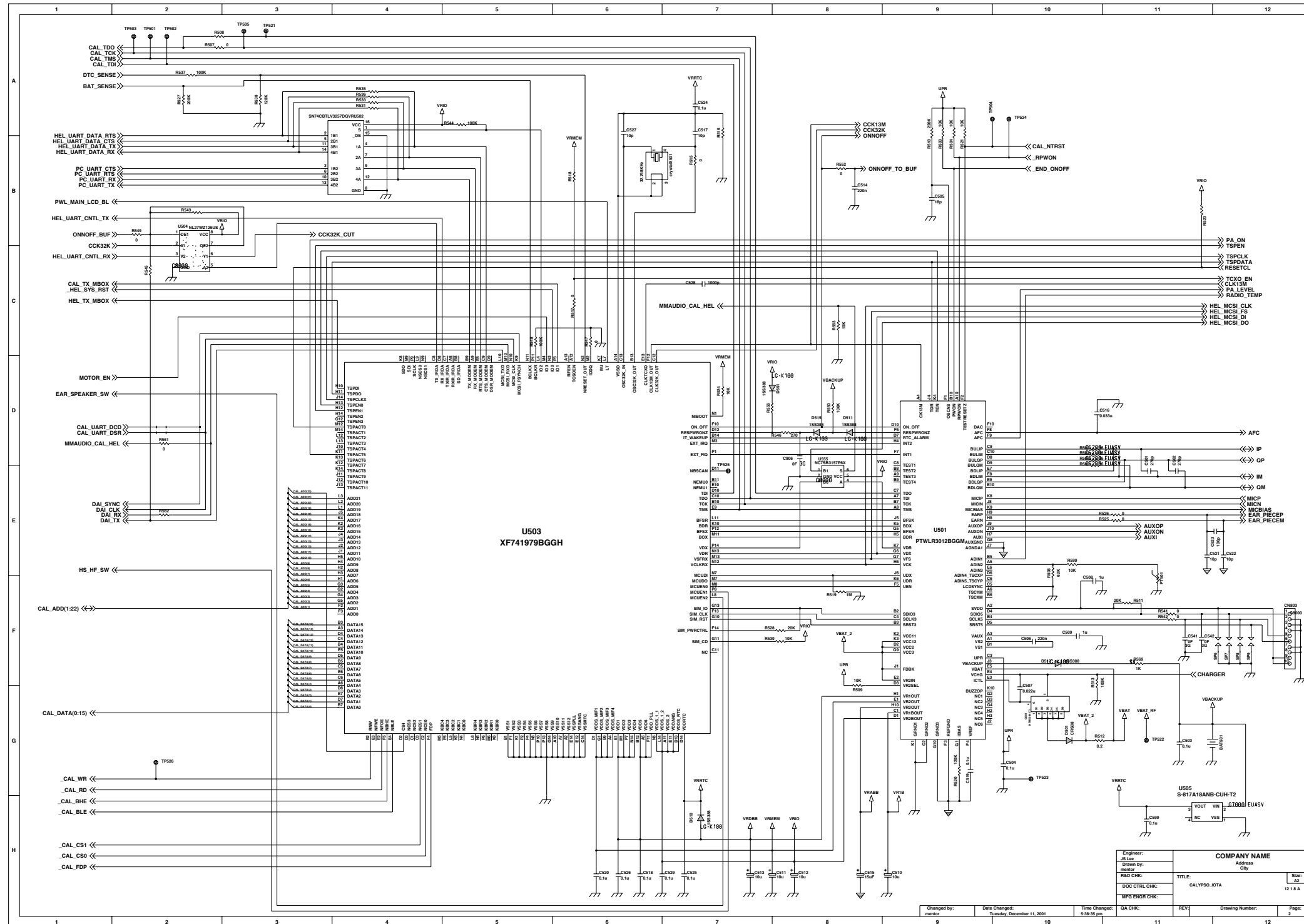
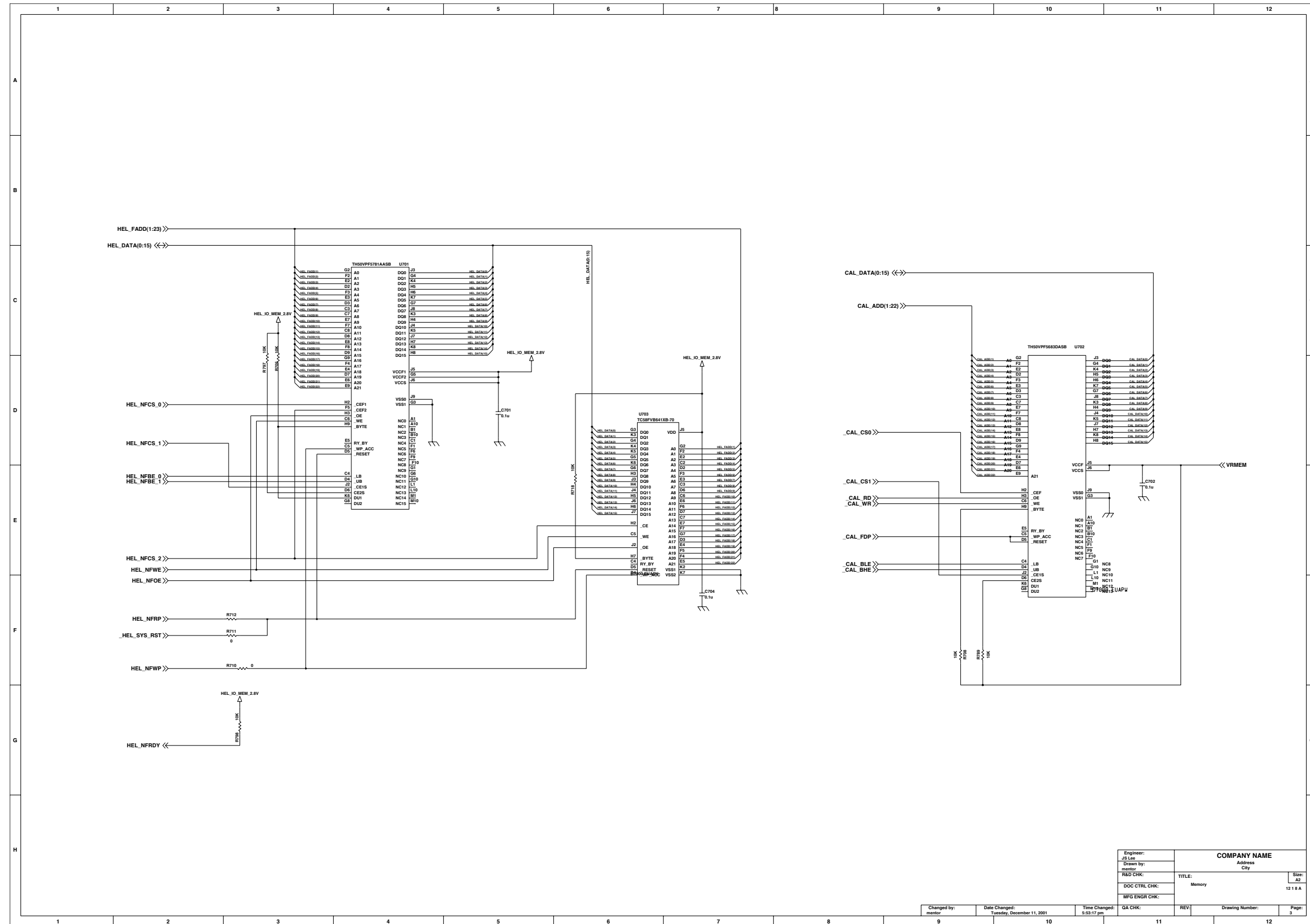


9. CIRCUIT DIAGRAM

Main PCB Circuit (1/5)



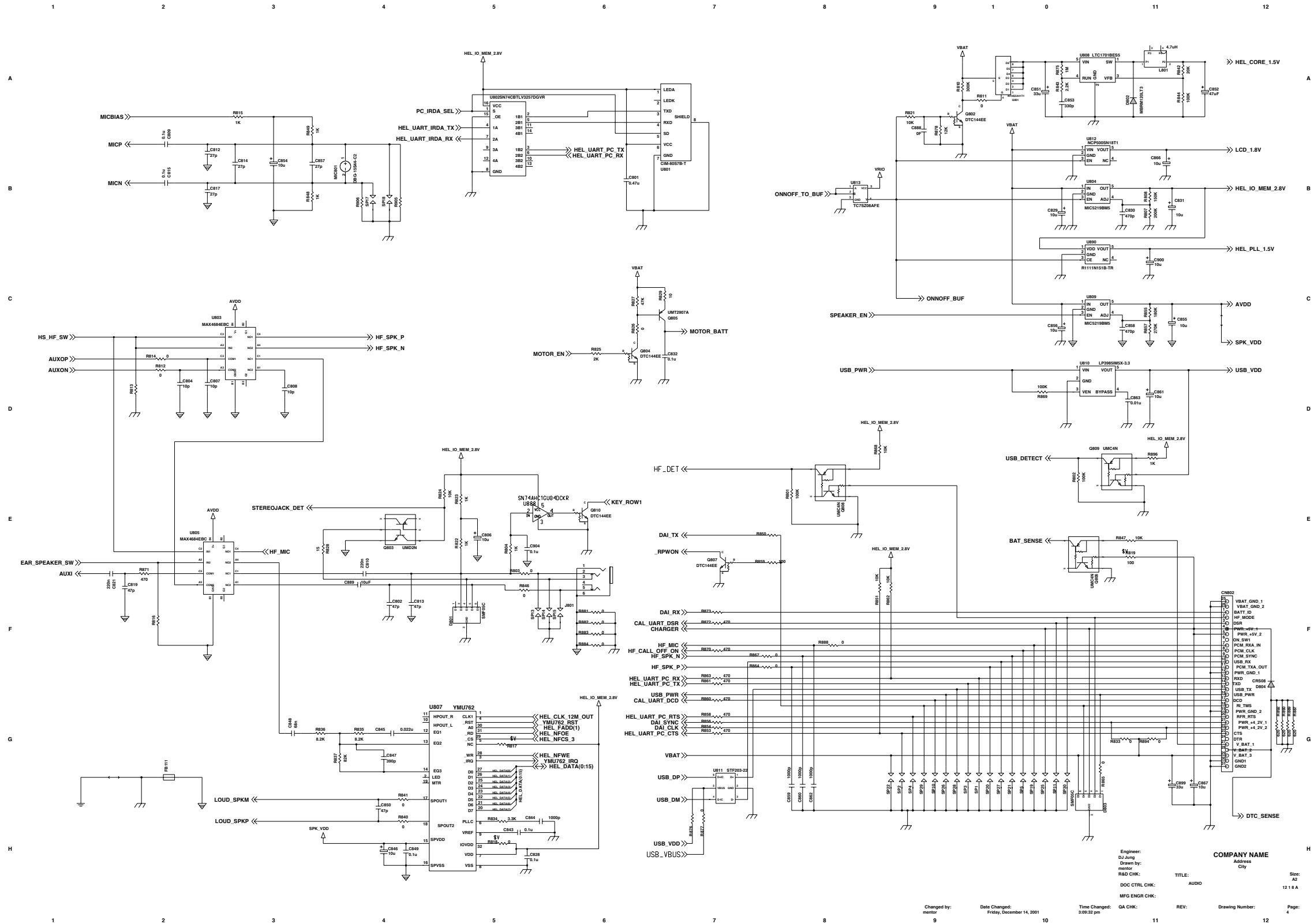
Main PCB Circuit (3/5)



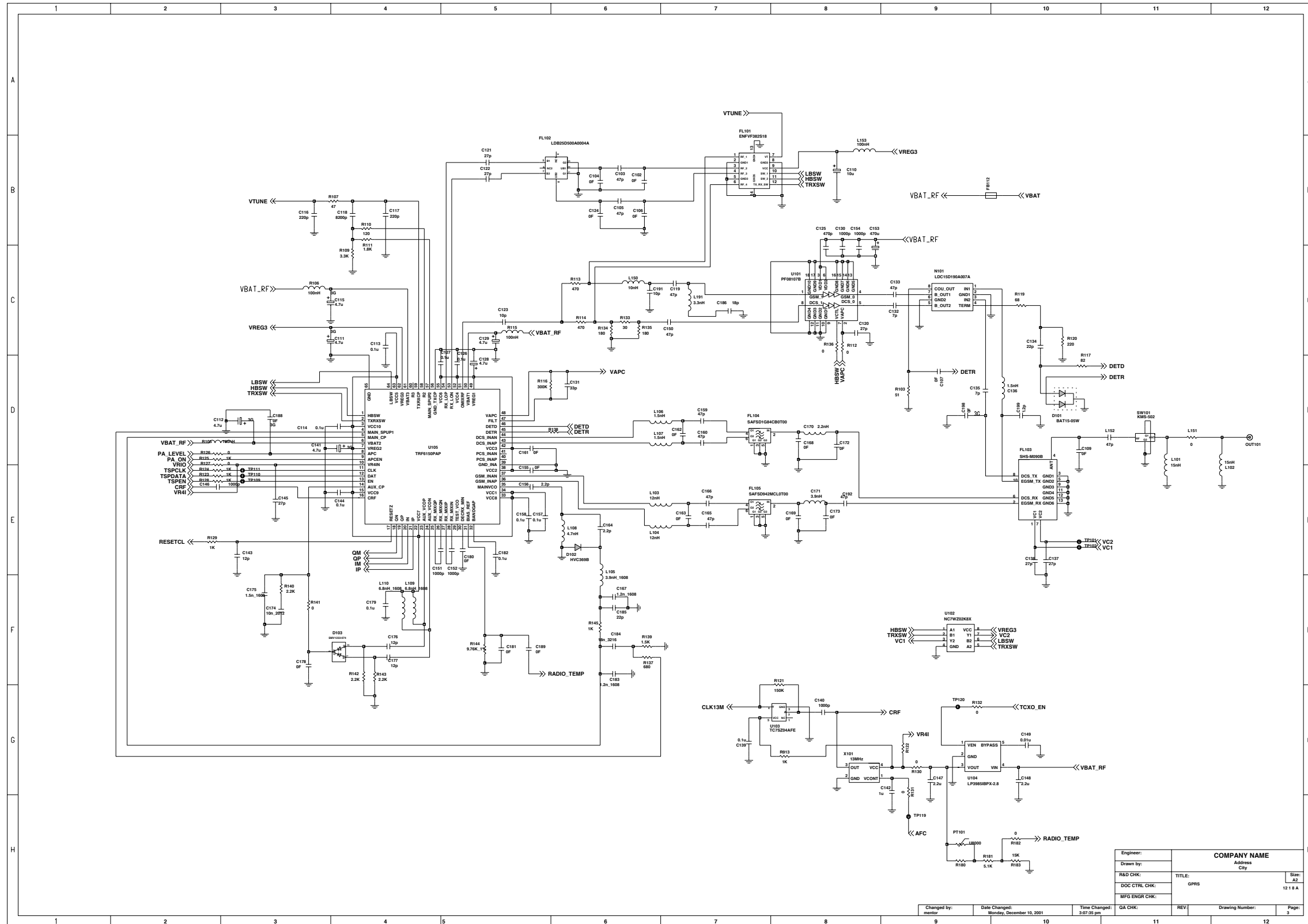
Engineer:	JF Lee	COMPANY NAME	Address	City	Size:
Drawn by:	me-mem	TITLE:	Memory	1218A	A2
RSD CHK:		DOC CTRL CHK:		MFG ENGR CHK:	

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
me-mem	Tuesday, December 11, 2001	5:23:17 pm				2

Main PCB Circuit (4/5)

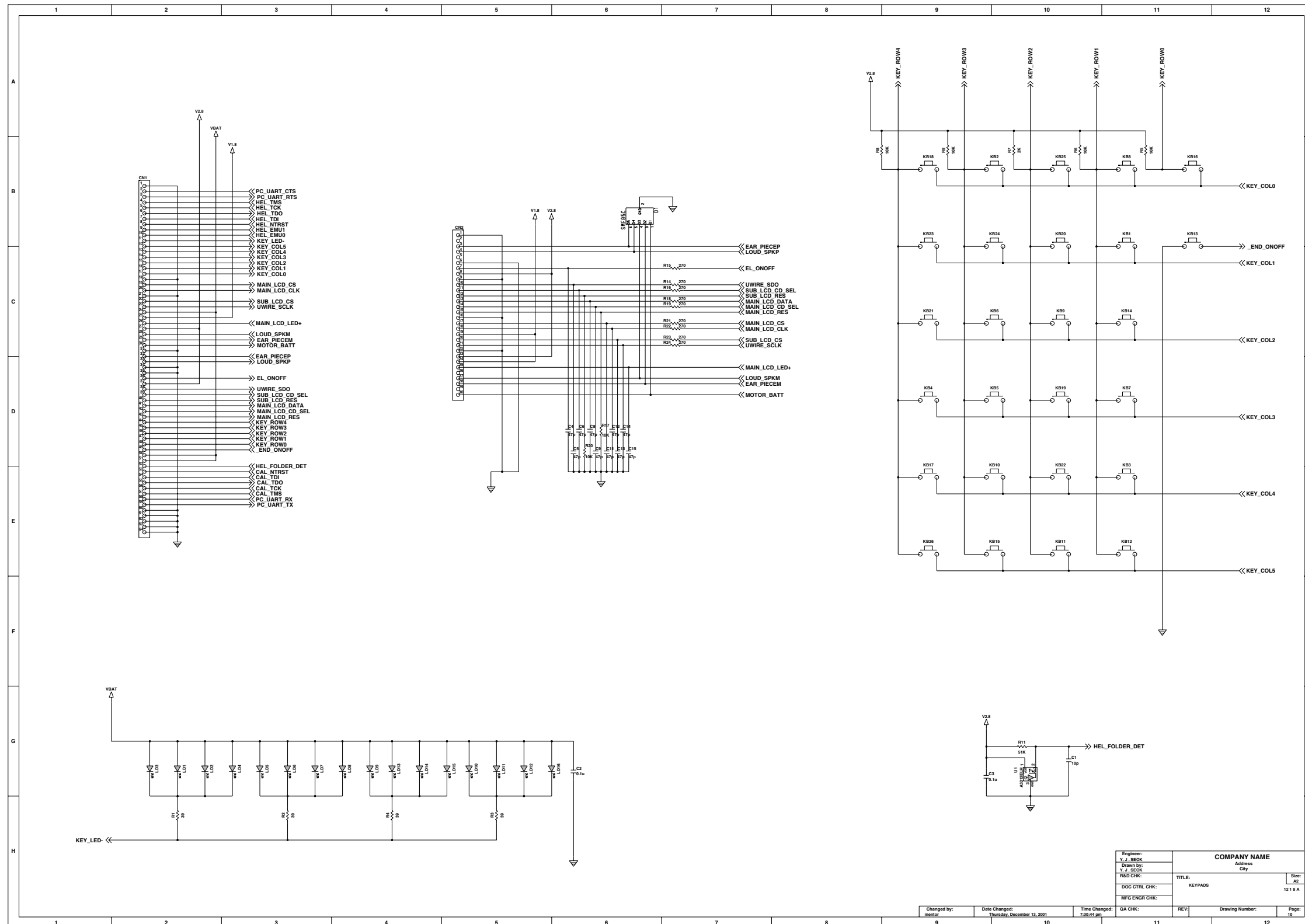


Main PCB Circuit (5/5)



9. CIRCUIT DIAGRAM

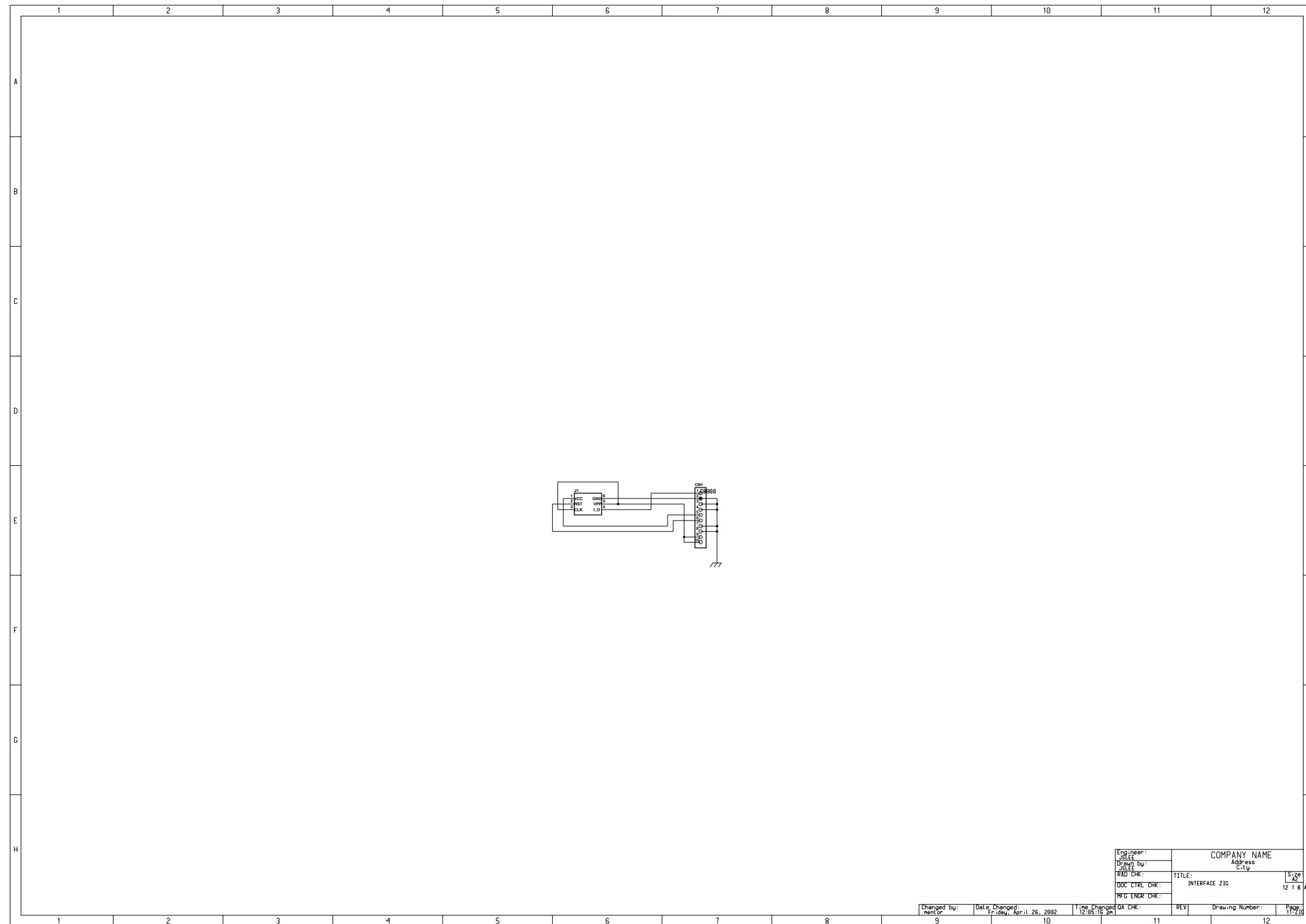
Keypad PCB Circuit (1/1)



Engineer:	COMPANY NAME	
Drawn by:	Address	
Drawn by:	City	
QA CHK:	TITLE:	Size:
DOC CTRL CHK:	KEYPADS	A2
MFG ENGR CHK:		1218A

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
emtr	Thursday, December 13, 2001	7:20:44 pm				10

SIM PCB Circuit (1/1)



Engineer:	COMPANY NAME	
Drawn by:	Address	Size
DATE	City	12 1 8 A
DDC' CTRL' CHK:	TITLE: INTERFACE ZIG	
MFG' ENGR' CHK:	REV	Drawing Number:

Changed by: Date Changed: Type Changed
 mentor 17-09-2002 12:05:10 pm

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