

UNISONIC TECHNOLOGIES CO., LTD

TDA7377

LINEAR INTEGRATED CIRCUIT

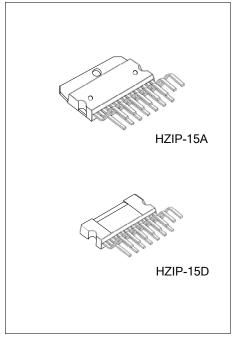
2 x 30W DUAL/OUAD POWER AMPLIFIER FOR CAR RADIO

DESCRIPTION

The UTC TDA7377 is a class AB car radio amplifier for car radio, it can work either in dual bridge or quad single ended configuration. The exclusive fully complementary structure of the output stage and the internally fixed gain guarantees the highest possible power performances with few external components. The on-board clip detector simplifies gain compression operation. The fault diagnostics makes it possible to detect mistakes during car radio set assembly and wiring in the car.

FEATURES

- * High Output Power@V_{CC}=14.4V, f=1KHz, RL=4Ω:
 - 2 x 35W Max.
 - 2 x 20W@THD= 10%
 - -4 x 6 W @10%
 - 4 x 10W / 2Ω@10%
 - -2×30 W / EIAJ@ V_{CC} =13.7V, RL=4 Ω
- * CMOS Compatible Stand-by Function (Low I_{CC})
- * No Audible pop During st-by Operations
- * Internally Fixed Gain (26dB BTL and 20dB single ended)
- * No Bootstrap Capacitors and boucherot Cells
- * Diagnostics Facility on pin10 when output Clipping, shorted to Vcc or GND, thermal shutdown and soft short at turn on.
- * Rail to rail output swing
- * Absolute Stability Without Any External Compensation.

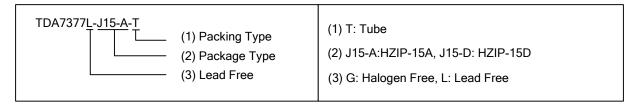


PROTECTIONS

- * Load Dump Voltages surge
- * Reversed Battery
- * Output DC Short Circuit protecttion with Low current when shorted to GND or V_{CC}.
- * Output AC short circuit protection: across the load
- * Silent Turn On/Off
- * thermal shutdown
- * Load very Inductive speakers
- * Fortuitous Open GND
- * ESD

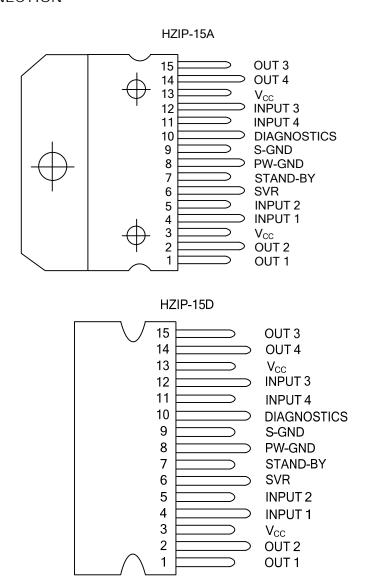
ORDERING INFORMATION

Ordering	Number	Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
TDA7266L-J15-A-T	TDA7266G-J15-A-T	HZIP-15A	Tube	
TDA7377L-J15-D-T	TDA7377G-J15-D-T	HZIP-15D	Tube	

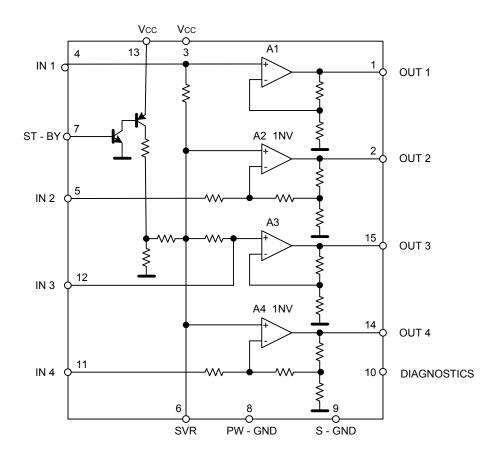


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■ PIN CONNECTION



■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

PARAME	SYMBOL	RATINGS	UNIT	
Operating Supply Voltage		V _{OP}	18	V
DC Supply Voltage		Vs	28	V
Peak Supply Voltage (for t = 50ms)		V _{S(PEAK)}	50	V
Output Peak Current	not Repetitive t = 100µs		4.5	Α
	Repetitive f > 10Hz	IO(PEAK)	3.5	Α
Dower Dissipation (T = 70°C)	HZIP-15A		33	W
Power Dissipation (T _C = 70°C)	HZIP-15D	P _D	30	
Junction Temperature	T_J	+150	°C	
Storage Temperature		T _{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Case	HZIP-15A	θ_{JC}	1.4	°C // //	
	HZIP-15D		1.8	°C/W	

■ ELECTRICAL CHARACTERISTICS

 $(V_S = 14.4V; R_L = 4\Omega; f = 1 \text{ KHz}; Ta = 25^{\circ}\text{C}, unless otherwise specified})$

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
Supply Voltage Range		Vs			8		18	V	
ST-BY Threshold Voltage OUT		$V_{I(ST-BY)}$					1.5	V	
		V _{O(ST-BY)}			3.5				
Voltage Saturation on pin	10	V_{SAT}	Sink Current a	nt Pin 10 = 1mA			0.7	V	
Output Offset Voltage		$V_{O(OFF)}$					150	mV	
			Rg = 0; S.E.	Non Inverting Channels		2		μV	
Input Noise Voltage		eN	"A" weighted, Inverting Channels			5		μν	
			Bridge, Rg = 0	Bridge, Rg = 0; 22Hz ~ 22KHz		3.5		μV	
Total Quiescent Drain Cur	rent	ΙQ	R _L = ∞				150	mA	
ST_RV Din Current(nin 7)		I _{ST-BY}	Max Driving Current Under Fault				5	mA	
ST-BY Pin Current(pin 7)		IST-BY	Play Mode Vpin7 = 5V				50	μΑ	
ST-BY Current Consumpti	T-BY Current Consumption I_{ST-BY} $V_{ST-BY} = 0 \sim 1.5V$		5V			100	μΑ		
Clipping Detector Output	OFF	I _{CD(OFF)}	d = 1% (Note 2)			90		μΑ	
Average Current	ON	I _{CD(ON)}	d = 5% (Note	2)		160		μΑ	
Input Impedance		R _{IN}	Single Ended		20	30		ΚΩ	
input impedance		IXIN	Bridge		10	15		IX12	
		P _{OUT}	THD = 10%;	Bridge	18	20		W	
Output Power				Single Ended	5.5	6			
				Single Ended, $R_L = 2\Omega$		10			
Output Power (Note 3)	Max	P _{O(MAX)}	$V_S = 14.4V, B$	ridge	31	35		W	
Output Fower (Note 3)	EIAJ	P _{O(EIAJ)}	V _S = 13.7V, Bridge		27	30		W	
Distortion		THD	$R_L = 4\Omega$ Single Ended, $P_{OUT} = 0.1 \sim 4W$ Bridge, $P_{OUT} = 0.1 \sim 10W$			0.02		%	
Distortion		וחט	Bridge, P _{OUT} = 0.1 ~ 10W			0.03	0.3	70	
Cross Talk		$CT \qquad \frac{f=1}{f=1}$	f = 1KHz Single Ended			70		dB	
			f = 10KHz Single Ended			60		dB	
			f = 1KHz Bridge		55			dB	
			f = 10KHz Bridge		- 55	60		dB	
Voltage Gain		Gv	Single Ended		19	20	21	dB	
			Bridge		25	26	27	dB	
Voltage Gain Match		G_V					0.5	dB	

■ ELECTRICAL CHARACTERISTICS(Cont.)

Supply Voltage Rejection	SVR	Rg = 0; f = 300Hz	50		dB
Stand-by Attenuation	A _{ST-BY}	$P_O = 1W$	80	90	dB

Note: 1. See built-in S/C protection description

2. Pin 10 Pulled-up to 5V with 10K Ω ; R_L = 4 Ω

3. Saturated square wave output.

■ TYPICAL TEST AND APPLICATION CIRCUIT

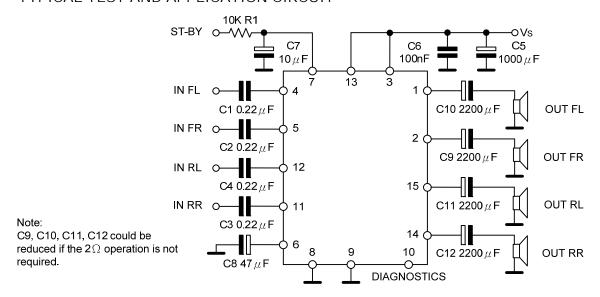


Figure 1. Quad Stereo

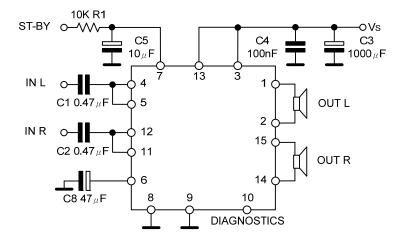


Figure 2. Double Bridge

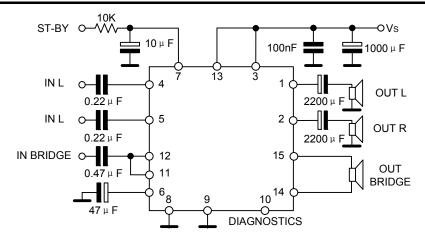


Figure 3. Stereo/Bridge

■ TYPICAL APPLICATION INFORMATION

Diagnostics Facility note:

UTC **TDA7377** built in a diagnostic circuitry, when following events appearing: clipping in the output signal, thermal shutdown, and output fault including short to GND, short to V_S and soft short at turn on.

When the event is detected, The information is available across an open collector output (pin 10) through a current sinking (see Fig 4). The current sinking at pin 10 is triggered when a certain distortion level is reached at any of the outputs. This function allows gain compression possibility whenever the amplifier is overdriven. The current sinking at pin 10 also can be triggered When the IC's operating temperature raise to about 10°C before the shutdown threshold.

Normally the clip detector signaling produces a low level at pin 10 that is shorter than that present under faulty conditions; This can be used to discriminate each event (clipping detection, output fault, thermal proximity).

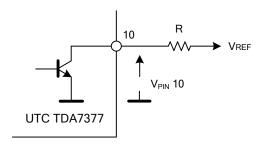


Figure 4. Pin10 Diagnostic Circuitry

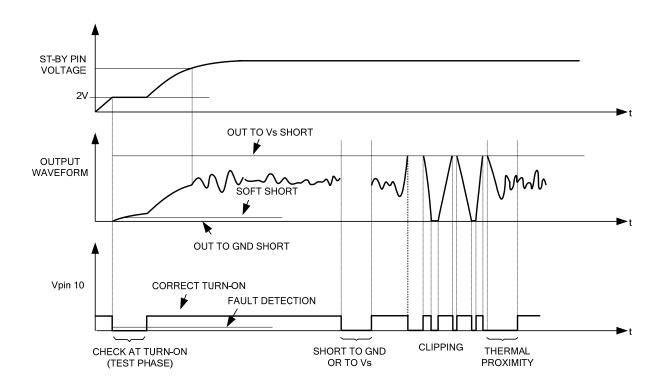
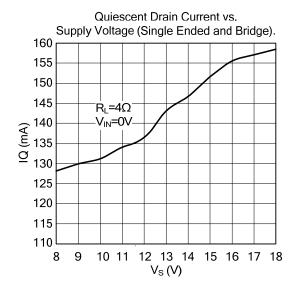
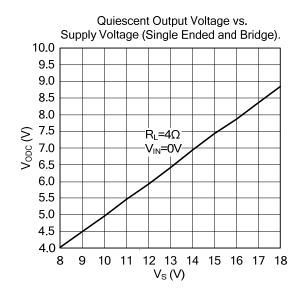
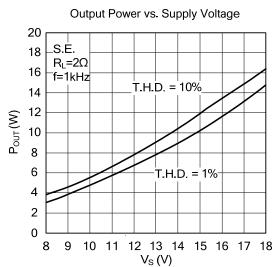


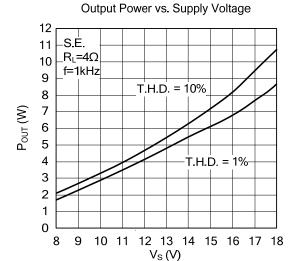
Figure 5. Waveforms

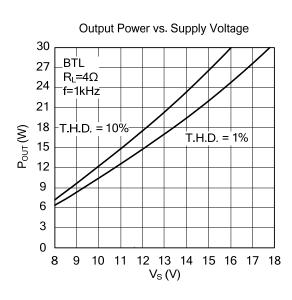
■ TYPICAL CHARACTERISTICS

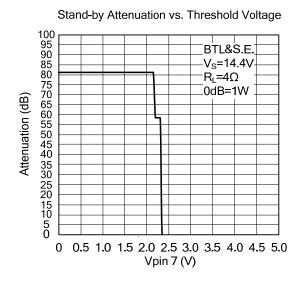












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