Design Services

Low Power ZigBee Module (Atmel)

TABLE OF AUTHORISATION

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DOCUMENT HISTORY

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1 INTRODUCTION

1.1 Purpose

The purpose of this document is to provide the product manual and Design Specifications for low power ZigBee (LPZigBee) module.

1.2 SCOPE

The scope of this document is to produce the detail design of LPZigBee module.

1.3 INTENDED AUDIENCE

This document is intended primarily for the EE Hardware Design.

1.4 DEFINITIONS AND ABBREVIATIONS

Term	Meaning
ADC	Analog to Digital Converter
GPIO	General Purpose Input/Output
I ² C	Inter Integrated Circuit
JTAG	Joint Test Action Group
FEM	Front-end module
SPI	Serial Peripheral Interface
UART	Universal Asynchronous Receiver Transmitter
NF	Noise figure
LPZigBee	Low power Zigbee Module

2 OVERVIEW

LPZigBee module mainly consists of three sections: the microcontroller ATXmega256D3 and periphery, the radio transceiver AT86RF231 and the RF frontend with power amplifier and low-pass filtering.

The ATXmega256D3 controls the AT86RF231 radio transceiver and serves as an SPI master. The radio transceiver handles all actions concerning RF modulation/demodulation, signal processing, frame reception and transmission. MAC hardware acceleration functions are implemented in the radio transceiver, too. Further information about the radio transceiver and the microcontroller are available in the appropriate datasheets, refer to 1.5 references.

The RF front-end incorporates signal amplification and filtering of the transmit signal. The degree of filtering depends on operating conditions as well as regional aspects. Switching between reception and transmission is directly controlled by the radio transceiver.

3 CONSTRAINTS, ASSUMPTIONS, AND DEPENDENCIES

- (1) Unless otherwise specified, all resistors, capacitors, inductors, & ferrite beads are 0402 size
- (2) C202, C400, L200, and L300 are 0603 size
- (3) The operating temperature range for all electrical components of LPZigBee module is **-40**°C to **+85**°C.
- (4) **With FEM function:** remove R200 & R203 & R208 and install R201 & R205 & R207
- (5) **Bypass FEM function for MMCX:** remove U201 & R201 & R205, R207 & R202 & R204 & C200 & C201 & C202 & C203 and install R200 & R203 & R208
- (6) **Bypass FEM function for PCB Antenna**: remove U201 & R207 & R201 & R202 & R204 & R208 & C200 & C201 & C202 & C203 and install R200 & R203 & R205
- (7) The impedances of Nets (RF_1, RF_2, RF_FEM, RF_BYPASS, ANT_1, ANT_2, ANT_F, and MMCX) are 50 ohm in PCB layout.
- (8) The following clocks of Nets (SPI_CLK, I2C_SCL, SCLK, MEM_SCK, CLKM and CLK_OUT) should follow the 3W rule for layout.

4 FEATURES

- Ultra compact size
- High RX sensitivity (-103 dBm)
- Outperforming link budget (123 dB)
- Up to +20 dBm output power
- Very low power consumption:
 - —<6 μA in Sleep mode,
 - 18 mA in RX mode,
 - 132 mA in TX mode@Pout is +20 dBm
- Ample memory resources (256 bytes of flash memory, 16K bytes RAM, 4K bytes EEPROM)
- Wide range of interfaces (both analog and digital):
 - 8 spare GPIO,
 - —2 ADC lines
 - --UART
 - I2C
 - —SPI
 - —JTAG
- Capability to use MAC address written into EEPROM
- IEEE 802.15.4 compliance
- 2.4 GHz ISM band
- BitCloud embedded software, including UART bootloader and AT command set

5 BLOCK DIAGRAM AND PORT FUNCTION

5.1 UART PORT

LPZigBee Module provides four signals (RX, TX) of standard RS-232 protocol to support user applications. The UART port is set the DCE port and connected with other devices by D-Sub-9 Female connector. But the RX, TX connection between RS-232 Transceiver and D-Sub-9 Female connector can be reversed by Jumpers, allowing use of connecting straight-though or cross-wires cable.

5.2 Programming and Debug Port

The LPZigBee module can be programmed and debugged by JTAG port. It is a 20 pins header and connects with Atmel's debugging tool.

5.3 **GPIO PORT**

The GPIO connectors provide the SPI, I2C, UART and GPIO interface for external system testing. The recommended operation voltage is +3.3Vdc.

6 DETAIL SPECIFICATIONS

6.1 PIN ASSIGNMENT

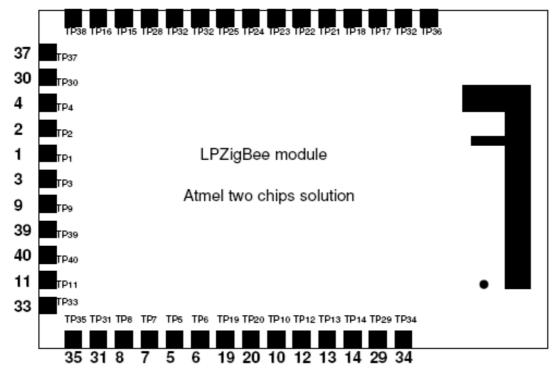
6.1.1 LPZigBee Module

Pin	Signal	Connection for 2-chip solution (ATxmega256D3)	
1	SPI_MISO	PD6/MISO	
2	SPI_MOSI	PD5/MOSI	
3	SPI_SCK	PD7/SCK	
4	SPI_SS_L	PD4/SSN	
5	UART_RXD	PE2/RXD0	
6	UART_TXD	PE3/TXD0	
7	I2C_SCL	PE1/SCL	
8	I2C_SDA	PE0/SDA	
9	JTAG_TCK	nc	
10	JTAG_TMS	nc	
11	JTAG_TDO	nc	
12	JTAG_TDI/PDI	PDI	
13	RESET_L	RESET	
14	CLK_OUT	PR0/xtal1	
15	ADC1	PB0/ACD8	
16	ADC2	PB1/ACD9	
17	PWM1_DC1	PF0/OC0A	
18	PWM1_DC2	PF1/OC0B	
19	PWM2_DC1	PE4/OC1A	
20	PWM2_DC2	PE5/OC1B	
21	GPIO1	PF2/INT/OC0C	
22	GPIO2	PF3/INT/OC0D	
23	GPIO3	PF4/INT	
24	GPIO4	PF5/INT	
25	GPIO5	PF6/INT	
26	GPIO6	PF7/INT	
27	GPIO7	PB2/INT/ADC10	
28	GPIO8	PB3/INT/ADC11	
29	VCC	Power Plane	
30	VCC	Power Plane	
31	VCC	Power Plane	
32	VCC	Power Plane	
33	GND	Ground Plane	
34	GND	Ground Plane	
35	GND	Ground Plane	
36	GND	Ground Plane	
37	GND	Ground Plane	

38	GND	Ground Plane
39	SPARE1	
40	SPARE2	

6.2 THE PCB OUTLINE FOR LPZIGBEE MODULE

38 16 15 28 27 26 25 24 23 22 21 18 17 32 36



6.3 ELECTRICAL SPECIFICATION

6.3.1 DC power supply range

System module	power supply range
Ateml solution w/o FEM	1.8~3.6V
Ateml solution with SE2431L@ NF of LNA=2 dB Gain of LNA=12.5 dB	1.8~3.6V (Transceiver) 2.0~3.6V (FEM)

6.3.2 System dBm max & min (maximum and minimum TX output at PCB antenna) and System current & power drawn.

System module	maximum TX output	minimum TX output
Ateml solution w/o FEM	2.1dBm@I=14mA	(-17.9)dBm@l=7.4mA
AtemI solution with SE2431L@ NF of LNA=2 dB Gain of LNA=12.5 dB	21dBm@lcc=134.13mA	5.6dBm@>37.4mA

6.3.3 System current & power drawn of TX@20dBm and RX

System module	TX@20dBm	RX
Ateml solution w/o FEM	NA	I=12.3 mA
Ateml solution with SE2431L@ NF of LNA=2 dB Gain of LNA=12.5 dB	I=130.83 mA	I=17.3 mA

6.3.4 System Current and Power consumption during idle and hibernate mode

	Idle mode	
System module		mode
	7mA @	100nA
Ateml solution w/o FEM	32MHz	
Ateml solution with SE2431L@ NF of LNA=2 dB	7mA @ 32MHz	100nA
Gain of LNA=12.5 dB		

6.3.5 System RX Sensitivity (dBm)

System module	Sensitivity (dBm)
Ateml solution w/o FEM	(-99.6)
Ateml solution with SE2431L@ NF of LNA=2 dB Gain of LNA=12.5 dB	(-103.63)

Federal Communication Commission Interference Statement

This module has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

FCC Caution: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

IMPORTANT NOTE:

FCC Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

This device is intended only for OEM integrators under the following conditions:

- 1) The antenna must be installed such that 20 cm is maintained between the antenna and users, and
- 2) The transmitter module may not be co-located with any other transmitter or antenna,

As long as 2 conditions above are met, further <u>transmitter</u> test will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed (for example, digital device emissions, PC peripheral requirements, etc.).

IMPORTANT NOTE: In the event that these conditions <u>can not be met</u> (for example certain laptop configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID <u>can not</u> be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

End Product Labeling

This transmitter module is authorized only for use in device where the antenna may be installed such that 20 cm may be maintained between the antenna and users. The final end product must be labeled in a visible area with the following: "Contains FCC ID: ZAR-TGPBA0010290".

Manual Information To the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.