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Realtek Bluetooth MP UI User Guide

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-1-





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Bluetooth MP Operation Flow

-2-

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Set "IsFirmwarePatchAtInit" in the HCISetup.ini, 0→Skip Patch, 1→Patch The patch file is "patch.bin" (located the same directory with RTK_BT_MP.exe)

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-3-

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The WiFi driver must download BT patch while DUT power on.

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Host Interface: Linux Platform

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Bluetooth MP UI Initial

-6-

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Host Interface: BT USB Port (8723AE,8723AE-VAU)

DUT

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Host Interface: BT USB Port (8723AE,8723AE-VAU)

H	lost Control Interface Selection	<u>n</u>	2
	-Select Host Control Interface	for Bluetooth	
	C BT USB USB1 💽	C BT UART COM1 . 115	200 • C BT PCIe PCIe1 •
	C WiFi USB	C WiFi SDIO	C WiFi PCIe
	C Linux by ADB	• Linux by UART COM1 💽	115200 -
		ок	
L			
/			

Select "BT USB", Choose USB port number, and Press "OK" button to start the MP.

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-8-

Host Interface: BT UART Port (8723AS)



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Host Interface: BT UART Port (8723AS)

Host Control Interface Selection	×
Select Host Control Interface for Bluetooth H5 C BT USB USB1 • C BT UART COM1 • 115200 • C BT PCIe PCIe1 •	
C WIFI USB C WIFI SDIO C WIFI PCIe	
ок	

Select "BT UART", Choose COM port number and baud rate (default: 115200), and Press "OK" button to start the MP.



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Host Interface: WiFi PCIe Port (8723AE)



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-11-

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Host Interface: WiFi PCIe Port (8723AE)

Host Control Interface Selection	n.	X
Select Host Control Interface	for Bluetooth	
C BT USB USB1 💽	C BT UART COM1 - 115200 -	C BT PCIe PCIe1 -
C WiFi USB	C WiFi SDIO	C WiFi PCIe
C Linux by ADB	• Linux by UART COM1 👤 11520	
	ок	

Select "WiFi PCIe", and Press "OK" button to start the MP.



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Host Interface: WiFi SDIO Port (8723AS)



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Host Interface: WiFi SDIO Port (8723AS)



Select "WiFi SDIO", and Press "OK" button to start the MP.



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Host Interface: WiFi USB Port (8723AS-VAU)



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Host Interface: WiFi USB Port (8723AS-VAU)

Host Control Interface Selection	n	
Select Host Control Interfac	e for Bluetooth	
C BT USB USB1 -	C BT UART COM1 - 115200 -	C BT PCIe PCIe1 -
C WIFI USB	C WiFi SDIO	C WiFi PCIe
C Linux by DB	€ Linux by UART COM1 ↓ 11520	00 🗸
	or	

Select "WiFi USB", and Press "OK" button to start the MP.



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Host Interface: Linux USB Port (8723AS,8723AS-VAU)



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Host Interface: Linux USB Port (8723AS,8723AS-VAU)

Host Control Interface Selection	L	
Select Host Control Interface	for Bluetooth	
C BT USB USB1 -	C BT UART COM1 . 115200 .	O BT PCIe PCIe1 -
C WiFi USB	C WIFI SDIO	C WiFi PCIe
C Linux by ADB	COM1 11520	0 -
	ОК	

Select "Linux by ADB", and Press "OK" button to start the MP.



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-18-

Host Interface: Linux COM Port (8723AS,8723AS-VAU)



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Select Host Control Interface

Host Control Interface Selection	1	
Select Host Control Interface	for Bluetooth	
C BT USB USB1 -	○ BT UART COM1 - 115200 -	C BT PCIe PCIe1 -
C WiFi USB	C WiFi SDIO	C WiFi PCIe
C Linux by ADB	C Linux by UART COM1 💽 115200	•
	ок	

Select "Linux by UART", Choose COM port number and baud rate (default: 115200), and Press "OK" button to start the MP.

Note: It is recommended to press the "OK" button after the system boot completely.

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-20-

The Main UI (BT Host type)

Actor room.	Please select A	Action	Start	Pause	Cle
Rx Packets:	0	Error Bits:	D BER (%):	0	Updat
Test Setup			14		
Data Rate:	1M 💌	Payload Type: 010101	Test Mode: Norr	mal Test 🗾 🔔	HLI Res
RF Channel:	0 💽	Payload bits: 2712	Hit Target: 0x00000	09e8b33 🗾	Test Mo
Tx Gain Index	: 7 💌	Packet Header: 0x3fff	E LE connect Ini	tiator 💌	Reg R
Tx Packet Co	int: 0	₩ Whiltening Coeff: 0x71	Tx DAC Curren	t: 🗗 🗾 🚽	Host Re-
USB Initial	ize success evice Addre	ful!! ss: 0x00E04C887232		-	

If initial ok, memo show as below

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The Main UI (WiFi Host type)

	u Realtek Bluetooth MP v2.7 RTL8723a (Control by Linux Console)	
	Action Item: Please select Action	Clear
	Tx Packets: 0 Error Bits: 0 BER (%): 0	
	Test Setup Data Rate: 1M - Payload Type: All 0's - Test Mode: RTK Test	HCI Reset
	RF Channel: 0 • Payload bits: 2712 Hit Target: 0x0000009e8b33 •	Test Mode
	Tx Gain Index: 7 Packet Header: 0x3ffff LE connect Initiator	Reg RW
	Tx Packet Count: 0 Vhiltening Coeff: 0x7f Tx DAC Current: 5	Host Re-Init
	2012/11/29 02:16:43	
If initial ok, memo show as below	Start Linux Console ok!! Driver already exists!! Enable WLAN Adapter ok!! Enter MP mode ok!! Download BT firmware ok!! BT is controlled by WiFi pow!!	
f initial fail, Check those:		
a. RS232/USB cable	BT Firmware version: 0x9a4d BT Firmware logic version: 0x9a	
b. UART setup /USB port	BT BD Address: 0x00e04c842713	Clear Log.
c. BT Module		

d. BT MP driver

c. BT Module

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Note: If initial fail (ex: enable WLAN adapter fail), it may result from the boot is not completed during MP UI start. You can press "Host Re-Init" button or reopen the MP UI. **REALTEK**

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Bluetooth DUT Test Mode Setup & Test Procedure



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-23-

BT DUT Test Mode Test Topology

The BT MP sends BT HCI command to DUT. The DUT enable Inqr/Page scan and enter DUT test mode. The Bluetooth tester established a link with the DUT over the RF channel using the normal Bluetooth protocol.



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UI Setup Step-1: Enter DUT Test Mode

188 Peeltek Binetooth MP v2.7 PTI 8793e (Control by Lingy Consola)			
Action Item: Please select Action	Start Pause	Clear	
Tx Packets: 0 Error Bits: 0 BE	R (%): 0		
Test Setup		1	
Data Rate: 1M 🔄 Payload Type: All 0's 🖵 Te	st Mode: RTK Test	HCI Reset	
RF Channel: 0 Payload bits: 2712 Hit	Target: 0x0000009e8b33 🗸	Test Mode	
Tx Gain Index: 7 Packet Header: 0x3ffff	LE connect Initiator	Reg RW	
Tx Packet Count: 0 Vhiltening Coeff: 0x7f	Tx DAC Current: 5	Host Re-Init	Press "Test Mode" button
			To enter BT DUT test mode.
Download BT firmware ok!!			
BT is controlled by WiFi now!!			
BT is ready!!			
BT Firmware version: 0x9a4d			
BT Firmware logic version: 0x9a BT BD Address: 0x00e0bc8b2713			
>> HCI Reset successful!!			
>> Enter DUT Test mode ok !!			
		Clear Log.	
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-25-

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After UI Setup Step-1

After Bluetooth test set creates a connection with DUT, the BT MP UI will show message as below. The RF test can kick off.

>>	[HCI Para]	-> Connection	Handle = Ox	002B		1
>>	[HCI Para]	-> BD_ADDR = 0	0x000272D199	C1		
>>	[HCI Para]	-> Link_Type =	= ACL			
>>	[HCI Para]	-> Encryption	= disabled			
>>	Write Link P	olicy to 0xf ((All On) OK!	•		
>	Write_Automa	tic_Flush_Time	eout (value :	= Øx3FF) comm	and succeeded	IT.
> >	Write_Automa Connect resu	tic_Flush_Time lts listing	eout (value : 	= Øx3FF) comm	and succeeded	
>> >>	Write_Automa Connect resu BD_ADDR	tic_Flush_Time lts listing Handle	eout (value · ··· Link_Type	= 0x3FF) comm Encryption	and succeeded [.] Mode	• •
>> (D	Write_Automa Connect resu BD_ADDR 0x000272D19	tic_Flush_Time lts listing Handle PC1 0x002B	eout (value - Link_Type ACL	= 0x3FF) comm Encryption Disable	and succeeded Mode Active	!!



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UI Setup Step-2: Exit DUT Test Mode (if required)

The connection can be disconnected by either BT test set or DUT. For DUT end, the connection will lost because of supervision timeout after HCI reset is executed.

🖉 Realtek Bluetooth MP v2.7	RTL8723a (Control by Linux Co	onsole)		
Action Item: Please select /	Action	Start Pause	Clear	
Tx Packets: 0	Error Bits: 0	BER (%): 0		
Test Setup			[h
Data Rate: 1M 🗸	Payload Type: All 0's 🗸	Test Mode: RTK Test	HCI Reset	Ц
RF Channel: 0	Payload bits: 2712	Hit Target: 0x0000009e8b33 🗸	Test Mode	
Tx Gain Index: 7	Packet Header: 0x3ffff	LE connect Initiator	Reg RW	Press "HCU Reset" button
Tx Packet Count: 0	☑ Whiltening Coeff: 0x7f	Tx DAC Current: 5	Host Re-Init	To exit BT DUT test mode.
Enter MP mode ok!! Download BT firmware o BT is controlled by Wi	к!! Fi пош!!			
BT is ready!! BT Firmware version: 0 BT Firmware logic vers BT BD Address: 0x00e04	x9a4d ion: 0x9a c842713			
>> HCI Reset successfu	111			
>> HCI Reset successfu	1**	ļ	Clear Log.	
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-27-

Bluetooth Non-Link Mode Setup & Test Procedure

-28-

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Non-Link Mode Test Topology

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For non-link mode test, the tester communicates with DUT over a cable via HCI with the DUT in a special test. The tester doesn't have to establish a protocol link with the DUT. The non-link mode of the 8723 series chip support "Packet-Tx", "Continue-Tx", and "Packet-Rx" for various RF performance test.





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Packet-Tx Setup

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Packet-Tx Run

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Action Item: Packet Tx		Start Pause	Clea
Tx Packets:	Error Bits: 0	BER (%): 0.0000	
Test Setup			HELDOS
Data Rate: 1M 🗾	Payload Type: Normal 💌	Test Mode: Normal Test.	
RF Channel:	Payload bits: 2712	Hit Target: 0x00004c123456	Test Mo
Tx Gain Index: 7	Packet Header: 0x3ffff	F LE connect Initiator	Reg R
Tx Packet Count: 0	□ Whiltening Coeff: 0x7f	Tx DAC Current: 5	Host Re-1
, in donce obdite 0	, solicing rubber 18 M		

if "Packet-Tx" ok, the message is shown as memo

"Packet-Tx" will stop after press "Pause" button

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Packet-Tx Measurement form IQ view

Date Rate: 1M/DH5, RF Channel: 0, Payload Length:2712 bits, Payload Type: 11110000, Whitening: Off



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Packet-Tx Measurement form IQ view

Date Rate: 1M/DH5, RF Channel: 0, Payload Length:2712 bits, Payload Type: 10101010, Whitening: Off



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Packet-Tx Measurement form IQ view

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Date Rate: 3M/3DH5, RF Channel: 0, Payload Length:8168 bits, Payload Type: Normal, Whitening: On



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Continue-Tx Setup

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Continue-Tx Run

1	nue Tx		Start	Pause	Clear
Packets:	0	Error Bits:	0 BER (%); 0.	0000	-
: Setup					
a Rate: 🛛 🔟	N T	Payload Type: 1010	Test Mode: RTK Te	est 🔽 🗕	HCI Reset
Channel:	-	Payload bits: 2712	Hit Target: 0x00004c1	23456	Test Mode
Gain Index: 7	<u>*</u>	Packet Header: 0x3fff	🗖 🗖 LE connect 🕅	tor 💌	Reg RW
Packet Count: 0		「 Whiltening Coeff: 0x7	Tx DAC Current:	5	
		16 A			Host Re-In
Enter TxRx To Write TxRx Co Start Continu 3T Status: RF Stop Test ok <u>}T Status: RF</u>	est mode onfigura Je-Tx Tr TxRx T TxRx T	e ok!! ation ok!! ransmitting fest Mode + under Cont fest Mode + idle	tinue Tx test	-	

if "Continue-Tx" ok, the message is shown as memo

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"Continue-Tx" will stop after press "Pause" button

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Continue-Tx Measurement form IQ view

"Continue-Tx" is used for Tx power measurement.



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Packet-Rx Setup

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Packet-Rx Run

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	🕮 Realtek Bluetooth MP v2.81 RTL8723a (Control by Linux Console)		
	Action Item: Packet Rx Start Pause	Clear	
"Packet-Rx" test result —	Rx Bits: 1870472 Error Bits: 0 BER (%): 0,0000	_	
	Test Setup		
	Data Rate: 3M Payload Type: 0x0~0x Test Mode: RTK:Test .	HCI Reset	
	RF Channel: 0 y Payload bits: 8168 Hit Target: 0x00004c123456 y	Test Mode	
	Tx Gain Index: 7 Packet Header: 0x3ffff T LE connect Initiator	Reg RW	
	Tx Packet Count: Image: Whilening Coeff: 0x7f Tx DAC Current: 5 Image: Second Sec	Host Re-Init	
if "Packet-Rx" ok, the message is shown as memo "Packet-Rx" will stop after press "Pause" button	<pre>>> HCI Reset successful!! >> Translate BD Address: 00004c123456 to Access Code >> Write Modem 0x1c AccessCode[52:67] as 0x120d >> Write Modem 0x1e AccessCode[36:51] as 0x5a8b >> Write Modem 0x20 AccessCode[20:35] as 0x61a6 >> Write Modem 0x22 AccessCode[4:19] as 0x03e4 >> Enter TxRx Test mode ok!! >> Write TxRx Configuration ok!! >> Start Packet-Rx Receiving >> BT Status: RF TxRx Test Mode + under Rx test</pre>	Clear Log.	
		REA	LTEK
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IQ view Vector Signal Generator

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Used the IQ view Vector Signal Generator to generate test pattern (*.mod)



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Bluetooth BT 4.0 LE

Direct Test Mode Setup

For **RF/PHY** Testing



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-41-

Most BT testers support host control port (USB,UART) that can be as a "upper tester" defined in the direct test mode of BT specifications. The diagram shown as below is the MT8852B test configuration for LE RF/PHY test.

The BT MP also supports Tx and Rx test when the BT tester can not be a "upper tester" (no host control port, ex: IQ view).





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Tx Test Setup:

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		Press "Start" to start Tx and BT Press "Stop" to stop Tx. tester can start measurement.
Select "Packet-Tx"	1	Image: Start Pause Clear Tx Packets: 0 Error Bits: 0 BER (%): 0 Update Rep
Select "Data Rate", "RF Channel", "Tx Gain Index", 'Payload Type", "Payload bytes"	2	Test Setup Data Rate: LE Payload Type: PRBS9 Test Mode: Normal Test HCI Reset RF Channel: 0 Payload bytes: 37 Hit Target: 0x0000009e8b33 Test Mode Tx Gain Index: 7 Packet Header: 0x3ffff LE connect Initiator Reg RW
'Date Rate" = [LE]		Tx Packet Count: Whiltening Coeff: Dx7f Tx DAC Current: Host Re-Init Host Re-Init Host Re-Init
		LMP_Version: 0x06 Manufacturer_Name: Realtek Semiconductor Corporation LMP_Subversion: 0x9b79 Supported Feature(0~7): 0xFF 0xFB 0xFF 0xFE 0xDB 0xFF 0x7B 0x87
		<pre>>> Start Transmitting >> Stop Transmitting!! </pre> ClearLog.
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Rx Test Setup:

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Select "Packet-Rx" 1 Select "Packet-Rx" 1 Action Item: Facket Rx Start Pause Clear Rx Bits: 0 Error Bits: 0 BER (%): 0 Update Rep HCI Reset Rx Bits: 0 Error Bits: 0 BER (%): 0 Update Rep HCI Reset HCI Reset In Control "Date Rate" = [LE] ** Holl Reset successful!! >> HCI Reset successful!! >> Pseudo_Duter/BER/Report Count Enable
Select "Packet-Rx" Action Item: Packet Rx Start Pause Clear Rx Bits: 0 Error Bits: 0 BER (%): 0 Update Rep Select "Data Rate", "RF 2 Test Setup HCI Reset HCI Reset HCI Reset "Payload bytes" Payload Type: PRS9 Test Mode: Normal Test Test Mode: Normal Test Test Mode: Normal Test Test Mode: "Data Rate" = [LE] Value Payload bytes: 37 Hit Target; 0x0000098eb33 Tx Connect Initiator Reg RW "Date Rate" = [LE] Value Whithening Coeff: 0x75 Tx DAC Current: Host Re-Init >> HGI Reset successful!! >> Pseudo_Outer/BER/Report Count Enable Normal Test Normal Test Normal Test
Select "Data Rate", "RF 2 Channel", "Payload Type", "Payload bytes" Image: Channel: Comparison of the section of the sect
Select "Data Rate", "RF Channel", "Payload Type", "Payload bytes" "Date Rate" = [LE] LE connect Initiator Reg RW Tx Packet Count: Whiltening Coeff: 0x75 Tx DAC Current: 4 Host Re-Init HIT Reset Header: >> HCI Reset successful?! >> HCI Reset successful?!
Select "Data Rate", "RF Channel", "Payload Type", "Payload bytes" "Date Rate" = [LE] Hit Reset successfult! >> HCI Reset successfult! >> Packet Header: Count Enable
"Payload bytes" "Date Rate" = [LE] Host Reset successful!! >> HCI Reset successful!! >> Pseudo_Outer/BER/Report Count Enable
'Date Rate" = [LE]
<pre>>> HCI Reset successful!! >> Pseudo_Outer/BER/Report Count Enable</pre>
<pre>>> Translate BD Address: 0000009e8b33 to Access Code >> Write Modem 0x1c AccessCode[52:67] as 0x5e72 >> Write Modem 0x1e AccessCode[36:51] as 0x7334 >> Write Modem 0x20 AccessCode[20:35] as 0x58cc >> Write Modem 0x22 AccessCode[4:19] as 0x475c</pre>
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BT 4.0 LE Setup For FCC AFH Measurement

-45-



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Step 1: Create LE connection (Initiator)

- 1. Checked [LE connect] and select "Initiator".
- 2. Key in the Target BD Address in the [Hit Target] and press "Enter" key
- 3. In the Action Items, select "Page + Create ACL connection"
- 4. press [Start] button

🐺 Realtek Bluetooth MP v2.82	- RTL8723a (Control by WiFi PC	le Host)	
Action II Page + Create /	4.	Start Pause	Clear
Rx Bits: 0	Error Bits: 0	BER (%): 0	🗖 Inverse Ant SW
_Test Setup			7
Data Rate: 1M 🖵	Payload Type: 010101	Test Mode: Normal Test 🚽	HCI Reset
RF Channel:	Connect Handle: 2712 1.	Hit Target: 0x00e04c335588 💌	Test Mode
Tx Gain Index: 7	Packet Header: 0x3ffff 2	🔽 LE connect 🛛 Initiator 💽	Reg R₩
Tx Packet Count: 0	Whiltening Coeff: 0x7f	Tx DAC Current: 5	Host Re-Init
<pre>>> Write Modem 0x20 Acc >> Write Modem 0x22 Acc >> Translate BD Address >> Write Modem 0x1c Acc >> Write Modem 0x1e Acc >> Write Modem 0x20 Acc >> Write Modem 0x22 Acc</pre>	essCode[20:35] as 0x58c essCode[4:19] as 0x475c : 00e04c335588 to Acces essCode[52:67] as 0xb30 essCode[36:51] as 0x046 essCode[20:35] as 0x40e essCode[20:35] as 0x199c	c 🕹 🗠 🖍	
 >> Enter Connect Test m >> Set Connet Target ok >> Start LE Connect Tes >> BT Status: Connect T 	ode ok!! !! t (initiator, target = est Mode + under waitin	0x00e04c335588) ok !! g connection	
K		>	Liear Log.



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-46-

Step 2: Create LE connection (Advertiser)

- 1. Checked [LE connect] and select "Advertiser".
- 2. In the Action Items, select "Page + Create ACL connection"
- 3. press [Start] button

蹦 Realtek Bluetooth MP v2.6	82 RTL8723a (Control by WiFi PCIe Host)					
2. Action Iten : Page + Cre	ate ACL Connection	Clear				
Rx Bits:	0 Error Bits: 0 BER (%): 0	🗖 Inverse Ant SW				
-Test Setup						
Data Rate: 1M	Payload Type: 010101 Test Mode: Normal Test	HCI Reset				
RF Channel:	Connect Handle: 2712 Hit Target: 0x00e04c335588	Test Mode				
Tx Gain Index: 7	Packet Header: 0x3fff 1, IV LE connect Advertiser 💽	Reg RW				
Tx Packet Count: 0	Whiltening Coeff: Dx7f Tx DAC Current: 5	Host Re-Init				
BT is ready!! BT is ready!! BT Firmware version: 0x9b79 BT Firmware logic version: 0x00 BT BD Address: 0x446d5731c5ff						
	view and the second second	Clear Log.				
<	>					

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Step 3: Create LE connection

Initiator will create LE connection with Advertiser and AFH start running.







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Setup Antenna Switch (support after v2.82)

瓣 Realtek Bluetooth MP v2.82	RTL8723a (Control by Linux C	onsole)				
Action Item: Please select A	ction 🔽	Start	Pause	Clear	,	If Checked
Tx Packets: 0	Error Bits: 0	BER (%):	0	🗌 Inverse Ant SW		eise Main -
Test Setup	Pavload Type: All 0's	Test Mode: RTK	Test	HCI Reset		
RF Channel: 0	Payload bits: 2712	Hit Target: 0x00000	09e8b33 👻	Test Mode		
Tx Gain Index: 7 💌	Packet Header: 0x3ffff	🗆 LE connect 🛛 Ini	tiator 💌	Reg RW		
Tx Packet Count: 0	☑ Whiltening Coeff: 0x7f	Tx DAC Curren	t: 5 🔻	Host Re-Init		
2013/1/2 10:57:18 Start Linux Console ok! No MP driver exists!! Insert MP driver ok!! Enable WLAN Adapter ok! Enter MP mode ok!! Download BT firmware ok BT is controlled by WiF 	" " " " " i now!! :9a4d		=			
BT Firmware logic versi BT BD Address: 0x00e04c	.on: 0x9a 842713		>	Clear Log.		

Main \rightarrow WiFi

→ BT



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FCC Warning Statement

Changes or modifications not expressly approved by the party responsible for complia nce could void the user's authority to operate the equipment. This equipment has been tested and found to comply with the limits for a Class B digi tal device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed an d used in accordance with the instructions, may cause harmful interference to radio co mmunications. However, there is no guarantee that interference will not occur in a par ticular installation. If this equipment does cause harmful interference to radio or televi sion reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measu res: - Reorient or relocate the receiving antenna.

- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

This module is intended for OEM integrator. The OEM integrator is still responsible for the FCC compliance requirement of the end product which integrates this module.

The final end product must be labeled in a visible area with the following" Contains TX FCC ID: 2AC23-WT4XR1210".

The FCC part 15.19 statement below has to also be available on the label: This device complies with Part 15 of FCC rules.

Operation is subject to the following two conditions: (1) this device may not cause harmful interference and

(2) this device must accept any interference received, including interference that may cause undesired operation.

The end user has to be informed that the FCC radio-frequency exposure guidelines for an uncontrolled environment can be satisfied. The end user has to also be informed that any changes or modifications not expressly approved by the manufacturer could void the user's authority to operate this equipment.

