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Command Window	
MSC(0x00) 2001-06-14 19:36:04 M2055 DISPLAY RS2 FORWARD POWER DATA LOCATION : BSC_0/BTS_0 FER_ID : 0 [0.5 PERCENT] INIT_DOWN_TIME : 1000 MAX_DOWN_TIME : 4000 MIN_DOWN_TIME : 200 TIME_UP_DELTA : 200 TIME_UP_DELTA : 200 STATE_CHG_THRESH : 250 ERASE_MEAS_FRAMES : 250 CONT_ERASE_EFTV : 2 CUMUL_ERASE_EFTV : 4 NOM_GAIN : 50 MAX_TX_GAIN : 100 MIN_TX_GAIN : 40 GAIN_DOWN_DELTA : 1 BIG_UP_DELTA : 10 SMALL_UP_DELTA : 5 SIGNAL_DELTA_GAIN : 96 PCSC_DELTA_GAIN3 : 112 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-RS2-FWDP:BSC=0,BTS=0,FER=P0INT_5; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-27 Forward Link Power Management Information (RS2) Verification

4.3.2.6. Backward Link Power Management Information (RS2) Verification

Rate Set 2 transmits data at the speed of 14400,7200,3600,1800 bps. Input the following command to check the parameter information for RS2 Backward Link Power Management. Among the input values, FER (Frame Error Rate) has the value ranging from 0.5%, 1 to 5%.

- Command DIS-RS2-REVP: BSC=a, BTS=b, FER=c;
 - a : BSC Number(0~11)
 - b: BTS Number(0~47) c:FER
- (POINT_5/PERCENT_1/PERCENT_2/PERCENT_3/PERCENT_4/PERCET_5)
- Input DIS-RS2-REVP: BSC=0, BTS=0, FER=POINT_5;
- Output



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Command Window	
MSC(0x00) 2001-06-14 19:37:17 M2056 DISPLAY RS2 REVERSE POWER DATA LOCATION : BSC_0/BTS_0 FER_ID : 0 [0.5 PERCENT] PWRCTL_NOMINAL : 24832 PWRCTL_MAX : 25856 PWRCTL_MIN : 23808 RPC_BIG_UP_DELTA : 1024 RPC_SMALL_UP_DELTA : 512 RPC_NON_ERASE_WAIT : 2 RPC_MAX_DOWN_DELTA : 32 RPC_MIN_DOWN_DELTA : 1 RPC_DOWN_DELTA : 1 RPC_CONT_ERASE_EFTV : 2 RPC_CUMUL_ERASE_EFTV : 2 RPC_CUMUL_ERASE_EFTV : 4 RPC_ERASE_MEAS_FRAMES : 200 RPC_STATE_CHG_THRESH : 100 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-RS2-REVP:BSC=0,BTS=0,FER=P0INT_5; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-28 Backward Link Power Management Information (RS2) Verification

4.3.2.7. Service Option FER Verification

Target FER can be designated by the Service Option. Input the following command to display this information. In the next display, 1% Target FER is applied for the "8K VOICE" Service Option and for this, the following power management information is used: RS1 forward link power management information, RS1 backward link power management information, and RS2 backward (or reverse-direction) link power management information.

- Command DIS-FER-PARA: BSC=a;
 - a:BSC Number(0~11)
- Input DIS-FER-PARA: BSC=0;
- Output



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Fig. 4.3-29 Service Option FER Verification

4.3.2.8. MAHHO Verification

- Command DIS-MAHH-PARA: BSC=a, BTS=b, SECT=c;
 - a : BSC Number(0~11)
 - b: BTS Number(0~47)
 - c: Sector Id .(ALPHA/BETA/GAMMA)
- Input DIS-MAHH-PARA: BSC=0,BTS=0,SECT=ALPHA;
- Output

Command Window	
MSC(0x00) 2001-06-14 19:39:45 M2061 DISPLAY MAHHO DATA LOCATION : BSC_0/BTS_0/ALPHA BOADER_FLAG : 0FF NUM_0F_CDMACH : 1 CDMA_FREQ[CDMACH_00] : 50 SF_TOTAL_EC_THR : 31 SF_TOTAL_EC_THR : 9 DIFF_RX_PWR_THR : 0 MIN_TOTAL_PILOT_EC_IO : 26 CF_T_ADD : 26 TF_WAIT_TIME : 12 CF_SRCH_WIN_N : 9 CF_SRCH_WIN_R : 10 COMPLETED	
TO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS	
[jhpark <lgicbsm>] DIS-MAHH-PARA:BSC=0,BTS=0,SECT=ALPHA; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-30 MAHHO Verification



4.3.2.9. LOCATION Verification

- Command DIS-LOC-PARA: BSC=a, BTS=b, SECT=c;
 - a:BSC Number(0~11)
 - b: BTS Number(0~47)
 - c: Sector Id .(ALPHA/BETA/GAMMA)
- Input DIS-LOC-PARA: BSC=0,BTS=0,SECT=ALPHA;
- Output

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Command Window	
MSC(0x00) 2001-06-14 19:41:10 M2062 DISPLAY LOCATION DATA LOCATION : BSC_0/BTS_0/ALPHA ACTION_TIME_FRAME : 0 PUF_SETUP_SIZE : 0 PUF_PULSE_SIZE : 0 PUF_INITERVAL : 0 PUF_INIT_PWR : 0 PUF_INIT_PWR : 0 PUF_PWR_STEP : 0 TOTAL_PUF_PROBES : 0 MAX_PWR_PUF : 0 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-LOC-PARA:BSC=0,BTS=0,SECT=ALPHA; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-31 LOCATION Verification



4.3.2.10. SCH Verification

- Command DIS-SCH-PARA: BSC=a, BTS=b, SECT=c;
 - a : BSC Number(0~11)
 - b: BTS Number(0~47)
 - c: Sector Id .(ALPHA/BETA/GAMMA)
- Input DIS-SCH-PARA: BSC=0,BTS=0,SECT=ALPHA;
- Output

	×
Command Window	
MSC(0x00) 2001-06-14 19:42:18 M2063 DISPLAY SCH DATA L0CATION : BSC_0/BTS_0/ALPHA SCH_T_ADD : 22 SCH_T_DROP : 26 T_MULCHAN : 0 BEGIN_PREAMBLE : 0 RESUME_PREAMBLE : 0 PS_MIN_DELTA : 0 ORDER_INTERVAL : 0 NUM_PILOTS : 0 PERIODIC_INTERVAL : 0 PS_FLOOR_HIGH : 0 PS_FLOOR_LOW : 0 THRESHOLD_INTERVAL : 0 T_SLOTTED : 0 COMPLETED	
<u>IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-SCH-PARA:BSC=0,BTS=0,SECT=ALPHA; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	Z Z

Fig. 4.3-32 SCH Verification



4.3.2.11. Power Control Data Verification

- Command DIS-PWR-CTRL: BSC=a, BTS=b, FER=c;
 - a:BSC Number(0~11)
 - b: BTS Number(0~47)
 - c: FER (0~30)
- Input DIS-PWR-CTRL: BSC=0, BTS=0,FER=0;
- Output

Command Window	
MSC(0x00) 2001-06-14 19:43:29 M2065 DISPLAY POWER CONTROL DATA LOCATION : BSC_0/BTS_0 FFR_ID : 0.2[%] PWR_CNTL_STEP : 0 FPC_MODE : 0 FPC_FCH_INI_SETPT : 64 FPC_FCH_MIN_SETPT : 0 FPC_DCCH_MIN_SETPT : 0 FPC_DCCH_MIN_SETPT : 0 FPC_SCH_MIN_SETPT : 0 FPC_SCH_MIN_SETPT : 128 FPC_SCH_MIN_SETPT : 128 FPC_SCH_MAX_SETPT : 10 FCL_SCH_MAX_ADJ_GAIN : 0 FCL_SCH_MAX_ADJ_GAIN : 0 SCH1_CHAN_ADJ_GAIN : 0 SCH1_CHAN_ADJ_GAIN : 0 RL_ATT_ADJ_GAIN : 0 NOT USE REV_PILOT : 0 USE REV_PILOT : 0 USE REV_PILOT : 0 USE REV_PILOT : 0 RL_ATT_ADJ_GAIN_4800 NOT USE REV_PILOT : 0 USE RE	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-PWR-CTRL:BSC=0,BTS=0,FER=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	L A
×	12



Fig. 4.3-33 Power Control Data Verification

4.3.2.12. BTS Name Display

Input the following command to display the BTS name:

- Command DIS-BTS-NAME: BSC=a, BTS=b;
 - a:BSC Number(0~11)
 - b: BTS Number(0~47)
- Input DIS-BTS-NAME: BSC=0, BTS=0;
- Output

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	Command Window	- X
MSC	(0x00) 2001-06-14 19:45:15 M2070 DISPLAY BTS NAME BSC_0 : 0 BSC_0/BTS_0 : ghjeong PLETED	
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS	
[jhp ACCI [jhp	park <lgicbsm>] DIS-BTS-NAME:BSC=0,BTS=0; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-34 BTS Name Display



4.3.2.13. PCP Timer Information Verification

- Command DIS-PCF-TIMER:PCP =a;
 - a : PCP Number(0~2)
- Input DIS-PCF-TIMER: PCP=0;
- Output

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Command Window	
MSC(0x00) 2001-06-14 19:46:30 M2092 DISPLAY PCP TIMER PARAMETER LOCATION : PCP_0 TRP_LIFETIME : 18000 [sec] TBSREQ9 : 5000 [msec] TDISCON9 : 5000 [msec] TWAITH09 : 10000 [msec] TREGREQ : 5000 [msec] REGREQ_RETRY_CNT : 2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-PCF-TIMER:PCP=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-35 PCP Timer Information Verification



4.3.2.14. PCF Parameter Information Verification

• Command DIS-PCF-PARA :PCP =a;

a : PCP Number(0~2)

- Input DIS-PCF-PARA: PCP=0;
- Output

Command Window	
MSC(0x00) 2001-06-14 19:47:37 M2093 DISPLAY PCF PARAMETER L0CATION : PCF_0 PCP[A_SIDE] IP : 0.0.0.0 PIP[00] IP : 10.0.0.0 PIP[00] IP : 10.0.0.0 NETMASK : 255.255.255.0 GATEWAY : 0.0.0.0 NETMASK : 0.0.0.0 NETMASK : 0.0.0.0 NETMASK : 0.0.0.0 PIP[01] IP : 0.0.0.0 NETMASK : 0.0.0.0 GATEWAY : 0.0.0.0 NETMASK : 0.0.0.0 PIP[03] IP : 0.0.0.0 NETMASK : 0.0.0.0 GATEWAY : 0.0.0.0 NETMASK : 0.0.0.0 NETMASK : 0.0.0.0 GATEWAY : 0.0.0.0 NETMASK : 0.0.0.0 NETMASK : 0.0.0.0 PIP[05] IP : 0.0.0.0 NETMASK : 0.0.0.0	
[jhpark <lgicbsm>] DIS-PCF-PARA:PCP=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	E E
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4.3.2.15. PCF MAC ADDRESS Information Verification

- Command DIS-PCF-MAC :PCP =a;
 - a : PCP Number(0~2)
- Input DIS-PCF-MAC: PCP=0;
- Output

Command Window 🛛 🗟	
M2096 DISPLAY PCF MAC ADDRESS L0CATION : PCF_0 PCP MAC ADDR : 0.0.0.0.0.0 PIP[00] MAC : 0.0.0.0.0.0 PIP[01] MAC : 0.0.0.0.0.0 PIP[02] MAC : 0.0.0.0.0.0 PIP[03] MAC : 0.0.0.0.0.0 PIP[04] MAC : 0.0.0.0.0.0 PIP[05] MAC : 0.0.0.0.0.0 PIP[06] MAC : 0.0.0.0.0.0 PIP[06] MAC : 0.0.0.0.0.0 PIP[07] MAC : 0.0.0.0.0.0 PIP[08] MAC : 0.0.0.0.0.0 PIP[09] MAC : 0.0.0.0.0.0 PIP[01] MAC : 0.0.0.0.0.0 PIP[02] MAC : 0.0.0.0.0.0 PIP[03] MAC : 0.0.0.0.0.0 PIP[04] MAC : 0.0.0.0.0.0 PIP[05] MAC : 0.0.0.0.0.0 PIP[03] MAC : 0.0.0.0.0.0 PIP[04] MAC : 0.0.0.0.0.0 PIP[03] MAC : 0.0.0.0.0.0 PIP[04] MAC : 0.0.0.0.0.0 PIP[05] MAC : 0.0.0.0.0.0 PIP[06] MAC : 0.0.0.0.0.0 PIP[07] MAC : 0.0.0.0.0.0 PIP[08] MAC : 0.0.0.0.0.0 PIP[09] M	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] DIS-PCF-MAC:PCP=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-37 PCF MAC ADDRESS Information Verification

4.3.3. NETWORK

Information

Display

(Display_Parameter_Information_3)

4.3.3.1. CAN ATM NODE Information Display

- Command DIS-CAN-NODE;
- Output

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Command Window	
MSC(0x00) 2001-06-14 19:52:44 M2400 DISPLAY CAN ATM NODE INFORMATION CARD LINK EQUIP LINK_TYPE LINK_STS CONN_TYPE FABRIC_NO 0 0 EQUIP STM-1 ACTIVE ASCA ASCA APCO 0 4 EQUIP STM-1 ACTIVE CNP_A ASCA APCO 0 5 EQUIP STM-1 ACTIVE CNP_B ASCA APCO 0 8 EQUIP STM-1 ACTIVE CNP_B ASCA APCO 1 0 EQUIP STM-1 ACTIVE BSM_B ASCA APCO 1 0 EQUIP STM-1 ACTIVE BSM_B ASCA APCO 1 0 EQUIP STM-1 ACTIVE BSCO ASIAO APCO 1 4 EQUIP STM-1 ACTIVE BSCO ASIAO APCO 1 5 EQUIP STM-1 ACTIVE BSCO ASIAO APCO 1 6 EQUIP STM-1 ACTIVE BSC1 ASIAO APCO 1 7 EQUIP STM-1 ACTIVE BSC2 ASIAO APCO 1 7 EQUIP STM-1 ACTIVE ENP ASIAO APCO 1 2 0 EQUIP STM-1 ACTIVE ENP ASIAO APCO 2 7 EQUIP STM-1 ACTIVE ASIA1 ASIA1 APCO 2 0 EQUIP STM-1 ACTIVE ASIA1 ASIA1 APCO 2 2 3 EQUIP STM-1 ACTIVE ASIA1 ASIA1 APCO 3 4 EQUIP STM-1 ACTIVE ATSAO-A ASIA1 APCO 3 5 EQUIP STM-1 ACTIVE ATSAO-B ASIA1 APCO 3 6 EQUIP STM-1 ACTIVE ATSA1-A ASIA2 APCO 3 7 EQUIP STM-1 ACTIVE ATSA1-A ASIA2 APCO 3 2 0 EQUIP STM-1 ACTIVE ATSA1-A ASIA2 APCO 3 4 EQUIP STM-1 ACTIVE ATSA1-A ASIA2 APCO 3 5 EQUIP STM-1 ACTIVE ATSA1-B ASIA2 APCO 3 6 EQUIP STM-1 ACTIVE ATSA1-B ASIA2 APCO 3 7 EQUIP STM-1 ACTIVE ATSA1-B ASIA2 APCO 3 20 EQUIP STM-1 ACTIVE ATSA3-A ASIA2 APCO 3 21 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 22 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 22 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 22 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 20 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 22 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 22 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 23 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 20 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 22 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO 3 23 EQUIP STM-1 ACTIVE ATSA3-B ASIA2 APCO	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-CAN-NODE; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

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Fig. 4.3-38 CAN ATM NODE Information Display

4.3.3.2. CAN PVC SETTING Information Display

- Command DIS-CAN-PVC;NODE_A=a;
- Input DIS-CAN-PVC: NODE_A=BSM_A;
 - a: BSM_A,BSM_B, BSC (0~11)
- Output

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		С	omm	and Wi	indow				
M2408 DIS	SPLAY CA E ENTRY	AN PVC S	ETTING	INFORMATI	ION				
ENTRY	EQUIP N	NODE_A	SUB_A	NODE_B	SUB_B	VC_N0	LINK_TYPE	LINK_	
0	EQUIP E	BSM_A	0	CNP_A	0	1	STM-1	256	
	EQUIP E	BSM_A	0	CNP_B	0	1	STM-1	256	
23	FULLE E	55M_A 25M_A	0	BSCO	2	1	SIM-I STM-1	256	
4	EQUIP E	SSM_A	ŏ	BSCO	3	i	STM-1	256	
5	EQUIP E	SM_A	Ō	BSCO	4	1	STM-1	256	
6	EQUIP E	BSM_A	0	BSCO	5	1	STM-1	256	
7	EQUIP E	SSM_A	0	BSCO	6	48	STM-1	256	
8	EQUIP E	550 <u>-</u> 8 200 a	U	BSUI PSC1	2	1	SIM-I STM_1	255	
10	FOUTP F	SSM_A	ň	BSC1	3	i	STM-1	256	
iĭ	EQUIP E	BSM_A	ŏ	BSC1	4	i	STM-1	256	
12	EQUIP E	BSM_A	Ō	BSC1	5	1	STM-1	256	
13	EQUIP E	BSM_A	0	BSC1	6	48	STM-1	256	
14	EQUIP E	BSM_A	0	BSC2	1	1	STM-1	256	
15	EQUIP E	55M_A 26M_A	U	BSU2 PSC2	2	1	SIM-I CTM-1	255	
10	FOUTP F	SOM_M	ů N	BSC2	5 4	ł	STM-1	256	
18	EQUIP E	SSM_A	ŏ	BSC2	5	i	STM-1	256	
19	EQUIP E	SM_A	ō	BSC2	6	48	STM-1	256	
20	EQUIP E	BSM_A	0	BSC3	1	1	STM-1	256	
21	EQUIP E	SSM_A	0	BSC3	2	1	STM-1	256	
22	EQUIP E	SSM_A	0	BSC3	3	1	SIM-1	256	
23	EQUIP E	55M_A 26M_A	0	BSC3	4	1	SIM-I STM-1	256	
	LUOIP L	53M_N	U	6303	3	•	310-1	230	
									ы
M									
TO LONG O		TEST	NO Z	STAT AL	FT DREU	TOUS			
	<u></u>		<u>n</u> v.,	31 <u>0</u> 1 NL		1003			
[ihnark <16T	CRSM>1 C	DTS-CAN-	PVC:NO	DE A=RSM A					Δ
ACCEPTED		20 Chin			.,				
[jhpark <lgi< th=""><td>CBSM>][</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></lgi<>	CBSM>][
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Fig. 4.3-39 CAN PVC SETTING Information Display

4.3.3.3. CAN NETWORK PARAMETER Information Display

- Command DIS-CAN-NETP;
- Input DIS-CAN-NETP;
- Output

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Command Window	
MSC(0x00) 2001-06-14 19:54:34 M2420 DISPLAY CAN NETWORK PARAMETER MSC_ID : 1 NUM OF ADDR HEADER : 0 BSC EQUIP CONN_TYPE ADDRESS 0 EQUIP BSC0 17.64.2.1 1 EQUIP BSC1 18.64.2.1 2 EQUIP BSC2 19.64.2.1 3 N_EQP BSC3 20.64.2.1 4 N_EQP BSC3 22.64.2.1 5 N_EQP BSC5 22.64.2.1 6 N_EQP BSC5 22.64.2.1 9 N_EQP BSC7 24.64.2.1 10 N_EQP BSC9 26.64.2.1 10 N_EQP BSC9 26.64.2.1 11 N_EQP BSC10 27.64.2.1 10 N_EQP BSC10 27.64.2.1 11 N_EQP BSC10 27.64.2.1 11 N_EQP BSC10 27.64.2.1 12 ATSA[00] CONN TYPE : ATSA0-B ATSA[01] CONN TYPE : ATSA1-B ATSA[02] CONN TYPE : ATSA1-B ATSA[03] CONN TYPE : ATSA2-B ATSA[04] CONN TYPE : ATSA3-A ATSA[05] CONN TYPE : ATSA3-A ATSA[05] CONN TYPE : ATSA3-A ATSA[05] CONN TYPE : ATSA3-B ATSA[05] CONN TYPE : ATSA3-B ATSA	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-CAN-NETP; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-40 CAN NETWORK PARAMETER Information Display

4.3.3.4. CAN INTER BSC AAL2 Setting Information Display

• Command DIS-CAN-IUR:BSC=a;

a:BSC Number (0~11)

- Input DIS-CAN-IUR:BSC=0;
- Output

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Command Window	
M2422 DISPLAY CAN INTER BSC AAL2 CONNECTION INFORMATION LOCATION : BSC_0 START VC ID [BSC_01] : 0x000b0040 START VC ID [BSC_02] : 0x000b0060 START VC ID [BSC_03] : 0x000b0080 START VC ID [BSC_04] : 0x000b00a0 START VC ID [BSC_05] : 0x000b00c0 START VC ID [BSC_06] : 0x000b00e0 START VC ID [BSC_07] : 0x000b0100 START VC ID [BSC_08] : 0x000b0120 START VC ID [BSC_08] : 0x000b0140 START VC ID [BSC_10] : 0x000b0140 START VC ID [BSC_11] : 0x000b0180 NUM OF VC : 32 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-CAN-IUR:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-41 CAN INTER BSC AAL2 Setting Information Display

4.3.3.5. CAN INTER BSC AAL5 Setting Information Display

- Command DIS-CAN-BSC;
- Output

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Command Window							
MSC(0x00) 2001-06-14 20:08:39 M2425 DISPLAY CAN INTER BSC AAL2/5 CONNECTION INFORMATION START AAL5 VC ID [00] : 0x000a0020 START AAL5 VC ID [01] : 0x000c0020 NUM OF AAL5 VC : 8160 COMPLETED							
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>							
[jhpark <lgicbsm>] DIS-CAN-BSC; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>							

Fig. 4.3-42 CAN INTER BSC AAL5 Setting Information Display

4.3.3.6. CAN ATM NODE Information Display

- Command DIS-CPN-NODE;
- Output

>	〈 INTERM							_ 🗆 ×
ſ				Comm	and W	indow		
ł	H0400 D							
I	MZ43U D. CARD	LSPLAY	CPN # EQUIP	LINK_TYPE	LINK_STS	CONN_TYPE	FABRIC_N0	
I	0	0	EQUIP	STM-1	ACTIVE	ASCA	ASCA APCO	
I	0	5	EQUIP	STM-1	ACTIVE	CAN_A	ASCA APCO	
I	0	8	EQUIP	STM-1	ACTIVE	CNP_B	ASCA APCO	
I	U 1	9	EQUIP	STM-1	ACTIVE	ASIA0	ASIAO APCO	
	1	4	EQUIP	STM-1	ACTIVE	BSC0	ASIAO APCO	
I	ł	5	EQUIP	STM-1	ACTIVE	BSC2	ASIAU APCU ASIAU APCO	
I	1	7	EQUIP	STM-1	ACTIVE	PCPO_A	ASIAO APCO	
I	2	23	EQUIP	STM-1	ACTIVE	ASIA1	ASIAU APCI ASIA1 APCO	
I	2	7	EQUIP	STM-1	ACTIVE	PMPO_A	ASIA1 APCO	
I	3	4	EQUIP	STM-1 STM-1	ACTIVE	ASTAZ PCP1 A	ASIAZ APCU ASIAZ APCO	
I	3	20	EQUIP	STM-1	ACTIVE	PCP1_B	ASIA2 APC1	
I	3	21 22	EQUIP	STM-1 STM-1	ACTIVE	PMP1_A PMP1_B	ASIAZ APCI ASIAZ APCI	
I	COMPLETED							
I								
ľ	4							
Ľ	N							
	<u>I</u> O <u>L</u> DNG	<u>c</u> dm <u>s</u>	<u>stm t</u>	EST <u>N</u> 0.7	ST <u>A</u> T A	L <u>F</u> T PREVI	IOUS	
	[jhpark <lg ACCEPTED [jhpark <lg< th=""><th>ECBSM> ECBSM></th><th>•] dis- •][</th><th>-CPN-NODE;</th><th></th><th></th><th></th><th>Z Z</th></lg<></lg 	ECBSM> ECBSM>	•] dis- •][-CPN-NODE;				Z Z
ļ	4							

Fig. 4.3-43 CAN ATM NODE Information Display

4.3.3.7. PCF ATM NODE Information Display

- Command DIS-PCF-NODE
- Output

X INTERM				_ 🗆 ×				
Command Window								
MSC(0x00) 2001-06 M2431 DISPLAY BLOCK LINK 0 0 0 1 0 2 0 3 0 4 0 5 0 6 0 8 0 10 0 11 0 12 0 13 0 14 0 15 0 16 0 17 1 0 1 1 1 2	-14 20:12:03 CPN PCF ATM NODI EQUIP LINK_TYPE EQUIP STM-1 EQUIP STM-1	E INFORMATION LINK_STS CONN_TYPE ACTIVE PCP_A ACTIVE PCP_B ACTIVE PIPO ACTIVE PIP1 ACTIVE PIP2 ACTIVE CRP ACTIVE ASIAO_A ACTIVE ASIAO_A ACTIVE PIP3 ACTIVE PIP3 ACTIVE PIP4 ACTIVE PIP5 ACTIVE PIP5	FABRIC_NO BCRA APCO BCRA APCO	A L				
A								
<u>IO LONG COM S</u>	TM <u>T</u> EST <u>N</u> 0.7	ST <u>A</u> T AL <u>F</u> T PREVI	OUS					
[jhpark <lgicbsm>] DIS-PCF-NODE; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>								

Fig. 4.3-44 PCF ATM NODE Information Display

4.3.3.8. CAN PVC SETTING Information Display

- Command DIS-CPN-PVC
- Output

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I	Command Window
	SC(0x00) 2001-06-14 20:14:34 M2438 DISPLAY CPN PVC SETTING INFORMATION NUN OF ENTRY : 144 ENTRY EQUIP CAN_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 EQUIP CAN_A 14 PNP_B 0 1 STM-1 256 1 EQUIP CAN_A 14 PCPD_A 0 1 STM-1 256 3 EQUIP CAN_A 14 PCPD_A 0 1 STM-1 256 4 EQUIP CAN_A 14 PCP1_A 0 1 STM-1 256 5 EQUIP CAN_A 14 PCP2_A 0 1 STM-1 256 6 EQUIP CAN_A 14 PCP2_A 0 1 STM-1 256 7 EQUIP CAN_A 14 PCP2_A 0 1 STM-1 256 8 EQUIP CAN_A 14 PCP2_A 0 1 STM-1 256 9 EQUIP CAN_A 14 PMPD_A 0 1 STM-1 256 10 EQUIP CAN_A 14 PMPD_A 0 1 STM-1 256 11 EQUIP CAN_A 14 PMPD_A 0 1 STM-1 256 12 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 13 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 14 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 15 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 16 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 17 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 18 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 19 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 10 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 11 EQUIP CAN_A 14 PMP1_A 0 1 STM-1 256 13 EQUIP CAN_A 14 PMP2_A 0 1 STM-1 256 14 EQUIP CAN_A 14 PMP2_A 0 1 STM-1 256 15 EQUIP CAN_A 14 PMP2_A 0 1 STM-1 256 16 EQUIP PCP0_A 0 CAN_A 14 1 STM-1 256 17 EQUIP PCP0_A 0 CAN_A 14 1 STM-1 256 16 EQUIP PCP1_A 0 CAN_A 14 1 STM-1 256 17 EQUIP PCP1_A 0 CAN_A
Ī	<u>LONG COM STM TEST NO.7 STAT ALFT PREVIOUS</u>
	jhpark <lgicbsm>] DIS-CPN-PVC; CCEPTED jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-45 CAN PVC SETTING Information Display

4.3.3.9. CAN PCF PVC SETTING Information Display

- Command DIS-PCF-PVC
- Output

XINTERM								_ [×
		Ca	omma	ind W	/indow				
MSC(0x00) 20 M2439 DI NUN 0 FNTRY	01-06-14 SPLAY CI F ENTRY FOUTP /	4 20:15 PN PCF P : 34	:44 VC SETT: SUR A	ING INF	ORMATION SUB B	VC NO	ITNK TYPE	ITNK	Δ
0	EQUIP I	PCP_A ASIAO_A	0	PIPO PIPO	0	0	STM-1 STM-1	256 256	
2	EQUIP / EQUIP	ASIA1_B PCP_A	0 0	PIPO PIP1	0	0	STM-1 STM-1	256 256	
4 5 6	EQUIP / EQUIP / FOUTP	ASIAU_A ASIA1_B PCP_A	0	PIPI PIP1 PTP2	0	0	STM-1 STM-1 STM-1	256 256 256	
7	EQUIP / EQUIP /	ASIAO_A ASIA1_B	Ŭ O	PIP2 PIP2	Ŭ O	Ŭ O	STM-1 STM-1	256 256	
9 10 11	EQUIP EQUIP /	PCP_A ASIAO_A ASIAI_P	0 0	PIP3 PIP3 DTD2	0 0	0 0	STM-1 STM-1 STM-1	256 256 256	
12 13	EQUIP / EQUIP / EQUIP /	PCP_A ASIAO_A	0 0	PIP4 PIP4 PIP4	0 0	0 0	STM-1 STM-1	256 256 256	
14 15	EQUIP / EQUIP	ASIA1_B PCP_A	0 0	PIP4 PIP5	0	0 0	STM-1 STM-1	256 256	
16	EQUIP /	ASIAU_A ASIA1_B	0	PIP5 PIP5	0	0	STM-1 STM-1	256 256	Ξ
, M									
<u>10 L</u> DNG <u>C</u>	IDM <u>s</u> tm	TEST	<u>N</u> 0.7	ST <u>a</u> t a	AL <u>F</u> T PREV	TOUS			
[jhpark <lgicbsm>] DIS-PCF-PVC; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>							AI D		

Fig. 4.3-46 CAN PCF PVC SETTING Information Display

4.3.3.10. CPN METWORK PARAMETER Information Display

- Command DIS-CPN-NETP
- Output

	×
Command Window	
MSC(0x00) 2001-06-14 20:16:14 M2440 DISPLAY CPN NETWORK PARAMETER MSC_ID : 1 PCF EQUIP CONN_TYPE1 CONN_TYPE2 0 EQUIP PCP0_A PCP0_B 1 EQUIP PCP0_A PCP1_B 3 EQUIP PCP1_A PCP2_B 5 N_EQP PMP2_A PMP2_B BSC EQUIP CONN_TYPE NCP_ADDR 0 EQUIP BSC0 30.64.5.1 1 EQUIP BSC1 30.64.5.1 1 EQUIP BSC3 30.64.5.1 1 EQUIP BSC4 30.64.8.1 4 N_EQP BSC4 30.64.9.1 5 N_EQP BSC5 30.64.10.1 6 N_EQP BSC5 30.64.2.1 7 N_EQP BSC6 17.64.2.1 9 N_EQP BSC10 21.64.2.1	KI N
	-
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-CPN-NETP; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-47 CPN METWORK PARAMETER Information Display

4.3.3.11. CPN DATA AAL2/5 Connection Information Display

- Command DIS-CPN-DATA;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 20:17:05 M2441 DISPLAY CPN DATA AAL5 CONNECTION INFORMATION START VC ID [BSC_00] : 0x000a0020 START VC ID [BSC_01] : 0x000a0020 START VC ID [BSC_02] : 0x000a0020 START VC ID [BSC_03] : 0x000a0020 START VC ID [BSC_04] : 0x000a0020 START VC ID [BSC_05] : 0x000a0020 START VC ID [BSC_06] : 0x000a0020 START VC ID [BSC_06] : 0x000a0020 START VC ID [BSC_07] : 0x000a0020 START VC ID [BSC_08] : 0x000a0020 START VC ID [BSC_09] : 0x000a0020 START VC ID [BSC_01] : 0x000a0020 START VC ID [BSC_10] : 0x000a0020 START VC ID [BSC_11] : 0x000a0020 STA	
- V	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-CPN-DATA; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	4
4	

Fig. 4.3-48 CPN DATA AAL2/5 Connection Information Display

4.3.3.12. CPN PCF AAK2/5 Connection Information Display

- Command DIS-CPN-PCF;
- Output

	- O ×
Command Window	
MSC(0x00) 2001-06-14 20:17:52 M2442 DISPLAY CPN PCF AAL5 CONNECTION INFORMATION START VC ID [PIP_00] : 0x000a0020 0x001e0020 START VC ID [PIP_01] : 0x000b0020 0x001f0020 START VC ID [PIP_02] : 0x000c0020 0x0020020 START VC ID [PIP_03] : 0x000d0020 0x00210020 START VC ID [PIP_04] : 0x000e0020 0x00220020 START VC ID [PIP_05] : 0x000f0020 0x00220020 START VC ID [PIP_05] : 0x00100020 0x00220020 START VC ID [PIP_06] : 0x00100020 0x00240020 START VC ID [PIP_08] : 0x00110020 0x00250020 START VC ID [PIP_09] : 0x00120020 0x00250020 START VC ID [PIP_01] : 0x00130020 0x00270020 START VC ID [PIP_10] : 0x00140020 0x00280020 NUM OF AAL5 VCS : 480 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-CPN-PCF; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-49 CPN PCF AAK2/5 Connection Information Display

4.3.3.13. BSC ATM NODE Information Display

• Command DIS-BSC-NODE: BSC=a;

a: BSC Number (0~11)

- Input DIS-BSC-NODE: BSC=0;
- Output

×INTERM	
Command Window	
M2450 DISPLAY BSC ATM NODE INFORMATION LOCATION : BSC_0CARD LINK EQUIP LINK_TYPE LINK_STS CONN_TYPE FA00EQUIP STM-1ACTIVEACTIVE ASCA04EQUIP STM-1ACTIVEACTIVE NCP_AAS05EQUIP STM-1ACTIVE NCP_AAS05EQUIP STM-1ACTIVE NCP_BAS09EQUIP STM-1ACTIVE CCP_BAS10EQUIP STM-1ACTIVE CANAS112EQUIP STM-1ACTIVE CANAS13122AS34EQUIP STM-1ACTIVE ALMA0-1AS332COMPLETED	BRIC_NO GCA APCO GCA APC
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOU</u>	S
[jhpark <lgicbsm>] DIS-BSC-NODE:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	N N N N N N N N N N N N N N N N N N N
4	

Fig. 4.3-50 BSC ATM NODE Information Display



4.3.3.14. SLB ATM NODE Information Display

• Command DIS-SLB-NODE: BSC=a;

a: BSC Number (0~11)

- Input DIS-SLB-NODE: BSC=0;
- Output

×I	NTERM									_ 🗆 X
	Command Window									
	M24E1	ртері	W PCC	CID AT	M NO		ΜΑΤΤΟΝ			
	L00	CATION	и вэс : BSC	эгь мі _0		DE INFOR	UNITON			
	SMF	P LINK	EQUIP	LINK_T	YPE	LINK_STS	CONN_TYPE	FABR	EC_N0	
	0	0	EQUIP	STM-1		ACTIVE	SMP	SLMA SKMA	APCO	
	U N	4	FOUTP	STM-1		ACTIVE	SLDU	SIMA	APCO	
	ŏ	5	EQUIP	STM-1	, i	ACTIVE	SLP1	SLMA	APCO	
	0	6	EQUIP	STM-1		ACTIVE	SLP2	SLMA	APCO	
	0	7	EQUIP	STM-1	ł	ACTIVE	SLP3	SLMA	APCO	
	0	8	EQUIP	STM-1	4	ACTIVE	SLP4	SLMA	APCO	
	U 0	9 10	FULLE	SIM-I STM-1		ACTIVE	SLPS	SIMA	APCO	
	ŏ	11	EOUIP	STM-1		ACTIVE	SLP7	SLMA	APCO	
	Ō	12	EQUIP	STM-1	, i	ACTIVE	SLP8	SLMA	APCO	
	0	13	EQUIP	STM-1		ACTIVE	SLP9	SLMA	APCO	
	0	14	EQUIP	STM-1	ł	ACTIVE	SLP10	SLMA	APCO	
	U	15	EQUIP	SIM-I STM_1		ACTIVE	SLF11 SLD12	SLMA	APCO	
	ň	17	FOUTP	STM-1	, ,	ACTTVE	SIP12	SLMA	APCO	
	ŏ	18	EQUIP	STM-1	, i	ACTIVE	SLP14	SLMA	APCO	
	0	19	EQUIP	STM-1		ACTIVE	SLP15	SLMA	APCO	
	0	20	EQUIP	STM-1	ł	ACTIVE	SLP16	SLMA	APCO	
	0	21	EQUIP	STM-1	4	ACTIVE	SLP17	SLMA	APCO	E
ĽЦ.										
		ADU	otu	TFOT		OTAT	ALET 000	UTAUA		
<u>_</u> 10	D <u>L</u> DNG	CDM	SIM	TEST	<u>N</u> U.7	SIAI	AL <u>F</u> T PRE	VIOUS		
T4										
	[]NPARK <lglubsm>] DIS-SLB-NUDE:BSC=U; ACCEPTED</lglubsm>									
Ēj	hpark <i< th=""><th>GICBS</th><th>H>]</th><th></th><th></th><th></th><th></th><th></th><th></th><th>E</th></i<>	GICBS	H>]							E
	M									

Fig. 4.3-51 SLB ATM NODE Information Display



4.3.3.15. VCB ATM NODE Information Display

• Command DIS-VCB-NODE: BSC=a;

a: BSC Number (0~11)

- Input DIS-VCB-NODE: BSC=0;
- Output

XINTERM					
	Comma	nd Window			
M2452 DISPL LOCATION VMP LINK 0 0 2 0 4 0 5 0 6 0 7 0 8 0 9 0 10 0 11 0 12 0 13 0 14 0 15 0 16 0 17 0 19	AY BSC VCB ATM NODE : : BSC_0 EQUIP LINK_TYPE LINI EQUIP STM-1 ACT: EQUIP STM-1 EQUIP STM-1	INFORMATION	IC_N0 APC0 APC0 APC0		
0 19 Completed	EQUIP STM-1 ACT	EVE VCP15 VCMA	APCO		
<u>I</u> O <u>L</u> DNG <u>C</u> DM	<u>s</u> tm <u>t</u> est <u>N</u> 0.7 s	T <u>A</u> T AL <u>F</u> T PREVIOUS			
[jhpark <lgicbsm>] DIS-VCB-NODE:BSC=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>					
4					

Fig. 4.3-52 VCB ATM NODE Information Display

4.3.3.16. ALB ATM NODE Information Display

• Command DIS-ALB-NODE: BSC=a;

a: BSC Number (0~11)

- Input DIS-ALB-NODE: BSC=0;
- Output

XINTERM	*					
Command Window						
MSC(0x00) 2001-06-14 20:21 M2453 DISPLAY BSC ALB A LOCATION : BSC_0 ALMA LINK EQUIP LINK 0 0 EQUIP STM- 0 1 EQUIP STM- 0 2 EQUIP STM- 0 3 EQUIP STM- 0 4 EQUIP STM- 0 5 EQUIP STM- 0 6 EQUIP STM- 1 0 EQUIP STM- 1 2 EQUIP STM- 1 3 EQUIP STM- 1 4 EQUIP STM- 1 5 EQUIP STM- 1 6 EQUIP STM- 1 8 EQUIP STM- 1 8 EQUIP STM- 1 8 EQUIP STM-	:27 TM NODE INFORMATION C_TYPE LINK_STS CONN_TYPE 1 ACTIVE ALPAO-0 1 ACTIVE ALPAO-1 1 ACTIVE ALPAO-2 1 ACTIVE ALPAO-3 1 ACTIVE ALPAO-3 1 ACTIVE ALPAO-4 1 ACTIVE ALPAO-6 1 ACTIVE ALPAO-6 1 ACTIVE ALPAO-6 1 ACTIVE ALPAO-6 1 ACTIVE ASIAO 1 ACTIVE ASIAO 1 ACTIVE ASIAO	FABRIC_NO ALMA APCO ALMA APCO				
		☑				
<u>IO LONG COM S</u> TM <u>T</u> EST	<u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREV	IOUS				
[jhpark <lgicbsm>] DIS-ALB-NODE:BSC=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>						

Fig. 4.3-53 ALB ATM NODE Information Display



4.3.3.17. BTS ATM NODE Information Display

- Command DIS-BTS-NODE: BSC=a ,BTS=b;
 - a: BSC Number (0~11)
 - b: BTS Number (0~47)
- Input DIS-BSC-NODE: BSC=0,BTS=0;
- Output

	<u>_ 0 ×</u>					
Command Window						
MSC(0x00) 2001-06-14 20:22:22 M2454 DISPLAY BTS ATM NODE INFORMATION LOCATION : BSC_0 BTS LINK EQUIP LINK_TYPE LINK_STS CONN_TYPE FABRIC_NO 0 0 EQUIP STM-1 ACTIVE BSP_A BCRA APCO 0 1 EQUIP STM-1 ACTIVE BSP_B BCRA APCO 0 2 EQUIP STM-1 ACTIVE LICA0 BCRA APCO 0 3 EQUIP STM-1 ACTIVE LICA1 BCRA APCO 0 4 EQUIP STM-1 ACTIVE LICA2 BCRA APCO 0 5 EQUIP STM-1 ACTIVE LICA2 BCRA APCO						
0 6 EQUIP STM-1 ACTIVE RCU0 BCRA APCO 0 8 EQUIP STM-1 ACTIVE RCU1 BCRA APCO 0 10 EQUIP STM-1 ACTIVE RCU2 BCRA APCO 0 12 EQUIP STM-1 ACTIVE RCU3 BCRA APCO 0 14 EQUIP STM-1 ACTIVE RCU3 BCRA APCO 0 16 EQUIP STM-1 ACTIVE RCU4 BCRA APCO 0 16 EQUIP STM-1 ACTIVE RCU5 BCRA APCO 0 18 EQUIP STM-1 ACTIVE RCU6 BCRA APCO 0 20 EQUIP STM-1 ACTIVE RCU6 BCRA APCO COMPLETED	T T					
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>						
<pre>[jhpark <lgicbsm>] DIS-ALB-NODE:BSC=0; ACCEPTED [jhpark <lgicbsm>] DIS-BTS-NODE:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm></lgicbsm></pre>						

Fig. 4.3-54 BTS ATM NODE Information Display

4.3.3.18. BSC-BTS TRUNK Information Display

• Command DIS-TRNK-DATA: BSC=a;

a: BSC Number (0~11)

- Input DIS-TRNK-DATA: BSC=0;
- Output

>		×						
Γ	Command Window							
	MSC(0x00) 2001-06-14 20:25:04 M2455 DISPLAY ESC-ETS TRUNK INFORMATION LOCATION : ESC_0 ALMA ALPA LINK ETS LINK_TYPE ALC_TYPE LICA LICA_LINK VPCI 0 0 0 0 E1 PVC_ACT 0 0 0x000b0020 0 0 1 1 E1 PVC_ACT 0 0 0x000b0021 0 0 2 0 E1 PVC_STB 0 1 0x000b0022 COMPLETED	KI N						
	<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>							
	[jhpark <lgicbsm>] DIS-TRNK-DATA:BSC=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>							

Fig. 4.3-55 BSC-BTS TRUNK Information Display



4.3.3.19. BSC PVC SETTING Information Display

- Command DIS-BSC-PVC: BSC=a,NODE_A=b;
 - a: BSC Number (0~11)
 - b: NCP_A, NCP_B, CCP_A, CCP_B, ALMA0_0 , ALMA0_1, ALMA1_0 ,
- ALMA1_1,CAN,CPN,ALP
- Input DIS-BSC-PVC: BSC=0,NODE_A=NCP_A;
- Output

XINTERM					_ 🗆 ×
	mma	nd Win	dow		
MSC(0x00) 2001-06-14 20:35:	10 TTTNC T				
LOCATION : BSC 0	TITNG T	NEUKMATIUN	•		
NUN OF ENTRY : 972					
ENTRY EQUIP NODE_A	SUB_A I	NODE_B	SUB_B_VC_NO	LINK_TYPE	LINK_
118 FOUTP NCP_A		CCP A	14 I 0 1	STM-1	256
119 EQUIP NCP_A	ŏ	CAN	ŏ i	STM-1	256
120 EQUIP NCP_A	0	РСР	0 1	STM-1	256
121 EQULP NCP_A 122 FOUTD NCP_A	0	SEMAU Semat	15 1	SIM-1 STM-1	256
122 EQUIP NCP_H	0	SLMA2	15 1	STM-1	256
124 EQUIP NCP_A	ŏ	SLMA3	15 1	STM-1	256
125 EQUIP NCP_A	0	SLMA4	15 1	STM-1	256
126 EQUIP NCP_A 127 FOUTD NCP A	0	VCMAU VCMA1	16 1 16 1	SIM-1 STM-1	256
127 EQUIP NCP_H	0 1	VCMA2	16 1	STM-1	256
129 EQUIP NCP_A	ŏ	VCMA3	16 1	STM-1	256
130 EQUIP NCP_A	0	VCMA4	16 1	STM-1	256
131 EQUIP NCP_A	0	VCMA5 VCMAC	16 1	STM-1	256
133 FOUTP NCP_A	0 7	VCMAG VCMAZ	16 1	STM-1	256
134 EQUIP NCP_A	ŏ,	ALP	o i	STM-1	256
136 EQUIP NCP_A	0	ALMAO-O	6 1	STM-1	256
137 EQUIP NCP_A	0	ALMAO-O	19 1	STM-1	256
138 EQUIP NCP_A 139 FOUTP NCP_A		ALMAU-U Alman-n	20 I 6 1	SIM-I STM-1	256
140 EQUIP NCP_A	Ŏ	ALMAO-O	19 i	STM-1	256
141 EQUIP NCP_A	0 /	ALMAO-O	20 1	STM-1	256 🗖
142 EQUIP NCP_A	0	ALMAO-O	6 1	STM-1	256
143 EQUIP NCP_A 144 FOUTD NCP A		ALMAU-U Alman-n	19 1	SIM-I STM-1	256
			20 1	514 1	U U
4					
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST	<u>N</u> 0.7 S	T <u>A</u> T AL <u>F</u> T	PREVIOUS		
[ibnark <lctcrsm>] DTE-PEC-D</lctcrsm>	WC·BSC-	O NODE A-N	ICP A.		
ACCEPTED	10.030-0	0,100L_1-N	icr_n,		
[jhpark <lgicbsm>]</lgicbsm>					
					7
4					

Fig. 4.3-56 BSC PVC SETTING Information Display

4.3.3.20. BSC SLB PVC SETTING Information Display

• Command DIS-SLB-PVC: BSC=a;

a: BSC Number (0~11)

- Input DIS-SLB-PVC: BSC=0;
- Output

XINTERM		
(Command Window	
MSC(0x00) 2001-06-14 20:	36:32	
M2459 DISPLAY BSC SLB LOCATION : BSC_O	PVC SETTING INFORMATION	
NUN OF ENTRY : 23		
0 EQUIP ASIA	3 SMP 0 1	STM-1 256
1 EQUIP ASIA	1 SMP 0 1	STM-1 256
3 EQUIP ASIA	0 SLPO 0 5	STM-1 256
4 EQUIP SMP	0 SLP1 0 5	STM-1 256
6 EQUIP SMP	0 SLP2 0 5	STM-1 256
7 EQUIP SMP	0 SLP4 0 5	STM-1 256
9 EQUIP SMP	0 SLPS 0 5	STM-1 256
10 EQUIP SMP	0 SLP7 0 5	STM-1 256
12 EQUIP SMP	0 SLP8 U 5 0 SLP9 0 5	STM-1 256
13 EQUIP SMP	0 SLP10 0 5	STM-1 256
14 EQUIP SMP 15 EQUIP SMP	0 SLP11 0 5	STM-1 256
16 EQUIP SMP	0 SLP13 0 5	STM-1 256
17 EQUIP SMP 18 EQUIP SMP	0 SLP14 0 5	STM-1 256
19 EQUIP SMP	0 SLP16 0 5	STM-1 256
20 EQUIP SMP 21 EQUIP SMP	0 SLP17 U S	STM-1 256
22 EQUIP SMP	0 SLP19 0 5	STM-1 256
COMPLETED		
		X
A		
<u>IO LONG COM STM TEST</u>	NO.7 STAT ALET PREVIOUS	
[jhpark <lgicbsm>] DIS-SL</lgicbsm>	3-PVC:BSC=0;	
[jhpark <lgicbsm>]]</lgicbsm>		
		1
4		

Fig. 4.3-57 BSC SLB PVC SETTING Information Display
4.3.3.21. BSC VCB PVC SETTING Information Display

• Command DIS-VCB-PVC: BSC=a;

a: BSC Number (0~11)

- Input DIS-VCB-PVC: BSC=0;
- Output

XINTE	RM		R						_ 🗆	×
			Co	mma	nd Win	dow	,			
MSC(0) M	x00) 200 2460 DIS LOCATJ NUN OF ENTRY 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 ETED	DI-06-1 PLAY B CON : B ENTRY EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP EQUIP	4 20:38: SC VCB PV SC_0 ': 19 NODE_A ASIA ASIA ASIA VMP VMP VMP VMP VMP VMP VMP VMP VMP VMP	55 C SETT SUB_A 3 1 5 0 0 0 0 0 0 0 0 0 0 0 0 0	ING INFORM NODE_B VMP VMP VCPO VCP1 VCP2 VCP3 VCP4 VCP5 VCP4 VCP5 VCP6 VCP7 VCP8 VCP9 VCP10 VCP10 VCP11 VCP12 VCP13 VCP14 VCP15	ATION SUB_B 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	VC_N0 1 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	LINK_TYPE STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1	LINK_ 256 256 256 256 256 256 256 256 256 256	
TO		NH 07	н тгет	NO 7	CTAT ALS		ITALIC			
10		<u></u>	<u>1</u> EST	<u>N</u> U.7		PKEV	1005			
[jhpa ACCEP [jhpa	rk <lgio TED rk <lgio< td=""><td>:BSM>] :BSM>]</td><td>DIS-VCB-F</td><td>PVC:BSC</td><td>=0;</td><td></td><td></td><td></td><td></td><td></td></lgio<></lgio 	:BSM>] :BSM>]	DIS-VCB-F	PVC:BSC	=0;					

Fig. 4.3-58 BSC VCB PVC SETTING Information Display

4.3.3.22. BSC ALB PVC SETTING Information Display

• Command DIS-ALB-PVC: BSC=a;

- Input DIS-ALB-PVC: BSC=0;
- Output

2		×
ſ	Command Window	
	SC(0x00) 2001-06-14 20:40:10 M2461 DISPLAY ESC ALE PVC SETTING INFORMATION L0CATION : BSC_0 NUN OF ENTRY : 116 ENTRY EQUIP NODE_A SUE_A NODE_B SUE_B VC_NO LINK_TYPE LINK_ 0 EQUIP ASIA_BTS 30 ALPA_BTS 30 0 STM-1 256 1 EQUIP ASIA_BTS 30 ASIA_BTS 30 0 STM-1 256 2 EQUIP ASIA_BTS 30 ASIA_BTS 30 0 STM-1 256 3 EQUIP ASIA_BTS 30 ASIA_BTS 30 0 STM-1 256 4 EQUIP ASIA_BTS 30 ASIA_BTS 30 0 STM-1 256 5 EQUIP ASIA_BTS 30 ASIA_BTS 30 0 STM-1 256 6 EQUIP ASIA_BTS 30 A	
Ī	<u>LO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
	jhpark <lgicbsm>] DIS-ALB-PVC:BSC=0; CCEPTED jhpark <lgicbsm>]</lgicbsm></lgicbsm>	
	51	

Fig. 4.3-59 BSC ALB PVC SETTING Information Display

4.3.3.23. BTS LOCAL PVC SETTING Information Display

• Command DIS-BTS-LPVC: BSC=a;

- Input DIS-BTS-LPVC: BSC=0;
- Output

XINTERN	М								_ □	×
			Ca	omma	nd I	Window	v			
										EX.
MSC(0x00	D) 200	1-06-1	4 20:41	:11						
M246	52 DIS LOCATT	PLAY E ON : F	SIS LUCAL RSC 0	PVC SE	TING	INFORMATIO	JN			
. N	NUN OF	ENTRY	7:70							
	ENTRY	EQUIP	NODE_A	SUB_A	NODE_	B SUB_F	3 VC_NO	LINK_TYPE	LINK_	
	J 1	FOUTP	BSP_A BSP_A	U N	RCU9	14	1	STM-1	256	
	2	EQUIP	BSP_A	ŏ	RCU9	3	i	STM-1	256	
	3	EQUIP	BSP_A	0	RCU9	20	1	STM-1	256	
4	+	EQUIP	BSP_A RSD_A	U	RCU9 RCU9	17	1	STM-1	256	
l é	5	EQUIP	BSP_A	ŏ	CRP	0	i	STM-1	256	
7	7	EQUIP	BSP_A	0	RCUO	27	1	STM-1	256	
	8	EQUIP	BSP_A	0	RCUO	28	10	STM-1	256	
	, 10	FOUTP	BSP_A	0	RCU1	28	10	STM-1	256	
i	iĭ	EQUIP	BSP_A	ŏ	RCU2	27	1	STM-1	256	
	12	EQUIP	BSP_A	0	RCU2	28	10	STM-1	256	
	13 14	FULLE	BSP_A RSD_A	U	RCU3	27	1	SIM-1 STM-1	256	
	15	EQUIP	BSP_A	ŏ	RCU4	27	1	STM-1	256	
1	16	EQUIP	BSP_A	0	RCU4	28	10	STM-1	256	
	17	EQUIP	BSP_A	0	RCU5	27	1	STM-1	256	
	18 19	FOUTP	BSP_A RSP_A	0	RCU5	28	10	STM-1	256	
	20	EQUIP	BSP_A	ŏ	RCU6	28	io	STM-1	256	
										М
70.101	10 0-				0717	ALET 022				
	NG <u>C</u> D	M <u>s</u> t	M <u>t</u> est	<u>N</u> 0.7	STAT	AL <u>F</u> T PRE	VIOUS			
Libnark	<1.610	RSM>1	DTS-RTS-		^=0·					Δ
ACCEPTED)	0.0004	010 010		,					
[jhpark	<lgic< td=""><td>BSM>]</td><td>Ι</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></lgic<>	BSM>]	Ι							

Fig. 4.3-60 BTS LOCAL PVC SETTING Information Display

4.3.3.24. BTS OUTER PVC SETTING Information Display

- Command DIS-BTS-OPVC: BSC=a, BTS=b;
 - a: BSC Number (0~11)
 - b: BTS Number(0~47)
- Input DIS-BTS-OPVC: BSC=0, BTS=0;
- Output

					_ 🗆 ×
	Col	mmand Win	dow		
MSC(0x00) 2001 M2463 DISP LOCATIO	I-06-14 20:42:0 PLAY BTS OUTER I IN : BSC_0/BTS_0	03 PVC SETTING INFO D	RMATION		
ENTRY E 0 E 1 E 2 E 3 E 4 E 5 E 6 E 7 E 8 E	EQUIP NODE_A EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO EQUIP ASIAO	SUB_A NODE_B 3 RCU9 14 RCU9 14 RCU9 1 RCU9 1 RCU9 1 RCU9 1 RCU9 1 RCU9 2 RCU9 29 RCU9	SUB_B VC_N0 6 1 6 1 7 1 6 1 19 1 20 1 17 1 25 1 19 1	LINK_TYPE STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1 STM-1	LINK_ 256 256 256 256 256 256 256 256 256 256
			(
<u>IO L</u> DNG <u>C</u> DM	H <u>s</u> th <u>t</u> est <u>i</u>	<u>N</u> O.7 ST <u>a</u> t al <u>f</u> 1	PREVIOUS		
[jhpark <lgicb ACCEPTED [jhpark <lgicb< td=""><td>3SM>] DIS-BTS-0 3SM>]</td><td>PVC:BSC=0,BTS=0;</td><td></td><td></td><td>4</td></lgicb<></lgicb 	3SM>] DIS-BTS-0 3SM>]	PVC:BSC=0,BTS=0;			4

Fig. 4.3-61 BTS OUTER PVC SETTING Information Display

4.3.3.25. BSC NETWORK PARAMETER Information Display

• Command DIS-BSC-NETP: BSC=a;

- Input DIS-BSC-NETP: BSC=0;
- Output

Fig. 4.3-62 BSC NETWORK PARAMETER Information Display

4.3.3.26. BSC ALP NETWORK PARAMETER Information Display

• Command DIS-ALS-NETP: BSC=a;

a: BSC Number (0~11)

- Input DIS-ALS-NETP: BSC=0;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 20.44:16 M2471 DISPLAY BSC ALS NETWORK PARAMETER L0CATION : BSC_0 MSC_ID : 1 NCP ADDR : 17.64.2.1 ASMU ALMA0_0 ADDRESS : 0.0.0.2 ASMU ALMA0_1 ADDRESS : 0.0.0.3 ASMU ALPA0_0 ADDRESS : 0.0.0.4 ASMU ALPA0_1 ADDRESS : 0.0.0.6 ASMU ALPA0_2 ADDRESS : 0.0.0.6 ASMU ALPA0_3 ADDRESS : 0.0.0.8 ASMU ALPA0_4 ADDRESS : 0.0.0.8 ASMU ALPA0_4 ADDRESS : 0.0.0.0 ASMU INVALID ADDRESS : 0.0.0.0 ASMU INVALID ADDRESS : 0.0.0.10 ASMU ALMA1_0 ADDRESS : 0.0.0.11 ASMU ALPA1_1 ADDRESS : 0.0.0.12 ASMU ALPA1_2 ADDRESS : 0.0.0.13 ASMU ALPA1_4 ADDRESS : 0.0.0.15 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-ALS-NETP:BSC=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	T T
NN	

Fig. 4.3-63 BSC ALP NETWORK PARAMETER Information Display

4.3.3.27. BSC INTER BSC AAL2 Information Display

• Command DIS-BSC-IUR: BSC=a;

- Input DIS-BSC-IUR: BSC=0;
- Output

XINTERM	
Command Window	
MSC(0x00) 2001-06-14 20:56:13 M2472 DISPLAY BSC INTER BSC AAL2 CONNECTION INFORMA LOCATION : BSC_0 START VC ID [BSC_01] : 0x000b0040 START VC ID [BSC_02] : 0x000b0060 START VC ID [BSC_03] : 0x000b0080 START VC ID [BSC_04] : 0x000b00a0 START VC ID [BSC_05] : 0x000b00c0 START VC ID [BSC_06] : 0x000b00c0 START VC ID [BSC_07] : 0x000b0100 START VC ID [BSC_08] : 0x000b0120 START VC ID [BSC_09] : 0x000b0140 START VC ID [BSC_10] : 0x000b0160 START VC ID [BSC_11] : 0x000b0180	ATION
M	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOU</u>	5
[jhpark <lgicbsm>] DIS-BSC-IVR:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-64 BSC INTER BSC AAL2 Information Display

4.3.3.28. BSC INTER BTS AAL2 Information Display

- Command DIS-BSC-IUB: BSC=a, BTS=b;
 - a: BSC Number (0~11)
 - b: BTS Number(0~47)
- Input DIS-BSC-IUB: BSC=0, BTS=0;
- Output

Command Window
MSC(0x00) 2001-06-14 20:57:40 M2473 DISPLAY BSC INTER BTS AAL2 CONNECTION INFORMATION L0CATION : BSC_0/BTS_0 LICA LINK AAL2_VC LICA LINKAALA LICA LINKAALA <
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jhpark <lgicbsm>] DIS-BSC-IVB:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-65 BSC INTER BTS AAL2 Information Display



4.3.3.29. BSC INTER CAN AAL2/5 Information Display

• Command DIS-BSC-CAN: BSC=a;

- Input DIS-BSC-CAN: BSC=0;
- Output



Fig. 4.3-66 BSC INTER CAN AAL2/5 Information Display

4.3.3.30. BSC INTER SLB AAL5 Information Display

• Command DIS-BSC-SLB: BSC=a;

- Input DIS-BSC-SLB: BSC=0;
- Output'

	_ 🗆 ×
Command Window	I
MSC(0x00) 2001-06-14 21:07:40 M2477 DISPLAY BSC INTER SLB AAL2/5 CONNECTION INFORMATION LOCATION : BSC_0 START VC ID [SLPA_00] : 0x00000028 START VC ID [SLPA_02] : 0x00000028 START VC ID [SLPA_02] : 0x00000028 START VC ID [SLPA_03] : 0x00000028 START VC ID [SLPA_04] : 0x00000028 START VC ID [SLPA_05] : 0x00010028 START VC ID [SLPA_06] : 0x00100028 START VC ID [SLPA_06] : 0x00100028 START VC ID [SLPA_07] : 0x00110028 START VC ID [SLPA_09] : 0x00110028 START VC ID [SLPA_09] : 0x00130028 START VC ID [SLPA_10] : 0x00140028 START VC ID [SLPA_11] : 0x00150028 START VC ID [SLPA_13] : 0x00160028 START VC ID [SLPA_13] : 0x00170028 START VC ID [SLPA_13] : 0x00170028 START VC ID [SLPA_14] : 0x00180028 START VC ID [SLPA_15] : 0x00190028 START VC ID [SLPA_17] : 0x0019028 START VC ID [SLPA_17] : 0x00190	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-BSC-SLB:BSC=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	

Fig. 4.3-67 BSC INTER SLB AAL5 Information Display



4.3.3.31. BSC INTER VCB AAL5 Information Display

• Command DIS-BSC-VCB: BSC=a;

- Input DIS-BSC-VCB: BSC=0;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 21:08:45 M2478 DISPLAY BSC INTER VCB AAL2/5 CONNECTION INFORMATION LOCATION : BSC_0 START VC ID [VCPA_00] : 0x000a0028 START VC ID [VCPA_01] : 0x000b0028 START VC ID [VCPA_02] : 0x000c0028 START VC ID [VCPA_03] : 0x000d0028 START VC ID [VCPA_04] : 0x000e0028 START VC ID [VCPA_06] : 0x00100028 START VC ID [VCPA_06] : 0x00100028 START VC ID [VCPA_06] : 0x00100028 START VC ID [VCPA_08] : 0x00110028 START VC ID [VCPA_09] : 0x00120028 START VC ID [VCPA_10] : 0x00140028 START VC ID [VCPA_11] : 0x00150028 START VC ID [VCPA_12] : 0x00160028 START VC ID [VCPA_13] : 0x00170028 START VC ID [VCPA_13] : 0x00170028 START VC ID [VCPA_14] : 0x00180028 START VC ID [VCPA_15] : 0x00190028 START VC ID [VCPA_15] : 0x001	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
ACCEPTED [jhpark <lgicbsm>] DIS-BSC-VCB:BSC=0; ACCEPTED</lgicbsm>	

Fig. 4.3-68 BSC INTER VCB AAL5 Information Display



4.3.3.32. BSC INTER ALB AAL5 Information Display

• Command DIS-BSC-ALB: BSC=a;

- Input DIS-BSC-ALB: BSC=0;
- Output

XINTERM	. 🗆 ×
Command Window	
M2479 DISPLAY BSC INTER ALB AAL2/5 CONNECTION INFORMATION LOCATION : BSC_0 NUM OF VC : 2016 ALMA ALPA AAL5_VC_ID1 AAL5_VC_ID2 0 0 0x000a0020 0x00140020 0 1 0x000b0020 0x00150020 0 2 0x000c0020 0x00160020 0 3 0x000d0020 0x00170020 0 4 0x000e0020 0x00180020 1 0 0x000a0020 0x00140020 1 1 0x000b0020 0x00150020 1 2 0x000c0020 0x00150020 1 3 0x000d0020 0x00170020 1 4 0x000e0020 0x00180020 Image: Mathematical Stress	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-BSC-ALB:BSC=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	L Z
N N N N N N N N N N N N N N N N N N N	

Fig. 4.3-69 BSC INTER ALB AAL5 Information Display

4.3.3.33. BTS NETWORK PARAMETER Information Display

- Command DIS-BTS-NETP: BSC=a ,BTS=b;
 - a: BSC Number (0~11)
 - b: BTS Number (0~47)
- Input DIS-BTS-NETP: BSC=0,BTS=0;
- Output

	- 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 21:10:52 M2481 DISPLAY BTS NETWORK PARAMETER LOCATION : BSC_0/BTS_0 MSC_ID : 1 NUM OF ADDR HEADER : 0 LICA[0] CONN_TYPE : LICA0 LICA[1] CONN_TYPE : LICA1 LICA[2] CONN_TYPE : LICA2 CRP ADDRESS : 17.128.2.1 BANU BCRA_A ADDRESS : 0.0.0.4 BANU BCRA_B ADDRESS : 0.0.0.5 BANU LICA0 ADDRESS : 0.0.0.6 BANU LICA1 ADDRESS : 0.0.0.7 BANU LICA2 ADDRESS : 0.0.0.8 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-BTS-NETP:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	AI N
X	N

Fig. 4.3-70 BTS NETWORK PARAMETER Information Display

4.3.3.34. BTS INTER BTS AAL2 Information Display

- Command DIS-BTS-IUB: BSC=a ,BTS=b;
 - a: BSC Number (0~11)
 - b: BTS Number (0~47)
- Input DIS-BTS-IUB: BSC=0,BTS=0;
- Output

Command Window 🗟	
MSC(0x00) 2001-06-14 21:11:44 M2482 DISPLAY BTS INTER BTS AAL2 CONNECTION INFORMATION L0CATION : BSC_0/BTS_0 LICA LINK AAL2_VC LICA LINK AAL2_VC O 0 0x00000000 2 0 0x0000000 0 0x00000000 2 0 0x0000000 0 1 0x00000000 2 0 0x00000000 0 1 0x00000000 2 0x00000000 0 2 0x000000000 0 2 0x000000000 0 3 0x000000000 2 3 0x00000000 0 3 0x00000000 2 3 0x00000000 0 4 0x00000000 2 3 0x00000000 0 5 0x00000000 2 5 0x00000000 0 5 0x00000000 0 5 0x00000000 0 0x00000000 0 0x00000000 0 0x00000000 0 0x000000000 0 0x0000000	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-BTS-IVB:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	
4	

Fig. 4.3-71 BTS INTER BTS AAL2 Information Display

4.3.3.35. BTS INTER RCU AAL5 Information Display

- Command DIS-BTS-RCU: BSC=a ,BTS=b;
 - a: BSC Number (0~11)
 - b: BTS Number (0~47)
- Input DIS-BTS-RCU: BSC=0,BTS=0;
- Output

Command Window
MSC(0x00) 2001-06-14 21:12:39 M2483 DISPLAY BTS INTER RCU AALS CONNECTION INFORMATION L0CATION : BSC_0/BTS_0 NUM 0F VC [LICA_0] : 480 NUM 0F VC [LICA_1] : 480 RCU LICA AAL5_VC_ID RCU LICA AAL5_VC_ID 0 0 0x000a0020 1 0 0x000b0020 0 1 0x00140020 1 1 0x00150020 0 2 0x001e0020 1 2 0x001f0020 2 0 0x000c0020 3 0 0x000d0020 2 1 0x00160020 3 1 0x00170020 2 2 0x001e0020 3 2 0x00210020 4 0 0x000e0020 5 0 0x000f0020 4 1 0x00180020 5 1 0x00190020 4 2 0x00220020 5 2 0x00230020 6 0 0x00100020 7 0 0x00110020 6 1 0x001a0020 7 1 0x00190020 8 0 0x00120020 9 1 0x00130020 8 1 0x00120020 9 1 0x00130020 8 2 0x00240020 9 1 0x00130020 8 2 0x00240020 9 2 0x00270020 1 0x00140020 9 1 0x00140020 1 0x00140020 9 1 0x00130020 1 0x00140020 9 1 0x00130020 1 0x00120020 9 1 0x00130020 1 0x00140020 9 1 0x00130020 1 0x00120020 9 1 0x00130020 1 0x00140020 9 1 0x00140020 1 0x00140020 9 1 0x00140020 1 0x00140020 9 1 0x00130020 1 0x00140020 9 1 0x00140020 1 0x00140020 9 100000 1 0x00140020 9 1000
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jhpark <lgicbsm>] DIS-BTS-RCU:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-72 BTS INTER RCU AAL5 Information Display



4.3.4. Parameter Information Change Command

(Change_Parameter_Information_1)

This section describes commands that are used to change the parameter information that is inquired. The command to change parameter information cannot be easily input by the keyboard since input parameter counts are too many. For this reason, this section will skip the command input in Text and demonstrate window input by mouse. Upon inputting the command, the part in blue is the parameter that the user can change and the user can change part or entire fields among them. For details of each parameter that is input, refer to Appendix.



2201	CHG-BTS-DATA	BTS parameter data change
2202	CHG-SECT-DATA	SECTOR Parameter data change
2203	CHG-CHAN-DATA	CDMA CHANNEL parameter data Change
2205	CHG-SYS1-PARA	SYSTEM PARAMETER(1) change
2206	CHG-SYS2-PARA	SYSTEM PARAMETER(2) change
2207	CHG-EXT1-SYS	EXTENDED SYSTEM PARAMETER(1) change
2208	CHG-EXT2-SYS	EXTENDED SYSTEM PARAMETER(2) change
2209	ADD-NGBR-DATA	Addition of Neighbor cell data
2210	RMV-NGBR-DATA	Deletion of Neighbor cell data
2211	CHG-NGBR-DATA	Neighbor cell data change
2213	CHG-NGBR-BCON	HOPPING BEACON PARAMETER change
2214	CHG-QOS-PARA	QOS parameter data change
2216	CHG-CHIP-PWR	Chip Power Control data change
2222	CHG-TIC-DATA	TIC parameter data change
2223	CHG-SECT-CHAN	Sector CDMA Channel change
2224	CHG-PWR-PARA	Power control parameter data change
2225	CHG-AC-PARA	ACCESS CHANNEL parameter information change
2226	CHG-TXMS-PARA	TXMS parameter data change
2227	START-BTS-CALB	BTS CALIBRATION start
2229	CHG-GSRM-PARA	PC GLOBAL REDIRECT parameter change
2230	CHG-ACC-PARA	ACCESS PARAMETER change
2232	CHG-PC-PARA	PAGING CHANNEL parameter data change
2233	CHG-PICH-PARA	PILOT CHANNEL parameter data change
2236	CHG-SC-PARA	SYNC CHANNEL parameter data change
2238	CHG-QPC-PARA	QUICH PAGING CHANNEL parameter data change
0020		HOPPING PILOT BEACON CHANNEL parameter data
2209	UIG-DUUN-PAKA	change

4.3.4.1. BTS Parameter Information Change

To change the BTS parameter information, click CDM->Change_Parameter_ Information->Change BTS Data on the Command Window in order and input the value that the command wants to change in each field.

- Command CHG-BTS-DATA :BSC=a ,BTS=b [,SID=c] [,NID=d] [,BASE_ID=e] [,BASE_CLASS=f] [,REG_ZONE=g] [,LTM_OFF=h] [,DAY_LT=i] [,BASE_LAT=j] [,BASE_LONG=k] [,TUB_ENC=I] [,REV_PWR=m];
- Input CHG-BTS-DATA :BSC=0,BTS=0,SID=3333;
- Output

	x
Command Window	
MSC(0x00) 2001-06-14 21:16:08 M2201 CHANGE BTS DATA L0CATION : BSC_0/BTS_0 [EQUIP] SID : 3333 NID : 15 [0xf] BASE_ID : 0 BASE_CLASS : PUBLIC_MICROCELLULA BTS_TYPE : STANDARD SECTOR_RANGE : 3 REC_20NE : 0 PKT_20NE : 0 LTM_0FF : 18 (30MIN) DAY_LT : STANDARD CDMA_FREQ : 50 EXT_CDMX_FREQ : 50 BASE_LAT : -1296000 [-90 degree, 0 minute, 0 seconds] BASE_LONG : -2592000 [-180 degree, 0 minute, 0 seconds] PILOT_INCREMENT : 4 TURB0_ENCODE_FLAG : NOT_USE REVERSE_PWR_FLAG : USE	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BTS-DATA:BSC=0,BTS=0,SID=3333; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-73 BTS Parameter Information Display

4.3.4.2. Sector Parameter Information Change

To change the sector parameter information, click CDM->Change_Parameter_ Information_1-> CHG-SECT-DATA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-SECT-DATA :BSC=a ,BTS=b ,SECT=c [,PN=d] [,CNTL_PARA=e];
- Input CHG-SECT-DATA :BSC=0,BTS=0,SECT=ALPHA,PN=40;
- Output

Command Window	
MSC(0x00) 2001-06-14 21:19:42 M2202 CHANGE SECTOR DATA LOCATION : BSC_0/BTS_0/ALPHA [EQUIP] NUM_OF_CDMA_CH : 2 PILOT_PN_OFFSET : 40 (64 PN CHIPS) CALL_CONTROL_PARA : DISABLE COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-SECT-DATA:BSC=0,BTS=0,SECT=ALPHA,PN=40; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-74 Sector Parameter Information Change

4.3.4.3. CDMA Channel Parameter Information Change

To change the CDMA parameter information, click CDM->Change_Parameter_ Information_1-> CHG-CHAN-DATA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-CHAN-DATA :BSC=a ,BTS=b ,CDMACH=c [,FREQ_BAND=d] [,CH_NUM=e] [,TCE_4HO=f] [,MAX_SCH=g];
- Input CHG-CHAN-DATA :BSC=0,BTS=0 ,CDMACH=0 ,FREQ_BAND=2222;
- Output

	□×
Command Window	
MSC(0x00) 2001-06-14 21:43:16 M2203 CHANGE CDMA CHANNEL DATA LOCATION : BSC_0/BTS_0/CDMACH_0 [EQUIP] CDMACH_NUM : 152 FREQ_BAND : 2222 [MHz] TCE_RESERVED_4H0 : 20 [PERCENT] MAX_SCH_ALLOC_RATE : 50 TEST_FLAG : NOT_TEST COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CHAN-DATA:BSC=0,BTS=0,CDMACH=0,FREQ_BAND=2222; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-75 CDMA Channel Parameter Information Display

4.3.4.4. SYSTEM PARAMETER(1) Change

To change the system parameter message, click CDM-> Change_Parameter_Information_1-> CHG-SYS1-PARA on the Command Window in order. As the System Parameter Message have many elements, they are divided into the three commands. The output format for each command is the same.

• Command CHG-SYS1-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,TOT_ZONE=e] [,ZONE_TIME=f] [,MULT_SIDS=g] [,MULT_NIDS=h] [,REP_THSH=i] [,REP_FRAM=j] [,SRCH_WINA=k] [,SRCH_WINN=1] [,SRCH_WINR=m] [,NGHB_MAGE=n] [,T_ADD=o] [,T_DROP=p] [,T_COMP=q] [,T_TDRP=r];

- Input CHG-SYS1-PARA :BSC=0,BTS=0 ,SECT=ALPHA,CDMACH=0,TOT_ZONE=5
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 21:45:56 M2205 CHANGE SYSTEM PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 TOTAL_ZONES : 5 ZONE_TIMER : 2 [5 MIN] MULT_SIDS : DISABLE MULT_NIDS : DISABLE PWR_REP_THRESH : 4 PWR_REP_FRAMES : 7 (FRAMES) SRCH_WIN_A : 7 [WIN_SIZE : 40 (PN CHIPS)] SRCH_WIN_N : 9 [WIN_SIZE : 80 (PN CHIPS)] SRCH_WIN_R : 10 [WIN_SIZE : 100 (PN CHIPS)] NGHBR_MAX_AGE : 1 T_ADD : 26 T_DROP : 30 T_COMP : 5 T_TDROP : 2 [2 SECS]	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-SYS1-PARA:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,TOT_ ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	ZON
	Δ

Fig. 4.3-76 System Parameter Change(1) Display

4.3.4.5. SYSTEM 4.3.4.6. PARAMETER(2) Change

To change the system parameter message, click CDM-> Change_Parameter_Information_1-> CHG-SYS2-PARA on the Command Window in order. Since the System Parameter Message have many elements, they are divided into three commands. The output format for each command is the same.

- Input CHG-SYS2-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,HOME_REG=e]
 [,MAX_SCI=f] [,NID_REG=g] [,SID_REG=h] [,PARM_REG=i] [,REG_PRD=j]
 [,REG_DIST=k] [,PWR_UP=1] [,PWR_DOWN=m]
 [,THSH_EABL=n] [,PRID_EABL=o] [,REP_DELY=p]
 [,RE_SCAN=q] [,EXT_SYS=r] [,EXT_NGHBR=s] [,GEN_NGHBR=t]
 [,REDIRECT=u] [,PRI_NGHBR=v] [,USER_ZONE=w]
 [,EXT_REDIRECT=x] [,EXT_CHAN=y] ;
- Output CHG-SYS2-PARA :BSC=0 ,BTS=0,SECT=ALPHA ,CDMACH=0, HOME_REG=MANUAL;

• Display



XINTERM		
Command Window		
MSC(0x00) 2001-06-14 21: M2206 CHANGE SYSTEM LOCATION : BSC_0/B HOME_REG MAX_SCI FOR_NID_REG FOR_SID_REG PARAMETER_REG REG_PRD REG_DIST POWER_UP_REG POWER_DOWN_REG PWR_THRESH_ENABLE PWR_PERIOD_ENABLE PWR_REP_DELAY RESCAN EXTEND_SYS EXTEND_SY	48:00 ▲ PARAMETER TS_0/ALPHA/CDMACH_0 : MANUAL :3 : YES :48:00 : YES :48:00 : YES :54 [90_MINUTE] : 0 :74:5 : YES :5 (MIN) : N0 :5 (MIN) : YES :74:5 : YES :74:5 : YES :74:5 : N0 :100 : YES :100 : YES :100 : YES :100	
	K	
<u>IO LONG COM STM TES</u>	T <u>N</u> O.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] CHG-SY ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	S2-PARA:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,HOME_RE	
AI		

Fig. 4.3-77 System Parameter Change(2) Display

4.3.4.7. EXTENDED SYSTEM PARAMETER(1) Change

To change the Extended System Parameter Message, click CDM-> Change_Parameter_Information_1-> CHG-EXT1-SYS on the Command Window in order. If the next input window is displayed, then input the value to be changed.

Change CHG-EXT1-SYS :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,PREF_MSID=e]
[,MCC=f] [,IMSI_11_12=g] [,TMSI_LEN=h]
[,TMSI_ZONE_1=i] [,TMSI_ZONE_2=j] [,TMSI_ZONE_3=k]
[,TMSI_ZONE_4=1] [,TMSI_ZONE_5=m]
[,TMSI_ZONE_6=n] [,TMSI_ZONE_7=o]
[,TMSI_ZONE_8=p] [,BCAST_IDX=q] [,SOFT_SLOPE=r]
[,ADD_INT=s] [,DROP_INT=t] [,NGBR_SET=u]
[,ACCESS_HO=v] [,HO_MSG_RSP=w] [,ACC_PRB_HO=x] [,PRB_HO_OT=y] ;

• Input CHG-EXT1-SYS :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0 , PREF_MSID=IMSI;

• Output



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Command Window
MSC(0x00) 2001-06-14 21:49:30 M2207 CHANGE EXTENDED SYSTEM PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 PREF_MSID_TYPE : IMSI MOBILE COUNTRY CODE : 450 [DECIMAL : 349(0x15d)] IMSI_11_12 : 0 [DECIMAL : 99(0x63)] TMSI_ZONE_LEN : 4 TMSI_ZONE_LEN : 4 TMSI_ZONE : 0x00000000 BR0ADCAST_INDEX : 6 SOFT_SLOPE : 0 ADD_INTERCEPT : 0 DROP_INTERCEPT : 0 NGHBR_SET_ACC_INFO : 0 ACCESS_H0 : 0 ACCESS_H0 : 0 ACCESS_PROBE_H0 : 0 ACC_PROBE_H0_ETC_MSG : 0 COMPLETED
<u>IO LONG COM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jhpark <lgicbsm>] CHG-EXT1-SYS:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,PREF_MSI ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-78 Extended System Parameter Change(1) Display

4.3.4.8. EXTENDED SYSTEM PARAMETER(2) Change

To change the Extended System Parameter Message, click CDM-> Change_Parameter_Information_1-> CHG-EXT2-SYS on the Command Window in order. If the next input window is displayed, then input the value to be changed.

• Command CHG-EXT2-SYS :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,IMSI_T_SUPRT=e]

[,P_REV=f] [,MIN_P_REV=g] [,MAX_ALT_SO=h] [,RESEL_INCL=i] [,EC_THRESH=j] [,EC_IO_THRESH=k] [,PILOT_REPORT=1] [,NGBR_SET_INF=m] [,ACC_HO_ORD=n] [,HO_LIST_UPD=o] [,MAX_PRB_HO=p] [,BRD_GPS_ASS=q] [,QPC_SUPPORT=r] [,NUM_QPCH=s] [,QPCH_RATE=t] [,QPC_PWR_LEV=u] [,QPC_CCI=v] [,QPC_PWR_CFG=w] [,SDB_SUPPORT=x] [,MAC_CF_SPRT=y] [,RLGAIN_PICH=z];

- Input CHG-EXT2-SYS :BSC=0 ,BTS=0,SECT=ALPHA ,CDMACH=,IMSI_T_SUPRT=1;
- Output



Command Window
MSC(0x00) 2001-06-14 21:54:19 M2208 CHANGE EXTENDED SYSTEM PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 IMST_T_SUPPORTED : 1 P_REV_LEVEL : 95C MIN_P_REV_LEVEL : J-STD-008(PCS) MAX_NUM_ALT_SO : 0 RESELECT_INCLUDED : 0 EC_THRESH : 0 PILOT_REPORT : 0 NGHBR_SET_ENTRY_INFO : 0 ACC_ENT_H0_ORDER : 0 ACC_ENT_H0_ORDER : 0 MAX_NUM_PROBE_H0 : 0 BR0ADCAST_GPS_ASST : 0 QPCH_SUPPORTED : 1 NUM_QPCH : 1 QPCH_RATE : 0[4800BPS] QPCH_PWR_LEVEL_CNFG : 5 SDB_SUPPORTED : 0 MAC_CF_SUPPORTED : 0 RLGAIN_TRAFFIC_PILOT : 0 COMPLETED
TO LONG COM STM TEST NO 7 STAT ALET PREVIOUS
ACCEPTED [jhpark <lgicbsm>] CHG-EXT2-SYS:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,IMSI_T_S</lgicbsm>

Fig. 4.3-79 Extended System Parameter Change(2) Display

4.3.4.9. Neighbor Cell Information Addition

To add the neighbor list, click the CDM->Change_Parameter_Information_1-> ADD-NGBR-DATA on the Command Window in order. If the next input window is displayed, then input the values to be changed.

- Command ADD-NGBR-DATA :BSC=a ,BTS=b ,SECT=c ,INDEX=d ,NGBR_CNFG=e ,NGBR_PN=f ,NGBR_SID=g ,NGBR_NID=h ,NGBR_BASE=i ,NGBR_MSC=j ,NGBR_BSC=k ,NGBR_BTS=l ,NGBR_SECT=m ,NGBR_MSC_T=n ,NGBR_BSC_T=o,NGBR_BCON=p ,SRCH_PRIO=q ,FREQ_INCL=r [,NGBR_BAND=s] [,NGBR_FREQ=t] [,TIME_INCL=u] [,TX_OFFSET=v] [,TX_DURATION=w] [,TX_PERIOD=x] [,SRCH_SET=y] [,ADD_PICH_REC=z] [,PICH_REC=] [,OTD_PWR=] [,SRCH_OFFSET=] [,ACC_HO=] [,ACC_HO_ALW=];
- Input ADD-NGBR-DATA :BSC=0 ,BTS=0 ,SECT=ALPHA ,INDEX=0 ,NGBR_CNFG=0 ,NGBR_PN=0 ,NGBR_SID=0 ,NGBR_NID=0 ,NGBR_BASE=0 ,NGBR_MSC=0 ,NGBR_BSC=0 ,NGBR_BTS=0 ,NGBR_SECT=ALPHA ,NGBR_MSC_T=LG_MSC ,NGBR_BSC_T=LG_BSC,NGBR_BCON=NO ,SRCH_PRIO=LOW ,FREQ_INCL=NO,NGBR_BAND=Mhz_800;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 21:56:02 M2209 ADD NEIGHBOR LIST LOCATION : BSC_0/BTS_0/ALPHA RESULT : 0K ADDED NEIGHBOR : BSC_0/BTS_0/ALPHA COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-NGBR-DATA:0,0,ALPHA,0,0,0,0,0,0,0,0,0,ALP ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	HA, LG_MS

Fig. 4.3-80 Neighbor Cell Addition Display

4.3.4.10. Neighbor Cell Information Deletion

To delete the neighbor list, click CDM->Change_Parameter_Information_1-> RMV-NGBR-DATA on the Command Window in order. If the next window is displayed, then input the sector and PN value of the sector to be deleted.

- Command RMV-NGBR-DATA :BSC=a ,BTS=b ,SECT=c ,NGBR_PN=d;
- Input RMV-NGBR-DATA :BSC=0,BTS=0,SECT=ALPHA,NGBR_PN=0;
- Output

× IN1	
	Command Window
COMF	(0x00) 2001-06-14 21:57:23 M2210 REMOVE NEIGHBOR LIST LOCATION : BSC_0/BTS_0/ALPHA RESULT : 0K REMOVED NEIGHBOR : BSC_0/BTS_0/ALPHA PLETED
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS
[jhp ACCI [jhp	park <lgicbsm>] RMV-NGBR-DATA:BSC=0,BTS=0,SECT=ALPHA,NGBR_PN=0; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-81 Neighbor Cell Information Deletion Display

4.3.4.11. Neighbor Cell Information Change

To change the neighbor list, click CDM->Change_Parameter_Information_1-> CHG-NGBR-DATA on the Command Window in order. If the next input Window is displayed, input the sector and the PN value of the sector to be deleted.

• Command DATA :BSC=a ,BTS=b ,SECT=c ,NGBR_PN=d ,NEW_INDEX=e

• Input

• Output



CHG-NGBR-

4.3.4.12. HOPPING BEACON PARAMETER Change

To change Hopping Beacon Parameter, click CDM->Change_Parameter_Information_1-> CHG-NGBR-BCON on the Command Window in order.

- Command CHG-NGBR-BCON :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,NGBR_SRCH=e] [,USE_TIMING=f] [,G_TIME_INCL=g] [,G_TX_DURATE=h] [,G_TX_PERIOD=i] [,SRCH_OFF_INC=j];
- Input CHG-NGBR-BCON :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0 , NGBR_SRCH=255;
- Output

XINTERM	_ 🗆 ×			
Command Window 🗟				
MSC(0x00) 2001-06-14 22:00:56 M2213 CHANGE NEIGHBOR HOPPING BEACON DATA LOCATION : BSC_0/BTS_0 SECTOR : ALPHA NGHBRS_IN_MSG : 11 NGHBRS_SRCH_MODE : 255 USE_TIMING : 0 GLOBAL_TIMING_INCL : 0 GLOBAL_TX_DURATION : 0 GLOBAL_TX_PERIOD : 0 SRCH_0FFSET_INCL : 0 COMPLETED				
IO LDNG CDM SIM TEST NO.7 SIAT ALFT PREVIOUS [jhpark <lgicbsm>] CHG-NGBR-BCON:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,NGBR_SR ACCEPTED [jhpark <lgicbsm>] [[jhpark <lgicbsm>] [</lgicbsm></lgicbsm></lgicbsm>				

Fig. 4.3-82 Hopping Beacon Parameter Change Display

4.3.4.13. QOS Parameter Change

To change Quality Of Service parameter information, click CDM->Change_Parameter_Information_1-> CHG-QOS-PARA on the Command Window in order.

- Command CHG-QOS-PARA :BSC=a ,BTS=b [,MAX_SCH_RATE=c];
- Input CHG-QOS-PARA :BSC=0 ,BTS=0,MAX_SCH_RATE=255;
- Output

Command Window	
MSC(0x00) 2001-06-14 22:04:40 M2214 CHANGE QOS PARAMETER LOCATION : BSC_0/BTS_0 MAX_SCH_RATE : 255 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-QOS-PARA:BSC=0,BTS=0,MAX_SCH_RATE=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-83 QOS Parameter Information Change Display

4.3.4.14. Chip Power Control Information Change

To change Chip Power Control information, click CDM->Change_Parameter_Information_1-> CHG-CHIP-PWR on the Command Window in order.

- Command CHG-CHIP-PWR :BSC=a ,BTS=b [,CH_PWR0=c] [,CH_PWR1=d] [,CH_PWR2=e] [,CH_PWR3=f] [,MIN_GAIN0=g] [,MIN_GAIN1=h] [,MIN_GAIN2=i] [,MIN_GAIN3=j][,MAX_GAIN0=k] [,MAX_GAIN1=l] [,MAX_GAIN2=m] [,MAX_GAIN3=n] [,STEP_UP_SIZE=o] [,STEP_DN_SIZE=p] [,FPC_PUNC=q] [,RPC_PUNC=r] [,PWR_CNT_PNT=s] [,PWR_CNT_PTN=t] ;
- Input CHG-CHIP-PWR :BSC=0 ,BTS=0 ,CH_PWR0=255;
- Output

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Command Window
MSC(0x00) 2001-06-14 22:05:56 M2216 CHANGE CHIP POWER PARAMETER LOCATION : BSC_0/BTS_0 CHAN_PWR[00] : 255 CHAN_PWR[01] : 224 CHAN_PWR[02] : 224 CHAN_PWR[03] : 224 MIN_GAIN[00] : 150 MIN_GAIN[01] : 150 MIN_GAIN[03] : 150 MIN_GAIN[03] : 124 MAX_GAIN[01] : 224 MAX_GAIN[01] : 224 MAX_GAIN[02] : 224 MAX_GAIN[03] : 224 STEP_UP_SIZE : 4 STEP_UP_SIZE : 4 FPC_PUNC_MODE : 0 RPC_PUNC_MODE : 0 RPC_PUNC_MODE : 0 PWR_CNTL_PATTERN : 3 COMPLETED
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jhpark <lgicbsm>] CHG-CHIP-PWR:BSC=0,BTS=0,CH_PWR0=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-84 Chip Power Control Information Change Display

4.3.4.15. TIC Parameter Change

To change Tx Gain value, click CDM->Change_ Parameter_Information_1-> CHG-TIC-DATA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-TIC-DATA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,TX_GAIN=e] ;
- Input CHG-TIC-DATA :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0 ,TX_GAIN=255 ;
- Output

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×IN.	TERM				4			
Command Window								
COM	(0x00) 2 M2222 LOCA TX_C PRIM PLETED	2001-06-14 CHANGE TI MTION : BS MIN MARY_FUNCT	22:07:13 C DATA C_O/BTS_O/ALF : 255 [ION : 1	PHA/CDMACH_O Oxff]				
<u>1</u> 0	<u>L</u> DNG	<u>C</u> DM <u>s</u> tm	<u>I</u> EST <u>N</u> 0.7	ST <u>A</u> T AL <u>F</u> T	PREVIOUS			
[jh; ACCI [jh;	oark <lq EPTED oark <lq< td=""><td>SICBSM>] C</td><td>HG-TIC-DATA:E</td><td>3SC=0,BTS=0,SI</td><th>ECT=ALPHA,CDM</th><td>IACH=0, TX_GAIN=</td></lq<></lq 	SICBSM>] C	HG-TIC-DATA:E	3SC=0,BTS=0,SI	ECT=ALPHA,CDM	IACH=0, TX_GAIN=		

Fig. 4.3-85 TIC Parameter Information Change Display

4.3.4.16. OCNS Parameter Change

To change OCNS Parameter value, click CDM->Change_ Parameter_Information_1-> CHG-OCNS-PARA on the Command Window in order. If the next input Window is displayed, then input the value to be changed.

- Command CHG-OCNS-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d
 [,OCNS_ENABLE=e]
 [,NUM_OCNS_CH=f] [,OCNS_TEST=g] [,OCNS_SO=h];
- Input CHG-OCNS-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA , CDMACH=0 ,OCNS_ENABLE=DISABLE,NUM_OCNS_CH=2;
- Output

×INTERM +	_ 🗆 ×				
Command Window					
MSC(0x00) 2001-06-14 22:08:34 M2223 CHANGE BTS OCNS PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 OCN_ENABLE : DISABLED NUM_OF_OCNS_CH : 2 OCNS_TEST_DATA : 1/4 OCNS_S0 : MARKOV 13K COMPLETED	N N				
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>					
[jhpark <lgicbsm>] CHG-OCNS-PARA:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,OC ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	NS_EN				

Fig. 4.3-86 OCNS Parameter Change Display
4.3.4.17. Power Control Information Change

To change OCNS Parameter value, click CDM->Change_ Parameter_Information_1-> CHG-PWR-PARA on the Command Window in order. If the next input Window is displayed, then input the value to be changed.

- Command CHG-PWR-PARA :BSC=a ,BTS=b ,CDMACH=d ,SECT=c [,T_RX_FILTER=e] [,G_RX_FILTER=f] [,G_TX_FILTER=g] [,GH_TX_FILTER=h] [,R_TX_PILOT=i] [,FLN_BLK_THSH=j] [,FLN_HO_THSH=k] [,FLN_GAIN_SCA=1] [,RLN_BLK_THSH=m] [,RLNK_HO_THSH=n] [,DELTA_A_TX=p] [,K_LEVEL=q] [,K_SLOPE=r] [,A_TX_MAX=o] [,K_DELTA=s] [,DELTA_T=t] [,P_TX_MAX=u] [,INIT_CALB=v] [,PRD_CALIB=w] [,BREATH_FLAG=x] [,PWR_EST_FLAG=y] [,OVPWR_LMT=z] [,FLN_CAP_LMT=] [,RLN_CAP_LMT=];
- Input CHG-PWR-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0, T_RX_FILTER=255;
- Output

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Command Wind	low
command mine	
MSC(0x00) 2001-06-14 22:09:58 M2224 CHANGE POWER MANAGEMENT PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 T_RX_FILTER : 255 G_RX_FILTER : 0 G_TX_FILTER : 0 GH_TX_PILOT : 0 R_TX_PILOT : 0 R_TX_PILOT : 0 FLNK_CALL_BLK_THESH : 0 FLNK_GAIN_SCALING_THESH : 0 RLNK_CALL_BLK_THESH : 0 RLNK_CALL_BLK_THESH : 0 RLNK_CALL_BLK_THESH : 0 RLNK_CALL_BLK_THESH : 0 DELTA_A_TX : 0 K_LEVEL : 0 K_SLOPE : 0 K_DELTA : 0 DELTA_T : 0 P_TX_MAX : 0 INITIAL_CALIBRATION : BLOSSOM PERIODIC_CALIBRATION : TPTL BREATHING_FLAG : DISABLE PWR_EST_FLAG : DISABLE RLNK_CAP_LIMIT : DISABLE RLNK_CAP_LIMIT : DISABLED COMPLETED	
2	
<u>IO LONG COM STM TEST NO.7 STAT ALFT</u>	PREVIOUS
[jhpark <lgicbsm>] CHG-PWR-PARA:BSC=0,BTS=0,SE ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	CT=ALPHA,CDMACH=0,T_RX_FIL

Fig. 4.3-87 Power Control Parameter Information Display

4.3.4.18. ACCESS CHANNEL Parameter Information Change

To change Access Channel Parameter information, click CDM-> Change_ Parameter_Information->CHG-AC-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-AC-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d ,PC=e ,AC=f [,SRCH_OFFSET=g] [,SRCH_WIN_SZ=h];
- Input CHG-AC-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA , CDMACH=0 , PC=0, SRCH_WIN_SZ=32;
- Output

× IN	TERM								÷	_ 🗆 ×
				C	omm	and	Wind	dow		
MSC Com	(0x00) M2225 LOC SRC SRC PLETED	2001-0 CHANG ATION H_STAF	DG-14 GE ACC : BSC RT_OFF _SIZE	22:11 ESS CH 0/BTS SET : :	:43 ANNEL 	PARAME 'HA/CDM	TER ACH_0/I	PC_0/AC_0	[EQUIP]	
<u>1</u> 0	LDNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS	i	
[jh ACC [jh	park <l EPTED park <l< td=""><td>GICBSN.</td><td>I>] CH</td><td>IG-AC-P</td><td>ARA : BS</td><td>SC=0,BT</td><td>S=0, SE(</td><td>CT=ALPHA, C</td><td>DMACH=0,P</td><td>C=0, SRCH</td></l<></l 	GICBSN.	I>] CH	IG-AC-P	ARA : BS	SC=0,BT	S=0, SE(CT=ALPHA, C	DMACH=0,P	C=0, SRCH

Fig. 4.3-88 Access Channel Parameter Information Change Display

4.3.4.19. TXMS Parameter Information Change

To change TXMS Parameter information, click CDM-> Change_ Parameter_Information->CHG-TXMS-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

$\bullet Command$

CHG-TXMS-

PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d ,OH_CH_ERP=e ;

- Input CHG-TXMS-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0 , OH_CH_ERP=25000 ;
- Output

×IN1	TERM									_	
				С	omm	and	Wind	dow			
MSC((0x00) ; M2226 ; LOC/ OVHI PLETED	2001-C CHANGI ATION DCH_EF	06-14 E TXMS : B XP : 2	22:12 PARAM SC_0/B 5000	::57 ETER TS_0/A	IPHA/C	DMACH_(
<u>1</u> 0	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS	i		
[jhp ACCI [jhp	oark <lo EPTED oark <lo< td=""><td>GICBSM GICBSM</td><td>I>] CH I>]]</td><td>IG-TXMS</td><td>-PARA :</td><td>BSC=0,</td><td>BTS=0,9</td><td>SECT=ALPHA</td><td>, CDMACH=</td><td>0,0H_CH_</td><td></td></lo<></lo 	GICBSM GICBSM	I>] CH I>]]	IG-TXMS	-PARA :	BSC=0,	BTS=0,9	SECT=ALPHA	, CDMACH=	0,0H_CH_	

Fig. 4.3-89 TXMS Parameter Information Change Display

4.3.4.20. BTS CALIBRATION Start

To start BTS Calibration, click CDM-> Change_ Parameter_Information->START-BTS-CALB on the Command Window in order. If the next input window is displayed, then input the value to be changed.

• Command START-BTS-CALB :BSC=a ,BTS=b [,SECT=c] [,CDMACH=d],METHOD=e ;

• Input START-BTS-CALB :BSC=0 ,BTS=0 ,SECT=ALPHA,METHOD=OVHD ;

• Output

XINTERM	. 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 22:13:56 M2227 START BTS CALIBRATION LOCATION : BSC_0/BTS_0 SECT : ALPHA CDMACH : ALL METHOD : OVHD CH CALIBRATION RESULT : OK COMPLETED	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> O.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] START-BTS-CALB:BSC=0,BTS=0,SECT=ALPHA,METHOD=OVHD; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-90 BTS Calibration Start Display

4.3.4.21. BTS Calibration Time Change

To change BTS Calibration time, click CDM-> Change_ Parameter_Information->CHG-CALB-TIME on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-CALB-DATA :BSC=a ,BTS=b ,HOUR=c ,MINUTE=d ,INTERVAL=e ,METHOD=f;
- Input CHG-CALB-DATA :BSC=0 ,BTS=0 ,HOUR=1 ,MINUTE=1 ,INTERVAL=1 ,METHOD=OVHD;

• Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 22:15:12 M2228 CHANGE CALIBRATION TIME LOCATION : BSC_0/BTS_0 HOUR : 1 MINUTE : 1 INTERVAL : 1 METHOD : OVHD CH CALIBRATION RESULT : OK COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CALB-DATA:BSC=0,BTS=0,HOUR=1,MINUTE=1,INTERVAL ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	L=1,

Fig. 4.3-91 BTS Calibration Time Change Display



4.3.4.22. PC GLOBAL REDIRECT Parameter Information Change

To change Paging Channel Global Redirect information, click CDM-> Change_ Parameter_Information->CHG-GSRM-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-GSRM-PARA :BSC=a ,BTS=b ,SECT=c [,ACCOLC=d] [,RET_IF_FAIL=e]
 [,P_REV_MS=f] [,RDIR_P_REV=g] [,EXCL_P_REV=h] [,RDIR_P_MIN=i]
 [,RDIR_P_MAX=j] [,RECORD_TYPE=k] [,RECORD_LEN=1]
 [,EXPECT_A_SID=m] [,IGNORE_CDMA=n] [,SYS_ORDER=o]
 [,BAND_CLASS=p] [,EXPECT_SID=q] [,EXPECT_NID=r] [,NUM_CHAN=s]
 [,CDMA_CH_0=t] [,CDMA_CH_1=u] [,CDMA_CH_2=v]
 [,CDMA_CH_3=w] [,CDMA_CH_4=x] [,CDMA_CH_5=y]
 [,CDMA_CH_6=z] [,CDMA_CH_7=] [,CDMA_CH_8=] [,CDMA_CH_9=];
- Input CHG-GSRM-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA,ACCOLC=255;
- Output



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	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-14 22:16:18 M2229 CHANGE GSRM PARAMETER LOCATION : BSC_0/BTS_0/ALPHA REDIRECT_ACCOLC : 0x00FF RETURN_IF_FAIL : YES DELETE_TMSI : 0 EXCL_P_REV_MS : 0 REDIRECT_P_REV_INCL : 0 REDIRECT_P_MIN : 6 REDIRECT_P_MAX : 6 RECORD_TYPE : CDMA RECORD_LENGTH : 3 EXPECTED_SID : 0 EXPECTED_SID : 0 NUM_CHANS : 0 COMPLETED	
4	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-GSRM-PARA:BSC=0,BTS=0,SECT=ALPHA,ACCOLC=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	
4	

Fig. 4.3-92 PC Global Redirect Parameter Information Change Display

4.3.4.23. ACCESS PARAMETER Change

To change Access Parameter information, click CDM-> Change_ Parameter_Information->CHG-ACC-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-ACC-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d ,PC=e [,NOM_PWR=f] [,INIT_PWR=g] [,PWR_STEP=h] [,NUM_STEP=i] [,MAX_CAP_SZ=j] [,PAM_SZ=k] [,PSST_09=1] [,PSST_10=m] [,PSST_11=n] [,PSST_12=o] [,PSST_13=p] [,PSST_10=m] [,PSST_15=r] [,MSG_PSST_12=o] [,PSST_13=p] [,PSST_14=q] [,PSST_15=r] [,MSG_PSST=s] [,REG_PSST=t] [,PRBE_RAN=u] [,ACC_TMO=v] [,PRBE_BKOF=w] [,BKOF=x] [,MREQ_SEQ=y] [,MRSP_SEQ=z] [,AUTH=] [,RAND=] [,NOM_PWR_EXT=];
- Input CHG-ACC-PARA :BSC=0 ,BTS=0,SECT=ALPHA ,CDMACH=0 ,PC=0 ,NOM_PWR=7;
- Output



Command Window
MSC(0x00) 2001-06-14 22:22:42 M2230 CHANGE ACCESS PARAMETER MESSAGE DATA LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0/PC_0 NOM_PWR : 7 (dB) INIT_PWR : 0 (dB) PWR_STEP : 3 NUM_STEP : 6 MAX_CAP_SZ : 3 PAM_SZ : 3 PSIST_0_9 : 0 PSIST_10 : 0 PSIST_11 : 0 PSIST_12 : 0 PSIST_12 : 0 PSIST_13 : 0 PSIST_14 : 0 A for the second seco
PSIST_15 : 0 MSG_PSIST : 0 REG_PSIST : 0 PROBE_PN_RAN : 0 ACC_TMO : 6 PROBE_BKOFF : 0 BKOFF : 1 MAX_REQ_SEQ : 2 MAX_RSP_SEQ : 2 AUTHENTICATION : NO RAND : 0 NOM_PWR_EXT : -8dB ~ 7dB COMPLETED
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jhpark <lgicbsm>] CHG-ACC-PARA:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,PC=0,NOM ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-93 Access Parameter Information Change Display

4.3.4.24. PAGING CHANNEL Parameter Information Change

To change Paging Channel Parameter information, click CDM-> Change_ Parameter_Information->CHG-PC-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-PC-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d ,PC=e [,PC_GAIN=f] [,FRM_DUR=g] [,DATA_RATE=h];
- Input CHG-PC-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0 ,PC=0 ,PC_GAIN=255;
- Output

×IN.	TERM									_ 🗆 ×
	Command Window									
MSC COMI	(0x00) M2232 LOC NUM PC_ FRA DAT ENC PLETED	2001- CHAN ATION LAC GAIN ME_DU A_RAT	DG-14 GE PAC : BSC RATE E _RATE	22:24 iING CH :_0/BTS : 1 : 255 : 2 : 0 : 1	:04 IANNEL 5_0/ALP [0xff	PARAME HA/CDM	TER ACH_0/I	PC_O [EQVIP]	
<u>1</u> 0	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS		
[jh ACC [jh	park <l EPTED park <l< td=""><td>GICBS</td><td>H>] C⊦ H>][</td><td>IG-PC-P</td><td>PARA : BS</td><td>C=0,BT</td><td>S=0, SE(</td><td>CT=ALPHA,CD</td><td>MACH=0,PC</td><td>=0,PC_G</td></l<></l 	GICBS	H>] C⊦ H>][IG-PC-P	PARA : BS	C=0,BT	S=0, SE(CT=ALPHA,CD	MACH=0,PC	=0,PC_G

Fig. 4.3-94 Paging Channel Parameter Information Display



4.3.4.25. PILOT CHANNEL Parameter Information Change

To change Pilot Channel Parameter information, click CDM-> Change_ Parameter_Information->CHG-PICH-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-PICH-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,PLOT_GAIN=e] [,PLOT_TD_GAIN=f];
- Input CHG-PICH-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA , CDMACH=0,PLOT_GAIN=255;
- Output

XINT	TERM	П×							
	Command Window								
COMF	(0x00) 2001-06-14 22:25:18 M2233 CHANGE PILOT CHANNEL PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 PILOT_GAIN : 255 PILOT_TD_GAIN : 0 PLETED								
<u>1</u> 0	LONG COM STM TEST NO.7 STAT ALET PREVIOUS								
[jh; ACCI [jh;	park <lgicbsm>] CHG-PICH-PARA:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,PLOT_G EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>								

Fig. 4.3-95 Pilot Channel Parameter Information Change Display

4.3.4.26. SYNC CHANNEL Parameter Information Change

To change Sync. Channel Parameter information, click CDM-> Change_ Parameter_Information->CHG-SC-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-SC-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d [,SC_GAIN=e];
- Input CHG-SC-PARA :BSC=0,BTS=b,SECT=ALPHA,CDMACH=0,SC_GAIN=255;
- Output

X INTERM	
Command Windov	V
MSC(0x00) 2001-06-14 22:26:36 M2236 CHANGE SYNC CHANNEL PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0 SYNC_GAIN : 255 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PRI</u>	WIOUS
[jhpark <lgicbsm>] CHG-SC-PARA:BSC=0,BTS=0,SECT=A ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	PHA, CDMACH=0, SC_GAIN=2

Fig. 4.3-96 Sync Channel Parameter Information Change Display

4.3.4.27. QUICH PAGING CHANNEL Parameter Information Change

To change Quick Paging Channel Parameter information, click CDM-> Change_ Parameter_Information->CHG-QPC-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-QPC-PARA :BSC=a ,BTS=b ,SECT=c ,CDMACH=d ,QPCH_ID=e [,FRAME_DUR=f] [,DATA_RATE=g];
- Input CHG-QPC-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA ,CDMACH=0 ,QPCH_ID=0 ,FRAME_DUR=255;
- Output

×IN1	TERM	_ 🗆 ×
	Command Window	
COMF	(0x00) 2001-06-14 22:27:34 M2238 CHANGE QUICK PAGING CHANNEL PARAMETER LOCATION : BSC_0/BTS_0/ALPHA/CDMACH_0/QPC_0 [EQUIP] FRAME_DURATION : 255 DATA_RATE : 0 ENCODING_RATE : 0 /LETED	4 Бц – Ц
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jh ACCI [jh	park <lgicbsm>] CHG-QPC-PARA:BSC=0,BTS=0,SECT=ALPHA,CDMACH=0,QPCH_ PTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-97 Quick Paging Channel Parameter Information Change Display



4.3.4.28. HOPPING PILOT BEACON CHANNEL Parameter Information Change

To change Hopping Pilot Beacon Channel Parameter information, click CDM-> Change_ Parameter_Information->CHG-BCON-PARA on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command CHG-BCON-PARA :BSC=a ,BTS=b ,SECT=c [,PILOT_GAIN=d] [,NUM_CDMA_CH=e] [,CDMA_FREQ1=f] [,CDMA_FREQ2=g] [,CDMA_FREQ3=h] [,CDMA_FREQ4=i] [,CDMA_FREQ5=j] [,CDMA_FREQ6=k] [,CDMA_FREQ7=1] [,CDMA_FREQ8=m] [,CDMA_FREQ9=n] [,CDMA_FREQ10=o] [,CDMA_FREQ11=p] [,CDMA_FREQ12=q];
- Input CHG-BCON-PARA :BSC=0,BTS=0,SECT=ALPHA,PILOT_GAIN=255;
- Output

XIN	TERM									_ 🗆 ×
	Command Window									
MSC	(0x00) M2239 LOC PIL NUM PLETED	2001- CHAN ATION .0T_GA _0F_C	DG-14 GE HOF : BSC IN DMACH	22:28 PPING P C_0/BTS : 255 : 0	::38 ILOT B _O/ALP	EACON 'HA	CHANNEI	L PARAMETER		N N N N N N N N N N N N N N N N N N N
то	LDMC	СЪМ	сты	тгет	NO 7	стат	ALET	DECTALLE		
<u></u>	LDNG	CDM	2111	1621	<u>N</u> 0.7	31 <u>4</u> 1		PREVIOUS		
[jhp ACCI [jhp	park <l EPTED park <l< td=""><td>GICBS</td><td>H>] C⊦ H>][</td><td>IG-BCON</td><td>-PARA :</td><td>BSC=0,</td><td>BTS=0,9</td><td>SECT=ALPHA,PJ</td><td>[LOT_GAIN=</td><td>255;</td></l<></l 	GICBS	H>] C⊦ H>][IG-BCON	-PARA :	BSC=0,	BTS=0,9	SECT=ALPHA,PJ	[LOT_GAIN=	255;

Fig. 4.3-98 Hopping Pilot Beacon Channel Parameter Information Change Display

4.3.4.29. CDMA Channel FA Test Start

To start the CDMA Channel FA test, click CDM-> Change_ Parameter_Information->STRT-FA-TEST on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command STRT-FA-TEST :BSC=a ,BTS=b ,FA=c;
- Input STRT-FA-TEST :BSC=0 ,BTS=0 ,FA=0;
- Output

XINTERM		÷ ×
	Command Win	dow
MSC(0×00) 2001-06 M2280 START F LOCATION : RESULT : COMPLETED	-14 22:29:29 A TEST BSC_0/BTS_0/CDMACH_0 OK	N
<u>IO L</u> DNG <u>C</u> DM <u>S</u>	TM <u>t</u> est <u>n</u> o.7 st <u>a</u> t al <u>f</u> t	PREVIOUS
[jhpark <lgicbsm> ACCEPTED [jhpark <lgicbsm></lgicbsm></lgicbsm>] STRT-FA-TEST:BSC=0,BTS=0,F]]	'A=0;

Fig. 4.3-99 CDMA Channel FA Test Start Display

4.3.4.30. Termination of CDMA Channel FA Test

To stop the CDMA Channel FA test, click CDM-> Change_ Parameter_Information->STOP-FA-TEST on the Command Window in order. If the next input window is displayed, then input the value to be changed.

- Command STOP-FA-TEST :BSC=a ,BTS=b ,FA=c;
- Input STOP-FA-TEST :BSC=0,BTS=0 ,FA=0;

• Output



Fig. 4.3-100 CDMA Channel FA Test Termination Display

4.3.5. Parameter Information Change Command

(Change_Parameter_Information_2)

This section describes commands that are used to change the parameter information that is inquired (required). The command to change parameter information cannot be easily input by the keyboard since input parameter counts are too many. For this reason, this section will skip the command input in text and demonstrate window input by mouse. Upon inputting the command, the part in blue is the parameter that the user can change and the user can change part or entire fields among them. For more information on parameters for each command, refer to the test procedures.

CRN	MMC	Description
2240	CHG-DORM-DATA	Dormant Timer Change
2241	CHG-PKZN-DATA	PACKET ZONE DATA Change
2243	CHG-FAC-TIMER	FACILITIES MANAGEMENT TIMER Change
2244	CHG-HO-TIMER	HANDOFF TIMER Change
2245	CHG-SUP-TIMER	BSC SUPPLEMENT SERVICES TIMER Change
2246	CHG-CALL-TIMER	BSC CALL PROCESSING TIMER Change
2247	CHG-MOB-TIMER	BSC MOBILITY MANAGEMENT TIMER Change
2248	CHG-A89-TIMER	A8 A9 INTERFACE TIMER Change
2249	CHG-A37-TIMER	A3, A7 INTERFACE TIMER Change
2250	CHC_DS1_EWDD	Forward Link Power Management Information (RS1)
2230	CHG-KSI-FWDF	change
2251	CHC-RS1-REVP	Backward (or Reverse) Link Power Management
2201	CHG KSI KEVI	Information (RS1) change
2255	CHC-RS2-FWDP	Forward Link Power Management Information (RS2)
2200	CHG K52 FWDI	change
2256	CHC-RS2-REVP	Backward (or Reverse) Link Power Management
2200	CHG K52 KEVI	Information (RS2) change
2257	CHG-FER-PARA	Service Option FER Change
2261	CHG-MAHO-DATA	MAHHO DATA change
2262	CHG-LOC-PARA	LOCATION PARA information change
2263	CHG-SCH-PARA	SCH parameter information change
2265	CHG-PWR1-CTRL	POWER CONTROL parameter information (1) change

Table 4.3-2 Parameter Information Change Command (2) List

2266	CHG-PWR2-CTRL	POWER CONTROL parameter information (2) change
2267	CHG-PWR3-CTRL	POWER CONTROL parameter information (3) change
2271	CHG-BTS-NAME	BTS name change
2292	CHG-PCF-TIMER	PCF TIMER change
2294	CHG-PCP-ADDR	PCP/PMP ADDRESS change
2295	CHG-PIP-ADDR	PIP ADDRESS change
2296	CHG-PCF-PARA	PCF PARAMETER change

4.3.5.1. Dormant Timer Change

To change Dormant Timer, click CDM->Change_Parameter_ Information_2-> CHG-DORM-DATA on the Command Window in order. Input the value to be changed in each field.

- Command CHG-DORM-DATA :BSC=a [,DORMANT_T=b] [,INACTIVE_T=c] [,BAND_FRAME_T=d] [,BAND_CLASS=e] ;
- Input CHG-DORM-DATA :BSC=0,DORMANT_T=255;
- Command

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 14:39:02 M2240 CHANGE DORMANT TIMER DATA LOCATION : BSC_0 INACTIVE TIMER : 30 DORMANT TIMER : 255 BAND FRAME TIME : 0 BAND_CLASS : 800MHz COMPLETED	
A	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] CHG-DORM-DATA:BSC=0,DORMANT_T=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-101 Dormant Timer Change Display

4.3.5.2. PACKET ZONE DATA Change

To change PACKET ZONE DATA, click CDM->Change_Parameter_ Information_2-> CHG-PKZN-DATA on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-PKZN-DATA :BSC=a [,PKT_ZONE=b] [,PCP_ID=c];
- Input CHG-PKZN-DATA :BSC=0,PKT_ZONE=255;
- Output

×IN1	TERM	_ 🗆 ×
	Command Window 🛛 🗟	
MSC	(0x00) 2001-06-17 14:40:27 M2241 CHANGE BSC PACKET ZONE DATA LOCATION : BSC_0 TRUNK_TYPE : E1 PCP_ID : 0 PKT_ZONE : 255 PLETED	
<u>1</u> 0	<u>LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhp ACCI [jhp	park <lgicbsm>] CHG-PKZN-DATA:BSC=0,PKT_ZONE=255; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-102 Packet Zone Data Change Display

4.3.5.3. FACILITIES MANAGEMENT TIMER Change

To change FACILITIES MANAGEMENT TIMER, click CDM->Change_Parameter_ Information_2-> CHG-FAC-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-FAC-TIMER :BSC=a [,T1=b] [,T2=c] [,T4=d] [,T5=e] [,T6=f] [,T12=g] [,T13=h] [,T16=i] [,T309=j];
- Input CHG-FAC-TIMER :BSC=0,T1=255;
- Output

XINTERM	_	
Command	Window	
MSC(0x00) 2001-06-17 14:41:35 M2243 CHANGE FACILITIES MANAGEMENT LOCATION : BSC_0 FACILITIES MANAGEMENT TIMER T1 : 255 [sec] T2 T5 : 60 [sec] T6 T13 : 55 [sec] T16 COMPLETED	TIMER : 60 [sec] T4 : 60 : 75 [sec] T12 : 60 : 60 [sec] T309 : 5	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T	AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] CHG-FAC-TIMER:BSC=0, ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	T1=255;	

Fig. 4.3-103 Facilities Management Timer Change Display



4.3.5.4. HANDOFF TIMER Change

To change HANDOFF TIMER, click CDM->Change_Parameter_ Information_2-> CHG-HO-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-HO-TIMER :BSC=a [,T7=b] [,T9=c] [,T10=d] [,T50=e] [,T52=f] [,T777=g] [,T778=h] [,T787=i] [,T789=j] [,T790=k];
- Input CHG-HO-TIMER :BSC=0,T7=255;
- Output

XINTERM			_ 🗆 ×
	Command Window	ß	
M2244 CHAN LOCATION HANDOFF T7 T50 T778 T790 COMPLETED	GE HANDOFF TIMER : BSC_O TIMER : 255 [sec] T9 : 10 [sec] : 60 [sec] T52 : 90 [sec] : 15 [sec] T787 : 90 [sec] : 10 [sec]	T10 : T777 : T789 :	5 [90 [10 [
<u>io l</u> dng <u>c</u> dm	<u>STM TEST NO.7 STAT ALFT PREVIOUS</u>		
[jhpark <lgicbs ACCEPTED [jhpark <lgicbs< td=""><td>M>] CHG-HO-TIMER:BSC=0,T7=255; M>]]</td><td></td><td> \</td></lgicbs<></lgicbs 	M>] CHG-HO-TIMER:BSC=0,T7=255; M>]]		\

FIG 4.3-104 Handoff Timer Change Display



4.3.5.5. BSC SUPPLEMENT SERVICES TIMER Change

To change BSC SUPPLEMENT SERVICES TIMER, click CDM->Change_Parameter_ Information_2-> CHG-SUP-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-SUP-TIMER :BSC=a [,T60=b] [,T61=c] [,T62=d] [,T63=e] ;
- Input CHG-SUP-TIMER :BSC=0,T60=99;
- Output

Command Window	
MSC(0x00) 2001-06-17 14:43:23 M2245 CHANGE SUPPLEMENT SERVICES TIMER LOCATION : BSC_0 SUPPLEMENT SERVICES TIMER T60 : 99 [sec] T61 : 5 [sec] T62 : 5 [sec] T63 : 5 [sec] COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] CHG-SUP-TIMER:BSC=0,T60=99; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-105 BSC Supplement Services Timer Change Display

4.3.5.6. BSC CALL PROCESSING TIMER Change

To change BSC CALL PROCESSING TIMER, click CDM->Change_Parameter_ Information_2-> CHG-CALL-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-CALL-TIMER :BSC=a [,T20=b] [,T30=c] [,T40=d] [,T300=e] [,T301=f] [,T302=g] [,T303=h] [,T306=i] [,T307=j] [,T308=k] [,T311=l] [,T312=m] [,T313=n] [,T315=o] [,T316=p] [,T325=q] [,T326=r] [,T3113=s] [,T3230=t] [,T3280=u] [,Tpaca1=v] [,Tpaca2=w];
- Input CHG-CALL-TIMER :BSC=0,T20=99;
- Output

	_ 🗆 ×
Command Window 🗟	
MSC(0x00) 2001-06-17 14:44:17 M2246 CHANGE CALL PROCESSING TIMER LOCATION : BSC_0 CALL PROCESSING TIMER T20 : 99 [sec] T30 : 5 [sec] T40 : T300 : 1500 [msec] T301 : 30 [sec] T302 : T303 : 6 [sec] T306 : 5 [sec] T307 : T308 : 5 [sec] T311 : 1000 [msec] T312 : T313 : 2 [sec] T315 : 5 [sec] T316 : T325 : 5 [sec] T326 : 5 [sec] T3113 : T3230 : 5 [sec] T3280 : 15 [sec] Tpaca1 : Tpaca2 : 5 [sec] COMPLETED	5 [5 [5 [5 [5 [5 [
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CALL-TIMER:BSC=0,T20=99; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	
A	

Fig. 4.3-106 BSC Call Processing Timer Change Display

4.3.5.7. BSC MOBILITY MANAGEMENT TIMER Change

To change BSC MOBILITY MANAGEMENT TIMER, click CDM->Change_Parameter_ Information_2-> **CHG-MOB-TIMER** on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-MOB-TIMER :BSC=a [,T3210=b] [,T3220=c] [,T3240=d]
 [,T3260=e]
 [,T3270=f] [,T3271=g] [,T3272=h];
- Input CHG-MOB-TIMER :BSC=0,T3210=99;
- Output

×IN.	TERM								_	X
			С	omn	and	Wir	ndow			
MSC(0x00) 2001-06-17 14:45:28 M2247 CHANGE MOBILITY MANAGEMENT TIMER LOCATION : BSC_0 MOBILITY MANAGEMENT TIMER T3210 : 99 [sec] T3220 : 10 [sec] T3240 : 5 [T3260 : 30 [sec] T3270 : 5 [sec] T3271 : 15 [T3272 : 5 [sec] COMPLETED										
<u>1</u> 0	LDNG CD	OM <u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u>	T PREVIOUS			
[jh ACC [jh	park <lgic EPTED park <lgic< td=""><td>:BSM>] (:BSM>]]</td><td>HG-MOB-</td><td>TIMER</td><td>:BSC=0,</td><td>T3210</td><td>=99;</td><td></td><td></td><td></td></lgic<></lgic 	:BSM>] (:BSM>]]	HG-MOB-	TIMER	:BSC=0,	T3210	=99;			

Fig. 4.3-107 BSC Mobility Management Timer Change Display



4.3.5.8. A8 A9 INTERFACE TIMER Change

To change A8 A9 INTERFACE TIMER, click CDM->Change_Parameter_ Information_2_2-> CHG-A89-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-A89-TIMER :BSC=a [,TA8_SETUP=b] [,Talc9=c] [,Tald9=d] [,Trel9=e];
- Input CHG-A89-TIMER :BSC=0,TA8_SETUP=99;
- Output

×IN1	TERM	_ 🗆 ×
	Command Window	
MSC (COMF	(0x00) 2001-06-17 14:46:39 M2248 CHANGE A8 A9 INTERFACE TIMER LOCATION : BSC_0 A8, A9 INTERFACE TIMER TA8_setup : 99 [sec] Talc9 : 500 [msec] Tald9 : 500 [msec] Trel9 : 1000 [msec] PLETED	
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jhp ACCE [jhp	park <lgicbsm>] CHG-A89-TIMER:BSC=0,TA8_SETUP=99; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-108 A8 A9 INTERFACE TIMER Change

4.3.5.9. A3, A7 INTERFACE TIMER Change

To change A3 A7 INTERFACE TIMER, click CDM->Change_Parameter_ Information_2_2-> CHG-A37-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-A37-TIMER :BSC=a [,Tacm=b] [,Tbstact=c] [,Tbsccom=d] [,Tchanstat=e] [,Tconn3=f] [,Tdiscon3=g] [,Tdrptgt=h] [,Ttgtrmv=i] [,Thoreq=j] [,Tpcm=k] [,Tphysical=l];
- Input CHG-A37-TIMER :BSC=0,Tacm=1000;
- Output

	. 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 14:47:28 M2249 CHANGE A3 A7 INTERFACE TIMER L0CATION : BSC_0 A3, A7 INTERFACE TIMER Tacm : 1000 [msec] Tbstact : 600 [msec] Tbstcom : 100 [msec] Tchanstat : 500 [msec] Tconn3 : 500 [msec] Tdiscon3 : 500 [msec] Tdrptgt : 5 [sec] Ttgtrmv : 5 [sec] Thoreq : 1000 [msec] Tpcm : 1000 [msec] Tphysical : 1 [sec] COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-A37-TIMER:BSC=0,Tacm=1000; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-109 A3, A7 INTERFACE TIMER Change



4.3.5.10. Forward Link Power Management Information (RS1) Change

To change forward link power management information (RS1), click CDM->Change_Parameter_ Information_2_2-> CHG-RS1-FWDP on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-RS1-FWDP :BSC=a ,BTS=b ,FER=c [,SLOW_TIME=d] [,FAST_TIME=e] [,STEP_FAST=f] [,SLOW_DLTA=g] [,FAST_DLTA=h] [,NOM_GAIN=i] [,MAX_TC_GAIN=j] [,MIN_TC_GAIN=k] [,FER_THRE=1] [,BGUP_DLTA=m] [,SMLL_DLTA=n] [,SIGL_DLTA=o] [,DLTA_GAN1=p] [,DLTA_GAN2=q] [,DLTA_GAN3=r];
- Input CHG-RS1-FWDP :BSC=0 ,BTS=0 ,FER=POINT_5,SLOW_TIME=20000;
- Output

	_ 🗆 ×
Command Window	
M2250 CHANGE RS1 FORWARD POWER DATA LOCATION : BSC_0/BTS_0 FER_ID : 0 [0.5 PERCENT] SLOW_DWN_TIME : 20000 FAST_DWN_TIME : 1600 STEP_TIL_FAST : 0 SLOW_DWN_DELTA : 1 FAST_DWN_DELTA : 1 NOMINAL_GAIN : 50 MAX_TX_GAIN : 80 MIN_TX_GAIN : 40 FER_THRES : 6 BIG_UP_DELTA : 10 SMALL_UP_DELTA : 5 SIGNAL_DELTA_GAIN: 96 PCSC_DELTA_GAIN1 : 64 PCSC_DELTA_GAIN3 : 112 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-RS1-FWDP:BSC=0,BTS=0,FER=P0INT_5,SLOW_TIME=200 ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	00; 7

Fig. 4.3-110 Forward Link Power Management Information (RS1) Change

4.3.5.11. Backward Link Power Management Information (RS1) Change To change Backward link power management information (RS1), click CDM->Change_Parameter_ Information_2_2-> CHG-RS1-REVP on the Command Window in order. Input the value to be changed in each field as shown below.

```
• Command CHG-RS1-REVP :BSC=a ,BTS=b ,FER=c [,PNOM=d] [,PMAX=e]
[,PMIN=f] [,PUPF=g] [,PFRR=h] [,PUPE=i] [,PUPEL=j]
[,PD=k] [,PVD=1] [,PFW=m] [,PERL=n];
```

- Input CHG-RS1-REVP :BSC=0 ,BTS=0 ,FER=POINT_5,PNOM= 255;
- Output

Command Window MSC(0x00) 2001-06-17 14:57:19 M2251 CHANGE RS1 REVERSE POWER DATA LOCATION : BSC_0/BTS_0 FER_ID : 0 [0.5 PERCENT] PWRCTL_NOMINAL : 255 DUBCTL MAX : 234098	Ы
MSC(0x00) 2001-06-17 14:57:19 M2251 CHANGE RS1 REVERSE POWER DATA LOCATION : BSC_0/BTS_0 FER_ID : 0 [0.5 PERCENT] PWRCTL_NOMINAL : 255 DWRCTL_MAX : 22409	
PWRCTL_MIN : 25400 PWRCTL_UP_FULL : 3072 PWRCTL_FULL_RUN_RESET : -2 PWRCTL_UP_ERASURE : 248 PWRCTL_UP_ERASURE_LITTLE: 50 PWRCTL_DOWN : 48 PWRCTL_VAR_DOWN : 4 PWRCTL_FULL_WAIT : 2 PWRCTL_FULL_WAIT : 5 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-RS1-REVP:BSC=0,BTS=00,FER=P0INT_5,PN0M=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	AI N

Fig. 4.3-111 Backward Link Power Management Information (RS1) Change

4.3.5.12. Forward Link Power Management Information (RS2) Change To change Forward link power management information (RS2), click CDM->Change_Parameter_ Information_2_2-> CHG-RS2-FWDP on the Command Window in order. Input the value to be changed in each field as shown below.

• Command CHG-RS2-FWDP :BSC=a ,BTS=b ,FER=c [,IN_DWNT=d] [,MAX_DWNT=e]

[,MIN_DWNT=f] [,TDWN_DLT=g] [,TUP_DLT=h] [,STT_THS=i] [,ERA_MSR=j] [,CONT_ERA=k] [,CUMU_ERA=1] [,NOM_GAIN=m] [,MAX_TX_GAIN=n] [,MIN_TX_GAIN=o] [,GAIN_DWN=p] [,BIG_UP=q] [,SMALL_UP=r] [,SIGL_DLT=s] [,DLT_GAN1=t] [,DLT_GAN2=u] [,DLT_GAN3=v];

• Input CHG-RS2-FWDP :BSC=0 ,BTS=0 ,FER=POINT_5,IN_DWNT=255;

• Output



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Command Window 🔍	
MSC(0x00) 2001-06-17 14:58:18 M2255 CHANGE RS2 FORWARD POWER DATA LOCATION : BSC_0/BTS_0 FER_ID : 0 [0.5 PERCENT] INIT_DOWN_TIME : 255 MAX_DOWN_TIME : 4000 MIN_DOWN_TIME : 200 TIME_DOWN_DELTA : 200 TIME_UP_DELTA : 400 STATE_CHG_THRESH : 250 ERASE_MEAS_FRAMES : 250 CONT_ERASE_EFTV : 2 CUMUL_ERASE_EFTV : 2 CUMUL_ERASE_EFTV : 4 NOM_GAIN : 50 MAX_TX_GAIN : 100 MIN_TX_GAIN : 40 GAIN_DOWN_DELTA : 1 BIG_UP_DELTA : 10	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] CHG-RS2-FWDP:BSC=0,BTS=0,FER=P0INT_5,IN_DWNT=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	4
4	

Fig. 4.3-112 Forward Link Power Management Information (RS2) Change

4.3.5.13. Backward Link Power Management Information (RS2) Change To change Backward link power management information (RS2), click CDM->Change_Parameter_ Information_2_2-> CHG-RS2-REVP on the Command Window in order. Input the value to be changed in each field as shown below.

Command CHG-RS2-REVP :BSC=a ,BTS=b ,FER=c [,PNOM=d] [,PMAX=e]
 [,PMIN=f] [,RBUD=g] [,RSUD=h] [,RNEW=i] [,RMAXDD=j]
 [,RMINDD=k] [,RDDIS=1] [,RCONTEE=m] [,RCUMULEE=n] [,REMF=o]
 [,RSCT=p];

- Input CHG-RS2-REVP :BSC=0 ,BTS=0 ,FER=POINT_5,PNOM=255;
- Output

	_ 🗆 ×
Command Window	
M2256 CHANGE RS2 REVERSE POWER DATA LOCATION : BSC_O/BTS_O FER_ID : 0 [0.5 PERCENT] PWRCTL_NOMINAL : 255 PWRCTL_MAX : 25856 PWRCTL_MIN : 23296 RPC_BIG_UP_DELTA : 1024 RPC_SMALL_UP_DELTA : 512 RPC_NON_ERASE_WAIT : 2 RPC_MAX_DOWN_DELTA : 32 RPC_MIN_DOWN_DELTA : 1 RPC_DOWN_DELTA : 1 RPC_CONT_ERASE_EFTV : 2 RPC_CUMUL_ERASE_EFTV : 2 RPC_CUMUL_ERASE_EFTV : 4 RPC_STATE_CHG_THRESH : 100 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-RS2-REVP:BSC=0,BTS=0,FER=P0INT_5,PN0M=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-113 Backward Link Power Management Information (RS2) Change

4.3.5.14. Service Option FER Change

To change Service Option FER, click CDM->Change_Parameter_ Information_2_2-> CHG-FER-PARA on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-FER-DATA :BSC=a [,VOICE_8K=b] [,LOOPBK_8K=c] [,EVRC=d] [,ASYNC=e] [,G3FAX=f] [,SMS=g] [,PPP_PKT=h] [,CDPP_PKT=i] [,LOOPBK_13K=j] [,STU_TR=k] [,STU_NTR=l] [,ASYNC_13K=m] [,G3FAX_13K=n] [,SMS_13K=o] [,VOICE_13K=p] [,IS96_VOICE=q] [,MARKOV_8K=r] [,DATA=s] [,IS96A_1BY8=t] [,MARKOV_13K=u] [,WLL_OFFHOOK=v] [,RS1_MARKOV=w] [,RS2_MARKOV=x] [,FCH=y] [,SCH=z] [,DCCH=] [,SCH_LB=] [,SCH_LB2=];
- Input CHG-FER-DATA :BSC=0,VOICE_8K=P_5;

• Output



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×INTERM <u></u> [_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:00:09 M2257 CHANGE TARGERT FER DATA LOCATION : BSC_0 8K VOICE : 0.5[%] 8K LOOPBACK : 1 [%] FVRC : 1 [%] G3FAX : 1 [%] G3FAX : 1 [%] PPP PKT : 1 [%] 13K LOOPBACK : 1 [%] STU III TR : 1 [%] STU III TR : 1 [%] G3FAX 13K : 1 [%] G3FAX 13K : 1 [%] MS 13K : 1 [%] STU III NTR : 1 [%] MS 13K : 1 [%]	
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-FER-DATA:BSC=0,VOICE_8K=P_5; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-114 Service Option FER Change

4.3.5.15. MAHHO DATA Change

To change MAHHO DATA, click the CDM->Change_Parameter_ Information_2-> CHG-MAHO-DATA on the Command Window in order. Input the value to be changed in each file as shown below.

- Command CHG-MAHO-DATA :BSC=a ,BTS=b ,SECT=c [,BORDER_FLAG=d] [,BD_CLS=e]
 [,NUM_CHAN=f] [,CDMA_FREQ0=g] [,CDMA_FREQ1=h] [,CDMA_FREQ2=i]
 [,CDMA_FREQ3=j] [,CDMA_FREQ4=k] [,CDMA_FREQ5=1]
 [,CDMA_FREQ6=m] [,CDMA_FREQ7=n] [,CDMA_FREQ8=o]
 [,CDMA_FREQ9=p] [,CDMA_FREQ10=q] [,CDMA_FREQ11=r] [,STET=s]
 [,STEIT=t] [,DRPT=u] [,MIN_TOT=v] [,CF_T_ADD=w]
 [,TF_WAIT_TIME=x] [,SRCH_N=y] [,SRCH_R=z];
- Input CHG-MAHO-DATA :BSC=0 ,BTS=0 ,SECT=ALPHA,BORDER_FLAG=ON;
- Output

XINTERM	
Command Window	4
MSC(0x00) 2001-06-17 15:01:09 M2261 CHANGE MAHHO DATA LOCATION : BSC_0/BTS_0/ALPHA BOADER_FLAG : 0N NUM_0F_CDMACH : 0 SF_TOTAL_EC_THR : 31 SF_TOTAL_EC_IO_THR : 26 DIFF_RX_PWR_THR : 0 MIN_TOTAL_PILOT_EC_IO : 0 CF_T_ADD : 26 TF_WAIT_TIME : 30 CF_SRCH_WIN_N : 9 CF_SRCH_WIN_R : 10 COMPLETED	
IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jhpark <lgicbsm>] CHG-MAHO-DATA:BSC=0,BTS=0,SECT=ALPHA,BORDE ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	R_FLAG=ON;

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Fig. 4.3–115 MAHHO DATA Change
4.3.5.16. LOCATION PARA Information Change

To change LOCATION PARA information, click CDM->Change_Parameter_ Information_2-> CHG-LOC-PARA on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-LOC-PARA :BSC=a ,BTS=b ,SECT=c [,ACT_T_FRM=d]
 [,PUF_ST_SZ=e][,PUF_P_SZ=f] [,PUF_INTERVAL=g] [,PUF_I_PWR=h]
 [,PUF_P_STEP=i] [,TOT_PUF_P=j] [,MAX_PWR_PUF=k];
- Input CHG-LOC-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA,ACT_T_FRM=ON;
- Output

XINTERM	
Command	Window
MSC(0x00) 2001-06-17 15:02:20 M2262 CHANGE LOCATION DATA LOCATION : BSC_0/BTS_0/ALPHA ACTION_TIME_FRAME : 255 PUF_SETUP_SIZE : 0 PUF_PULSE_SIZE : 0 PUF_INTERVAL : 0 PUF_INTT_PWR : 0 PUF_INTT_PWR : 0 PUF_PWR_STEP : 0 TOTAL_PUF_PROBES : 0 MAX_PWR_PUF : 0 COMPLETED	
<u>I</u> O <u>L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T	AL <u>F</u> T PREVIOUS
[jhpark <lgicbsm>] CHG-LOC-PARA:BSC=0,B ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	TS=0, SECT=ALPHA, ACT_T_FRM=255;

Fig. 4.3-116 LOCATION PARA Information Change

4.3.5.17. SCH Parameter Information Change

To change SCH PARA information, click CDM->Change_Parameter_ Information_2-> CHG-SCH-PARA on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-SCH-PARA :BSC=a ,BTS=b ,SECT=c [,SCH_T_ADD=d] [,SCH_T_DROP=e] [,T_MULCHAN=f] [,BEGIN_PRMBL=g] [,RES_PRMBL=h] [,PS_MIN_DELTA=i] [,ORD_INTERVAL=j] [,NUM_PILOTS=k] [,PRD_INTERVAL=1] [,FLOOR_HIGH=m] [,FLOOR_LOW=n] [,PS_CEIL_HIGH=o] [,PS_CEIL_LOW=p] [,THSH_INTERVAL=q] [,T_SLOTTED=r];
- Input CHG-SCH-PARA :BSC=0 ,BTS=0 ,SECT=ALPHA,SCH_T_ADD=ON;
- Output

XINTERM	
Commo	and Window
M2263 CHANGE SCH PARAMETER LOCATION : BSC_0/BTS_0/ALPH SCH_T_ADD : 255 SCH_T_DROP : 26 T_MULCHAN : 0 BEGIN_PREAMBLE : 0 RESUME_PREAMBLE : 0 PS_MIN_DELTA : 0 ORDER_INTERVAL : 0 PERIODIC_INTERVAL : 0 PS_FLOOR_HIGH : 0 PS_FLOOR_LOW : 0	A
<u>IO LDNG CDM STM TEST NO.7</u>	STAT ALET PREVIOUS
[jhpark <lgicbsm>] CHG-SCH-PARA:BS ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	C=0,BTS=0,SECT=ALPHA,SCH_T_ADD=255;

Fig. 4.3-117 SCH Parameter Information Change

4.3.5.18. POWER CONTROL Parameter Information (1) Change

To change POWER CONTROL Parameter information (1), click CDM->Change_Parameter_ Information_2-> CHG-PWR1-CTRL on the Command Window in order. Input the value to be changed in each field as shown below.

Command CHG-PWR1-CTRL:BSC=a,BTS=b,FER=c[,PWR_CNT_STEP=d]
[,FPC_MODE=e] [,FPC_FC_INIT=f] [,FPC_FC_MIN=g]
[,FPC_FCH_MAX=h] [,FPC_DCC_INIT=i] [,FPC_DCC_MIN=j]
[,FPC_DCC_MAX=k] [,FPC_SC_INIT=1] [,FPC_SC_MIN=m]
[,FPC_SC_MAX=n]
[,FPC_THRESH=o] [,FCH_THSH_SC=p] [,FCH_ADJ_GAIN=q]
[,DCC_ADJ_GAIN=r] [,SC0_ADJ_GAIN=s] [,SC1_ADJ_GAIN=t]
[,FPC_SUBCH=u] [,RL_GAIN_ADJ=v] [,RL_TC_PICH=w]
[,RL_SC_PILOT=x];

- Input CHG-PWR1-CTRL :BSC=0 ,BTS=0 ,FER=30,PWR_CNT_STEP=255;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:04:31 M2265 CHANGE POWER CONTROL PARAMETER(1) LOCATION : BSC_0/BTS_0 FER_ID : 30 [%] PWR_CNTL_STEP : 255 FPC_MODE : 0 FPC_FCH_INIT_SETPT : 64 FPC_FCH_MIN_SETPT : 16 FPC_FCH_MIN_SETPT : 96 FPC_DCCH_INIT_SETPT : 0 FPC_DCCH_MIN_SETPT : 0 FPC_DCCH_MAX_SETPT : 0	
<u>م</u>	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-PWR1-CTRL:BSC=0,BTS=0,FER=30,PWR_CNT_STEP=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	
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Fig. 4.3-118 POWER CONTROL Parameter Information (1) Change

4.3.5.19. POWER CONTROL Parameter Information (2) Change

To change POWER CONTROL Parameter information (2), click CDM->Change_Parameter_ Information_2-> CHG-PWR2-CTRL on the Command Window in order. Input the value to be changed in each field as shown below.

• Command CHG-PWR2-CTRL :BSC=a ,BTS=b ,FER=c ,USE_REV_P=d [,GAIN_1500=e]

[,GAIN_2700=f] [,GAIN_4800=g] [,GAIN_9600=h] [,GAIN_1800=i] [,GAIN_3600=j] [,GAIN_7200=k] [,GAIN_14400=1] [,NORM_9600_5MS=m];

- Input CHG-PWR2-CTRL :BSC=0 ,BTS=0 ,FER=c ,USE_REV_P=USE_REV_P, GAIN_1500=255;
- Output

	_ 🗆 X
Command Window	
MSC(0x00) 2001-06-17 15:05:29 M2266 CHANGE POWER CONTROL PARAMETER(2) LOCATION : BSC_0/BTS_0 FER_ID : 0.2[%] RL_ATT_ADJ_GAIN_1500 NOT USE REV_PILOT : 0 USE REV_PILOT : 255 RL_ATT_ADJ_GAIN_2700 NOT USE REV_PILOT : 0 USE REV_PILOT : 0 RL_ATT_ADJ_GAIN_4800 NOT USE REV_PILOT : 0 USE REV_PILOT : 0 USE REV_PILOT : 0	
2	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-PWR2-CTRL:BSC=0,BTS=0,FER=0,USE_REV_P=USE_REV_ ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	.P,G

Fig. 4.3-119 POWER CONTROL Parameter Information (2) Change

4.3.5.20. POWER CONTROL Parameter Information (3) Change

To change POWER CONTROL Parameter information (3), click CDM->Change_Parameter_ Information_2-> CHG-PWR3-CTRL on the Command Window in order. Input the value to be changed in each field as shown below.

- Command
 CHG-PWR3

 CTRL :BSC=a ,BTS=b ,FER=c ,USE_REV_P=d ,USE_TUB_ENC=e
 [,GAIN_19200=f] [,GAIN_38400=g] [,GAIN_76800=h] [,GAIN_153600=i]

 [,GAIN_307200=j] [,GAIN_38400=g] [,GAIN_614400=k] [,GAIN_28800=l]
 [,GAIN_307200=j] [,GAIN_115200=n] [,GAIN_230400=o]

 [,GAIN_460800=p] [,GAIN_1036800=q];
 [,GAIN_460800=p] [,GAIN_1036800=q];
- Input CHG-PWR3-CTRL :BSC=0 ,BTS=0 ,FER=0 ,USE_REV_P=NOUSE_REV_P , USE_TUB_ENC=NOUSE_TUB_ENC,GAIN_19200=255;
- Output

	. 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:06:31 M2267 CHANGE POWER CONTROL PARAMETER(3) LOCATION : BSC_0/BTS_0 FER_ID : 0.2[%] RL_ATT_ADJ_GAIN_19200 NOT USE REV_PILOT & NOT USE TURBO ENCODE : 255 NOT USE REV_PILOT & USE TURBO ENCODE : 0 USE REV_PILOT & NOT USE TURBO ENCODE : 0 USE REV_PILOT & NOT USE TURBO ENCODE : 0 RL_ATT_ADJ_GAIN_38400 NOT USE REV_PILOT & NOT USE TURBO ENCODE : 0 NOT USE REV_PILOT & USE TURBO ENCODE : 0 NOT USE REV_PILOT & USE TURBO ENCODE : 0 USE REV_PILOT & USE TURBO ENCODE : 0 NOT USE REV_PILOT & USE TURBO ENCODE : 0 VSE REV_PILOT & NOT USE TURBO ENCODE : 0	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-PWR3-CTRL:BSC=0,BTS=0,FER=0,USE_REV_P=NOUSE_REV_ ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	.P \\ \\ \\ \\

Fig. 4.3-120 POWER CONTROL Parameter Information (3) Change

4.3.5.21. BTS Name Change

To change BTS name, click CDM->Change_Parameter_ Information_2-> CHG-BTS-NAME on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-BTS-NAME :BSC=a ,BTS=b ,NAME=c;
- Input CHG-BTS-NAME :BSC=0 ,BTS=0 ,NAME=jhpark;
- Output

🗙 IN TE		- 🗆 ×
	Command Window	
MSC(O Compl	Jx00) 2001-06-17 15:21:15 M2271 CHANGE BTS NAME BSC_0/BTS_0 NAME IS CHANGED TO jhpark. LETED	
<u>I</u> O	LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS	
[jhpa ACCEP [jhpa	ark <lgicbsm>] CHG-BTS-NAME:BSC=0,BTS=0,NAME=jhpark; PTED ark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-121 BTS Name Change

4.3.5.22. PCF TIMER Change

To change PCF TIMER, click CDM->Change_Parameter_ Information_2-> CHG-PCF-TIMER on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-PCF-TIMER :PCP=a [,TRP_LIFETIME=b] [,TBSREQ9=c] [,TDISCON9=d] [,TWAITHO9=e] [,TREGREQ=f] [,RRQ_RETRY_CNT=g];
- Input CHG-PCF-TIMER :PCP=0,TRP_LIFETIME=255;
- Output

	_ 🗆 X
Command Window	
MSC(0x00) 2001-06-17 15:22:25 M2292 CHANGE PCF TIMER PARAMETER LOCATION : PCP_0 TRP_LIFETIME : 255 [sec] TBSREQ9 : 5000 [msec] TDISCON9 : 5000 [msec] TWAITH09 : 10000 [msec] TREGREQ : 5000 [msec] REGREQ_RETRY_CNT : 2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-PCF-TIMER:PCP=0,TRP_LIFETIME=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-122 PCF TIMER Change

4.3.5.23. PCP/PMP ADDRESS Change

To change PCP/PMP ADDRESS, click CDM->Change_Parameter_ Information_2-> CHG-PCP-ADDR on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-PCP-ADDR :PCF=a ,SHELF_ID=b ,SIDE=c ,IP_ADDR=d;
- Input CHG-PCP-ADDR :PCF=0 ,SHELF_ID=0 ,SIDE=A_SIDE , IP_ADDR=255.255.255.255;
- Output

×interm 🖖	_ 🗆 X	
Command Window		
MSC(0x00) 2001-06-17 15:23:55 M2294 CHANGE PCP/PMP ADDRESS RESULT LOCATION : PCF_0 PCP[A_SIDE] IP : 255.255.255 PCP[B_SIDE] IP : 0.0.0.0 COMPLETED		
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS		
[jhpark <lgicbsm>] CHG-PCP-ADDR:PCF=0,SHELF_ID=0,SIDE=A_SIDE,I ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	P_ADDR=255	

Fig. 4.3-123 PCP/PMP ADDRESS Change

4.3.5.24. PIP ADDRESS Change

To change PIP ADDRESS, click CDM->Change_Parameter_ Information_2-> CHG-PIP-ADDR on the Command Window in order. Input the value to be changed in each field as shown below.

- Command CHG-PIP-ADDR :PCF=a ,SHELF_ID=b ,PIP_ID=c [,IP_ADDR=d] [,NETMASK=e]
- Input CHG-PIP-ADDR :PCF=0 ,SHELF_ID=0 ,PIP_ID=0, NETMASK=255.255.0.0;
- Output

Command Window 🤷 🗟
MSC(0x00) 2001-06-17 15:25:27 M2295 CHANGE PIP ADDRESS RESULT L0CATION : PCF_0 PCP[A_SIDE] IP : 255.255.255 PCP[B_SIDE] IP : 0.0.0.0 PIP[00] IP : 10.160.6.69 NETMASK : 255.255.0.0 GATEWAY : 0.0.0.0 PIP[01] IP : 0.0.0.0 NETMASK : 0.0.0.0
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jhpark <lgicbsm>] CHG-PIP-ADDR:PCF=0,SHELF_ID=0,PIP_ID=0,NETMASK=255.25 ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-124 PIP ADDRESS Change

4.3.5.25. PCF PARAMETER Change

To change PCF PARAMETER, click CDM->Change_Parameter_ Information_2-> CHG-PCF-PARA on the Command Window in order. Input the value to be changed in each field as shown below.

Command CHG-PCF-PARA :PCF=a [,AAA_TYPE=b] [,SID=c] [,NID=d]
 [,LTM_OFF=e]
 [,DAY_LT=f] [,PKZN_ID=g] [,ID_TYPE=h] [,GRE_SEQ=i] [,SEQ_TIMER=j]
 [,MSID_TYPE=k];

- Input CHG-PCF-PARA :PCF=0,AAA_TYPE=255,SEQ_TIMER=255;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:26:55 M2296 CHANGE PCF PARAMETER RESULT LOCATION : PCF_0 ID_TYPE : NONCE AAA TYPE : 255 SID : 22222 NID : 15 PACKET ZONE ID : 0 LTM_OFF : 18 (30MIN) DAY_LT : STANDARD GRE_SEQ_FLAG : NOT_USE SEQ_TIMER : 255 (msec) UDR MSID TYPE : MIN COMPLETED	
، ۲	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-PCF-PARA:PCF=0,AAA_TYPE=255,SEQ_TIMER=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-125 PCF PARAMETER Change

4.3.6. Network Parameter Information Change

(Change_Parameter_Info_3)

4.3.6.1. CAN INTER BSC AAL2 Setting Information Change

• Command CHG-CAN-IUR: BSC=a, [BSC0_AAL2=b], [BSC1_AAL2=c], [BSC2_AAL2=d], [BSC3_AAL2=e], [BSC4_AAL2=f], [BSC5_AAL2=g], [BSC6_AAL2=h], [BSC7_AAL2=i], [BSC8_AAL2=j],[BSC9_AAL2=k],[BSC10_AAL2=l], [BSC11_AAL2=m], [N0_AAL2_VC=n];

- Input CHG-CAN-IUR: BSC=0, BSC0_AAL2=255
- Output

Command Window	
MSC(0x00) 2001-06-17 15:28:16 M2522 CHANGE CAN INTER BSC AAL2 CONNECTION RESULT L0CATION : BSC_0 START VC ID [BSC_01] : 0x000b0040 START VC ID [BSC_02] : 0x000b0060 START VC ID [BSC_03] : 0x000b0080 START VC ID [BSC_04] : 0x000b0000 START VC ID [BSC_05] : 0x000b0000 START VC ID [BSC_05] : 0x000b0000 START VC ID [BSC_06] : 0x000b0000 START VC ID [BSC_06] : 0x000b0000 START VC ID [BSC_07] : 0x000b0100 START VC ID [BSC_08] : 0x000b0120 START VC ID [BSC_09] : 0x000b0140 START VC ID [BSC_10] : 0x000b0180 NUM OF VC : 32	
<u>ব</u>	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CAN-IUR:BSC=0,BSC0_AAL2=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

4.3.6.2. CAN INTER BSC AAL5 Setting Information Change

• Command CHG-CAN-BSC: [CAN0_START_AAL5=a],

[CAN1_START_AAL5=b], [NO_AAL5_VC=0~],

a ,b: 0~0xffffff

c: 0∼

- Input CHG-CAN-BSC: CAN0_START_AAL5=255
- Output

XINTERM	_ 🗆 ×
Command Window 🤷	
MSC(0x00) 2001-06-17 15:29:28 M2525 CHANGE CAN INTER BSC AAL2/5 CONNECTION RESULT START AAL5 VC ID [00] : 0x000000ff START AAL5 VC ID [01] : 0x000c0020 NUM OF AAL5 VC : 8160 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CAN-BSC:CANO_START_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	T T
N N	



4.3.6.3. CPN INTER DATA AAL5 Setting Information Change

```
Command CHG-CPN-DATA: [BSC0_AAL5=a], [BSC1_AAL5=b], [BSC2_AAL5=c],
[BSC3_AAL5=d], [BSC4_AAL5=e], [BSC5_AAL5=f],
[BSC6_AAL5=g], [BSC7_AAL5=h], [BSC8_AAL5=i],
[BSC9_AAL5=j], [BSC10_AAL5=k], [BSC11_AAL5=l],
[NO_AAL5_VC=m];
a ~n: BSC AAL5 (32~0xfffff)
m: 0~32
```

- Input CHG-CPN-DATA: BSC0_AAL5=255;
- Output

Command Window					
MSC(0x00) 2001-06-17 15:30:25 M2541 CHANGE CPN DATA AAL5 CONNECTION INFORMATION START VC ID [BSC_00] : 0x000000ff START VC ID [BSC_01] : 0x000a0020 START VC ID [BSC_02] : 0x000a0020 START VC ID [BSC_03] : 0x000a0020 START VC ID [BSC_03] : 0x000a0020 START VC ID [BSC_03] : 0x000a0020 START VC ID [BSC_04] : 0x000a0020 START VC ID [BSC_05] : 0x000a0020 START VC ID [BSC_06] : 0x000a0020					
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>					
[jhpark <lgicbsm>] CHG-CPN-DATA:BSC0_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>					



4.3.6.4. CPN INTER PCF AAL5 Setting Information Change

```
• Command CHG-CPN-PCF: [PIP0_0_AAL5=a], [PIP0_1_AAL5=b], [PIP1_0_AAL5=c],

[PIP1_1_AAL5=d], [PIP2_0_AAL5=e], [PIP2_1_AAL5=f],

[PIP3_0_AAL5=g], [PIP3_1_AAL5=h], [PIP4_0_AAL5=i],

[PIP4_1_AAL5=j], [PIP5_0_AAL5=k], [PIP5_1_AAL5=1],

[PIP6_0_AAL5=m], [PIP6_1_AAL5=n], [PIP7_0_AAL5=o],

[PIP7_1_AAL5=p], [PIP8_0_AAL5=q], [PIP8_1_AAL5=r],

[PIP9_0_AAL5=s], [PIP9_1_AAL5=t], [PIP10_0_AAL5=u],

[PIP10_1_AAL5=v], [NO_AAL5_VC=w]
```

a~v: PIP AAL5 (32~0xffffff)

w: 0~480

- Input CHG-CPN-PCF: PIP0_0_AAL5=255 ;
- Output

Command Window 🗟	
MSC(0x00) 2001-06-17 15:31:37 M2542 CHANGE CPN PCF AAL5 CONNECTION RESULT START VC ID [PIP_00] : 0x000000ff 0x001e0020 START VC ID [PIP_01] : 0x000b0020 0x001f0020 START VC ID [PIP_02] : 0x000c0020 0x00200020 START VC ID [PIP_03] : 0x000d0020 0x00210020 START VC ID [PIP_04] : 0x000e0020 0x00220020 START VC ID [PIP_05] : 0x000f0020 0x00230020 START VC ID [PIP_06] : 0x00100020 0x00240020	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CPN-PCF:PIPO_0_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.5. BSC INTER BSC AAL2 Setting Information Change

 Command CHG-BSC-IUR: BSC=a, [BSC0_AAL2=b], [BSC1_AAL2=c], [BSC2_AAL2=d], [BSC3_AAL2=e], [BSC4_AAL2=f], [BSC5_AAL2=g], [BSC6_AAL2=h], [BSC7_AAL2=i], [BSC8_AAL2=j], [BSC9_AAL2=k], [BSC10_AAL2=1], [BSC11_AAL2=m], [N0_AAL2_VC=n];
 a : BSC Number(0~11)
 b~m: BSC AAL2 (0~0xfffff)

n: 0~

- Input CHG-BSC-IUR: BSC=0, BSC0_AAL2=255;
- Output

Command Window	
MSC(0x00) 2001-06-17 15:33:09 M2572 CHANGE BSC INTER BSC AAL2 CONNECTION RESULT LOCATION : BSC_0 START VC ID [BSC_01] : 0x000b0040 START VC ID [BSC_02] : 0x000b0060 START VC ID [BSC_03] : 0x000b0080 START VC ID [BSC_04] : 0x000b0080 START VC ID [BSC_05] : 0x000b0080 START VC ID [BSC_06] : 0x000b0080 START VC ID [BSC_06] : 0x000b0080 START VC ID [BSC_06] : 0x000b0080	N Al Al
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-IUR:BSC=0,BSC0_AAL2=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.6. BSC INTER BTSC AAL2 Setting Information Change

• Command CHG-BSC-IUB: BSC=a, BTS=b, LICA=c, LINK=d,

[LINK0_AAL2=e], [LINK1_AAL2=f], [LINK2_AAL2=g], [LINK3_AAL2=h], [LINK4_AAL2=i], [LINK5_AAL2=j], [LINK6_AAL2=k], [LINK7_AAL2=1], [LINK8_AAL2=m], [LINK9_AAL2=n], [LINK10_AAL2=o], [LINK11_AAL2=p], [LINK12_AAL2=q], [LINK13_AAL2=r], [LINK14_AAL2=s], [LINK15_AAL2=t]

- a : BSC Number(0~11)
- b: BTS Number(0~47)
- c : LICA Number(0~2)
- d : LINK Number(0~15)

 $e \sim t: 0 \sim 0xffffff$

- Input CHG-BSC-IUB: BSC=0, BTS=0, LICA=0, LINK0_AAL2=255;
- Output

× IN T	ERM										. 🗆 ×
				Ca	omn	nan	d Winde	w			
MSC(MSC(0x00) 2001-06-17 15:34:06 M2573 CHANGE BSC INTER BTS AAL2 CONNECTION RESULT LOCATION : BSC_0/BTS_0 LICA LINK AAL2_VC LICA LINK AAL2_VC O 0 0x00000000 2 0x00000000 0 0x00000000 0 0x00000000 0 1 0x00000000 2 0x00000000 0 0x00000000 0 1 0x00000000 2 0x00000000 0 0x000000000 <t< th=""><th>d </th></t<>							d			
<u>1</u> 0	LDNG	CDM	<u>SIM</u>	TEST	<u>N</u> 0.7	ST <u>A</u>	<u>I ALFI I</u>	KEV10	US		
[jhpark <lgicbsm>] CHG-BSC-IVB:BSC=0,BTS=0,LICA=0,LINK0_AAL2=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>							N N				



4.3.6.7. BSC INTER CAN AAL2/5 Setting Information Change

- Command CHG-BSC-CAN: BSC=a, [CAN0_START_AAL5=b], [CAN1_START_AAL5=c], [NO_AAL5_VC=d]
 - a: BSC Number(0~11) b,c: 32~0xffffff d: 0~8160
- Input CHG-BSC-CAN: BSC=0, CAN0_START_AAL5=255;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:35:19 M2575 CHANGE BSC INTER CAN AAL2/5 CONNECTION RESULT LOCATION : BSC_0 START AAL5 VC ID [00] : 0x000000ff START AAL5 VC ID [01] : 0x000c0020 NUM OF AAL5 VC : 8160 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-CAN:BSC=0,CAN0_START_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.8. BSC INTER SLB AAL5 Setting Information

 Command CHG-BSC-SLB: BSC=a, [SLP0_AAL5=b], [SLP1_AAL5=c], [SLP2_AAL5=d], [SLP3_AAL5=e], [SLP4_AAL5=f], [SLP5_AAL5=g], [SLP6_AAL5=h], [SLP7_AAL5=i], [SLP8_AAL5=j], [SLP9_AAL5=k], [SLP10_AAL5=l], [SLP11_AAL5=m], [SLP12_AAL5=n], [SLP13_AAL5=o], [SLP14_AAL5=p], [SLP15_AAL5=q], [SLP16_AAL5=r], [SLP17_AAL5=s], [N0_AAL5_VC=t]

a: BSC Number(0~11)

b∼s: 40~0xffffff

t: 0~984

• Input CHG-BSC-SLB: BSC=0, SLP0_AAL5=255;

• Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:37:11 M2577 CHANGE BSC INTER SLB AAL2/5 CONNECTION RESULT LOCATION : BSC_0 START VC ID [SLPA_00] : 0x00000000000000000000000000000000	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-SLB:BSC=0,SLP0_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.9. BSC INTER VCB AAL5 Setting Information Change

• Command CHG-BSC-VCB: BSC=a,

[VCP0_AAL5=b], [VCP1_AAL5=c], [VCP2_AAL5=d], [VCP3_AAL5=e], [VCP4_AAL5=f], [VCP5_AAL5=g], [VCP6_AAL5=h], [VCP7_AAL5=i], [VCP8_AAL5=j], [VCP9_AAL5=k], [VCP10_AAL5=l], [VCP11_AAL5=m], [VCP12_AAL5=n], [VCP13_AAL5=o], [VCP14_AAL5=p], [VCP15_AAL5=q], [NO_AAL5_VC=r]

a: BSC Number(0~11)

b∼q: 40~0xffffff

r: 0~88

- Input CHG-BSC-VCB: BSC=0, VCP0_AAL5=255;
- Output

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			~	C	omm	and	Wind	dow		
MSC(0x00) 2001-06-17 15:38:19 M2578 CHANGE BSC INTER VCB AAL2/5 CONNECTION RESULT LOCATION : BSC_0 START VC ID [VCPA_00] : 0x000000ff START VC ID [VCPA_01] : 0x000b0028 START VC ID [VCPA_02] : 0x000c0028 START VC ID [VCPA_03] : 0x000d0028 START VC ID [VCPA_04] : 0x000e0028										
ĪO	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS		
[jhp ACCE [jhp	oark <l EPTED oark <l< td=""><td>GICBSM GICBSM</td><td>⊳] (+ ⊳]]</td><td>IG-BSC-</td><td>VCB : BS</td><td>5C=0,VC</td><td>PO_AAL</td><th>5=255;</th><td></td><td></td></l<></l 	GICBSM GICBSM	⊳] (+ ⊳]]	IG-BSC-	VCB : BS	5C=0,VC	PO_AAL	5=255;		

4.3.6.10. BSC INTER ALB AAL5 Setting Information Change

• Command CHG-BSC-ALB: BSC=a,

[ALMA0_ALP0_0=b], [ALMA0_ALP0_1=c], [ALMA0_ALP1_0=d], [ALMA0_ALP1_1=e], [ALMA0_ALP2_0=f], [ALMA0_ALP2_1=g], [ALMA0_ALP3_0=h], [ALMA0_ALP3_1=i], [ALMA0_ALP4_0=j], [ALMA0_ALP4_1=k], [ALMA1_ALP0_0=l], [ALMA1_ALP0_1=m], [ALMA1_ALP1_0=n], [ALMA1_ALP1_1=o], [ALMA1_ALP2_0=p], [ALMA1_ALP2_1=q], [ALMA1_ALP3_0=r], [ALMA1_ALP3_1=s], [ALMA1_ALP4_0=t], [ALMA1_ALP4_1=u], [N0_AAL5_VC=v]

a: BSC Number(0~11) b~u: 32~0xffffff v: 0~2016

- Input CHG-BSC-ALB: BSC=0, ALMA0_ALP0_0=255;
- Output

×INTERM	_ 🗆 ×				
Command Window					
MSC(0x00) 2001-06-17 15:39:23 M2579 CHANGE BSC INTER ALB AAL2/5 CONNECTION RESULT LOCATION : BSC_0 NUM OF VC : 2016 ALMA ALPA AAL5_VC_ID1 AAL5_VC_ID2 0 0 0x000000ff 0x00140020 0 1 0x000b0020 0x00150020 0 2 0x000c0020 0x00160020					
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>					
[jhpark <lgicbsm>] CHG-BSC-ALB:BSC=0,ALMA0_ALPO_0=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>					



4.3.6.11. BTS INTER RCU AAL5 Setting Information Change

• Command CHG-BTS-RCU: BSC=a, BTS=b, RCU=c,

[LICA0_AAL5=d], [LICA1_AAL5=e], [LICA2_AAL5=f], [LICA0_NO_VC=g], [LICA1_NO_VC=h], [LICA2_NO_VC=i]

- a: BSC Number(0~11)
- b:BTS Number(0~47)
- c: RCU Number(0~9)

d~i: 0~

- Input CHG-BTS-RCU: BSC=0,BTS=0,RCU=0, LICA0_AAL5=255;
- Output





4.3.7. Configuration

Information

Display(Display_Configuration_Data)

This section describes the comands that are used to inquire the configuration information which is related to processors, devices, and overhead channels which are currently used in BTS and BSC.

CRN	MMC	Description
2101	DIG DCC_CONE	BSS configuration information
2101	DI2-D22-COIVL	verification
2102	DIG-SMD-CONE	SMP configuration information
2103	DIS-SMP-COMP	verification
2105	DIS-WMP-CONF	VMP configuration information
2105	DIS-VIMP-CONF	verification
9119	DIC DTC_CONE	BTS configuration information
2112	DIS-DIS-CONF	verification
9115	DIG CHID_CONE	DBPA CHIP configuration information
2110	DIS-CHIF-CONF	verification
2125	DIG OVHD_CONE	OVERHEAD CHANNEL configuration
2120	DIS-OVID-CONF	information verification
0100	DIC DDCN CONE	PDSN configuration information
2133	DIS-PDSN-CONF	verification

Table 4.3-3 Configuration Information Display

4.3.7.1. BSS Configuration Information Verification

This is a command to check the BTS, Processors and PCF counts in the BSC.

- Command DIS-BSS-CONF: BSC=a;
- Input DIS-BSS-CONF: BSC=0;
- Output

	_ 🗆 X
Command Window	
MSC(0x00) 2001-06-17 21:06:14 M2101 DISPLAY BSS CONFIGURATION BSC BTS SMP VMP PCF 0 1 1 1 2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] DIS-BSS-CONF:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-126 BSS Configuration Information Display

4.3.7.2. SMP Configuration Information Verification

• Command DIS-SMP-CONF: BSC=a;

a: BSC Number(#0~11)

- Input DIS-SMP-CONF: BSC=0;
- Output

×INTERM	A	_ 🗆 ×
Command Window		
MSC(0x00) 2001-06-17 21:07:28 M2103 DISPLAY SMP CONFIGURATION LOCATION : BSC_0 SMP_0 : EQUIP SLP[00] SLP[01] SLP[02] SLP[03] SLP[04] EQUIP EQUIP EQUIP EQUIP SLP[06] SLP[07] SLP[08] SLP[09] SLP[10] EQUIP EQUIP EQUIP EQUIP EQUIP SLP[12] SLP[13] SLP[14] SLP[15] SLP[16] EQUIP EQUIP EQUIP EQUIP EQUIP COMPLETED] SLP[05] EQUIP] SLP[11] EQUIP] SLP[17] EQUIP	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>		
[jhpark <lgicbsm>] DIS-SMP-CONF:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>		
V		

Fig. 4.3-127 SMP Configuration Information Display

4.3.7.3. VMP Configuration Information Verification

• Command DIS-VMP-CONF: BSC=a;

a: BSC Number(#0~11)

- Input DIS-VMP-CONF: BSC=0;
- Output

Command Window						
MSC(0x00) 2001-06-17 21:08:35 M2105 DISPLAY VMP CONFIGURATION LOCATION : BSC_0 VMP_0 : EQUIP VCP[00] VCP[01] VCP[02] VCP[03] EQUIP EQUIP EQUIP EQUIP VCP[04] VCP[05] VCP[06] VCP[07] EQUIP EQUIP EQUIP EQUIP VCP[08] VCP[09] VCP[10] VCP[11] EQUIP EQUIP EQUIP EQUIP VCP[12] VCP[13] VCP[14] VCP[15] EQUIP EQUIP EQUIP EQUIP COMPLETED						
<u>IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS</u>						
[jhpark <lgicbsm>] DIS-VMP-CONF:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>						

Fig. 4.3-128 VMP Configuration Information Display

4.3.7.4. BTS Configuration Information Verification

- Command DIS-BTS-CONF: BSC=a, BTS=b;
 - a: BSC Number(#0~11)
 - b: BTS Number(#0~47)
- Input DIS-BTS-CONF: BSC=0, BTS=0;

• Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:09:41 M2112 DISPLAY BTS CONFIGURATION LOCATION : BSC_0/BTS_0 OVHD_MODE : DYNAMIC PA_TYPE : MPD_LPA ANT_TYPE : STANDARD TX_DIVERSITY : NOT_USE RX_DUPLEX : SIMPLEX RX_DIVERSITY : USE LNA_TYPE : NOR_LNA RISA_EQUIP : EQUIP BOTA_EQUIP : EQUIP COMPLETED	
2	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-BTS-CONF:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	
A	

Fig. 4.3-129 BTS Configuration Information Display

4.3.7.5. DBPA CHIP Configuration Information Verification

- Command DIS-CHIP-CONF: BSC=a, BTS=b;
 - a: BSC Number(#0~11)
 - b: BTS Number(#0~47)
- Input DIS-CHIP-CONF: BSC=0, BTS=0;
- Output

Command Wikdow	
MSC(0x00) 2001-06-17 21:10:34 M2115 DISPLAY CHIP CONFIGURATION LOCATION : ESC_0/BTS_0 NUM_CHIP_PER_CHC : 2 SECTOR_GAIN [ALPHA] : 208, [BETA] : 208, [GAMMA] : 208 IDELTA] : 208, [EPSILON] : 208, [ZETA] : 208 IX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [BETA] : 320, [GAMMA] : 320 IDELTA] : 320, [EPSILON] : 320, [ZETA] : 320 IX_DIV_TIMING_ADV [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_IO_FORMAT: 0 CELL_RADIVS : 511 REV_CELL_MODE : 0 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 1 MAX_RACH_SEPERATE : 1 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 1 DIVERSITY_SC_2000 : 3 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
ACCEPTED [jhpark <lgicbsm>] DIS-SMP-CONF:BSC=0; ACCEPTED</lgicbsm>	
<u>م</u>	Ν

Fig. 4.3-130 DBPA CHIP Configuration Information Display

4.3.7.6. OVERHEAD CHANNEL Configuration Information Verification

- Command DIS-OVHD-CONF: BSC=a, BTS=b;
 - a: BSC Number(#0~11)
 - b: BTS Number(#0~47)
- Input DIS-OVHD-CONF: BSC=0, BTS=0;
- Output

×IN	TERM	_ 🗆 ×
	Command Window	
MSC COM	(0x00) 2001-06-17 21:12:20 M2125 DISPLAY OVERHEAD CHANNEL CONFIGURATION MMS REQUEST FAIL TARGET NO RESPONSE PLETED	
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jh ACC [jh	park <lgicbsm>] DIS-OVHD-CONF:BSC=0,BTS=0; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-131 OVHD Channel Configuration Information Display



4.3.7.7. PDSN Configuration Information Verification

- Command DIS-PDSN-CONF: PCP=a;
 - a: PCP Number(#0~2)
- Input DIS-PDSN-CONF: PCP=0;
- Output

×IN	ERM	_ 🗆 ×
	Command Winkow	
MSC COM	0x00) 2001-06-17 21:13:22 M2133 DISPLAY PDSN DATA LOCATION : PCP_0 NUMBER OF PDSN : 1 PDSN[00] IP : 0.0.0.0 NUM_OF_NODE : 1 NODE[00] IP : 10.160.6.66 SSK : LETED	
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS	
[jh ACC [jh	ark <lgicbsm>] DIS-PDSN-CONF:PCP=0; PTED ark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-132 PDSN Configuration Information Display



4.3.8. Configuration

Information

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Change
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(Change_Configuration_Data)

This section describes commands that are used to add or delete BTS and BSC processors and devices. The configuration information that can be added and deleted are presented below. For the command that has many parameters to input, input image on the inpout Widow. This section does not cover details of each parameter.

CRN	MMC	Description
C2312	CHG-BTS-CONF	BTS configuration information change
C2315		Channel Card Chip configuration
	CHG-CHIP1-CONF	information(1) change
C2317		Channel Card Chip configuration
	CHG-CHIP2-CONF	information(2) change
C2333	ADD-PDSN-CONF	PDSN CONFIG addition
C2334	RMV-PDSN-CONF	PDSN CONFIG deletion
C2335	CHG-PDSN-CONF	PDSN CONFIG change
C2337	ADD-PDSN-NODE	PDSN NODE addition
C2338	RMV-PDSN-NODE	PDSN NODE deletion
C2339	CHG-PDSN-NODE	PDSN NODE change
C2601	MOV-BSC-NODE	BSC Node movement
C2602	MOV-PCF-NODE	PCF Node movement
C2603	MOV-SMP-NODE	SMP Node movement
C2604	MOV-VMP-NODE	VMP Node movement
C2605	MOV-BTS-ID	BTS ID movement
C2606	MOV-BTS-TRNK	BTS TRUNK Node movement
C2607	MOV-LICA-LINK	LICA LINK movement
C2610	MOV-OVHD-CONE	OVERHEAD CHANNEL configuration
		information movement
C2701	ADD-BSC-CONF	BSC configuration addition
C2702	RMV-BSC-CONF	BSC configuration deletion
C2711	ADD-PCF-CONF	PCF configuration addition
C2712	RMV-PCF-CONF	PCF configuration deletion

Table 4.3-4 Configuration Information Change

STAREX-IS BSM Manual

C2721	ADD-SMP-CONF	SMP configuration addition
C2722	RMV-SMP-CONF	SMP configuration deletion
C2731	ADD-VMP-CONF	VMP configuration addition
C2732	RMV-VMP-CONF	VMP configuration deletion
C2741	ADD-BTS-CONF	BTS configuration addition
C2742	RMV-BTS-CONF	BTS configuration deletion
C2751	ADD-SECT-CONF	SECTOR configuration addition
C2752	RMV-SECT-CONF	SECTOR configuration deletion
C2761	ADD-FA-CONF	FA configuration addition
C2762	RMV-FA-CONF	FA configuration deletion
C2771	ADD TDNIK CONE	BSC-BTS TRUNK configuration
	ADD-TRINK-CONF	addition
C2772	RMV-TRNK-CONF	BSC-BTS TRUNK configuration
		deletion
C2781	ADD-CAN-PVC	CAN PVC configuration addition
C2782	RMV-CAN-PVC	CAN PVC configuration deletion
C2783	ADD-CPN-PVC	CPN PVC configuration addition
C2784	RMV-CPN-PVC	CPN PVC configuration deletion
C2785	ADD-BSC-PVC	BSC PVC configuration addition
C2786	RMV-BSC-PVC	BSC PVC configuration deletion

4.3.8.1. BTS Configuration Information Change

- Command CHG-BTS-CONF :BSC=a ,BTS=b [,PA_TYPE=c] [,ANT_TYPE=d] [,ANT_DUP=e] [,RX_DIV=f] [,LNA_EQP=g] [,RISA_EQP=h] [,BOTA_EQP=i];
- Input CHG-BTS-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output

	- 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:25:27 M2312 CHANGE BTS CONFIGURATION LOCATION : BSC_0/BTS_0 OVHD_MODE : DYNAMIC PA_TYPE : PA_NEQ ANT_TYPE : STANDARD TX_DIVERSITY : NOT_USE RX_DUPLEX : SIMPLEX RX_DIVERSITY : USE LNA_TYPE : NOR_LNA RISA_EQUIP : EQUIP BOTA_EQUIP : EQUIP 	
COMPLETED	Į
, ⊲	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BTS-CONF:BSC=0,BTS=0,PA_TYPE=PA_NEQ; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-133 BTS Configuration Information Change Display

4.3.8.2. Channel Card Chip Configuration Information (1) Change

Command CHG-CHIP1-CONF :BSC=a ,BTS=b [,SECT_GAIN_A=c] [,SECT_GAIN_B=d] [,SECT_GAIN_G=e] [,SECT_GAIN_D=f] [,SECT_GAIN_E=g] [,SECT_GAIN_Z=h] [,T_DIV_SECT_A=i] [,T_DIV_SECT_B=j] [,T_DIV_SECT_G=k] [,T_DIV_SECT_D=1] [,T_DIV_SECT_E=m] [,T_DIV_SECT_Z=n] [,SECT_T_ADV_A=o] [,SECT_T_ADV_B=p] [,SECT_T_ADV_G=q] [,SECT_T_ADV_D=r] [,SECT_T_ADV_E=s] [,SECT_T_ADV_Z=t] [,T_DIV_T_ADV_A=u] [,T_DIV_T_ADV_B=v] [,T_DIV_T_ADV_G=w] [,T_DIV_T_ADV_D=x] [,T_DIV_T_ADV_E=y] $[,T_DIV_T_ADV_Z=z];$

- Input CHG-CHIP1-CONF:BSC=0, BTS=0,SECT_GAIN_A=255;
- Output

Command Window MSC(0x00) 2001-06-17 21:28:43 M2315 CHANGE BTS DRU CHIP CONFIGURATION(1)	
MSC(0x00) 2001-06-17 21:28:43 M2315 CHANGE BTS DRU CHIP CONFIGURATION(1)	
LOCATION : ESC_0/BTS_0 NUM_CHIP_PER_CHC : 2 SECTOR_GAIN [ALPHA] : 255, [BETA] : 208, [GAMMA] : 208 [DELTA] : 208, [EPSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [BETA] : 320, [GAMMA] : 320 [DELTA] : 320, [EPSILON] : 320, [ZETA] : 320 TX_DIV_TIMING_ADV [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_10_FORMAT: 0 CELL_RADIUS : 511 REV_INPUT_FORMAT : 1 REV_CELL_MODE : 0 MAX_RACH_SEPERATE : 80 MAX_REACH_FRACTION : 10 MAX_RACH_SEPERATE : 80 MAX_REACH_FRACTION : 10 MAX_REACH_FRACTION : 10 MAX_REACH_FRACTION : 10 MAX_REACH_FRACTION : 10 MAX_REACH_FRACTION : 10 MAX_FINGER_CHAN35 : 6 MAX_FINGER_CHAN35 : 7 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CHIP1-CONF:BSC=0,BTS=0,SECT_GAIN_A=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-134 Channel Card Chip Configuration Information (1) Change Display

4.3.8.3. Channel Card Chip Configuration Information (2) Change

• Command CHG-CHIP2-CONF :BSC=a ,BTS=b [,NUM_CHIP=c] [,SECT_T_IO=d] [,CELL_RADIUS=e] [,REV_IN_FORM=f] [,R_CELL_MODE=g] [,MAX_RACH_F=h] [,MAX_RACH_S=i] [,MAX_REACH_F=j] [,MAX_REACH_S=k] [,SRCH_WIN_ADJ=1] [,MAX_CH95=m] [,MAX_CDMA2K=n] [,CSM_MODE=o] [,DIV_SCALE_2K=p];


- Input CHG-CHIP2-CONF: BSC=0, BTS=0,NUM_CHIP=255;
- Output

Command Window MSC(0x00) 2001-06-17 21:30:11 M2317 CHANCE ETS DRU CHIP CONFIGURATION(2) LOCATION : ESC_0/BTS_0 NUM_CHIP_PER_CHC : 255 SECTOR_GAIN [ALPHA] : 255, [BETA] : 208, [CAMMA] : 208 IDELTA] : 208, [EPSILON] : 208, [ZETA] : 208 IX_DIV_SECTOR_GAIN [ALPHA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMINC_ADV [ALPHA] : 320, [EFA] : 320, [ZETA] : 320 IDELTA] : 320, [EFA] : 320, [ZETA] : 320 IDELTA] : 320, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMINC_ADV [ALPHA] : 0, [BETA] : 0, [CAMMA] : 0 DELTA] : 320, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_ID_FORMAT : 0 CELL_RADIUS : 511 REV_INPUT_FORMAT : 1 REV_INPUT_FORMAT : 1 REV_TUPUT_FORMAT : 1 MAX_REACH_EPERATE : 80 MAX_REACH_EPERATE : 80 MAX_REACH_EPERATE : 80 MAX_REACH_SEPERATE : 80 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DIVERSITY_SC_2000 : 3 COMPLETED IO LDNC_CDM_SIM_IEST_NO.7_STAT_ALFT_PREVIOUS I ID_LDNC_CDM_SIM_IEST_NO.7_STAT_ALFT_PREVIOUS I Ippark <lgicesm>] CHG-CHIP2-CONF:BSC=0, BTS=0, NUM_CHIP=255; ACCEPTED [jhpark <lgicesm>] [</lgicesm></lgicesm>	
MSC(0x00) 2001-06-17 21:30:11 M2317 CHANGE BTS DRU CHIP CONFIGURATION(2) IOCATION : BSC_0/BTS_0 NUM_CHIP_PER_CHC : 255 SECTOR_GAIN [ALPHA] : 255, [BETA] : 208, [ZETA] : 208 IAUPHA] : 0, [EFSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [EFSILON] : 0, [ZETA] : 320 [DELTA] : 320, [EFSILON] : 320, [ZETA] : 320 IDELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_ID_FORMAT : 0 [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_ID_FORMAT : 1 REV_INPUT_FORMAT : 1 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_FINGER_CHAN95 : 6 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DUTVERSITY_SC_2000 : 3 COMPLETED IDING CDM STM IEST NO.7 STAT ALFI PREVIOUS [jhpark <lgicesm>] [</lgicesm>	Command Window
IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS [jhpark <lgicbsm>] CHG-CHIP2-CONF:BSC=0,BTS=0,NUM_CHIP=255; ACCEPTED [jhpark <lgicbsm>] []</lgicbsm></lgicbsm>	MSC(0x00) 2001-06-17 21:30:11 M2317 CHANGE BTS DRU CHIP CONFIGURATION(2) LOCATION : BSC_0/BTS_0 NUM_CHIP_PER_CHC : 255 SECTOR_GAIN [ALPHA] : 208, [EPSILON] : 208, [ZETA] : 208 DELTA] : 208, [EPSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [BETA] : 320, [ZETA] : 320 TX_DIV_TIMING_ADV [ALPHA] : 320, [EPSILON] : 320, [ZETA] : 320 TX_DIV_TIMING_ADV [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_IO_FORMAT : 0 CELL_RADUS : 511 REV_INPUT_FORMAT : 1 REV_CELL_MODE : 0 MAX_RACH_SEPERATE : 80 MAX_REACH_FRACTION : 10 MAX_REACH_SEPERATE : 80 ENABLE_SRCH_WIN_ADJ : 1 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DIVERSITY_SC_2000 : 3 COMPLETED
[jhpark <lgicbsm>] CHG-CHIP2-CONF:BSC=0,BTS=0,NUM_CHIP=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
	[jhpark <lgicbsm>] CHG-CHIP2-CONF:BSC=0,BTS=0,NUM_CHIP=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-135 Channel Card Chip Configuration Information (2) Change Display

4.3.8.4. PDSN CONFIG Addition

- Command ADD-PDSN-CONF :PCF=a ,PDSN_IDX=b ,PDSN_IP=c;
- Input ADD-PDSN-CONF: BSC=0, BTS=0,PDSN_IP=255.255.255.0;
- Output

	_ 🗆 ×
Comman ^d Window	
MSC(0x00) 2001-06-17 21:31:04 M2333 ADD PDSN IP RESULT PCP_0/PDSN_0/IP_255.255.255.0 ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-PDSN-CONF:PCF=0,PDSN_IDX=0,PDSN_IP=255.255.2 ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	55.0;

Fig. 4.3-136 PDSN Configuration Addition Display

4.3.8.5. PDSN CONFIG Deletion

- Command RMV-PDSN-CONF :PCF=a ,PDSN_IDX=b;
- Input RMV-PDSN-CONF: BSC=0, BTS=0,PDSN_IDX=1;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:32:16 M2334 REMOVE PDSN IP RESULT REPORT PCP_0/PDSN_1 REMOVED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] RMV-PDSN-CONF:PCF=0,PDSN_IDX=1; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	
<u>a</u>	

Fig. 4.3-137 PDSN Configuration Deletion Display

4.3.8.6. PDSN CONFIG Change

- Command CHG-PDSN-CONF :PCF=a ,PDSN_IDX=b ,PDSN_IP=c;
- Input CHG-PDSN-CONF: BSC=0, BTS=0,PDSN_IP=127.0.0.1;
- Output

×IN.	TERM	- 🗆 ×
	Command Window	
COM	(0x00) 2001-06-17 21:33:22 M2335 CHANGE PDSN IP RESULT PCP_0/PDSN_0/IP_127.0.0.1 PLETED	
10	LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
 Acci [jhp	park <lgicbsm>] CHG-PDSN-CONF:PCF=0,PDSN_IDX=0,PDSN_IP=127.0.0.1; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-138 PDSN Configuration Change Display

4.3.8.7. PDSN NODE Addition

- Command ADD-PDSN-NODE :PCF=a ,PDSN_IDX=b ,PDSN_NODE_IDX=c , PDSN_NODE_IP=d ,SSK_VALUE=e
- Input ADD-PDSN-NODE: BSC=0, BTS=0,PDSN_NODE_IDX=0,

PDSN_NODE_IP:128.128.128.128;

• Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:34:28 M2337 ADD PDSN NODE RESULT REPORT PCP_0/PDSN_0/NODE_0 ADDED IP : 128.128.128.128 SSK : jhpark COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-PDSN-NODE:PCF=0,PDSN_IDX=0,PDSN_NODE_IDX=0,PDS ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-139 PDSN NODE Addition Display

4.3.8.8. PDSN NODE Deletion

- Command RMV-PDSN-NODE :PCF=a ,PDSN_IDX=b ,PDSN_NODE_IDX=c;
- Input RMV-PDSN-NODE: BSC=0, BTS=0,PDSN_IDX=0,PDSN_NODE_IDX=0;
- Output

×IN1	TERM									_ 🗆 ×
				С	omm	and	Wind	tow		
MSC (COMF	(0x00) M2338 PCP Pleted	2001- REMO' _0/PD	D6-17 VE PD9 SN_0/M	21:35 SN NODE IODE_O	:35 RESUL REMOVE	T REPO D	RT			
<u>1</u> 0	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS		
[jhp ACCI [jhp	oark <l EPTED oark <l< td=""><td>GICBS GICBS</td><td>H>] RM H>]∏</td><td>IV-PDSN</td><td>-NODE :</td><td>PCF=0,</td><td>PDSN_II</td><td>DX=0,PDSN_NODI</td><td>E_IDX=0;</td><td></td></l<></l 	GICBS GICBS	H>] RM H>]∏	IV-PDSN	-NODE :	PCF=0,	PDSN_II	DX=0,PDSN_NODI	E_IDX=0;	

Fig. 4.3-140 PDSN NODE Deletion Display



4.3.8.9. PDSN NODE Change

- Command CHG-PDSN-NODE :PCF=a ,PDSN_IDX=b ,PDSN_NODE_IDX=c
 [,PDSN_NODE_IP=d] [,SSK_VALUE=e]
- Input CHG-PDSN-NODE: BSC=0, BTS=0,PDSN_IDX=0,PDSN_NODE_IDX=0,

PDSN_NODE_IP=100.100.0.1, SSK_VALUE=gamdok;

• Output



Fig. 4.3-141 PDSN NODE Change Display

4.3.8.10. BSC Node Movement

- Command MOV-BSC-NODE :T_PROC=a ,BSC=b ,CARD=c ,LINK=d;
- Input MOV-BSC-NODE: T_PROC=CNP,BSC=0,CARD=1,LINK=6;;
- Output

×INTERM	. 🗆 🗵
Command Window	
MSC(0x00) 2001-06-18 17:51:54 M2601 MOVE BSC NODE CONFIGURATION LOCATION : BSC_0 PREVIOUS : CNP/CARD_1/LINK_4 CURRENT : CNP/CARD_1/LINK_6 COMPLETED	AI N
IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jhpark <lgicbsm>] MOV-BSC-NODE:T_PROC=CNP,BSC=0,CARD=1,LINK=6; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	N Al D

Fig. 4.3-142 BSC NODE Movement Display

4.3.8.11. PCF Node Movement

- Command MOV-PCF-NODE :PCF=a ,CARD0=b ,LINK0=c ,CARD1=d ,LINK1=e ,CARD2=f ,LINK2=g ,CARD3=h ,LINK3=i;
- Input MOV-PCF-NODE: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.12. SMP Node Movement

- Command MOV-SMP-NODE :BSC=a ,SMP=b ,CARD=c ,LINK=d;
- Input MOV-SMP-NODE: BSC=0, SMP=0,CARD=1,LINK=6
- Output

	_ 🗆 X
Command Window	
MSC(0x00) 2001-06-18 18:00:59 M2603 MOVE SMP NODE CONFIGURATION LOCATION : BSC_0/SMP_0 PREVIOUS : CARD_1/LINK_7 CURRENT : CARD_1/LINK_6 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] MOV-SMP-NODE:BSC=0,SMP=0,CARD=1,LINK=6; ACCEPTED [jhpark <lgicbsm>] [</lgicbsm></lgicbsm>	

Fig. 4.3-143 SMP NODE Movement Display

4.3.8.13. VMP Node Movement

- Command MOV-VMP-NODE :BSC=a ,VMP=b ,CARD=c ,LINK=d;
- Input MOV-VMP-NODE: BSC=0, VMP=0, CARD=1, LINK=6;
- Output

×IN.	ERM	- 🗆 ×
	Command Window	
COMI	x00) 2001-06-20 21:22:58 M2604 MOVE VMP NODE CONFIGURATION LOCATION : BSC_0/VMP_0 PREVIOUS : CARD_1/LINK_5 CURRENT : CARD_1/LINK_6 .ETED	N N N
<u>1</u> 0	LDNG <u>C</u> DM <u>s</u> tm <u>t</u> est <u>n</u> 0.7 st <u>a</u> t al <u>e</u> t previous	
[jh ACC [jh	urk <lgicbsm>] MOV-VMP-NODE:BSC=0,VMP=0,CARD=1,LINK=6; TED urk <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-144 VMP NODE Movement Display

4.3.8.14. BTS ID Movement

- Command MOV-BTS-ID :BSC=a ,OLD_BTS=b ,NEW_BTS=c;
- Input MOV-BTS-ID: BSC=0, OLD_BTS=0,NEW_BTS=2;
- Output

×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-20 21:17:24 M2605 MOVE BTS ID RESULT PREVIOUS : BSC_0/BTS_0 CURRENT : BSC_0/BTS_2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] MOV-BTS-ID:BSC=0,OLD_BTS=0,NEW_BTS=2; ACCEPTED [jhpark <lgicbsm>] ĭ</lgicbsm></lgicbsm>	

Fig. 4.3-145 BTS ID Movement Display

4.3.8.15. BTS TRUNK Node Movement

For this command, execute DIS-TRNK-DATA first to input the parameter value.

- Command MOV-BTS-TRNK :BSC=a ,BTS=b ,OLD_ALMA=c ,OLD_ALPA=d , OLD_ALPA_LINK=e ,NEW_ALMA=f ,NEW_ALPA=g ,NEW_ALPA_LINK=h;
- Input MOV-BTS-TRNK: BSC=0, BTS=2,

OLD_ALMA=0,OLD_ALPA=0,OLD_ALPA_LINK=0, NEW_ALMA=1,NEW_ALPA=1,NEW_ALPA_LINK=1;

• Output

×IN.	TERM	□ ×
	Command Window	
MSC COMI	(OxOO) 2001-06-20 21:26:01 M2606 MOVE BTS TRUNK NODE CONFIGURATION LOCATION : BSC_0/BTS_2 PREVIOUS : ALMA_0/ALPA_0/LINK_0 CURRENT : ALMA_1/ALPA_1/LINK_1 IPLETED	
<u>1</u> 0	<u>LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jh ACCI [jh	park <lgicbsm>] MOV-BTS-TRNK:BSC=0,BTS=2,OLD_ALMA=0,OLD_ALPA=0,OLD_AL EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-146 BTS TRUNK Movement display

4.3.8.16. LICA LINK Movement

• Command MOV-LICA-LINK :BSC=a ,BTS=b ,OLD_LICA=c ,OLD_LINK=d ,NEW_LICA=e ,NEW_LINK=f;

• Input MOV-LICA-LINK: BSC=0, BTS=1,

OLD_LICA=0, OLD_LINK=0,

NEW_LICA=1, NEW_LINK=1;

• Output

] ×
Command Window	
MSC(0x00) 2001-06-20 21:35:44 M2607 MOVE BTS LICA LINK CONFIGURATION LOCATION : BSC_0/BTS_1 PREVIOUS : LICA_0/LINK_0 CURRENT : LICA_1/LINK_1 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] MOV-LICA-LINK:BSC=0,BTS=1,OLD_LICA=0,OLD_LINK=0,NEW_L ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-147 LICA LINK Movement Display

4.3.8.17. OVERHEAD CHANNEL Configuration Information Movement

Refer to DIS-OVHD-CONF command

- Command MOV-OVHD-CONF :BSC=a ,BTS=b ,SECT=c ,CDMACH=d , NEW_CHC=e ;
- Input MOV-OVHD-CONF: BSC=0, BTS=0,

SECTOR=ALPHA,CDMACH=0,NEW_CHC=1;

• Output

XINTERM									_ 🗆 ×
		Com	mand	Wi	ndo	w			
M2610 M	OVE OVERHI	FAD CHANNE		IRAT	TON				
LOCAT	ION : BSC_	_0/BTS_0	L CONTIN		LON				
CDMAC	H SECTOR	OV_TYPE	OV_ID[PO	CH]	RCU	SLOT	NODE		
	ALPHA	PILOT	0		0	1	0		
	ALPHA	SYNC	U		U	1	1		
	ALPHA	OPACING	ŭ		U N	-	2		
	ALP HA	OPACING	ň		ň	4	3		
ll ŏ	AL PHA	ACCESS	n n	1	ň	i	ñ		
ll ŏ	BETA	PILOT	ŏĽ	-	ŏ	i	ŏ		
Ō	BETA	SYNC	Ō		Ō	1	1		
0	BETA	PAGING	0		0	1	2		
0	BETA	QPAGING	0		0	1	3		
	BETA	ACCESS	0 (0]	0	1	0		
	GAMMA	PILOT	U		U U	3	0		
	САММА	SYNU	0		0	3	1		
	САММА	ODACTNC	U N		U N	3	2		
	CAMMA	ACCESS	0 1 0	1	ň	2	5 N		
1	ALPHA	PTIOT	0 10	- 1	ň	ň	5		
ll i	ALPHA	SYNC	ŏ		ŏ	ŏ	Ğ		
ll i	ALPHA	PAGING	ō		ō	ō	7		
1	ALPHA	QPAGING	Ō		Ō	Ō	8		
1	ALPHA	ACCESS	0 [0]	0	0	1		
1	BETA	PILOT	0		0	1	4		
	BETA	SYNC	0		0	1	5		
	BETA	PAGING	0		0	1	6		
	BEIA	QPAGING	0 10	-	U	1			
	CAMMA	DTLAT	0 10	1	N N	5	4		
	CAMMA	SANC	ň		ň	2	5		
ll i	GAMMA	PAGTNG	ň		ň	3	6 6		
ll i	GAMMA	OPAGTNG	ň		ň	ă	ž		
i	GAMMA	ACCESS	ŏ [0	1	ŏ	3	i		
COMPLETED				-					
				_					
				_		_			
			7 6141		т п	PEUTOU	c		
	<u> 100 310</u>	<u>1631 M</u> U.	/ <u>эін</u> і	ALT.		NEVI00	3		
ACCEPTED			F. DOG. C. 1			T 41.00			
LINPARK <lgi< td=""><td>CR2W>1 WO/</td><td>V-OVHD-CON</td><td>F:BSC=U,E</td><td>s I S=I</td><td>U, SEC</td><td>T=ALPH</td><td>IA, CDMACI</td><td>H=U,NE₩_</td><td>CHC</td></lgi<>	CR2W>1 WO/	V-OVHD-CON	F:BSC=U,E	s I S=I	U, SEC	T=ALPH	IA, CDMACI	H=U,NE₩_	CHC
Libnark CLCT	CRSM>1 T								
	CP30/>11								
<u> </u>									

Fig. 4.3-148 OVHD Channel Configuration Information Movement Display

4.3.8.18. BSC Configuration Addition

- Command ADD-BSC-CONF :T_PROC=a ,BSC=b ,CARD=c ,LINK=d;
- Input ADD-BSC-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.19. BSC Configuration Deletion

- Command RMV-BSC-CONF :T_PROC=a ,BSC=b;
- Input RMV-BSC-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.20. PCF Configuration Addition

- Command ADD-PCF-CONF :PCF=a ,CARD0=b ,LINK0=c ,CARD1=d ,LINK1=e ,CARD2=f ,LINK2=g ,CARD3=h ,LINK3=i;
- Input ADD-PCF-CONF:PCF=1,CARD0=3,LINK0=4,CARD1=3,LINK1=4,CARD2=3,LINK2=4,CARD3=3,LI NK3=4;
- Output

	- 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:48:32 M2711 ADD PCF CONFIGURATION LOCATION : CARD[3 3 3 3]/LINK[4 4 4 4] PCF_1 IS ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-PCF-CONF:PCF=1,CARD0=3,LINK0=4,CARD1=3,LINK1=4, ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	CA A

Fig. 4.3-149 PCF Configuration Addition Display

4.3.8.21. PCF Configuration Deletion

- Command RMV-PCF-CONF :PCF=a;
- Input RMV-PCF-CONF: PCF=1;
- Output

×IN1	TERM										_ 🗆 ×
				C	omm	and	Win	dow			
MSC	(0x00) M2712 LOC PCF PLETED	2001- REMO ATION _1 RE	DG-18 VE PCI : CAF MOVED	20:46 - CONFI RD[3	:56 GURATJ 3 3	(ON 3]/LIN	K[4	567]		
ĪO	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVI	DUS		
[jhp ACCI [jhp	park <l EPTED park <l< td=""><td>GICBS</td><td>M>] RN M>]]</td><td>IV-PCF-</td><td>CONF : F</td><td>PCF=1;</td><td></td><td></td><td></td><td></td><td></td></l<></l 	GICBS	M>] RN M>]]	IV-PCF-	CONF : F	PCF=1;					

Fig. 4.3-150 PCF Configuration Deletion Display

4.3.8.22. SMP Configuration Addition

- Command ADD-SMP-CONF :BSC=a ,SMP=b ,CARD=c ,LINK=d;
- Input ADD-SMP-CONF: BSC=0,SMP=0,CARD=1,LINK=7;
- Output

XINTER	M			k				_ 🗆 ×
			Comm	and W	ina	low		
MSC(Oxt Mi COMPLE	00) 2001– 2721 ADD LOCATION SMP_O IS FED	06-18 2 SMP CONF : BSC_0 Added	0:50:17 IGURATION /CARD_1/LI	NK_7				
<u>10 L</u>	NG <u>C</u> DM	<u>s</u> tm <u>t</u>	EST <u>N</u> 0.7	ST <u>A</u> T A	L <u>F</u> T	PREVIOUS		
[jhparl ACCEPTI [jhparl	< <lgicbs ED < <lgicbs< th=""><td>M>] ADD- M>]]</td><td>SMP-CONF:B</td><td>SC=0,SMP=</td><td>0,CA</td><th>RD=1,LINK=7</th><td>;</td><td></td></lgicbs<></lgicbs 	M>] ADD- M>]]	SMP-CONF:B	SC=0,SMP=	0,CA	RD=1,LINK=7	;	

Fig. 4.3-151 SMP Configuration Addition Display

4.3.8.23. SMP Configuration Deletion

- Command RMV-SMP-CONF :BSC=a ,SMP=b;
- Input RMV-SMP-CONF: BSC=0, SMP=0;
- Output

	м							
			Ca	omm	and	Wind	dow	
MSC(0x0 M2 COMPLET	00) 2001– 722 REMO LOCATION SMP_O IS TED	06-18 VE SMP : BSC REMOV	20:49 CONFI O/CARI ED	:07 GURATI D_1/LI	0N NK_7			
<u>1</u> 0 <u>L</u> 0	NG <u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS	
[jhparl ACCEPTI [jhparl	< <lgicbs D < <lgicbs< td=""><td>M>] RM M>][</td><td>IV-SMP-(</td><td>CONF:B</td><td>SC=0,S</td><th>MP=0;</th><th></th><td></td></lgicbs<></lgicbs 	M>] RM M>][IV-SMP-(CONF:B	SC=0,S	MP=0;		

Fig. 4.3-152 SMP Configuration Deletion Display

4.3.8.24. VMP Configuration Addition

- Command ADD-VMP-CONF :BSC=a ,VMP=b ,CARD=c ,LINK=d;
- Input ADD-VMP-CONF: BSC=0,VMP=0,CARD=1,LINK=5;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:51:26 M2731 ADD VMP CONFIGURATION LOCATION : BSC_0/CARD_1/LINK_5 VMP_0 IS ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-VMP-CONF:BSC=0,VMP=0,CARD=1,LINK=5; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-153 VMP Configuration Addition Display

4.3.8.25. VMP Configuration Deletion

- Command RMV-VMP-CONF :BSC=a ,VMP=b;
- Input RMV-VMP-CONF: BSC=0, VMP=0;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:50:58 M2732 REMOVE VMP CONFIGURATION LOCATION : BSC_0/CARD_1/LINK_5 VMP_0 IS REMOVED COMPLETED	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] RMV-VMP-CONF:BSC=0,VMP=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-154 VMP Configuration Deletion Display

4.3.8.26. BTS Configuration Addition

• Command ADD-BTS-CONF :BSC=a ,BTS=b ,B_TYPE=c ,SECT_EQP=d ,SECT_RANGE=e ,ALMA=f ,ALPA=g ,ALPA_LINK=h ,LICA=i ,LICA_LINK=j ,FA0_CH_NUM=k ,PN_ALPHA=1 [,PN_BETA=m] [,PN_GAMMA=n] [,PN_DELTA=o] [,PN_EPSILON=p] [,PN_ZETA=q] [,PA_TYPE=r] [,ANT_TYPE=s] [,LNA_TYPE=t] [,RISA_EQP=u] [,BOTA_EQP=v];

• Input Input ADD-BTS-CONF: BSC=0, BTS=0; -> ADD-BTS-CONF: BSC=1, BTS=0,B_TYPE=STANDARD,SECT_EQP=OMNI;

• Output

XINTERM	. 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:54:20 M2741 ADD BTS CONFIGURATION LOCATION : BSC_1/ALMA_0/ALPA_0/LINK_0 BTS_0[STANDARD]/FA_1/OMNI/LICA_0/LINK_0 IS ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-BTS-CONF:BSC=1,BTS=0,B_TYPE=STANDARD,SECT_EQP=0 ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	HN I



4.3.8.27. BTS Configuration Deletion

- Command RMV-BTS-CONF :BSC=a ,BTS=b;
- Input RMV-BTS-CONF: BSC=1, BTS=0;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-20 17:58:36 M2742 REMOVE BTS CONFIGURATION LOCATION : BSC_1/ALMA_0/ALPA_0/LINK_0 BTS_0 IS REMOVED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] RMV-BTS-CONF:BSC=1,BTS=0; ACCEPTED [jhpark <lgicbsm>] [</lgicbsm></lgicbsm>	

Fig. 4.3-155 BTS Configuration Deletion Display

4.3.8.28. SECTOR Configuration Addition

- Command ADD-SECT-CONF :BSC=a ,BTS=b ,SECT=c ,PN=d;
- Input ADD-SECT-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.29. SECTOR Configuration Deletion

- Command RMV-SECT-CONF :BSC=a ,BTS=b ,SECT=c;
- Input RMV-SECT-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.30. FA Configuration Addition

- Command ADD-FA-CONF :BSC=a ,BTS=b ,FA=c ,CH_NUM=d;
- Input ADD-FA-CONF: BSC=1, BTS=0,FA=0,CH_NUM=25;
- Output

	K	_ 🗆 ×
Command Window		
MSC(0x00) 2001-06-18 21:04:01 M2761 ADD FA CONFIGURATION LOCATION : BSC_1/BTS_0/FA_0 IS ADDED COMPLETED		
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>		
[jhpark <lgicbsm>] ADD-FA-CONF:BSC=1,BTS=0,FA=0,CH_NUM=25; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>		

Fig. 4.3-156 FA Configuration Addition Display



4.3.8.31. FA Configuration Deletion

- Command RMV-FA-CONF :BSC=a ,BTS=b ,FA=c;
- Input RMV-FA-CONF: BSC=1, BTS=0,FA=0;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 21:02:53 M2762 REMOVE FA CONFIGURATION LOCATION : BSC_1/BTS_0/FA_0 IS REMOVED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] RMV-FA-CONF:BSC=1,BTS=0,FA=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-157 FA Configuration Deletion Display



4.3.8.32. BSC-BTS TRUNK Configuration Addition

- Command ADD-TRNK-CONF :BSC=a ,BTS=b ,ALMA=c ,ALPA=d ,ALPA_LINK=e ,LICA=f ,LICA_LINK=g ,ALLOC_TYPE=h;
- Input ADD-TRNK-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.33. BSC-BTS TRUNK Configuration Deletion

- Command RMV-TRNK-CONF :BSC=a ,BTS=b ,ALMA=c ,ALPA=d ,ALPA_LINK=e ;
- Input RMV-TRNK-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.34. CAN PVC Configuration Addition

- Command ADD-CAN-PVC :NODE_A=a ,NODE_B=b ,VPCI_A=c ,VPCI_B=d [,NO_VC=e] ;
- Input ADD-CAN-PVC: NODE_A=CTYPE_BSM_A, NODE_B=CTYPE_CNP_A, VPCL_A=0, VPCL_B=0;
- Output

XINTERM	
Command Window	
MSC(0x00) 2001-06-18 21:09:32 M2781 ADD CAN PVC CONFIGURATION NUN OF ENTRY : 476 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B V O EQUIP BSM_A O CNP_A O 1 87 EQUIP CNP_A O BSM_A O 1 COMPLETED	✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓
4	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVI</u>	COUS
[jhpark <lgicbsm>] ADD-CAN-PVC:NODE_A=CTYPE_BSM_A,NO ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	DDE_B=CTYPE_CNP_A, VPC

Fig. 4.3-158 CAN PVC Configuration Addition Display

4.3.8.35. CAN PVC Configuration Deletion

- Command RMV-CAN-PVC :INDEX=a;
- Input RMV-CAN-PVC: INDEX=0;
- Output

>			
Command Window			
	MSC(0x00) 2001-06-18 21:07:17 M2782 REMOVE CAN PVC CONFIGURATION NUN OF ENTRY : 474 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 N_EQP BSM_A 0 CNP_A 0 1 STM-1 256 87 N_EQP CNP_A 0 BSM_A 0 1 STM-1 256 COMPLETED		
	<u>IO</u> <u>LDNG</u> <u>CDM</u> <u>STM</u> <u>T</u> EST <u>NO.7</u> ST <u>A</u> T AL <u>F</u> T PREVIOUS		
	[jhpark <lgicbsm>] RMV-CAN-PVC:INDEX=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>		
ŀ			

Fig. 4.3-159 CAN PVC Configuration Deletion Display

4.3.8.36. CPN PVC Configuration Addition

- Command ADD-CPN-PVC :NODE_A=a ,NODE_B=b ,VPCI_A=c ,VPCI_B=d [,NO_VC=e] ;
- Input ADD-CPN-PVC:NODE_A=CTYPE_CAN_A, NODE_B=CTYPE_CAN_B, VPCI_A=0, VPCI_B=0;
- Output

>	<pre><interm< pre=""></interm<></pre>			
Γ	Command Window			
	MSC(0x00) 2001-06-18 21:12:49 M2783 ADD CPN PVC CONFIGURATION NUN OF ENTRY : 144 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 EQUIP CAN_A 0 CAN_B 0 1 STM-1 256 75 EQUIP CAN_B 0 CAN_A 0 1 STM-1 256 COMPLETED			
Γ	<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>			
	[jhpark <lgicbsm>] ADD-CPN-PVC:NODE_A=CTYPE_CAN_A,NODE_B=CTYPE_CAN_B,VPC ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>			
ŀ				

Fig. 4.3-160 CPN PVC Configuration Addition Display

4.3.8.37. CPN PVC Configuration Deletion

- Command RMV-CPN-PVC :INDEX=a;
- Input RMV-CPN-PVC: INDEX=0;
- Output

	x		
Command Window			
MSC(0x00) 2001-06-18 21:10:10 M2784 REMOVE CPN PVC CONFIGURATION NUN OF ENTRY : 142 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 N_EQP CAN_A 14 CNP_A 0 1 STM-1 256 75 N_EQP CNP_A 0 CAN_A 14 1 STM-1 256 COMPLETED	Ā		
<u>IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS</u>			
[jhpark <lgicbsm>] RMV-CPN-PVC:INDEX=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	A T		

Fig. 4.3-161 CPN PVC Configuration Deletion Display

4.3.8.38. BSC PVC Configuration Addition

- Command ADD-BSC-PVC :BSC=a ,NODE_A=b ,NODE_B=c ,VPCI_A=d ,VPCI_B=e [,NO_VC=f];
- Input ADD-BSC-PVC:BSC=0, NODE_A=CTYPE_CCP_A, NODE_B=CTYPE_CCP_B, VPCI_A=0, VPCI_B=0;
- Output

2				
	Command Window			
	MSC(0x00) 2001-06-18 21:14:27 M2785 ADD BSC PVC CONFIGURATION LOCATION : BSC_0 NUN OF ENTRY : 972 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 EQUIP CCP_A 0 CAN 0 1 STM-1 256 907 EQUIP CAN 0 CCP_A 0 1 STM-1 256 COMPLETED			
Ī	<u>IO L</u> DNG <u>CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS			
	[jhpark <lgicbsm>] ADD-BSC-PVC:BSC=0,NODE_A=CTYPE_CCP_A,NODE_B=CTYPE_CAN ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>			

Fig. 4.3-162 BSC PVC Configuration Addition Display
4.3.8.39. BSC PVC Configuration Deletion

- Command RMV-BSC-PVC :BSC=a ,INDEX=b;
- Input RMV-BSC-PVC:BSC=0, INDEX=0;
- Output

Command Window	
MSC(0x00) 2001-06-18 21:13:22 M2786 REMOVE BSC PVC CONFIGURATION LOCATION : BSC_0 NUN OF ENTRY : 970 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK 0 N_EQP CCP_A 0 CAN 14 1 STM-1 256 907 N_EQP CAN 14 CCP_A 0 1 STM-1 256 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>	
[jhpark <lgicbsm>] RMV-BSC-PVC:BSC=0,INDEX=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-163 BSC PVC Configuration Deletion Display



4.4. STATUS COMMAND

4.4.1 PROCESSOR STATUS CONTROL

Table 4.4-1 Processor Status LIST

Status Types	Definition
NORM	NORMAL
ABNM	Abnormal
DCPY	Dual Copy
LDNG	StandBy Loading
NORM(OLD)	Normal (After StandBy Loading, Old version)
NORM(NEW)	Normal (After StandBy Loading, New version)
ABN_K	Abnormal with Keep Alive Fault
ABN_1	Abnormal with Process Isolation
UNDEF	Undefined Status

4.4.1.1. BSM CAN PROCESSOR STATUS DISPLAY COMMAND

Function to display processor status inserted in CAN. Command : DIS-CAN-PRC; Input : DIS-CAN-PRC;

		Com	mand V	Vindow	
			internet i		F
MSC(0x00) 2001	-06-14 PLAY C	11:07:09	R STATUS		lĺ
PROC	ACT	A_STS	B_STS	PROC_TYPE	
PNP	Ä	NORM	ABNM	DUAL	
PCPOO PMPOO	A *	ABNM	ABNM	DUAL DUAL	
PCP01	*	ABNM_K	ABNM_K	DUAL	
AMPOO	A	NORM	****	SINGLE	
AMP01 COMPLETED	A	NORM	****	SINGLE	

Fig. 4.4-1 Result of CAN Processor Status Display Command

4.4.1.2. BSC Processor Status Display Command

Function to display Processor status inserted in BSC.

Command : DIS-BSC-PRC[:BSC=a];

a : BSC Number(0~11)

Input : DIS-BSC-PRC:BSC=0;

XINTERM						
		C	omman	d Winde	2W	
MSC(0x00) 20 M3001 D BSC 00 COMPLETED	01-06-14 ISPLAY BS PROC CCP SCP NCP ALP SMP00 VMP00	11:31 SC PROC ACT A A A A A A A	:36 ESSOR STAT A_STS NORM NORM NORM NORM NORM	US ABNM ABNM ABNM ABNM *****	PROC_TYPE DUAL DUAL DUAL DUAL SINGLE SINGLE	

Fig. 4.4-2 Result of BSC Processor Status Display

4.4.1.3. Status Display Command of BTS Processor

Function to display operation status of processors mounted in all the BTSs within the corresponding BSC or in each BTS

Command : DIS-BTS-STS:BSC=a[,BTS=b];

a:BSC Number (0~11)

b:BTS Number (0~47)

Input : DIS-BTS-STS:BSC=0,BTS=0;

Output

×	INTERM	
	Command Window	
	SC(0x00) 2001-06-14 11:34:43 M3002 DISPLAY BTS PROCESSOR STATUS LOCATE = BSCO0/BTSO0/GHJEONG [STANDARD] PROC ACT A_STS B_STS PROC_TYPE BSP A NORM ABNM NUAL BPP A NORM **** SINGLE CRP A NORM ABNM DUAL RCPOO A NORM ABNM DUAL OMPLETED	



4.4.1.4. Processor Restart Command

Function to restart Processors

- Command : RST-PRC[:BSC=a][,BTS=b],RANGE=c,SIDE=d,CLS=e;
 - a : BSC Number (0~11)
 - b : BTS Number (0~47)
 - c : Scope of restart(All the Processors of CCP, PNP,NCP,PCP., etc.)
 - d: Side to restart (A,B,BOTH)
 - e : Class (RESTART, REBOOT, FLASH)

RESTART : Restart O/S and receive loading of PLD only.

<u>**REBOOT</u></u> : It executes BOOTER. In case of the processors equipped with Flash ROM, they check upper level processors and version of each block. If they are different, they receive loading from the upper level processors. However, if they are the same, they do not receive loading from the upper level processors. The processors with no Flash ROM receive loading from the upper level processors without checking version.</u>**

. For reference, Active Side before and after reboot does not change.

FLASH : Delete Flash content of the Processor with Flash ROM equipped and reboot it to receive loading of all the files from the upper level Processor. For reference, Active Side before and after Flash Reboot changes.

Input : RST-PRC:BSC=0,BTS=0,RANGE=RCP00,SIDE=A,CLS=RESTART;





4.4.1.5. CAN Processor H/W RESET(ISOLATION) COMMAND

Function to reset CAN Processor H/W.

Command : RMT-CAN-PRC:PROC=a,SIDE=b,CLS=c;

a: Processor Name : CNP,PNP,PCP,PMP

b: Side : A,B

c: CLASS : HARDRST, ISOLAT, UNISOL

HARDRST : Function to reset Processor on H/W Level (using Register Setting).

ISOLAT : Function to isolate Processor on H/W Level (maintaining Status of RESET)

UNISOL : Function to release the isolation

Input : RMT-CAN-PRC:PROC=PNP,SIDE=A,CLS=ISOLAT;

× INTERM	
Command W	indow
MSC(0x00) 2001-06-14 16:24:27 M3020 MMC CAN PROCESSOR H/W RESET LOCATE = CAN/PNP_A CLASS = ISOLAT RESULT = 0.K COMPLETED	

Fig. 4.4-5 CAN Processor H/W Command Result

4.4.1.6. BSC Processor H/W RESET(ISOLATION) COMMAND

Function to reset BSC Processor H/W.

Command : RMT-BSC-PRC:BSC=a,PROC=b,[SIDE=c],CLS=d;

a: BSC Number

b: Processor Name : CCP,NCP,SCP,ALP,SMP,VMP

c: Side : A,B

d: CLASS : HARDRST, ISOLAT, UNISOL

 ${\rm HARDRST}$: Function to RESET Processor on ${\rm H/W}$ Level (using Register Setting).

ISOLAT : Function to isolate Processor on H/W Level (RESET Status maintained)

UNISOL : Function to release isolation



Input : RMT-BSC-PRC:BSC=0,PROC=NCP,SIDE=A,CLS=ISOLAT;



4.4.1.7. BTS Processor H/W RESET(ISOLATION) COMMAND

Function to BSC Processor H/W.

Command : RMT-BTS-PRC:BSC=a,BTS=b,PROC=c,[SIDE=d],CLS=e;

- a: BSC Number
- b: BTS Number

c: Processor Name : BSP, BPP, CRP, RCP(00~05)

- d: Side : A.B
- e: CLASS : HARDRST, ISOLAT, UNISOL

HARDRST : Function to RESET Processor on H/W Level (using Register Setting).

- : Function to isolate Processor on H/W Level (RESET Status maintained) ISOLAT
- UNISOL : Function to release isolation

Input : RMT-BTS-PRC:BSC=0,BTS=0,PROC=BSP,SIDE=A,CLS=ISOLAT;

Comm	and Window	
MSC(0x00) 2001-06-14 16:39:45 M3022 MMC BTS PROCESSOR H/W LOCATE = BSCO0/BTSO0/BSP_E CLASS = ISOLAT RESULT = 0.K COMPLETED	RESET	

Fig. 4.4-7 BTS Processor H/W RESET(ISOLATION) Command Display Result

4.4.1.8. Processor Switch Over(Switch) Command

Function to Switch over Processor.

Switching Over Command is executed for duplicated Processors and is performed only when both sides of Processors are in a normal status. Command : SWT-PRC [:BSC=a] [,BTS=b] ,PROC=c;

a: BSC Number

b: BTS Number

c: Processor Name : CNP, PNP, PCP00, PCP01,PCP02, PMP00, PMP01,PMP02,

CCP, NCP, SCP, ALP, BSP, CRP, RCP00, RCP01, RCP02, RCP03, RCP04, RCP05

Input : SWT-PRC :BSC=1 ,PROC=CCP;

XINTERM	_ 🗆 🗵
Command Window	
MSC(0x00) 2001-06-30 10:42:32 M3030 MMC SWITCH OVER PROCESSOR RESULT LOCATE = BSC01/CCP RESULT = 0.K COMPLETED	

Fig. 4.4-8 Processor Switch Over(Switch) Command Display Result

4.4.2. Network Status Control

Status Types	Definition	Description
NORM	Normal	Normal Operation
	Normal Act	While normally operated, Act Status is
NUN_A		maintained (Duplicated node)
	Normal Standby	While normally operated, Standby Status is
NUK_S		maintained (Duplicated node)

Table 4.4-2 Network Node Status LIST



ABN_D	Abnormal Deletion	card is removed
ABN_F	Abnormal Fault	Local Fault occurred
ABN_M	Abnormal MMC Block	Blocked Status by User's MMC
		Even equipped to PLD, a processor managing the
INIT	Initial	corresponding device does not normally operate
		until now
		Based on judgment that a normal call is
AB_0B	Abnormal Online Block	impossible due to faults in other devices, the
		corresponding device is blocked
N_EQP	Not Equipped	Card Type is not defined in PLD

4.4.2.1. Network Status Display Command

Function to display the BSS Network status.

Command : DIS-NET-STS: [BSC=a],[BTS=b],SHELF=c,CARD=d,ID=e,[CHIP=f];

- a: BSC Number
- b: BTS Number
- c: SHELF NAME(CAMU,CAMDU,ASMU,ALSU,BANU)
- d: CARD NAME(ASCA, ASIA, AOTA, ATSA, ALMA, LICA)
- e: CARD ID(0~3)
- f: CHIP Number($0 \sim 1$)

Input : DIS-NET-STS:BSC=0,BTS=0,SHELF=BANU,CARD=LICA,ID=0;

					_ 🗆 ×
		C	omma	nd Window	
M3100 DIS LOCATE CARDID LICA00	PLAY N = BSC0 LINK 00 01 02 03 04 05 06 07 08 09 10 112 13 14 15	ETWORK 0/BTS00 TYPE STM-1 STM-1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1	LINK STA BANU/LI STATUS NOR_A ABN_F	ATUS ICAOO/CHIPOO DESTINATION CRP CRP ALPA ALPA ALPA ALPA ALPA ALPA ALPA AL	
A					

Fig. 4.4-9 Result of Network Status Display Command

4.4.2.2. ALPA Network Status Display

Function to display the ALPA Network status.

Command : DIS-ALPA-STS:BSC=a,ALMA=b,ALPA=c;

- a: BSC Number(0~11)
- b: ALMA ID(0~1)
- c∶ ALPA ID(0~4)

			<u> </u>									
	Command Window											
M3101 DTS	DIAV A	I PA NET	WORK ITN	IK STATUS								
LOCATE	= BSCO	O ALSUZ	ALMAGO A	LPAOD								
CARDID	LINK	TYPE	STATUS	DESTINATION								
ALPAOO	00	STM-1	NOR_A	ALMA								
	01	STM-1	ABN_F	ALMA								
	00	E1	NOR_A	BTSOO								
	01	E1	NOR_A	BTS01								
	02	E1	ABN_F	BTSOO								
	03	E	ABN_F	****								
	04		ABN_F	****								
	05	E 1 E 1	ADN_F	****								
	07	F1	ARN F	****								
	08	Fi	ABN F	****								
	09	Ēİ	ABN F	****								
	10	Ēİ	ABN F	****								
	11	E1	ABN_F	****								
	12	E1	ABN_F	****								
	13	E1	ABN_F	****								
	14	E1	ABN_F	****								
	15	E1	ABN_F	****								
COMPLETED												
E1												
121	_											

Fig. 4.4-10 Result of ALPA Network Status Display

4.4.2.3. PDSN NODE Status Display

Function to display the PDSN NODE Network status Command : DIS-PDSN-STS:SHELF=a,PIP=b; a: SHELF(PCP(00~02),PMP(00~02)) b: PIP(0~10)

Input : DIS-PDSN-STS:SHELF=PCP00,PIP=0;

Command Window										
MSC(0x00) 200 M3105 DI LOCATE PDSN 00	1-06-14 1 SPLAY PDSN = CAN/PCP ID STS 00 NORM 05 N_EQP 10 N_EQP 15 N_EQP	7:05:56 NODE STAT 00_PIP00 ID STS I 01 N_EQP 0 06 N_EQP 1 11 N_EQP 1 16 N_EQP 1	TUS ID STS 12 N_EQP 17 N_EQP 2 N_EQP 7 N_EQP 7 N_EQP	ID STS 03 N_EQP 08 N_EQP 13 N_EQP 18 N_EQP	ID STS 04 N_EQP 09 N_EQP 14 N_EQP 19 N_EQP					
01 COMPLETED	00 N_EQP 05 N_EQP 10 N_EQP 15 N_EQP	01 N_EQP 0 06 N_EQP 0 11 N_EQP 1 16 N_EQP 1	12 N_EQP 17 N_EQP 2 N_EQP 7 N_EQP	03 N_EQP 08 N_EQP 13 N_EQP 18 N_EQP	04 N_EQP 09 N_EQP 14 N_EQP 19 N_EQP	₹				

Fig. 4.4-11 Result of PDSN NODE Status Display

4.4.2.4. PCFU Network Status Display Command

Function to display the PCFU Network Status Command : DIS-PCF-NET:PROC=a,TYPE=b; a: PCP(00~02),PMP(00~02) b: PIP_FERA,FETA_PDSN

Input : DIS-PCF-NET:PROC=PCP00,TYPE=PIP_FERA;

	. <u> </u>										
Command Window											
MSC(0x00) 2001-06-14 17:12:00 M3106 DISPLAY PCFU NETWORK STATUS LOCATE = CAN/PCP00/PIP_FERA_FE DEV STS A_PORT B_PORT PIP00 NORM NORM ABN_F PIP01 ABN_D ABN_D ABN_D PIP02 ABN_D ABN_D ABN_D PIP03 ABN_D ABN_D ABN_D PIP04 ABN_D ABN_D ABN_D PIP05 ABN_D ABN_D ABN_D PIP07 ABN_D ABN_D ABN_D PIP07 ABN_D ABN_D ABN_D PIP08 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D											



4.4.2.5. ALPA Network Block Command

Function to block the ALPA Network. Command : BLK-ALPA:BSC=a,ALMA=b,ALPA=c,[TYPE=d],[LINK=e]; a: BSC Number(00~11) b: ALMA ID(0~1) c: ALPA ID(0~4) d: TYPE(STM_1,E1) e: LINK(0~15)

Input : BLK-ALPA:BSC=0,ALMA=0,ALPA=0,TYPE=STM_1,LINK=0;



Fig. 4.4-13 Result of ALPA Network Block Command

4.4.2.6. UNBlock Command

Function to unblock the ALPA Network.

Command : UBLK-ALPA:BSC=a,ALMA=b,ALPA=c,[TYPE=d],[LINK=e];

- a: BSC Number(00~11)
- b: ALMA ID(0~1)
- c: ALPA ID(0~4)
- d: TYPE(STM_1,E1)
- e: LINK(0~15)

Input : UBLK-ALPA:BSC=0,ALMA=0,ALPA=0,TYPE=STM_1,LINK=0;



×INTERM	
Command Window	
MSC(0x00) 2001-06-14 17:31:58 M3121 MMC UNBLOCK ALPA LINK RESULT LOCATE = BSCOO_ALSU/ALMAOO/ALPAOO/STM-1/LINKOO RESULT = UNBLOCKED COMPLETED	<u> </u>
4	



4.4.3. Can Device Status Control

Status	Definition									
Types										
NORM	Normal									
ABN_D	Abnormal Deletion (Even equipped to PLD, it is in the Status of Removal)									
ABN_F	Abnormal Fault(Status that normal operation is impossible due to fault in									
	Device)									
	(Test: As a result of DSP Chip Hardware Test, NOK occurs)									
ABN_M	Abnormal MMC Block									
INIT	Initial (Even equipped to PLD, a processor managing the corresponding									
	device does not operate normally until now)									
AB_0B	Abnormal Online Block (Based on judgment that a normal call is impossible									
	due to faults in other devices, the appropriate device is blocked)									
IDLE	Even if it is normal, call resources are not allocated (CE, VCE)									
BUSY	normal and call resources are allocated (CE, VCE)									
N_EQP	Status defined as Not Equipped to PLD									
READY	Even if not defined in PLD, Device is inserted									
UNDEF	Status that un-defined status is inserted									
ABN_I	Status being separated as H/W Reset									
ABN_B	BER Test Status by User									
NOR_PB	In case that call exists when CHC, Chip is blocked, it indicates the status that									
	awaits until a call is terminated									
	it indicates, the Status that waits for the call termination in order to perform									

Table 4.4-3 DEVICE Status List

	the Vocoder test.
REDNCY	In duplicated Device, it indicates Redundancy Status of Standby side(FETA
	Only)
CB_OPN	For device that is managed only as fault, it is the case that is opened to Fault
	Cable
CLK_F	During Vocoder Channel test, as a result of Timing-Module test, NOK
	occurred (Test Only)
TSW_F	As a result of TSLU Loopback test, NOK is occurred (Test Only)
ABN_AT	Status in which Vocoder is put to Automatic(Online) test (Test Only)
ABN_MT	Status in which Vocoder Manual(Ondemand) is put to test (Test Only)
QAT0_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 0 (Test Only)
QAT1_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 1 (Test Only))
QAT2_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 2 (Test Only)
QAT3_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 3 (Test Only)
VPLB_F	As a result of VCPA Loopback test during Vocoder channel test, NOK
	occurred (Test Only)
VMLB_F	As a result of VCMA Loopback test during Vocoder channel test, NOK
	occurred (Test Only)
VLLB_F	As a result of VLIA Loopback test during Vocoder channel test, NOK occurred
	(Test Only)

4.4.3.1. CAN Device Status Display Command

Function to display Device(BOARD) Status mounted to CAN Command : DIS-CAN-DEV:PROC=a; a : CNP,PNP,PCP(00~02)

Input : DIS-CAN-DEV:PROC=CNP;

Command Window													
Command In made													
MSC(0x00) 2001	-06-1	4 17:4	5:57										
M3600 DISPLAY CAN DEVICES STATUS													
LOCATE	= CAN	I/CNP											
DEV	STS												
AOTAOO	ABN_D												
AUTAUT	NUKM												
DEV	АСТ	а сте	р ете										
ATSAOO	*	N FOD	N FOD										
ASCADO	А	NORM	ARN D										
DEV	ACT	A_STS	B_STS	DEV	ACT	A_STS	B_STS						
ASIAOO		NORM	ABN_D	ASIA01		ABN_D	ABN_D						
APCOO	A	NORM	ABN_D	APCOO	*	ABN_D	ABN_D						
APC01	A	NORM	ABN_D	APC01	*	ABN_D	ABN_D						
ASIA02		ABN_D	ABN_D	ASIA03		N_EQP	N_EQP						
APCOO	**	ABN_D	ABN_D	APCOO	**	N_EQP	N_EQP						
	*	ABN_D	ABN_D	APCU1	*	N_EQP	N_EQP						
COMPLETED													

Fig. 4.4-15 Result of CAN Device Status Display Command

4.4.3.2. GPS(CAN) Status Display Command

Function to display Device and Information of CAN GPS. Command : DIS-GPS-STS:TYPE=a; a : ALL,GPS_DEV,GPS_INFO

```
Input : DIS-GPS-STS:TYPE=ALL;
```

Command Window	
MSC(0v00) 2001-06-14 17:49:44	
M3601 DISPLAY GPS DEVICES STATUS/INFORMATION	
DEV ACT A_STS B_STS	
GPSR1 A NORM NORM	
GPSD1 NORM	
GPSD2 NORM	
GPSD3 NORM	
GPSD4 NUKM	
# GPSR01 DATA	
GPSR01_A GPSR01_B	
CABLE DELAY Ons Ons	
LATITUDE 37,22.4568'N 37,22.4567'N	
LONGITUDE 126,57,1551'E 126,57,1548'E	
ALITIONE 109.00 m 108.05 m	
TIME MODE : GPS MODE	
TIME DIFF. : 2ns	
TFOM : 2	
OPER. MODE : TIME LOCKED	
# CPSR02 DATA	
GPSR02_A GPSR02_B	
DAC 32449 34806	
LABLE DELAY UNS UNS IATTUDE 37 22 4618'N 37 22 4616'N	
LONGITUDE 126,57.1595'E 126,57.1596'E	
ALTITUDE 105.03 m 105.04 m	
TIME MODE : GPS MODE	
TIME DIFF. : 8ns	
TEOM 2	
OPER. MODE : TIME LOCKED	
TOD : 2001-06-14 17:53:39	
COMPLETED	
4	

Fig. 4.4-16 Result of GPS(CAN) Status Display Command

4.4.3.3. H/W RESET CAN DEVICE Command

Function to reset CAN Device on H/W Level Command : RMT-CAN-DEV:PROC=a,DEV=b,ID=c,[SIDE=d],CLS=e; a: Processor :CNP,PNP,PCP(00~02),PMP(00~02) b: Device Name: ASCA,ASIA,AOTA,ATSA,PIP,FERA,FETA,BCRA c: Device ID : 0~10 d: SIDE:A_SIDE,B_SIDE

e: CLASS : HARDRST,ISOLAT,UNISOL

Input : RMT-CAN-DEV:PROC=PNP,DEV=ASCA,ID=0,SIDE=A_SIDE,CLS=HARDRST;

Command Window	
MSC(0x00) 2001-06-14 17:54:36 M3630 H/W RESET CAN DEVICE LOCATE = CAN_PNP/ASCA00_A CLASS = HARDRST RESULT = 0.K COMPLETED	

Fig. 4.4-17 Result of H/W RESET CAN DEVICE Command

4.4.4. BSC Device Status Control

4.4.4.1. BSC Device Status Display Command

Function to display Status of various Boards mounted to BSC Command : DIS-BSC-DEV:BSC=a,PROC=b; a : BSC Number(00~11) b : PROC Name(NCP,SCP,ALP,SMP(00~04),VMP(00~08))

Input : DIS-BSC-DEV:BSC=1,PROC=NCP;

XINTERN	4								- O ×				
	Command Window												
MSC(0x0 M3	0) 2001 202 DIS LOCATE DEV ATSA00	-06-1 PLAY = BSC ACT A	4 19:00 BSC DEVI 01/NCP A_STS NORM): 42 ICES STAT B_STS ABN_D	US								
	ASCAUU DEV ASIAOO APCOO APCO1	ACT A A	A_STS NORM NORM NORM	ABN_D B_STS ABN_D ABN_D ABN_D	DEV ASIAO1 APCOO APCO1	ACT A A	A_STS NORM NORM NORM	B_STS NORM NORM NORM					
COMPLET	ASIAO2 APCOO APCO1 ED	* *	N_EQP N_EQP N_EQP	N_EQP N_EQP N_EQP	ASIAO3 APCOO APCO1	* *	N_EQP N_EQP N_EQP	N_EQP N_EQP N_EQP	Į				
, []													

Fig. 4.4-18 Result of BSC Device Status Display

4.4.4.2. SLPA Status Display Command

Function to display the SLPA Status

Command : DIS-SLPA-STS:BSC=a,SMP=b,[SLPA=c];

- a : BSC Number(00~11)
- b : SMP Number(00~04)
- c : SLPA Number(00~17)

Input : DIS-SLPA-STS:BSC=0,SMP=0,SLPA=0;

×	NTERM	×										
	Command Window											
C C	SC(0x00) 2001-06-14 19:05:42 M3203 DISPLAY SLPA STATUS LOCATE = BSCOO/SMPOO SLPA SLV CALL STS SLV CALL STS SLV CALL STS SLV CALL STS 00 0 00/00 ABN_M 1 01/00 NORM 2 00/00 ABN_M 3 00/00 ABN_M DMPLETED											



4.4.4.3. VCPA Status Display Command

Function to display the VCPA Status



Command : DIS-VCPA-STS:BSC=a,VMP=b,[VCPA=c];

- a : BSC Number(00~11)
- b : VMP Number($00 \sim 07$)
- c : VCPA Number(00~15)

Input : DIS-VCPA-STS:BSC=0,VMP=0,VCPA=0;

X INTERM														
Command Window														
MSC(0x00) 2 M3204 L0C/ VCI 00 COMPLETED	2001-00 DISPL/ ATE = 1 PA SLV 0 1	6–14 AY V(BSCO(DSP 0 0	19 CPA D/VMI CAL 0 0	:11:31 STATUS POO L STS NORM ABN_M	DSP 1 1	CAL O O	L STS Norm Abn_m	DSP 2 2	CAL O O	L STS Norm Abn_m	DSP 3 3	CALL O O	. STS NORM ABN_M	

Fig. 4.4-20 Result of VCPA Status Display Command

4.4.4.4. E1 LINK Status Display Command

Function to display E1 Link Status of VLIA Command : DIS-E1-STS:BSC=a,VMP=b,[VLIA=c]; a : BSC Number(00~11) b: VMP Number(00~07) c: VLIA Number(00~01)

XINTERM Command Window MSC(0x00) 2001-06-14 19:15:31 M3205 DISPLAY E1 STATUS LOCATE = BSC00/VMP00 E1 02 06 10 VLIA STS E1 STS STS E1 STS E1 00 00 NORM 01 NORM NORM 03 NORI 04 05 07 ABI **ABN** 08 09 ARN ١R 13 ARN COMPLETED - -

Input : DIS-E1-STS:BSC=0,VMP=0,VLIA=0;



Fig. 4.4-21 Result of E1 LINK Status Display Command

4.4.4.5. TS Network LINK Status Display Command

Function to display the Status of TS Network Link of VLIA.

Command : DIS-TS-STS:BSC=a,VMP=b,VLIA=c,E1=d;

- a : BSC Number(00~11)
- b: VMP Number(00~07)
- c : VLIA Number(00~15)
- d:E1 Number(00~15)

Input : DIS-TS-STS:BSC=0,VMP=0,VLIA=0,E1=0;

X INTERM								
Command Window								
MSC(0x00) M3200 E1 00 COMPLETED	2001-06-1 6 DISPLAY CATE = BSC TS STS 00 RSRVD 04 IDLE 08 IDLE 12 IDLE 16 N0_7 20 IDLE 24 IDLE 28 IDLE	4 19:19:2 TS STATUS COO/VMPOO/V TS STS 01 IDLE 05 IDLE 09 IDLE 13 IDLE 13 IDLE 21 IDLE 25 IDLE 29 IDLE	22 /LIA00 TS STS 02 BUSY 06 IDLE 10 IDLE 14 IDLE 18 IDLE 22 IDLE 26 IDLE 30 IDLE	TS STS 03 IDLE 07 IDLE 11 IDLE 15 IDLE 19 IDLE 23 IDLE 27 IDLE 31 IDLE				

Fig. 4.4-22 Result of TS Network LINK Status Display Command

4.4.4.6. VCE(Vocoder Channel Element) Status Display Command

Status Types	Definition	Description				
IDLE Idle		Normal status without a Call				
8K_Qcelp 8k Qcelp Call		8k QCELP Call Seized Status				
8K_EVRC 8k EVRC Call		8k EVRC Call Seized Status				

Table 4.4-4 Vocoder Channel Element Status LIST

13K_Qcelp	13k Qcelp Call	13k QCELP Call Seized Status				
13K_EVRC	13k EVRC Call	13k EVRC Call Seized Status				
ABN_M	Abnormal MMC Block	Blocked Status by user's MMC				
UNDEF	Undefined Status	Status with Input of undefined Status				

Function to display the Channel Element Status of VCE.

Command : DIS-VCE-STS:BSC=a,VMP=b,[VCPA=c];

a : BSC Number(00~11)

b: VMP Number(00~07)

c: VCPA Number(00~15)

Input : DIS-VCE-STS:BSC=0,VMP=0,VCPA=0;

2	XINTERM							na konser			<u>_ 🗆 ×</u>
	Command Window										
	MSC(0x00) 2 M3208	001-0 DTSPI)6-14 AY V(19:22: CODER (: 36 CHANNEL	ELEMENT	STATU	5			
l	LOCA	TE = DSP	BSCO)/VMP00/ STS	VCPADO VCE	STS	VCE	STS	VCE	STS	
l	00 00 00	00 01 02	00 00 00		01 01 01		02 02 02	IDLE IDLE TDLF	03 03 03		
l	00	03 00	00 00	IDLE ABN_M	01 01	IDLE ABN_M	02 02	IDLE ABN_M	03 03	IDLE ABN_M	
l	01	01 02 02	00 00	ABN_M ABN_M	01 01 01	ABN_M ABN_M	02 02 02	ABN_M ABN_M ARN_M	03 03 02	ABN_M ABN_M	
		03			UI		02	MDM_M	05		d
	×										

Fig. 4.4-23 Result of VCE(Vocoder Channel Element) Status Display Command

4.4.4.7. SLPA BLOCK Command

Function to block SLPA. Command : BLK-SLPA:BSC=a,SMP=b,SLPA=c,[SLV=d]; a : BSC Number(00~11) b : SMP Number(00~04) c : SLPA Number(00~17) d : SLV Number(00~03)

Input : BLK-SLPA:BSC=0,SMP=0,SLPA=0,SLV=0;

×INTERM							
Command Window							
MSC(0x00) 2001-06-14 19:28:12 M3210 BLOCK SLPA SLAVE CARD LOCATE = BSC00/SMP00/SLPA00/SLV00 RESULT = BLOCKED COMPLETED	12 2 2						

Fig. 4.4-24 Result of SLPA BLOCK Command

4.4.4.8. SLPA UNBLOCK Command

Function to unblock SLPA. Command : UBLK-SLPA:BSC=a,SMP=b,SLPA=c,[SLV=d]; a : BSC Number(00~11) b : SMP Number(00~04) c : SLPA Number(00~17) d : SLV Number(00~03)

Input : UBLK-SLPA:BSC=0,SMP=0,SLPA=0,SLV=0;



Fig. 4.4-25 Result of SLPA UNBLOCK Command

4.4.4.9. VCPA BLOCK Command

Function to block VCPA. Command : BLK-VCPA:BSC=a,VMP=b,VCPA=c,[SLV=d],[DSP=e]; a : BSC Number(00~11)