4.3.6. Network Parameter Information Change

(Change_Parameter_Info_3)

4.3.6.1. CAN INTER BSC AAL2 Setting Information Change

• Command CHG-CAN-IUR: BSC=a, [BSC0_AAL2=b], [BSC1_AAL2=c], [BSC2_AAL2=d], [BSC3_AAL2=e], [BSC4_AAL2=f], [BSC5_AAL2=g], [BSC6_AAL2=h], [BSC7_AAL2=i], [BSC8_AAL2=j],[BSC9_AAL2=k],[BSC10_AAL2=l], [BSC11_AAL2=m], [N0_AAL2_VC=n];

- Input CHG-CAN-IUR: BSC=0, BSC0_AAL2=255
- Output

Command Window	
MSC(0x00) 2001-06-17 15:28:16 M2522 CHANGE CAN INTER BSC AAL2 CONNECTION RESULT L0CATION : BSC_0 START VC ID [BSC_01] : 0x000b0040 START VC ID [BSC_02] : 0x000b0060 START VC ID [BSC_03] : 0x000b0080 START VC ID [BSC_04] : 0x000b0000 START VC ID [BSC_05] : 0x000b0000 START VC ID [BSC_05] : 0x000b0000 START VC ID [BSC_06] : 0x000b0000 START VC ID [BSC_06] : 0x000b0000 START VC ID [BSC_07] : 0x000b0100 START VC ID [BSC_08] : 0x000b0120 START VC ID [BSC_09] : 0x000b0140 START VC ID [BSC_10] : 0x000b0180 NUM OF VC : 32	
<u>ব</u>	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CAN-IUR:BSC=0,BSC0_AAL2=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

4.3.6.2. CAN INTER BSC AAL5 Setting Information Change

• Command CHG-CAN-BSC: [CAN0_START_AAL5=a],

[CAN1_START_AAL5=b], [NO_AAL5_VC=0~],

a ,b: 0~0xffffff

c: 0∼

- Input CHG-CAN-BSC: CAN0_START_AAL5=255
- Output

XINTERM	_ 🗆 ×
Command Window 🤷	
MSC(0x00) 2001-06-17 15:29:28 M2525 CHANGE CAN INTER BSC AAL2/5 CONNECTION RESULT START AAL5 VC ID [00] : 0x000000ff START AAL5 VC ID [01] : 0x000c0020 NUM OF AAL5 VC : 8160 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CAN-BSC:CANO_START_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	T T
N N	



4.3.6.3. CPN INTER DATA AAL5 Setting Information Change

```
Command CHG-CPN-DATA: [BSC0_AAL5=a], [BSC1_AAL5=b], [BSC2_AAL5=c],
[BSC3_AAL5=d], [BSC4_AAL5=e], [BSC5_AAL5=f],
[BSC6_AAL5=g], [BSC7_AAL5=h], [BSC8_AAL5=i],
[BSC9_AAL5=j], [BSC10_AAL5=k], [BSC11_AAL5=l],
[NO_AAL5_VC=m];
a ~n: BSC AAL5 (32~0xfffff)
m: 0~32
```

- Input CHG-CPN-DATA: BSC0_AAL5=255;
- Output

Command Window	
MSC(0x00) 2001-06-17 15:30:25 M2541 CHANGE CPN DATA AAL5 CONNECTION INFORMATION START VC ID [BSC_00] : 0x0000000ff START VC ID [BSC_01] : 0x00000020 START VC ID [BSC_02] : 0x00000020 START VC ID [BSC_03] : 0x00000020 START VC ID [BSC_04] : 0x00000020 START VC ID [BSC_05] : 0x00000020 START VC ID [BSC_06] : 0x00000020 START VC ID [BSC_06] : 0x00000020	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CPN-DATA:BSC0_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.4. CPN INTER PCF AAL5 Setting Information Change

```
• Command CHG-CPN-PCF: [PIP0_0_AAL5=a], [PIP0_1_AAL5=b], [PIP1_0_AAL5=c],

[PIP1_1_AAL5=d], [PIP2_0_AAL5=e], [PIP2_1_AAL5=f],

[PIP3_0_AAL5=g], [PIP3_1_AAL5=h], [PIP4_0_AAL5=i],

[PIP4_1_AAL5=j], [PIP5_0_AAL5=k], [PIP5_1_AAL5=1],

[PIP6_0_AAL5=m], [PIP6_1_AAL5=n], [PIP7_0_AAL5=o],

[PIP7_1_AAL5=p], [PIP8_0_AAL5=q], [PIP8_1_AAL5=r],

[PIP9_0_AAL5=s], [PIP9_1_AAL5=t], [PIP10_0_AAL5=u],

[PIP10_1_AAL5=v], [NO_AAL5_VC=w]
```

a~v: PIP AAL5 (32~0xffffff)

w: 0~480

- Input CHG-CPN-PCF: PIP0_0_AAL5=255 ;
- Output

Command Window 🗟	
MSC(0x00) 2001-06-17 15:31:37 M2542 CHANGE CPN PCF AAL5 CONNECTION RESULT START VC ID [PIP_00] : 0x000000ff 0x001e0020 START VC ID [PIP_01] : 0x000b0020 0x001f0020 START VC ID [PIP_02] : 0x000c0020 0x00200020 START VC ID [PIP_03] : 0x000d0020 0x00210020 START VC ID [PIP_04] : 0x000e0020 0x00220020 START VC ID [PIP_05] : 0x000f0020 0x00230020 START VC ID [PIP_06] : 0x00100020 0x00240020	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-CPN-PCF:PIPO_0_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.5. BSC INTER BSC AAL2 Setting Information Change

 Command CHG-BSC-IUR: BSC=a, [BSC0_AAL2=b], [BSC1_AAL2=c], [BSC2_AAL2=d], [BSC3_AAL2=e], [BSC4_AAL2=f], [BSC5_AAL2=g], [BSC6_AAL2=h], [BSC7_AAL2=i], [BSC8_AAL2=j], [BSC9_AAL2=k], [BSC10_AAL2=1], [BSC11_AAL2=m], [N0_AAL2_VC=n];
 a : BSC Number(0~11)
 b~m: BSC AAL2 (0~0xfffff)

n: 0~

- Input CHG-BSC-IUR: BSC=0, BSC0_AAL2=255;
- Output

Command Window	
MSC(0x00) 2001-06-17 15:33:09 M2572 CHANGE BSC INTER BSC AAL2 CONNECTION RESULT LOCATION : BSC_0 START VC ID [BSC_01] : 0x000b0040 START VC ID [BSC_02] : 0x000b0060 START VC ID [BSC_03] : 0x000b0080 START VC ID [BSC_04] : 0x000b0080 START VC ID [BSC_05] : 0x000b0080 START VC ID [BSC_06] : 0x000b0080 START VC ID [BSC_06] : 0x000b0080 START VC ID [BSC_06] : 0x000b0080	N Al Al
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-IUR:BSC=0,BSC0_AAL2=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	N N N



4.3.6.6. BSC INTER BTSC AAL2 Setting Information Change

• Command CHG-BSC-IUB: BSC=a, BTS=b, LICA=c, LINK=d,

[LINK0_AAL2=e], [LINK1_AAL2=f], [LINK2_AAL2=g], [LINK3_AAL2=h], [LINK4_AAL2=i], [LINK5_AAL2=j], [LINK6_AAL2=k], [LINK7_AAL2=1], [LINK8_AAL2=m], [LINK9_AAL2=n], [LINK10_AAL2=o], [LINK11_AAL2=p], [LINK12_AAL2=q], [LINK13_AAL2=r], [LINK14_AAL2=s], [LINK15_AAL2=t]

- a : BSC Number(0~11)
- b: BTS Number(0~47)
- c : LICA Number(0~2)
- d : LINK Number(0~15)

 $e \sim t: 0 \sim 0xffffff$

- Input CHG-BSC-IUB: BSC=0, BTS=0, LICA=0, LINK0_AAL2=255;
- Output

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				Ca	omn	nan	d Winde	w			
MSC(0x00) 7 M2573 (LOC/ 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2001-0 CHANGE ATION A LINK 0 1 2 3 4 5 6 7 8	6-17 BSC : BSC 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x	15:34 INTER I _0/BTS_ VC 000000 000000 000000 000000 000000 0000	:06 3TS A _0 1 1 1 1 1 1 1 1 1	AL2 C LINK 0 1 2 3 4 5 6 7 8	ONNECTION I AAL2_VC 0x00000000 0x00000000 0x000000000 0x000000	RESULT LICA 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	LINK 0 1 2 3 4 5 6 7 8	AAL2_VC 0×00000000 0×00000000 0×00000000 0×000000	d
<u>1</u> 0	LDNG	CDM	<u>SIM</u>	TEST	<u>N</u> 0.7	ST <u>A</u>	<u>I ALFI I</u>	KEV10	US		
[jhp ACCE [jhp	ark <l(PTED ark <l(< td=""><td>GICBSM GICBSM</td><td>>] СН >][</td><td>G-BSC-1</td><td>EVB : B</td><td>SC=0,</td><td>BTS=0,LICA</td><td>=O,LIN</td><td>KO_AA</td><td>L2=255;</td><td>N N</td></l(<></l(GICBSM GICBSM	>] СН >][G-BSC-1	EVB : B	SC=0,	BTS=0,LICA	=O,LIN	KO_AA	L2=255;	N N



4.3.6.7. BSC INTER CAN AAL2/5 Setting Information Change

- Command CHG-BSC-CAN: BSC=a, [CAN0_START_AAL5=b], [CAN1_START_AAL5=c], [NO_AAL5_VC=d]
 - a: BSC Number(0~11) b,c: 32~0xffffff d: 0~8160
- Input CHG-BSC-CAN: BSC=0, CAN0_START_AAL5=255;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 15:35:19 M2575 CHANGE BSC INTER CAN AAL2/5 CONNECTION RESULT LOCATION : BSC_0 START AAL5 VC ID [00] : 0x000000ff START AAL5 VC ID [01] : 0x000c0020 NUM OF AAL5 VC : 8160 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-CAN:BSC=0,CAN0_START_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.8. BSC INTER SLB AAL5 Setting Information

 Command CHG-BSC-SLB: BSC=a, [SLP0_AAL5=b], [SLP1_AAL5=c], [SLP2_AAL5=d], [SLP3_AAL5=e], [SLP4_AAL5=f], [SLP5_AAL5=g], [SLP6_AAL5=h], [SLP7_AAL5=i], [SLP8_AAL5=j], [SLP9_AAL5=k], [SLP10_AAL5=l], [SLP11_AAL5=m], [SLP12_AAL5=n], [SLP13_AAL5=o], [SLP14_AAL5=p], [SLP15_AAL5=q], [SLP16_AAL5=r], [SLP17_AAL5=s], [N0_AAL5_VC=t]

a: BSC Number(0~11)

b∼s: 40~0xffffff

t: 0~984

• Input CHG-BSC-SLB: BSC=0, SLP0_AAL5=255;

• Output

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Command Window	
MSC(0x00) 2001-06-17 15:37:11 M2577 CHANGE BSC INTER SLB AAL2/5 CONNECTION RESULT L0CATION : BSC_0 START VC ID [SLPA_00] : 0x0000000ff START VC ID [SLPA_01] : 0x000b0028 START VC ID [SLPA_02] : 0x000c0028 START VC ID [SLPA_03] : 0x000d0028 START VC ID [SLPA_04] : 0x000e0028 START VC ID [SLPA_04] : 0x000e0028	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-SLB:BSC=0,SLP0_AAL5=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.9. BSC INTER VCB AAL5 Setting Information Change

• Command CHG-BSC-VCB: BSC=a,

[VCP0_AAL5=b], [VCP1_AAL5=c], [VCP2_AAL5=d], [VCP3_AAL5=e], [VCP4_AAL5=f], [VCP5_AAL5=g], [VCP6_AAL5=h], [VCP7_AAL5=i], [VCP8_AAL5=j], [VCP9_AAL5=k], [VCP10_AAL5=l], [VCP11_AAL5=m], [VCP12_AAL5=n], [VCP13_AAL5=o], [VCP14_AAL5=p], [VCP15_AAL5=q], [NO_AAL5_VC=r]

a: BSC Number(0~11)

b∼q: 40~0xffffff

r: 0~88

- Input CHG-BSC-VCB: BSC=0, VCP0_AAL5=255;
- Output

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			14	C	omm	and	Wind	dow	
MSC ((0x00) M2578 LOC Sta Sta Sta Sta	2001-0 CHANGE ATION RT VC RT VC RT VC RT VC RT VC	06-17 E BSC ID [\ ID [\ ID [\ ID [\ ID [\ ID [\	15:38 INTER 20 CPA_00 CPA_01 CPA_02 CPA_03 CPA_04	2:19 VCB АА] : Ом] : Ом 2] : Ом 2] : Ом 4] : Ом	AL2/5 C 000000 000b00 000c00 000d00 000d00 000e00	0NNECT: 28 28 28 28 28 28	ION RESULT	
ĪO	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS	
[jhp ACCE [jhp	oark <l EPTED oark <l< td=""><td>GICBSM GICBSM</td><td>⊳] (+ ⊳]]</td><td>IG-BSC-</td><td>VCB : BS</td><td>5C=0,VC</td><td>PO_AAL</td><th>5=255;</th><td></td></l<></l 	GICBSM GICBSM	⊳] (+ ⊳]]	IG-BSC-	VCB : BS	5C=0,VC	PO_AAL	5=255;	

4.3.6.10. BSC INTER ALB AAL5 Setting Information Change

• Command CHG-BSC-ALB: BSC=a,

[ALMA0_ALP0_0=b], [ALMA0_ALP0_1=c], [ALMA0_ALP1_0=d], [ALMA0_ALP1_1=e], [ALMA0_ALP2_0=f], [ALMA0_ALP2_1=g], [ALMA0_ALP3_0=h], [ALMA0_ALP3_1=i], [ALMA0_ALP4_0=j], [ALMA0_ALP4_1=k], [ALMA1_ALP0_0=l], [ALMA1_ALP0_1=m], [ALMA1_ALP1_0=n], [ALMA1_ALP1_1=o], [ALMA1_ALP2_0=p], [ALMA1_ALP2_1=q], [ALMA1_ALP3_0=r], [ALMA1_ALP3_1=s], [ALMA1_ALP4_0=t], [ALMA1_ALP4_1=u], [N0_AAL5_VC=v]

a: BSC Number(0~11) b~u: 32~0xffffff v: 0~2016

- Input CHG-BSC-ALB: BSC=0, ALMA0_ALP0_0=255;
- Output

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Command Window	
MSC(0x00) 2001-06-17 15:39:23 M2579 CHANGE BSC INTER ALB AAL2/5 CONNECTION RESULT LOCATION : BSC_0 NUM OF VC : 2016 ALMA ALPA AAL5_VC_ID1 AAL5_VC_ID2 0 0 0x000000ff 0x00140020 0 1 0x000b0020 0x00150020 0 2 0x000c0020 0x00160020	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BSC-ALB:BSC=0,ALMA0_ALPO_0=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	



4.3.6.11. BTS INTER RCU AAL5 Setting Information Change

• Command CHG-BTS-RCU: BSC=a, BTS=b, RCU=c,

[LICA0_AAL5=d], [LICA1_AAL5=e], [LICA2_AAL5=f], [LICA0_NO_VC=g], [LICA1_NO_VC=h], [LICA2_NO_VC=i]

- a: BSC Number(0~11)
- b:BTS Number(0~47)
- c: RCU Number(0~9)

d~i: 0~

- Input CHG-BTS-RCU: BSC=0,BTS=0,RCU=0, LICA0_AAL5=255;
- Output





4.3.7. Configuration

Information

Display(Display_Configuration_Data)

This section describes the comands that are used to inquire the configuration information which is related to processors, devices, and overhead channels which are currently used in BTS and BSC.

CRN	MMC	Description
2101	DIG DCC_CONE	BSS configuration information
2101	DI2-D22-COIAL	verification
2102	DIG-SMD-CONE	SMP configuration information
2103	DIS-SMP-COMP	verification
2105	DIS-WMP-CONF	VMP configuration information
2105	DIS-VIMP-CONF	verification
9119	DIC DTC_CONE	BTS configuration information
2112	DIS-DIS-CONF	verification
9115	DIG CHID_CONE	DBPA CHIP configuration information
2110	DIS-CHIF-CONF	verification
2125	DIG OVHD_CONE	OVERHEAD CHANNEL configuration
2120	DIS-OVID-CONF	information verification
0100	DIC DDCN CONE	PDSN configuration information
2133	DIS-PDSN-CONF	verification

Table 4.3-3 Configuration Information Display

4.3.7.1. BSS Configuration Information Verification

This is a command to check the BTS, Processors and PCF counts in the BSC.

- Command DIS-BSS-CONF: BSC=a;
- Input DIS-BSS-CONF: BSC=0;
- Output

	_ 🗆 X
Command Window	
MSC(0x00) 2001-06-17 21:06:14 M2101 DISPLAY BSS CONFIGURATION BSC BTS SMP VMP PCF 0 1 1 1 2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] DIS-BSS-CONF:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-126 BSS Configuration Information Display

4.3.7.2. SMP Configuration Information Verification

• Command DIS-SMP-CONF: BSC=a;

a: BSC Number(#0~11)

- Input DIS-SMP-CONF: BSC=0;
- Output

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Command Window		
MSC(0x00) 2001-06-17 21:07:28 M2103 DISPLAY SMP CONFIGURATION LOCATION : BSC_0 SMP_0 : EQUIP SLP[00] SLP[01] SLP[02] SLP[03] SLP[04] EQUIP EQUIP EQUIP EQUIP SLP[06] SLP[07] SLP[08] SLP[09] SLP[10] EQUIP EQUIP EQUIP EQUIP EQUIP SLP[12] SLP[13] SLP[14] SLP[15] SLP[16] EQUIP EQUIP EQUIP EQUIP EQUIP COMPLETED] SLP[05] EQUIP] SLP[11] EQUIP] SLP[17] EQUIP	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>		
[jhpark <lgicbsm>] DIS-SMP-CONF:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>		
V		

Fig. 4.3-127 SMP Configuration Information Display

4.3.7.3. VMP Configuration Information Verification

• Command DIS-VMP-CONF: BSC=a;

a: BSC Number(#0~11)

- Input DIS-VMP-CONF: BSC=0;
- Output

Command Window	
MSC(0x00) 2001-06-17 21:08:35 M2105 DISPLAY VMP CONFIGURATION LOCATION : BSC_0 VMP_0 : EQUIP VCP[00] VCP[01] VCP[02] VCP[03] EQUIP EQUIP EQUIP EQUIP VCP[04] VCP[05] VCP[06] VCP[07] EQUIP EQUIP EQUIP EQUIP VCP[08] VCP[09] VCP[10] VCP[11] EQUIP EQUIP EQUIP EQUIP VCP[12] VCP[13] VCP[14] VCP[15] EQUIP EQUIP EQUIP EQUIP COMPLETED	
<u>IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-VMP-CONF:BSC=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-128 VMP Configuration Information Display

4.3.7.4. BTS Configuration Information Verification

- Command DIS-BTS-CONF: BSC=a, BTS=b;
 - a: BSC Number(#0~11)
 - b: BTS Number(#0~47)
- Input DIS-BTS-CONF: BSC=0, BTS=0;

• Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:09:41 M2112 DISPLAY BTS CONFIGURATION LOCATION : BSC_0/BTS_0 OVHD_MODE : DYNAMIC PA_TYPE : MPD_LPA ANT_TYPE : STANDARD TX_DIVERSITY : NOT_USE RX_DUPLEX : SIMPLEX RX_DIVERSITY : USE LNA_TYPE : NOR_LNA RISA_EQUIP : EQUIP BOTA_EQUIP : EQUIP COMPLETED	
2	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] DIS-BTS-CONF:BSC=0,BTS=0; ACCEPTED [jhpark <lgicbsm>]</lgicbsm></lgicbsm>	
A	

Fig. 4.3-129 BTS Configuration Information Display

4.3.7.5. DBPA CHIP Configuration Information Verification

- Command DIS-CHIP-CONF: BSC=a, BTS=b;
 - a: BSC Number(#0~11)
 - b: BTS Number(#0~47)
- Input DIS-CHIP-CONF: BSC=0, BTS=0;
- Output

	. 🗆 ×
Command Wildow	
MSC(0x00) 2001-06-17 21:10:34 M2115 DISPLAY CHIP CONFIGURATION LOCATION : ESC_0/BTS_0 NUM_CHIP_PER_CHC : 2 SECTOR_GAIN [ALPHA] : 208, [BETA] : 208, [CAMMA] : 208 [DELTA] : 208, [EPSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 IDELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [BETA] : 320, [GAMMA] : 320 [DELTA] : 320, [EPSILON] : 320, [ZETA] : 320 TX_DIV_TIMING_ADV [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_I0_FORMAT: 0 CELL_RADIUS : 511 REV_INPUT_FORMAT : 1 REV_CELL_MODE : 0 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 CSM_NODE : 1 DIVERSITY_SC_2000 : 3 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
ACCEPTED [jhpark <lgicbsm>] DIS-SMP-CONF:BSC=0; ACCEPTED</lgicbsm>	
<u>م</u>	

Fig. 4.3-130 DBPA CHIP Configuration Information Display

4.3.7.6. OVERHEAD CHANNEL Configuration Information Verification

- Command DIS-OVHD-CONF: BSC=a, BTS=b;
 - a: BSC Number(#0~11)
 - b: BTS Number(#0~47)
- Input DIS-OVHD-CONF: BSC=0, BTS=0;
- Output

×IN	TERM	_ 🗆 ×
	Command Window	
MSC COM	(0x00) 2001-06-17 21:12:20 M2125 DISPLAY OVERHEAD CHANNEL CONFIGURATION MMS REQUEST FAIL TARGET NO RESPONSE PLETED	
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jh ACC [jh	park <lgicbsm>] DIS-OVHD-CONF:BSC=0,BTS=0; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-131 OVHD Channel Configuration Information Display



4.3.7.7. PDSN Configuration Information Verification

- Command DIS-PDSN-CONF: PCP=a;
 - a: PCP Number(#0~2)
- Input DIS-PDSN-CONF: PCP=0;
- Output

×IN	ERM	_ 🗆 ×
	Command Winkow	
MSC COM	0x00) 2001-06-17 21:13:22 M2133 DISPLAY PDSN DATA LOCATION : PCP_0 NUMBER OF PDSN : 1 PDSN[00] IP : 0.0.0.0 NUM_OF_NODE : 1 NODE[00] IP : 10.160.6.66 SSK : LETED	
<u>1</u> 0	LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS	
[jh ACC [jh	ark <lgicbsm>] DIS-PDSN-CONF:PCP=0; PTED ark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-132 PDSN Configuration Information Display



4.3.8. Configuration

Information

```
Change
```

(Change_Configuration_Data)

This section describes commands that are used to add or delete BTS and BSC processors and devices. The configuration information that can be added and deleted are presented below. For the command that has many parameters to input, input image on the inpout Widow. This section does not cover details of each parameter.

CRN	MMC	Description
C2312	CHG-BTS-CONF	BTS configuration information change
C2315		Channel Card Chip configuration
	CHG-CHIP1-CONF	information(1) change
C2317		Channel Card Chip configuration
	CHG-CHIP2-CONF	information(2) change
C2333	ADD-PDSN-CONF	PDSN CONFIG addition
C2334	RMV-PDSN-CONF	PDSN CONFIG deletion
C2335	CHG-PDSN-CONF	PDSN CONFIG change
C2337	ADD-PDSN-NODE	PDSN NODE addition
C2338	RMV-PDSN-NODE	PDSN NODE deletion
C2339	CHG-PDSN-NODE	PDSN NODE change
C2601	MOV-BSC-NODE	BSC Node movement
C2602	MOV-PCF-NODE	PCF Node movement
C2603	MOV-SMP-NODE	SMP Node movement
C2604	MOV-VMP-NODE	VMP Node movement
C2605	MOV-BTS-ID	BTS ID movement
C2606	MOV-BTS-TRNK	BTS TRUNK Node movement
C2607	MOV-LICA-LINK	LICA LINK movement
C2610	MOV-OVHD-CONE	OVERHEAD CHANNEL configuration
		information movement
C2701	ADD-BSC-CONF	BSC configuration addition
C2702	RMV-BSC-CONF	BSC configuration deletion
C2711	ADD-PCF-CONF	PCF configuration addition
C2712	RMV-PCF-CONF	PCF configuration deletion

Table 4.3-4 Configuration Information Change

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C2721	ADD-SMP-CONF	SMP configuration addition	
C2722	RMV-SMP-CONF	SMP configuration deletion	
C2731	ADD-VMP-CONF	VMP configuration addition	
C2732	RMV-VMP-CONF	VMP configuration deletion	
C2741	ADD-BTS-CONF	BTS configuration addition	
C2742	RMV-BTS-CONF	BTS configuration deletion	
C2751	ADD-SECT-CONF	SECTOR configuration addition	
C2752	RMV-SECT-CONF	SECTOR configuration deletion	
C2761	ADD-FA-CONF	FA configuration addition	
C2762	RMV-FA-CONF	FA configuration deletion	
C2771	ADD TDNK CONF	BSC-BTS TRUNK configuration	
	ADD-TRINK-CONF	addition	
C2772	DMV_TDNK_CONE	BSC-BTS TRUNK configuration	
	KINIV I KINK CONF	deletion	
C2781	ADD-CAN-PVC	CAN PVC configuration addition	
C2782	RMV-CAN-PVC	CAN PVC configuration deletion	
C2783	ADD-CPN-PVC	CPN PVC configuration addition	
C2784	RMV-CPN-PVC	CPN PVC configuration deletion	
C2785	ADD-BSC-PVC	BSC PVC configuration addition	
C2786	RMV-BSC-PVC	BSC PVC configuration deletion	

4.3.8.1. BTS Configuration Information Change

- Command CHG-BTS-CONF :BSC=a ,BTS=b [,PA_TYPE=c] [,ANT_TYPE=d] [,ANT_DUP=e] [,RX_DIV=f] [,LNA_EQP=g] [,RISA_EQP=h] [,BOTA_EQP=i];
- Input CHG-BTS-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:25:27 M2312 CHANGE BTS CONFIGURATION LOCATION : BSC_0/BTS_0 OVHD_MODE : DYNAMIC PA_TYPE : PA_NEQ ANT_TYPE : STANDARD TX_DIVERSITY : NOT_USE RX_DUPLEX : SIMPLEX RX_DIVERSITY : USE LNA_TYPE : NOR_LNA RISA_EQUIP : EQUIP BOTA_EQUIP : EQUIP 	
COMPLETED	Į
, A	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] CHG-BTS-CONF:BSC=0,BTS=0,PA_TYPE=PA_NEQ; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-133 BTS Configuration Information Change Display

4.3.8.2. Channel Card Chip Configuration Information (1) Change

Command CHG-CHIP1-CONF :BSC=a ,BTS=b [,SECT_GAIN_A=c] [,SECT_GAIN_B=d] [,SECT_GAIN_G=e] [,SECT_GAIN_D=f] [,SECT_GAIN_E=g] [,SECT_GAIN_Z=h] [,T_DIV_SECT_A=i] [,T_DIV_SECT_B=j] [,T_DIV_SECT_G=k] [,T_DIV_SECT_D=1] [,T_DIV_SECT_E=m] [,T_DIV_SECT_Z=n] [,SECT_T_ADV_A=o] [,SECT_T_ADV_B=p] [,SECT_T_ADV_G=q] [,SECT_T_ADV_D=r] [,SECT_T_ADV_E=s] [,SECT_T_ADV_Z=t] [,T_DIV_T_ADV_A=u] [,T_DIV_T_ADV_B=v] [,T_DIV_T_ADV_G=w] [,T_DIV_T_ADV_D=x] [,T_DIV_T_ADV_E=y] $[,T_DIV_T_ADV_Z=z];$

- Input CHG-CHIP1-CONF:BSC=0, BTS=0,SECT_GAIN_A=255;
- Output

Command Window MSC(0x00) 2001-06-17 21:28:43 M2315 CHANGE BTS DRU CHIP CONFIGURATION(1) L0CATION : BSC_0/BTS_0 NUM_CHIP_PER_CHC : 2 SECTOR_GAIN [ALPHA]: 208, [EPSILON]: 208, [ZETA]: 208 IDELTA]: 208, [EPSILON]: 208, [ZETA]: 208 TX_DIV_SECTOR_GAIN [ALPHA]: 0, [BETA]: 0, [GAMMA]: 0 [DELTA]: 0, [EPSILON]: 0, [ZETA]: 0 SECTOR_TIMING_ADV [ALPHA]: 320, [EPSILON]: 320, [ZETA]: 320 TX_DIV_TIMING_ADV [ALPHA]: 0, [BETA]: 0, [GAMMA]: 320 [DELTA]: 320, [EPSILON]: 320, [ZETA]: 320 TX_DIV_TIMING_ADV [ALPHA]: 0, [BETA]: 0, [GAMMA]: 0 [DELTA]: 0, [EPSILON]: 0, [ZETA]: 0 SECTOR_TX_IO_FORMAT: 0 [DELTA]: 0, [EPSILON]: 0, [ZETA]: 0 SECTOR_TX_IO_FORMAT: 1 REV_INPUT_FORMAT REV_INPUT_FORMAT 1 REV_CELL_MODE 0 MAX_RACH_FRACTION 10
MSC(0x00) 2001-06-17 21:28:43 M2315 CHANGE BTS DRU CHIP CONFIGURATION(1) L0CATION : BSC_0/BTS_0 NUM_CHIP_PER_CHC : 2 SECTOR_GAIN [ALPHA] : 255, [BETA] : 208, [CAMMA] [DELTA] : 208, [EPSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [BETA] : 0, [ZETA] : 0 Sector_TIMINC_ADV [ALPHA] : 320, [BETA] : 320, [CAMMA] : 320 [DELTA] : 320, [BETA] : 320, [ZETA] : 320 [DELTA] : 320, [BETA] : 320, [ZETA] : 320 [DELTA] : 320, [BETA] : 320, [ZETA] : 320 [TX_DIV_TIMINC_ADV [ALPHA] : 320, [ZETA] : 320 [TX_DIV_TIMINC_ADV [ALPHA] : 0, [EPSILON] : 0, [ZETA] : 0 [DELTA] : 320, [EPSILON] : 0, [ZETA] : 320 : 320 [TX_DIV_TIMINC_ADV [ALPHA] : 0, [EPSILON] : 0, [ZETA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 : 0
MAX_RACH_SEPERATE : 80 MAX_REACH_FRACTION : 10 MAX_REACH_SEPERATE : 80 ENABLE_SRCH_WIN_ADJ : 1 MAX_FINGER_CHAN95 : 6 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DIVERSITY_SC_2000 : 3 COMPLETED
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>
[jhpark <lgicbsm>] CHG-CHIP1-CONF:BSC=0,BTS=0,SECT_GAIN_A=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-134 Channel Card Chip Configuration Information (1) Change Display

4.3.8.3. Channel Card Chip Configuration Information (2) Change

• Command CHG-CHIP2-CONF :BSC=a ,BTS=b [,NUM_CHIP=c] [,SECT_T_IO=d] [,CELL_RADIUS=e] [,REV_IN_FORM=f] [,R_CELL_MODE=g] [,MAX_RACH_F=h] [,MAX_RACH_S=i] [,MAX_REACH_F=j] [,MAX_REACH_S=k] [,SRCH_WIN_ADJ=1] [,MAX_CH95=m] [,MAX_CDMA2K=n] [,CSM_MODE=o] [,DIV_SCALE_2K=p];



- Input CHG-CHIP2-CONF: BSC=0, BTS=0,NUM_CHIP=255;
- Output

Command Window MSC(0x00) 2001-06-17 21:30:11 M2317 CHANCE ETS DRU CHIP CONFIGURATION(2) LOCATION : ESC_0/BTS_0 NUM_CHIP_PER_CHC : 255 SECTOR_GAIN [ALPHA] : 255, [BETA] : 208, [CAMMA] : 208 IDELTA] : 208, [EPSILON] : 208, [ZETA] : 208 IX_DIV_SECTOR_GAIN [ALPHA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMINC_ADV [ALPHA] : 320, [EFA] : 320, [ZETA] : 320 IDELTA] : 320, [EFA] : 320, [ZETA] : 320 IDELTA] : 320, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMINC_ADV [ALPHA] : 0, [BETA] : 0, [CAMMA] : 0 DELTA] : 320, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_ID_FORMAT : 0 CELL_RADIUS : 511 REV_INPUT_FORMAT : 1 REV_INPUT_FORMAT : 1 REV_TUPUT_FORMAT : 1 MAX_REACH_EPERATE : 80 MAX_REACH_EPERATE : 80 MAX_REACH_EPERATE : 80 MAX_REACH_SEPERATE : 80 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DIVERSITY_SC_2000 : 3 COMPLETED I LONG_CDM_SIM_IEST_NO.7_STAT_ALFT_PREVIOUS I IDANC_CHIP2-CONF:BSC=0, BTS=0, NUM_CHIP=255; ACCEPTED [jhpark <lgicesm>][</lgicesm>	
MSC(0x00) 2001-06-17 21:30:11 M2317 CHANGE BTS DRU CHIP CONFIGURATION(2) IOCATION : BSC_0/BTS_0 NUM_CHIP_PER_CHC : 255 SECTOR_GAIN [ALPHA] : 255, [BETA] : 208, [ZETA] : 208 IAUPHA] : 0, [EFSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [EFSILON] : 0, [ZETA] : 320 [DELTA] : 320, [EFSILON] : 320, [ZETA] : 320 IDELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_ID_FORMAT : 0 [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_ID_FORMAT : 1 REV_INPUT_FORMAT : 1 MAX_RACH_SEPERATE : 80 MAX_RACH_SEPERATE : 80 MAX_FINGER_CHAN95 : 6 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DUTVERSITY_SC_2000 : 3 COMPLETED IDING CDM STM IEST NO.7 STAT ALFI PREVIOUS [jhpark <lgicesm>] [</lgicesm>	Command Window
IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS [jhpark <lgicbsm>] CHG-CHIP2-CONF:BSC=0,BTS=0,NUM_CHIP=255; ACCEPTED [jhpark <lgicbsm>] []</lgicbsm></lgicbsm>	MSC(0x00) 2001-06-17 21:30:11 M2317 CHANGE BTS DRU CHIP CONFIGURATION(2) LOCATION : BSC_0/BTS_0 NUM_CHIP_PER_CHC : 255 SECTOR_GAIN [ALPHA] : 208, [EPSILON] : 208, [ZETA] : 208 DELTA] : 208, [EPSILON] : 208, [ZETA] : 208 TX_DIV_SECTOR_GAIN [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TIMING_ADV [ALPHA] : 320, [BETA] : 320, [ZETA] : 320 TX_DIV_TIMING_ADV [ALPHA] : 320, [EPSILON] : 320, [ZETA] : 320 TX_DIV_TIMING_ADV [ALPHA] : 0, [BETA] : 0, [GAMMA] : 0 [DELTA] : 0, [EPSILON] : 0, [ZETA] : 0 SECTOR_TX_IO_FORMAT : 0 CELL_RADUS : 511 REV_INPUT_FORMAT : 1 REV_CELL_MODE : 0 MAX_RACH_SEPERATE : 80 MAX_REACH_FRACTION : 10 MAX_REACH_SEPERATE : 80 ENABLE_SRCH_WIN_ADJ : 1 MAX_FINGER_CHAN2000 : 8 CSM_NODE : 1 DIVERSITY_SC_2000 : 3 COMPLETED
[jhpark <lgicbsm>] CHG-CHIP2-CONF:BSC=0,BTS=0,NUM_CHIP=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
	[jhpark <lgicbsm>] CHG-CHIP2-CONF:BSC=0,BTS=0,NUM_CHIP=255; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>

Fig. 4.3-135 Channel Card Chip Configuration Information (2) Change Display

4.3.8.4. PDSN CONFIG Addition

- Command ADD-PDSN-CONF :PCF=a ,PDSN_IDX=b ,PDSN_IP=c;
- Input ADD-PDSN-CONF: BSC=0, BTS=0,PDSN_IP=255.255.255.0;
- Output

	_ 🗆 ×
Comman ^d Window	
MSC(0x00) 2001-06-17 21:31:04 M2333 ADD PDSN IP RESULT PCP_0/PDSN_0/IP_255.255.255.0 ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-PDSN-CONF:PCF=0,PDSN_IDX=0,PDSN_IP=255.255.2 ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	55.0;

Fig. 4.3-136 PDSN Configuration Addition Display

4.3.8.5. PDSN CONFIG Deletion

- Command RMV-PDSN-CONF :PCF=a ,PDSN_IDX=b;
- Input RMV-PDSN-CONF: BSC=0, BTS=0,PDSN_IDX=1;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:32:16 M2334 REMOVE PDSN IP RESULT REPORT PCP_0/PDSN_1 REMOVED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] RMV-PDSN-CONF:PCF=0,PDSN_IDX=1; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	
<u>a</u>	

Fig. 4.3-137 PDSN Configuration Deletion Display

4.3.8.6. PDSN CONFIG Change

- Command CHG-PDSN-CONF :PCF=a ,PDSN_IDX=b ,PDSN_IP=c;
- Input CHG-PDSN-CONF: BSC=0, BTS=0,PDSN_IP=127.0.0.1;
- Output

XIN	TERM	- 🗆 ×
	Command Window	
MSC(COMF	(0x00) 2001-06-17 21:33:22 M2335 CHANGE PDSN IP RESULT PCP_0/PDSN_0/IP_127.0.0.1 PLETED	
10	LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
_ [jh; ACCI [jh;	park <lgicbsm>] CHG-PDSN-CONF:PCF=0,PDSN_IDX=0,PDSN_IP=127.0.0.1; EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-138 PDSN Configuration Change Display

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4.3.8.7. PDSN NODE Addition

- Command ADD-PDSN-NODE :PCF=a ,PDSN_IDX=b ,PDSN_NODE_IDX=c , PDSN_NODE_IP=d ,SSK_VALUE=e
- Input ADD-PDSN-NODE: BSC=0, BTS=0,PDSN_NODE_IDX=0,

PDSN_NODE_IP:128.128.128.128;

• Output

×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 21:34:28 M2337 ADD PDSN NODE RESULT REPORT PCP_0/PDSN_0/NODE_0 ADDED IP : 128.128.128.128 SSK : jhpark COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-PDSN-NODE:PCF=0,PDSN_IDX=0,PDSN_NODE_IDX=0,PDS ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-139 PDSN NODE Addition Display

4.3.8.8. PDSN NODE Deletion

- Command RMV-PDSN-NODE :PCF=a ,PDSN_IDX=b ,PDSN_NODE_IDX=c;
- Input RMV-PDSN-NODE: BSC=0, BTS=0,PDSN_IDX=0,PDSN_NODE_IDX=0;
- Output

×IN1	TERM									_ 🗆 ×
				С	omm	and	Wind	tow		
MSC (COMF	(0x00) M2338 PCP Pleted	2001- REMO' _0/PD	06-17 VE PDS SN_0/M	21:35 SN NODE NODE_O	:35 RESUL REMOVE	T REPO D	RT			
<u>1</u> 0	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS		
[jhp ACCI [jhp	oark <l EPTED oark <l< td=""><td>GICBS GICBS</td><td>M>] RM M>]∏</td><td>IV-PDSN</td><td>-NODE :</td><td>PCF=0,</td><td>PDSN_II</td><td>DX=0,PDSN_NODE</td><td>_IDX=0;</td><td></td></l<></l 	GICBS GICBS	M>] RM M>]∏	IV-PDSN	-NODE :	PCF=0,	PDSN_II	DX=0,PDSN_NODE	_IDX=0;	

Fig. 4.3-140 PDSN NODE Deletion Display



4.3.8.9. PDSN NODE Change

- Command CHG-PDSN-NODE :PCF=a ,PDSN_IDX=b ,PDSN_NODE_IDX=c
 [,PDSN_NODE_IP=d] [,SSK_VALUE=e]
- Input CHG-PDSN-NODE: BSC=0, BTS=0,PDSN_IDX=0,PDSN_NODE_IDX=0,

PDSN_NODE_IP=100.100.0.1, SSK_VALUE=gamdok;

• Output



Fig. 4.3-141 PDSN NODE Change Display

4.3.8.10. BSC Node Movement

- Command MOV-BSC-NODE :T_PROC=a ,BSC=b ,CARD=c ,LINK=d;
- Input MOV-BSC-NODE: T_PROC=CNP,BSC=0,CARD=1,LINK=6;;
- Output

	١×
Command Window	
MSC(0x00) 2001-06-18 17:51:54 M2601 MOVE BSC NODE CONFIGURATION LOCATION : BSC_0 PREVIOUS : CNP/CARD_1/LINK_4 CURRENT : CNP/CARD_1/LINK_6 COMPLETED	AI D
IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS	
[jhpark <lgicbsm>] MOV-BSC-NODE:T_PROC=CNP,BSC=0,CARD=1,LINK=6; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-142 BSC NODE Movement Display

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4.3.8.11. PCF Node Movement

- Command MOV-PCF-NODE :PCF=a ,CARD0=b ,LINK0=c ,CARD1=d ,LINK1=e ,CARD2=f ,LINK2=g ,CARD3=h ,LINK3=i;
- Input MOV-PCF-NODE: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.12. SMP Node Movement

- Command MOV-SMP-NODE :BSC=a ,SMP=b ,CARD=c ,LINK=d;
- Input MOV-SMP-NODE: BSC=0, SMP=0,CARD=1,LINK=6
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 18:00:59 M2603 MOVE SMP NODE CONFIGURATION LOCATION : BSC_0/SMP_0 PREVIOUS : CARD_1/LINK_7 CURRENT : CARD_1/LINK_6 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] MOV-SMP-NODE:BSC=0,SMP=0,CARD=1,LINK=6; ACCEPTED [jhpark <lgicbsm>] [</lgicbsm></lgicbsm>	

Fig. 4.3-143 SMP NODE Movement Display

4.3.8.13. VMP Node Movement

- Command MOV-VMP-NODE :BSC=a ,VMP=b ,CARD=c ,LINK=d;
- Input MOV-VMP-NODE: BSC=0, VMP=0, CARD=1, LINK=6;
- Output

×IN.	ERM	- 🗆 ×
	Command Window	
COMI	XOO) 2001-06-20 21:22:58 M2604 MOVE VMP NODE CONFIGURATION LOCATION : BSC_0/VMP_0 PREVIOUS : CARD_1/LINK_5 CURRENT : CARD_1/LINK_6 .ETED	N A
<u>1</u> 0	<u>LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jh ACC [jh	urk <lgicbsm>] MOV-VMP-NODE:BSC=0,VMP=0,CARD=1,LINK=6; TED urk <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-144 VMP NODE Movement Display
4.3.8.14. BTS ID Movement

- Command MOV-BTS-ID :BSC=a ,OLD_BTS=b ,NEW_BTS=c;
- Input MOV-BTS-ID: BSC=0, OLD_BTS=0,NEW_BTS=2;
- Output

×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-20 21:17:24 M2605 MOVE BTS ID RESULT PREVIOUS : BSC_0/BTS_0 CURRENT : BSC_0/BTS_2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] MOV-BTS-ID:BSC=0,OLD_BTS=0,NEW_BTS=2; ACCEPTED [jhpark <lgicbsm>] ĭ</lgicbsm></lgicbsm>	

Fig. 4.3-145 BTS ID Movement Display

4.3.8.15. BTS TRUNK Node Movement

For this command, execute DIS-TRNK-DATA first to input the parameter value.

- Command MOV-BTS-TRNK :BSC=a ,BTS=b ,OLD_ALMA=c ,OLD_ALPA=d , OLD_ALPA_LINK=e ,NEW_ALMA=f ,NEW_ALPA=g ,NEW_ALPA_LINK=h;
- Input MOV-BTS-TRNK: BSC=0, BTS=2,

OLD_ALMA=0,OLD_ALPA=0,OLD_ALPA_LINK=0, NEW_ALMA=1,NEW_ALPA=1,NEW_ALPA_LINK=1;

• Output

×IN.	TERM	□ ×
	Command Window	
MSC COMI	(OxOO) 2001-06-20 21:26:01 M2606 MOVE BTS TRUNK NODE CONFIGURATION LOCATION : BSC_0/BTS_2 PREVIOUS : ALMA_0/ALPA_0/LINK_0 CURRENT : ALMA_1/ALPA_1/LINK_1 IPLETED	
<u>1</u> 0	<u>LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jh ACCI [jh	park <lgicbsm>] MOV-BTS-TRNK:BSC=0,BTS=2,OLD_ALMA=0,OLD_ALPA=0,OLD_AL EPTED park <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-146 BTS TRUNK Movement display

4.3.8.16. LICA LINK Movement

• Command MOV-LICA-LINK :BSC=a ,BTS=b ,OLD_LICA=c ,OLD_LINK=d ,NEW_LICA=e ,NEW_LINK=f;

• Input MOV-LICA-LINK: BSC=0, BTS=1,

OLD_LICA=0, OLD_LINK=0,

NEW_LICA=1, NEW_LINK=1;

• Output

] ×
Command Window	
MSC(0x00) 2001-06-20 21:35:44 M2607 MOVE BTS LICA LINK CONFIGURATION LOCATION : BSC_0/BTS_1 PREVIOUS : LICA_0/LINK_0 CURRENT : LICA_1/LINK_1 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] MOV-LICA-LINK:BSC=0,BTS=1,OLD_LICA=0,OLD_LINK=0,NEW_L ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-147 LICA LINK Movement Display

4.3.8.17. OVERHEAD CHANNEL Configuration Information Movement

Refer to DIS-OVHD-CONF command

- Command MOV-OVHD-CONF :BSC=a ,BTS=b ,SECT=c ,CDMACH=d , NEW_CHC=e ;
- Input MOV-OVHD-CONF: BSC=0, BTS=0,

SECTOR=ALPHA,CDMACH=0,NEW_CHC=1;

• Output

XINTERM									_ 🗆 ×
		Com	mand	Wi	ndo	w			
M2610 M	OVE OVERHI	FAD CHANNE		IRAT	TON				
LOCAT	ION : BSC_	_0/BTS_0			LON				
CDMAC	H SECTOR	OV_TYPE	OV_ID[PO	CH]	RCU	SLOT	NODE		
	ALPHA	PILOT	0		0	1	0		
	ALPHA	SYNC	U		U	1	1		
	ALPHA	OPACING	ŭ		U N	-	2		
	ALP HA	OPACING	ň		ň	4	3		
ll ŏ	AL PHA	ACCESS	n n	1	ň	i	ñ		
ll ŏ	BETA	PILOT	ŏĽ	-	ŏ	i	ŏ		
Ō	BETA	SYNC	Ō		Ō	1	1		
0	BETA	PAGING	0		0	1	2		
0	BETA	QPAGING	0		0	1	3		
	BETA	ACCESS	0 (0]	0	1	0		
	GAMMA	PILOT	U		U U	3	0		
	САММА	SYNU	0		0	3	1		
	САММА	ODACTNC	U N		U N	3	2		
	CAMMA	ACCESS	0 1 0	1	ň	2	5 N		
1	ALPHA	PTIOT	0 10	- 1	ň	ň	5		
ll i	ALPHA	SYNC	ŏ		ŏ	ŏ	Ğ		
ll i	ALPHA	PAGING	ō		ō	ō	7		
1	ALPHA	QPAGING	Ō		Ō	Ō	8		
1	ALPHA	ACCESS	0 [0]	0	0	1		
1	BETA	PILOT	0		0	1	4		
	BETA	SYNC	0		0	1	5		
	BETA	PAGING	0		0	1	6		
	BEIA	QPAGING	0 10	-	U	1			
	CAMMA	DTLAT	0 10	1	N N	5	4		
	CAMMA	SANC	ň		ň	2	5		
ll i	GAMMA	PAGTNG	ň		ň	3	6 6		
ll i	GAMMA	OPAGTNG	ň		ň	ă	ž		
i	GAMMA	ACCESS	ŏ [0	1	ŏ	3	i		
COMPLETED				-					
				_					
				_		_			
			7 6141		т п	PEUTOU	c		
	<u> 100 310</u>	<u>1631 M</u> U.	/ <u>эін</u> і	ALT.		NEVI00	3		
ACCEPTED			F. DOG. C. 1			T 41.00			
LINPARK <lgi< td=""><td>CR2W>1 WO/</td><td>-ovhd-con</td><td>F:BSC=U,E</td><td>s I S=I</td><td>U, SEC</td><td>T=ALPH</td><td>IA, CDMACI</td><td>H=U,NE₩_</td><td>CHC</td></lgi<>	CR2W>1 WO/	-ovhd-con	F:BSC=U,E	s I S=I	U, SEC	T=ALPH	IA, CDMACI	H=U,NE₩_	CHC
Libnark CLCT	CRSM>1 T								
	CP30/>11								
<u> </u>									

Fig. 4.3-148 OVHD Channel Configuration Information Movement Display

4.3.8.18. BSC Configuration Addition

- Command ADD-BSC-CONF :T_PROC=a ,BSC=b ,CARD=c ,LINK=d;
- Input ADD-BSC-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.19. BSC Configuration Deletion

- Command RMV-BSC-CONF :T_PROC=a ,BSC=b;
- Input RMV-BSC-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.20. PCF Configuration Addition

- Command ADD-PCF-CONF :PCF=a ,CARD0=b ,LINK0=c ,CARD1=d ,LINK1=e ,CARD2=f ,LINK2=g ,CARD3=h ,LINK3=i;
- Input ADD-PCF-CONF:PCF=1,CARD0=3,LINK0=4,CARD1=3,LINK1=4,CARD2=3,LINK2=4,CARD3=3,LI NK3=4;
- Output

	- 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:48:32 M2711 ADD PCF CONFIGURATION LOCATION : CARD[3 3 3 3]/LINK[4 4 4 4] PCF_1 IS ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-PCF-CONF:PCF=1,CARD0=3,LINK0=4,CARD1=3,LINK1=4, ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	CA A

Fig. 4.3-149 PCF Configuration Addition Display

4.3.8.21. PCF Configuration Deletion

- Command RMV-PCF-CONF :PCF=a;
- Input RMV-PCF-CONF: PCF=1;
- Output

×IN1	TERM										_ 🗆 ×
				C	omm	and	Win	dow			
MSC	(0x00) M2712 LOC PCF PLETED	2001- REMO ATION _1 RE	DG-18 VE PCI : CAF MOVED	20:46 - CONFI RD[3	:56 GURATJ 3 3	(ON 3]/LIN	K[4	567]		
ĪO	<u>L</u> DNG	<u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVI	DUS		
[jhp ACCI [jhp	park <l EPTED park <l< td=""><td>GICBS</td><td>M>] RN M>]]</td><td>IV-PCF-</td><td>CONF : F</td><td>PCF=1;</td><td></td><td></td><td></td><td></td><td></td></l<></l 	GICBS	M>] RN M>]]	IV-PCF-	CONF : F	PCF=1;					

Fig. 4.3-150 PCF Configuration Deletion Display

4.3.8.22. SMP Configuration Addition

- Command ADD-SMP-CONF :BSC=a ,SMP=b ,CARD=c ,LINK=d;
- Input ADD-SMP-CONF: BSC=0,SMP=0,CARD=1,LINK=7;
- Output

XINTER	M			k				_ 🗆 ×
			Comm	and W	ina	low		
MSC(Oxt Mi COMPLE	00) 2001– 2721 ADD LOCATION SMP_O IS FED	06-18 2 SMP CONF : BSC_0 Added	0:50:17 IGURATION /CARD_1/LI	NK_7				
<u>10 L</u>	NG <u>C</u> DM	<u>s</u> tm <u>t</u>	EST <u>N</u> 0.7	ST <u>A</u> T A	L <u>F</u> T	PREVIOUS		
[jhparl ACCEPTI [jhparl	< <lgicbs ED < <lgicbs< th=""><td>M>] ADD- M>]]</td><td>SMP-CONF:B</td><td>SC=0,SMP=</td><td>0,CA</td><th>RD=1,LINK=7</th><td>;</td><td></td></lgicbs<></lgicbs 	M>] ADD- M>]]	SMP-CONF:B	SC=0,SMP=	0,CA	RD=1,LINK=7	;	

Fig. 4.3-151 SMP Configuration Addition Display

4.3.8.23. SMP Configuration Deletion

- Command RMV-SMP-CONF :BSC=a ,SMP=b;
- Input RMV-SMP-CONF: BSC=0, SMP=0;
- Output

	м							
			Ca	omm	and	Wind	dow	
MSC(0x0 M2 COMPLET	00) 2001– 722 REMO LOCATION SMP_O IS TED	06-18 VE SMP : BSC REMOV	20:49 CONFI O/CARI ED	:07 GURATI D_1/LI	0N NK_7			
<u>1</u> 0 <u>L</u> 0	NG <u>C</u> DM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS	
[jhparl ACCEPTI [jhparl	< <lgicbs D < <lgicbs< td=""><td>M>] RM M>]∐</td><td>IV-SMP-(</td><td>CONF:B</td><td>SC=0,S</td><th>MP=0;</th><th></th><td></td></lgicbs<></lgicbs 	M>] RM M>]∐	IV-SMP-(CONF:B	SC=0,S	MP=0;		

Fig. 4.3-152 SMP Configuration Deletion Display

4.3.8.24. VMP Configuration Addition

- Command ADD-VMP-CONF :BSC=a ,VMP=b ,CARD=c ,LINK=d;
- Input ADD-VMP-CONF: BSC=0,VMP=0,CARD=1,LINK=5;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:51:26 M2731 ADD VMP CONFIGURATION LOCATION : BSC_0/CARD_1/LINK_5 VMP_0 IS ADDED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] ADD-VMP-CONF:BSC=0,VMP=0,CARD=1,LINK=5; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-153 VMP Configuration Addition Display

4.3.8.25. VMP Configuration Deletion

- Command RMV-VMP-CONF :BSC=a ,VMP=b;
- Input RMV-VMP-CONF: BSC=0, VMP=0;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 20:50:58 M2732 REMOVE VMP CONFIGURATION LOCATION : BSC_0/CARD_1/LINK_5 VMP_0 IS REMOVED COMPLETED	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] RMV-VMP-CONF:BSC=0,VMP=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-154 VMP Configuration Deletion Display

4.3.8.26. BTS Configuration Addition

• Command ADD-BTS-CONF :BSC=a ,BTS=b ,B_TYPE=c ,SECT_EQP=d ,SECT_RANGE=e ,ALMA=f ,ALPA=g ,ALPA_LINK=h ,LICA=i ,LICA_LINK=j ,FA0_CH_NUM=k ,PN_ALPHA=1 [,PN_BETA=m] [,PN_GAMMA=n] [,PN_DELTA=o] [,PN_EPSILON=p] [,PN_ZETA=q] [,PA_TYPE=r] [,ANT_TYPE=s] [,LNA_TYPE=t] [,RISA_EQP=u] [,BOTA_EQP=v];

• Input Input ADD-BTS-CONF: BSC=0, BTS=0; -> ADD-BTS-CONF: BSC=1, BTS=0,B_TYPE=STANDARD,SECT_EQP=OMNI;

• Output

XINTERM	. 🗆 ×						
Command Window							
MSC(0x00) 2001-06-18 20:54:20 M2741 ADD BTS CONFIGURATION LOCATION : BSC_1/ALMA_0/ALPA_0/LINK_0 BTS_0[STANDARD]/FA_1/OMNI/LICA_0/LINK_0 IS ADDED COMPLETED							
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>							
[jhpark <lgicbsm>] ADD-BTS-CONF:BSC=1,BTS=0,B_TYPE=STANDARD,SECT_EQP=OMN ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>							



4.3.8.27. BTS Configuration Deletion

- Command RMV-BTS-CONF :BSC=a ,BTS=b;
- Input RMV-BTS-CONF: BSC=1, BTS=0;
- Output

XINTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-20 17:58:36 M2742 REMOVE BTS CONFIGURATION LOCATION : BSC_1/ALMA_0/ALPA_0/LINK_0 BTS_0 IS REMOVED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jhpark <lgicbsm>] RMV-BTS-CONF:BSC=1,BTS=0; ACCEPTED [jhpark <lgicbsm>] [</lgicbsm></lgicbsm>	

Fig. 4.3-155 BTS Configuration Deletion Display

4.3.8.28. SECTOR Configuration Addition

- Command ADD-SECT-CONF :BSC=a ,BTS=b ,SECT=c ,PN=d;
- Input ADD-SECT-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.29. SECTOR Configuration Deletion

- Command RMV-SECT-CONF :BSC=a ,BTS=b ,SECT=c;
- Input RMV-SECT-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.30. FA Configuration Addition

- Command ADD-FA-CONF :BSC=a ,BTS=b ,FA=c ,CH_NUM=d;
- Input ADD-FA-CONF: BSC=1, BTS=0,FA=0,CH_NUM=25;
- Output

	K	_ 🗆 ×
Command Window		
MSC(0x00) 2001-06-18 21:04:01 M2761 ADD FA CONFIGURATION LOCATION : BSC_1/BTS_0/FA_0 IS ADDED COMPLETED		
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>		
[jhpark <lgicbsm>] ADD-FA-CONF:BSC=1,BTS=0,FA=0,CH_NUM=25; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>		

Fig. 4.3-156 FA Configuration Addition Display



4.3.8.31. FA Configuration Deletion

- Command RMV-FA-CONF :BSC=a ,BTS=b ,FA=c;
- Input RMV-FA-CONF: BSC=1, BTS=0,FA=0;
- Output

	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-18 21:02:53 M2762 REMOVE FA CONFIGURATION LOCATION : BSC_1/BTS_0/FA_0 IS REMOVED COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jhpark <lgicbsm>] RMV-FA-CONF:BSC=1,BTS=0,FA=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.3-157 FA Configuration Deletion Display



4.3.8.32. BSC-BTS TRUNK Configuration Addition

- Command ADD-TRNK-CONF :BSC=a ,BTS=b ,ALMA=c ,ALPA=d ,ALPA_LINK=e ,LICA=f ,LICA_LINK=g ,ALLOC_TYPE=h;
- Input ADD-TRNK-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.33. BSC-BTS TRUNK Configuration Deletion

- Command RMV-TRNK-CONF :BSC=a ,BTS=b ,ALMA=c ,ALPA=d ,ALPA_LINK=e ;
- Input RMV-TRNK-CONF: BSC=0, BTS=0,PA_TYPE=FA_NEQ;
- Output



4.3.8.34. CAN PVC Configuration Addition

- Command ADD-CAN-PVC :NODE_A=a ,NODE_B=b ,VPCI_A=c ,VPCI_B=d [,NO_VC=e] ;
- Input ADD-CAN-PVC: NODE_A=CTYPE_BSM_A, NODE_B=CTYPE_CNP_A, VPCL_A=0, VPCL_B=0;
- Output

XINTERM	
Command Window	
MSC(0x00) 2001-06-18 21:09:32 M2781 ADD CAN PVC CONFIGURATION NUN OF ENTRY : 476 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B V O EQUIP BSM_A O CNP_A O 1 87 EQUIP CNP_A O BSM_A O 1 COMPLETED	✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓
4	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVI</u>	COUS
[jhpark <lgicbsm>] ADD-CAN-PVC:NODE_A=CTYPE_BSM_A,NO ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>	DDE_B=CTYPE_CNP_A, VPC

Fig. 4.3-158 CAN PVC Configuration Addition Display

4.3.8.35. CAN PVC Configuration Deletion

- Command RMV-CAN-PVC :INDEX=a;
- Input RMV-CAN-PVC: INDEX=0;
- Output

>						
	Command Window					
	MSC(0x00) 2001-06-18 21:07:17 M2782 REMOVE CAN PVC CONFIGURATION NUN OF ENTRY : 474 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 N_EQP BSM_A 0 CNP_A 0 1 STM-1 256 87 N_EQP CNP_A 0 BSM_A 0 1 STM-1 256 COMPLETED					
	IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS					
	[jhpark <lgicbsm>] RMV-CAN-PVC:INDEX=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>					

Fig. 4.3-159 CAN PVC Configuration Deletion Display

4.3.8.36. CPN PVC Configuration Addition

- Command ADD-CPN-PVC :NODE_A=a ,NODE_B=b ,VPCI_A=c ,VPCI_B=d [,NO_VC=e] ;
- Input ADD-CPN-PVC:NODE_A=CTYPE_CAN_A, NODE_B=CTYPE_CAN_B, VPCI_A=0, VPCI_B=0;
- Output

>	<pre><interm< pre=""></interm<></pre>					
Γ	Command Window					
	MSC(0x00) 2001-06-18 21:12:49 M2783 ADD CPN PVC CONFIGURATION NUN OF ENTRY : 144 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 EQUIP CAN_A 0 CAN_B 0 1 STM-1 256 75 EQUIP CAN_B 0 CAN_A 0 1 STM-1 256 COMPLETED					
Γ	<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>					
	[jhpark <lgicbsm>] ADD-CPN-PVC:NODE_A=CTYPE_CAN_A,NODE_B=CTYPE_CAN_B,VPC ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>					
ŀ						

Fig. 4.3-160 CPN PVC Configuration Addition Display

4.3.8.37. CPN PVC Configuration Deletion

- Command RMV-CPN-PVC :INDEX=a;
- Input RMV-CPN-PVC: INDEX=0;
- Output

	x				
Command Window					
MSC(0x00) 2001-06-18 21:10:10 M2784 REMOVE CPN PVC CONFIGURATION NUN OF ENTRY : 142 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ O N_EQP CAN_A 14 CNP_A O 1 STM-1 256 75 N_EQP CNP_A O CAN_A 14 1 STM-1 256 COMPLETED	Z				
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>					
[jhpark <lgicbsm>] RMV-CPN-PVC:INDEX=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>					

Fig. 4.3-161 CPN PVC Configuration Deletion Display

4.3.8.38. BSC PVC Configuration Addition

- Command ADD-BSC-PVC :BSC=a ,NODE_A=b ,NODE_B=c ,VPCI_A=d ,VPCI_B=e [,NO_VC=f];
- Input ADD-BSC-PVC:BSC=0, NODE_A=CTYPE_CCP_A, NODE_B=CTYPE_CCP_B, VPCI_A=0, VPCI_B=0;
- Output

2							
	Command Window						
	MSC(0x00) 2001-06-18 21:14:27 M2785 ADD BSC PVC CONFIGURATION LOCATION : BSC_0 NUN OF ENTRY : 972 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK_ 0 EQUIP CCP_A 0 CAN 0 1 STM-1 256 907 EQUIP CAN 0 CCP_A 0 1 STM-1 256 COMPLETED						
Ī	<u>IO L</u> DNG <u>CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS						
	[jhpark <lgicbsm>] ADD-BSC-PVC:BSC=0,NODE_A=CTYPE_CCP_A,NODE_B=CTYPE_CAN ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>						

Fig. 4.3-162 BSC PVC Configuration Addition Display

4.3.8.39. BSC PVC Configuration Deletion

- Command RMV-BSC-PVC :BSC=a ,INDEX=b;
- Input RMV-BSC-PVC:BSC=0, INDEX=0;
- Output

Command Window					
MSC(0x00) 2001-06-18 21:13:22 M2786 REMOVE BSC PVC CONFIGURATION LOCATION : BSC_0 NUN OF ENTRY : 970 ENTRY EQUIP NODE_A SUB_A NODE_B SUB_B VC_NO LINK_TYPE LINK 0 N_EQP CCP_A 0 CAN 14 1 STM-1 256 907 N_EQP CAN 14 CCP_A 0 1 STM-1 256 COMPLETED					
<u>IO LDNG CDM STM TEST NO.7 STAT ALET PREVIOUS</u>					
[jhpark <lgicbsm>] RMV-BSC-PVC:BSC=0,INDEX=0; ACCEPTED [jhpark <lgicbsm>]]</lgicbsm></lgicbsm>					

Fig. 4.3-163 BSC PVC Configuration Deletion Display



4.4. STATUS COMMAND

4.4.1 PROCESSOR STATUS CONTROL

Table 4.4-1 Processor Status LIST

Status Types	Definition
NORM	NORMAL
ABNM	Abnormal
DCPY	Dual Copy
LDNG	StandBy Loading
NORM(OLD)	Normal (After StandBy Loading, Old version)
NORM(NEW)	Normal (After StandBy Loading, New version)
ABN_K	Abnormal with Keep Alive Fault
ABN_1	Abnormal with Process Isolation
UNDEF	Undefined Status

4.4.1.1. BSM CAN PROCESSOR STATUS DISPLAY COMMAND

Function to display processor status inserted in CAN. Command : DIS-CAN-PRC; Input : DIS-CAN-PRC;

		Com	mand V	Vindow	
			internet i		F
MSC(0x00) 2001	-06-14 PLAY C	11:07:09	R STATUS		lĺ
PROC	ACT	A_STS	B_STS	PROC_TYPE	
PNP	Ä	NORM	ABNM	DUAL	
PCPOO PMPOO	A *	ABNM	ABNM	DUAL DUAL	
PCP01	*	ABNM_K	ABNM_K	DUAL	
AMPOO	A	NORM	****	SINGLE	
AMP01 COMPLETED	A	NORM	****	SINGLE	

Fig. 4.4-1 Result of CAN Processor Status Display Command

4.4.1.2. BSC Processor Status Display Command

Function to display Processor status inserted in BSC.

Command : DIS-BSC-PRC[:BSC=a];

a : BSC Number(0~11)

Input : DIS-BSC-PRC:BSC=0;

XINTERM						
		C	omman	d Winde	2W	
MSC(0x00) 20 M3001 D BSC 00 COMPLETED	01-06-14 ISPLAY BS PROC CCP SCP NCP ALP SMP00 VMP00	11:31 SC PROC ACT A A A A A A A	:36 ESSOR STAT A_STS NORM NORM NORM NORM NORM	US ABNM ABNM ABNM ABNM *****	PROC_TYPE DUAL DUAL DUAL DUAL SINGLE SINGLE	

Fig. 4.4-2 Result of BSC Processor Status Display

4.4.1.3. Status Display Command of BTS Processor

Function to display operation status of processors mounted in all the BTSs within the corresponding BSC or in each BTS

Command : DIS-BTS-STS:BSC=a[,BTS=b];

a:BSC Number (0~11)

b:BTS Number (0~47)

Input : DIS-BTS-STS:BSC=0,BTS=0;

Output

×	INTERM	
	Command Window	
	SC(0x00) 2001-06-14 11:34:43 M3002 DISPLAY BTS PROCESSOR STATUS LOCATE = BSCO0/BTSO0/GHJEONG [STANDARD] PROC ACT A_STS B_STS PROC_TYPE BSP A NORM ABNM NUAL BPP A NORM **** SINGLE CRP A NORM ABNM DUAL RCPOO A NORM ABNM DUAL OMPLETED	



4.4.1.4. Processor Restart Command

Function to restart Processors

- Command : RST-PRC[:BSC=a][,BTS=b],RANGE=c,SIDE=d,CLS=e;
 - a : BSC Number (0~11)
 - b : BTS Number (0~47)
 - c : Scope of restart(All the Processors of CCP, PNP,NCP,PCP., etc.)
 - d: Side to restart (A,B,BOTH)
 - e : Class (RESTART, REBOOT, FLASH)

RESTART : Restart O/S and receive loading of PLD only.

<u>**REBOOT</u></u> : It executes BOOTER. In case of the processors equipped with Flash ROM, they check upper level processors and version of each block. If they are different, they receive loading from the upper level processors. However, if they are the same, they do not receive loading from the upper level processors. The processors with no Flash ROM receive loading from the upper level processors without checking version.</u>**

. For reference, Active Side before and after reboot does not change.

FLASH : Delete Flash content of the Processor with Flash ROM equipped and reboot it to receive loading of all the files from the upper level Processor. For reference, Active Side before and after Flash Reboot changes.

Input : RST-PRC:BSC=0,BTS=0,RANGE=RCP00,SIDE=A,CLS=RESTART;





4.4.1.5. CAN Processor H/W RESET(ISOLATION) COMMAND

Function to reset CAN Processor H/W.

Command : RMT-CAN-PRC:PROC=a,SIDE=b,CLS=c;

a: Processor Name : CNP,PNP,PCP,PMP

b: Side : A,B

c: CLASS : HARDRST, ISOLAT, UNISOL

HARDRST : Function to reset Processor on H/W Level (using Register Setting).

ISOLAT : Function to isolate Processor on H/W Level (maintaining Status of RESET)

UNISOL : Function to release the isolation

Input : RMT-CAN-PRC:PROC=PNP,SIDE=A,CLS=ISOLAT;

×INTERM					
Command Window					
MSC(0x00) 2001-06-14 16:24:27 M3020 MMC CAN PROCESSOR H/W RESET LOCATE = CAN/PNP_A CLASS = ISOLAT RESULT = 0.K COMPLETED					

Fig. 4.4-5 CAN Processor H/W Command Result

4.4.1.6. BSC Processor H/W RESET(ISOLATION) COMMAND

Function to reset BSC Processor H/W.

Command : RMT-BSC-PRC:BSC=a,PROC=b,[SIDE=c],CLS=d;

a: BSC Number

b: Processor Name : CCP,NCP,SCP,ALP,SMP,VMP

c: Side : A,B

d: CLASS : HARDRST, ISOLAT, UNISOL

 ${\rm HARDRST}$: Function to RESET Processor on ${\rm H/W}$ Level (using Register Setting).

ISOLAT : Function to isolate Processor on H/W Level (RESET Status maintained)

UNISOL : Function to release isolation



Input : RMT-BSC-PRC:BSC=0,PROC=NCP,SIDE=A,CLS=ISOLAT;



4.4.1.7. BTS Processor H/W RESET(ISOLATION) COMMAND

Function to BSC Processor H/W.

Command : RMT-BTS-PRC:BSC=a,BTS=b,PROC=c,[SIDE=d],CLS=e;

- a: BSC Number
- b: BTS Number

c: Processor Name : BSP, BPP, CRP, RCP(00~05)

- d: Side : A.B
- e: CLASS : HARDRST, ISOLAT, UNISOL

HARDRST : Function to RESET Processor on H/W Level (using Register Setting).

- : Function to isolate Processor on H/W Level (RESET Status maintained) ISOLAT
- UNISOL : Function to release isolation

Input : RMT-BTS-PRC:BSC=0,BTS=0,PROC=BSP,SIDE=A,CLS=ISOLAT;

× INTERM		
Comn	nand Window	
MSC(0x00) 2001-06-14 16:39:45 M3022 MMC BTS PROCESSOR H/W LOCATE = BSC00/BTS00/BSP_I CLASS = IS0LAT RESULT = 0.K COMPLETED	RESET	

Fig. 4.4-7 BTS Processor H/W RESET(ISOLATION) Command Display Result

4.4.1.8. Processor Switch Over(Switch) Command

Function to Switch over Processor.

Switching Over Command is executed for duplicated Processors and is performed only when both sides of Processors are in a normal status. Command : SWT-PRC [:BSC=a] [,BTS=b] ,PROC=c;

a: BSC Number

b: BTS Number

c: Processor Name : CNP, PNP, PCP00, PCP01,PCP02, PMP00, PMP01,PMP02,

CCP, NCP, SCP, ALP, BSP, CRP, RCP00, RCP01, RCP02, RCP03, RCP04, RCP05

Input : SWT-PRC :BSC=1 ,PROC=CCP;

XINTERM	_ 🗆 🗵
Command Window	
MSC(0x00) 2001-06-30 10:42:32 M3030 MMC SWITCH OVER PROCESSOR RESULT LOCATE = BSC01/CCP RESULT = 0.K COMPLETED	

Fig. 4.4-8 Processor Switch Over(Switch) Command Display Result

4.4.2. Network Status Control

Status Types	Definition	Description
NORM	Normal	Normal Operation
	Normal Act	While normally operated, Act Status is
NON_A		maintained (Duplicated node)
	Normal Standby	While normally operated, Standby Status is
NOH_S		maintained (Duplicated node)

Table 4.4-2 Network Node Status LIST



ABN_D	Abnormal Deletion	card is removed
ABN_F	Abnormal Fault	Local Fault occurred
ABN_M	Abnormal MMC Block	Blocked Status by User's MMC
		Even equipped to PLD, a processor managing the
INIT	Initial	corresponding device does not normally operate
		until now
		Based on judgment that a normal call is
AB_OB Abnormal Online Bl	Abnormal Online Block	impossible due to faults in other devices, the
		corresponding device is blocked
N_EQP	Not Equipped	Card Type is not defined in PLD

4.4.2.1. Network Status Display Command

Function to display the BSS Network status.

Command : DIS-NET-STS: [BSC=a],[BTS=b],SHELF=c,CARD=d,ID=e,[CHIP=f];

- a: BSC Number
- b: BTS Number
- c: SHELF NAME(CAMU,CAMDU,ASMU,ALSU,BANU)
- d: CARD NAME(ASCA, ASIA, AOTA, ATSA, ALMA, LICA)
- e: CARD ID(0~3)
- f: CHIP Number($0 \sim 1$)

Input : DIS-NET-STS:BSC=0,BTS=0,SHELF=BANU,CARD=LICA,ID=0;

		C	omma	ind Window	
M3100 DIS LOCATE CARDID LICA00	PLAY N = BSC0 LINK 00 01 02 03 04 05 06 07 08 09 10 112 13 14 15	ETWORK 0/BTS00 TYPE STM-1 STM-1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1 E1	LINK STA BANU/LI STATUS NOR_A ABN_F	ATUS ICA00/CHIP00 DESTINATION CRP CRP ALPA	
A					

Fig. 4.4-9 Result of Network Status Display Command

4.4.2.2. ALPA Network Status Display

Function to display the ALPA Network status.

Command : DIS-ALPA-STS:BSC=a,ALMA=b,ALPA=c;

- a: BSC Number(0~11)
- b: ALMA ID(0~1)
- c∶ ALPA ID(0~4)

Command Window						
M3101 DTS	MAV A	I DA INET	WORK ITM	K STATUS		
LOCATE =	= BSCO	O ALSU/	ALMAGO A	LPAOO		
CARDID	LINK	TYPE	STATUS	DESTINATION		
ALPAOO	00	STM-1	NOR_A	ALMA		
	01	STM-1	ABN_F	ALMA		
	00	E1	NOR_A	BTSOO		
	01	E1	NOR_A	BTS01		
	02	E1	ABN_F	BTSOO		
	03	- E]	ABN_F	****		
	04		ABN_F	****		
	05	E1 E1	ADN_F	****		
	07	E1	ARN F	****		
	08	Fi	ABN F	****		
	09	Ēİ	ABN F	****		
	10	Ē1	ABN F	****		
1	11	E1	ABN_F	****		
	12	E1	ABN_F	****		
	13	E1	ABN_F	****		
	14	E1	ABN_F	****		
	15	E1	ABN_F	****		
COMPLETED						
E1						
154						

Fig. 4.4-10 Result of ALPA Network Status Display

4.4.2.3. PDSN NODE Status Display

Function to display the PDSN NODE Network status Command : DIS-PDSN-STS:SHELF=a,PIP=b; a: SHELF(PCP(00~02),PMP(00~02)) b: PIP(0~10)

Input : DIS-PDSN-STS:SHELF=PCP00,PIP=0;

		Comm	and I	Nindow	v	
MSC(0x00) 200 M3105 DI LOCATE PDSN 00	1-06-14 1 SPLAY PDSN = CAN/PCPI ID STS 00 NORM 05 N_EQP 10 N_EQP 15 N_EQP	7:05:56 NODE STAT 00_PIP00 ID STS] 01 N_EQP (06 N_EQP (11 N_EQP 1 16 N_EQP 1	TUS ED STS D2 N_EQP D7 N_EQP I2 N_EQP I2 N_EQP	ID STS 03 N_EQP 08 N_EQP 13 N_EQP 18 N_EQP	ID STS 04 N_EQP 09 N_EQP 14 N_EQP 19 N_EQP	
01 COMPLETED	00 N_EQP 05 N_EQP 10 N_EQP 15 N_EQP	01 N_EQP (06 N_EQP (11 N_EQP 1 16 N_EQP 1)2 N_EQP)7 N_EQP 2 N_EQP 7 N_EQP	03 N_EQP 08 N_EQP 13 N_EQP 18 N_EQP	04 N_EQP 09 N_EQP 14 N_EQP 19 N_EQP	₹

Fig. 4.4-11 Result of PDSN NODE Status Display

4.4.2.4. PCFU Network Status Display Command

Function to display the PCFU Network Status Command : DIS-PCF-NET:PROC=a,TYPE=b; a: PCP(00~02),PMP(00~02) b: PIP_FERA,FETA_PDSN

Input : DIS-PCF-NET:PROC=PCP00,TYPE=PIP_FERA;

× INTERM	
Command Window	
MSC(0x00) 2001-06-14 17:12:00 M3106 DISPLAY PCFU NETWORK STATUS LOCATE = CAN/PCP00/PIP_FERA_FE DEV STS A_PORT B_PORT PIP00 NORM NORM ABN_F PIP01 ABN_D ABN_D ABN_D PIP02 AEN_D AEN_D AEN_D PIP03 ABN_D AEN_D AEN_D PIP04 ABN_D ABN_D ABN_D PIP06 ABN_D ABN_D ABN_D PIP07 AEN_D AEN_D AEN_D PIP07 AEN_D AEN_D AEN_D PIP08 ABN_D AEN_D AEN_D PIP09 ABN_D ABN_D ABN_D PIP09 ABN_D ABN_D ABN_D PIP10 ABN_D ABN_D ABN_D	



4.4.2.5. ALPA Network Block Command

Function to block the ALPA Network. Command : BLK-ALPA:BSC=a,ALMA=b,ALPA=c,[TYPE=d],[LINK=e]; a: BSC Number(00~11) b: ALMA ID(0~1) c: ALPA ID(0~4) d: TYPE(STM_1,E1) e: LINK(0~15)

Input : BLK-ALPA:BSC=0,ALMA=0,ALPA=0,TYPE=STM_1,LINK=0;



Fig. 4.4-13 Result of ALPA Network Block Command

4.4.2.6. UNBlock Command

Function to unblock the ALPA Network.

Command : UBLK-ALPA:BSC=a,ALMA=b,ALPA=c,[TYPE=d],[LINK=e];

- a: BSC Number(00~11)
- b: ALMA ID(0∼1)
- c: ALPA ID(0~4)
- d: TYPE(STM_1,E1)
- e: LINK(0~15)

Input : UBLK-ALPA:BSC=0,ALMA=0,ALPA=0,TYPE=STM_1,LINK=0;


×INTERM	
Command Window	
MSC(0x00) 2001-06-14 17:31:58 M3121 MMC UNBLOCK ALPA LINK RESULT LOCATE = BSCOO_ALSU/ALMAOO/ALPAOO/STM-1/LINKOO RESULT = UNBLOCKED COMPLETED	<u> </u>
4	



4.4.3. Can Device Status Control

Status	Definition
Types	
NORM	Normal
ABN_D	Abnormal Deletion (Even equipped to PLD, it is in the Status of Removal)
ABN_F	Abnormal Fault(Status that normal operation is impossible due to fault in
	Device)
	(Test: As a result of DSP Chip Hardware Test, NOK occurs)
ABN_M	Abnormal MMC Block
INIT	Initial (Even equipped to PLD, a processor managing the corresponding
	device does not operate normally until now)
AB_0B	Abnormal Online Block (Based on judgment that a normal call is impossible
	due to faults in other devices, the appropriate device is blocked)
IDLE	Even if it is normal, call resources are not allocated (CE, VCE)
BUSY	normal and call resources are allocated (CE, VCE)
N_EQP	Status defined as Not Equipped to PLD
READY	Even if not defined in PLD, Device is inserted
UNDEF	Status that un-defined status is inserted
ABN_I	Status being separated as H/W Reset
ABN_B	BER Test Status by User
NOR_PB	In case that call exists when CHC, Chip is blocked, it indicates the status that
	awaits until a call is terminated
	it indicates, the Status that waits for the call termination in order to perform

Table 4.4-3 DEVICE Status List

	the Vocoder test.
REDNCY	In duplicated Device, it indicates Redundancy Status of Standby side(FETA
	Only)
CB_OPN	For device that is managed only as fault, it is the case that is opened to Fault
	Cable
CLK_F	During Vocoder Channel test, as a result of Timing-Module test, NOK
	occurred (Test Only)
TSW_F	As a result of TSLU Loopback test, NOK is occurred (Test Only)
ABN_AT	Status in which Vocoder is put to Automatic(Online) test (Test Only)
ABN_MT	Status in which Vocoder Manual(Ondemand) is put to test (Test Only)
QAT0_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 0 (Test Only)
QAT1_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 1 (Test Only))
QAT2_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 2 (Test Only)
QAT3_F	As a result of QCELP Algorithm test during Vocoder channel test, NOK
	occurred in State 3 (Test Only)
VPLB_F	As a result of VCPA Loopback test during Vocoder channel test, NOK
	occurred (Test Only)
VMLB_F	As a result of VCMA Loopback test during Vocoder channel test, NOK
	occurred (Test Only)
VLLB_F	As a result of VLIA Loopback test during Vocoder channel test, NOK occurred
i	(Test Only)

4.4.3.1. CAN Device Status Display Command

Function to display Device(BOARD) Status mounted to CAN Command : DIS-CAN-DEV:PROC=a; a : CNP,PNP,PCP(00~02)

Input : DIS-CAN-DEV:PROC=CNP;

		С	omma	nd Wir	ndo	w.		
MSC(0x00) 200 M3600 DJ LOCATE DEV	11-06-1 SPLAY E = CAN STS	4 17:45 CAN DEV: V/CNP	5:57 ICES STAT	US				
	O ABN_D NORM							
DEV ATSAOC ASCAOC	ACT * A	A_STS N_EQP NORM	B_STS N_EQP ABN_D					
DEV ASIAOO APCOO	АСТ	A_STS NORM NORM	B_STS ABN_D ABN_D	DEV ASIA01 APCO0	АСТ *	A_STS ABN_D ABN_D	B_STS ABN_D ABN_D	
APC01 ASIA02 APC00 APC01	• • *	ABN_D ABN_D ABN_D	ABN_D ABN_D ABN_D	APC01 ASIA03 APC00 APC01	*	ABN_D N_EQP N_EQP N_EQP	ABN_D N_EQP N_EQP	
COMPLETED		HBH_D		HECOT		n <u>e eq</u> r	n_ L qr	

Fig. 4.4-15 Result of CAN Device Status Display Command

4.4.3.2. GPS(CAN) Status Display Command

Function to display Device and Information of CAN GPS. Command : DIS-GPS-STS:TYPE=a; a : ALL,GPS_DEV,GPS_INFO

```
Input : DIS-GPS-STS:TYPE=ALL;
```

Command Window	
MSC(0v00) 2001-06-14 17:49:44	
M3601 DISPLAY GPS DEVICES STATUS/INFORMATION	
DEV ACT A_STS B_STS	
GPSR1 A NORM NORM	
GPSD1 NORM	
GPSD2 NORM	
GPSD3 NORM	
GPSD4 NUKM	
# GPSR01 DATA	
GPSR01_A GPSR01_B	
CABLE DELAY Ons Ons	
LATITUDE 37,22.4568'N 37,22.4567'N	
LONGITUDE 126,57,1551'E 126,57,1548'E	
ALITIONE 109.00 m 108.05 m	
TIME MODE : GPS MODE	
TIME DIFF. : 2ns	
TFOM : 2	
OPER. MODE : TIME LOCKED	
# CPSR02 DATA	
GPSR02_A GPSR02_B	
DAC 32449 34806	
LABLE DELAY UNS UNS IATTUDE 37 22 4618'N 37 22 4616'N	
LONGITUDE 126,57.1595'E 126,57.1596'E	
ALTITUDE 105.03 m 105.04 m	
TIME MODE : GPS MODE	
TIME DIFF. : 8ns	
TEOM 2	
OPER. MODE : TIME LOCKED	
TOD : 2001-06-14 17:53:39	
COMPLETED	
4	

Fig. 4.4-16 Result of GPS(CAN) Status Display Command

4.4.3.3. H/W RESET CAN DEVICE Command

Function to reset CAN Device on H/W Level Command : RMT-CAN-DEV:PROC=a,DEV=b,ID=c,[SIDE=d],CLS=e; a: Processor :CNP,PNP,PCP(00~02),PMP(00~02) b: Device Name: ASCA,ASIA,AOTA,ATSA,PIP,FERA,FETA,BCRA c: Device ID : 0~10 d: SIDE:A_SIDE,B_SIDE

e: CLASS : HARDRST,ISOLAT,UNISOL

Input : RMT-CAN-DEV:PROC=PNP,DEV=ASCA,ID=0,SIDE=A_SIDE,CLS=HARDRST;

Command Window	
MSC(0x00) 2001-06-14 17:54:36 M3630 H/W RESET CAN DEVICE LOCATE = CAN_PNP/ASCA00_A CLASS = HARDRST RESULT = 0.K COMPLETED	

Fig. 4.4-17 Result of H/W RESET CAN DEVICE Command

4.4.4. BSC Device Status Control

4.4.4.1. BSC Device Status Display Command

Function to display Status of various Boards mounted to BSC Command : DIS-BSC-DEV:BSC=a,PROC=b; a : BSC Number(00~11) b : PROC Name(NCP,SCP,ALP,SMP(00~04),VMP(00~08))

Input : DIS-BSC-DEV:BSC=1,PROC=NCP;

XINTERN	4								- O ×
	Command Window								
MSC(0x0 M3	0) 2001 202 DIS LOCATE DEV ATSA00	-06-1 PLAY = BSC ACT A	4 19:00 BSC DEVI 01/NCP A_STS NORM): 42 ICES STAT B_STS ABN_D	US				
	ASCAUU DEV ASIAOO APCOO APCO1	ACT A A	A_STS NORM NORM NORM	ABN_D B_STS ABN_D ABN_D ABN_D	DEV ASIAO1 APCOO APCO1	ACT A A	A_STS NORM NORM NORM	B_STS NORM NORM NORM	
COMPLET	ASIAO2 APCOO APCO1 ED	* *	N_EQP N_EQP N_EQP	N_EQP N_EQP N_EQP	ASIAO3 APCOO APCO1	* *	N_EQP N_EQP N_EQP	N_EQP N_EQP N_EQP	Į
, []									

Fig. 4.4-18 Result of BSC Device Status Display

4.4.4.2. SLPA Status Display Command

Function to display the SLPA Status

Command : DIS-SLPA-STS:BSC=a,SMP=b,[SLPA=c];

- a : BSC Number(00~11)
- b : SMP Number(00~04)
- c : SLPA Number(00~17)

Input : DIS-SLPA-STS:BSC=0,SMP=0,SLPA=0;

×	NTERM	×
	Command Window	
C C	SC(0x00) 2001-06-14 19:05:42 M3203 DISPLAY SLPA STATUS LOCATE = BSCOO/SMPOO SLPA SLV CALL STS SLV CALL STS SLV CALL STS SLV CALL STS 00 0 00/00 ABN_M 1 01/00 NORM 2 00/00 ABN_M 3 00/00 ABN_M DMPLETED	



4.4.4.3. VCPA Status Display Command

Function to display the VCPA Status



Command : DIS-VCPA-STS:BSC=a,VMP=b,[VCPA=c];

- a : BSC Number(00~11)
- b : VMP Number($00 \sim 07$)
- c : VCPA Number(00~15)

Input : DIS-VCPA-STS:BSC=0,VMP=0,VCPA=0;

XINTERM														
				C	omn	an	d Wii	ndo	w					
MSC(0x00) 2 M3204 L0C/ VCI 00 COMPLETED	2001-00 DISPL/ ATE = 1 PA SLV 0 1	6–14 AY V(BSCO(DSP 0 0	19 CPA D/VMI CAL 0 0	:11:31 STATUS POO L STS NORM ABN_M	DSP 1 1	CAL O O	L STS Norm Abn_m	DSP 2 2	CAL O O	L STS NORM ABN_M	DSP 3 3	CALL O O	. STS Norm Abn_m	

Fig. 4.4-20 Result of VCPA Status Display Command

4.4.4.4. E1 LINK Status Display Command

Function to display E1 Link Status of VLIA Command : DIS-E1-STS:BSC=a,VMP=b,[VLIA=c]; a : BSC Number(00~11) b: VMP Number(00~07) c: VLIA Number(00~01)

XINTERM Command Window MSC(0x00) 2001-06-14 19:15:31 M3205 DISPLAY E1 STATUS LOCATE = BSC00/VMP00 E1 02 06 10 VLIA STS E1 STS STS E1 STS E1 00 00 NORM 01 NORM NORM 03 NORI 04 05 07 ABI **ABN** 08 09 ARN ١R 13 ARN COMPLETED - -

Input : DIS-E1-STS:BSC=0,VMP=0,VLIA=0;



Fig. 4.4-21 Result of E1 LINK Status Display Command

4.4.4.5. TS Network LINK Status Display Command

Function to display the Status of TS Network Link of VLIA.

Command : DIS-TS-STS:BSC=a,VMP=b,VLIA=c,E1=d;

- a : BSC Number(00~11)
- b: VMP Number(00~07)
- c : VLIA Number(00~15)
- d:E1 Number(00~15)

Input : DIS-TS-STS:BSC=0,VMP=0,VLIA=0,E1=0;

X INTERM									
	Command Window								
MSC(0x00) M320 L07 E1 00 C0MPLETED	2001-06-1 6 DISPLAY CATE = BSC TS STS 00 RSRVD 04 IDLE 08 IDLE 12 IDLE 16 N0_7 20 IDLE 24 IDLE 28 IDLE	4 19:19:2 TS STATUS COO/VMPOO/V TS STS 01 IDLE 05 IDLE 09 IDLE 13 IDLE 13 IDLE 17 IDLE 21 IDLE 25 IDLE 29 IDLE	22 /LIA00 TS STS 02 BUSY 06 IDLE 10 IDLE 14 IDLE 18 IDLE 22 IDLE 26 IDLE 30 IDLE	TS STS 03 IDLE 07 IDLE 11 IDLE 15 IDLE 19 IDLE 23 IDLE 27 IDLE 31 IDLE					
, Ale									

Fig. 4.4-22 Result of TS Network LINK Status Display Command

4.4.4.6. VCE(Vocoder Channel Element) Status Display Command

Status Types	Definition	Description
IDLE	ldle	Normal status without a Call
8K_Qcelp	8k Qcelp Call	8k QCELP Call Seized Status
8K_EVRC	8k EVRC Call	8k EVRC Call Seized Status

Table 4.4-4 Vocoder Channel Element Status LIST

13K_Qcelp	13k Qcelp Call	13k QCELP Call Seized Status
13K_EVRC	13k EVRC Call	13k EVRC Call Seized Status
ABN_M	Abnormal MMC Block	Blocked Status by user's MMC
UNDEF	Undefined Status	Status with Input of undefined Status

Function to display the Channel Element Status of VCE.

Command : DIS-VCE-STS:BSC=a,VMP=b,[VCPA=c];

a : BSC Number(00~11)

b: VMP Number(00~07)

c: VCPA Number(00~15)

Input : DIS-VCE-STS:BSC=0,VMP=0,VCPA=0;

2	XINTERM							nia konesa			<u>_ </u>
					Com	mana	l Win	dow			
	MSC(0x00) 2 M3208	001-0 DTSPI)6-14 AY V(19:22: CODER (: 36 CHANNEL	ELEMENT	STATU	5			
l	LOCA	TE = DSP	BSCO)/VMP00/ STS	VCPADO VCE	STS	VCE	STS	VCE	STS	
l	00 00 00	00 01 02	00 00 00		01 01 01		02 02 02	IDLE IDLE TDLF	03 03 03		
l	00	03 00	00 00	IDLE ABN_M	01 01	IDLE ABN_M	02 02	IDLE ABN_M	03 03	IDLE ABN_M	
l	01	01 02 02	00 00	ABN_M ABN_M	01 01 01	ABN_M ABN_M	02	ABN_M ABN_M	03 03 02	ABN_M ABN_M	
		03			UI		02	MDM_M	05		

Fig. 4.4-23 Result of VCE(Vocoder Channel Element) Status Display Command

4.4.4.7. SLPA BLOCK Command

Function to block SLPA. Command : BLK-SLPA:BSC=a,SMP=b,SLPA=c,[SLV=d]; a : BSC Number(00~11) b : SMP Number(00~04) c : SLPA Number(00~17) d : SLV Number(00~03)

Input : BLK-SLPA:BSC=0,SMP=0,SLPA=0,SLV=0;

×INTERM	
Command Window	
MSC(0x00) 2001-06-14 19:28:12 M3210 BLOCK SLPA SLAVE CARD LOCATE = BSC00/SMP00/SLPA00/SLV00 RESULT = BLOCKED COMPLETED	1 <u>8</u> 2 2

Fig. 4.4-24 Result of SLPA BLOCK Command

4.4.4.8. SLPA UNBLOCK Command

Function to unblock SLPA. Command : UBLK-SLPA:BSC=a,SMP=b,SLPA=c,[SLV=d]; a : BSC Number(00~11) b : SMP Number(00~04) c : SLPA Number(00~17) d : SLV Number(00~03)

Input : UBLK-SLPA:BSC=0,SMP=0,SLPA=0,SLV=0;



Fig. 4.4-25 Result of SLPA UNBLOCK Command

4.4.4.9. VCPA BLOCK Command

Function to block VCPA. Command : BLK-VCPA:BSC=a,VMP=b,VCPA=c,[SLV=d],[DSP=e]; a : BSC Number(00~11)

- b: VMP Number(00~07)
- c : VCPA Number(00~15)
- d: SLV Number(00~01)
- e : DSP Number(00~03)

Input : BLK-VCPA:BSC=0,VMP=0,VCPA=0,SLV=0,DSP=0;



Fig. 4.4-26 Result of VCPA BLOCK Command

4.4.4.10. VCPA UNBLOCK Command

Function to unblock VCPA

Command : UBLK-VCPA:BSC=a,VMP=b,VCPA=c,[SLV=d],[DSP=e];

- a : BSC Number(00~11)
- b: VMP Number(00~07)
- c : VCPA Number(00~15)
- d : SLV Number(00~01)
- e : DSP Number(00~03)

Input : UBLK-VCPA:BSC=0,VMP=0,VCPA=0,SLV=0,DSP=0;







4.4.4.11. VLIA BLOCK Command

Function to block VLIA.

Command : BLK-VLIA:BSC=0,VMP=0,VLIA=0,[E1=0],[TS=1];

- a : BSC Number(00~11)
- b: VMP Number(00~07)
- c : VLIA Number($0 \sim 1$)
- d : E1 Number(00~15)
- e : TS Number(00~31)

Input : BLK-VLIA:BSC=0,VMP=0,VLIA=0,E1=0,TS=1;

XINTERM							
		. C	Comm	and V	Vindow		
MSC(0x00) M3212 LOC RES COMPLETED	2001–06–14 : BLOCK TS ATE = BSCOO ULT = BLOCK	19:39:3 //VMPOO/V ED	16 'LIAOO/E1	00/TS01			

Fig. 4.4-28 Result of VLIA BLOCK Command

4.4.4.12. VLIA UNBLOCK Command

Function to unblock VLIA Command : UBLK-VLIA:BSC=0,VMP=0,VLIA=0,[E1=0],[TS=1]; a : BSC Number(00~11) b : VMP Number(00~07)

- c : VLIA Number(0~1)
- d : E1 Number(00~15)
- e : TS Number(00~31)

Input : UBLK-VLIA:BSC=0,VMP=0,VLIA=0,E1=0,TS=1;



Fig. 4.4-29 Result of VLIA UNBLOCK Command

4.4.4.13. H/W RESET BSC Device Command

Function to reset BSC Device on H/W Level.

Command : RMT-BSC-DEV:BSC=a,PROC=b,DEV=c,ID=d,[SUBID=e],CLS=f;

- a : BSC Number(00~11)
- b: Processor Name(NCP,SCP,ALP,SMP(00~05),VMP(00~07))
- c : Device Name(SLPA,VCPA,VLIA,STIA,ASCA,ASIA,AOTA,ATSA,ALMA)
- d:Device ID(00~17)
- e : Sub_id(A_SIDE,B_SIDE)
- f: Class(HARDRST, ISOLAT, UNISOL)

Input : RMT-BSC-DEV:BSC=0,PROC=SMP00,DEV=SLPA,ID=0,CLS=HARDRST;

Command Window	
MSC(0x00) 2001-06-14 19:45:02 M3230 H/W RESETS BSC DEVICE L0CATE = BSC00/SMP00/SLPA00 CLASS = HARDRST RESULT = 0.K COMPLETED	



Fig. 4.4-30 Result of BSC Device H/W Reset Command

4.4.4.14. H/W RESET ALPA Command

Function to reset ALPA on H/W Level. Command : RMT-ALPA:BSC=0,ALMA=0,ALPA=0,CLS=HARDRST;

- a : BSC Number(00~11)
- b : ALMA ID(0~1)
- c : ALPA ID(0~4)
- d : Class(HARDRST,ISOLAT,UNISOL)

Input : RMT-ALPA:BSC=0,ALMA=0,ALPA=0,CLS=HARDRST;



Fig. 4.4-31 Result of H/W RESET ALPA Command

4.4.5. Bts Device Status Control

4.4.5.1. BTS Device Status Display Command

Function to display the BTS Device status

Command : DIS-BTS-DEV:BSC=a,BTS=b,PROC=c;

- a : BSC Number(00~11)
- b: BTS Number(00~47)
- c: Processor Name(BSP,BPP,CRP,RCP(00~06)

Input : DIS-BTS-DEV:BSC=0,BTS=0,PROC=CRP;

XINTERM							
			Com	mand I	Window		
MSC(0x00) M330 L(DI L] COMPLETED) 2001- DO DISP DCATE = EVICE ICAOO)	06-14 20 PLAY BTS D BSCOO/BT Status Norm	I:06:30 IEVICES STA SOO/CRP DEVICE LICAO1	TUS STATUS N_EQP	DEVICE LICAO2	STATUS N_EQP	

Fig. 4.4-32 Result of BTS Device Status Display Command

4.4.5.2. FA Status Display Command

Function to display FA Status.

Command : DIS-FA-STS:BSC=a,BTS=bFA=c;

- a : BSC Number(00~11)
- b : BTS Number($00 \sim 47$)
- c : FA Number($0 \sim 7$)

Input : DIS-FA-STS:BSC=0,BTS=0,FA=0;



Command Window								
0x00) 2001	-06-14	20:09:23	l					
LOCATE =	BSC00/	BTS00/FA0	0					
CDMACH_N	IUM : 25	:						
DEV GPS	АСТ А	A_STS NORM	B_STS NORM					
DEV S	TATUS							
ARIA N BADA A	IORM BN_D							
PACA N	IORM							
		ALPHA	BETA	GAMMA	DELTA	EPSILON	ZETA	
SEC_EQP ACTIVITY		EQUIP DEAD	EQUIP ACT	EQUIP DÉAD	N_EQP DEAD	N_EQP DEAD	N_EQP DEAD	
= OVHD C	H STS =							
PICH		AB_0B	NORM	AB_0B	N_EQP	N_EQP	N_EQP	
PCH		AB_OB	NORM	AB_0B	N_EQP	N EOP	N EOP	
PCH		N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	
PCH		N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	
QPCH		AB_0B	NORM	AB_0B	N_EQP	N_EQP	N_EQP	
UPCH ADCH		N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	
 	н ту са	TN =	N_EQP	N_EQP	N_EQP	N_EQP	N_EQP	
PICH		0xea	Oxea	Oxea	Oxea	Oxea	Oxea	
SYNCH		0x9f	0x9f	0x9f	0x9f	0x9f	0x9f	
РСН		0xd9	0xd9	0xd9	0xd9	0xd9	0xd9	
РСН		0xd9	0xd9	0xd9	0xd9	0xd9	0xd9	
PCH		Oxd9	0xd9	0xd9	0xd9	0xd9	0xd9	
UPCH ODCH		Uxea Ov00	Uxea 0v00	Uxea Ov00	Uxea Ov00	Uxea 0v00	Uxea	
ОРСН		0x00	0x00	0x00	0x00	0x00	Oxee	
= ACCESS	сн Грс	H.ACH1ZMA	X SECTOR	=	0700	0700	UNCC	
ACH_STS	CHO0,00	AB_0B	NORM	AB_0B	*****	****	****	
ACH_STS	CHO0,01	*****	*****	*****	*****	*****	****	
ACH_STS	CHO0,02	*****	****	*****	****	****	****	
ACH_STS	CH01,00	*****	*****	****	****	****	****	
ACH_STS	CH01,01	*****	an an an an an an	*****	*****	****	****	
ACH_STS	CH01,02	*****	*****	*****	*****	*****	****	
ACH STS	CH02,01	*****	*****	*****	****	****	****	
ACH_STS	CH02.02	*****	*****	*****	****	****	****	
BUDA	,,	NORM	NORM	NORM	N_EQP	N_EQP	N_EQP	
LNA_A		NORM	NORM	NORM	N_EQP	N_EQP	N_EQP	
LNA_B		NORM	NORM	NORM	N_EQP	N_EQP	N_EQP	
TIC_TX_C	AIN	0x960	0x960	0x980	0x00	0x00	0x00	
DOWED(Ma	(GAIN (tt)	0.15	2 49	08980	0,00	0 00	00800	
TEMP(C)		29	2.45	29	0.00	0.00	0.00	
HPA		ABN F	NORM	ABN F	N EOP	N EOP	N_EOP	
	Pl	CV-1 Dr	C 4 UD-0					
	— БС	51.1 KE	5_4_n0.0					

Fig. 4.4-33 Result of FA Status Display Command

4.4.5.3. BLOCK DBPA Command

Function to block DBPA.

Command : BLK-DBPA:BSC=a,BTS=b,RCP=c,DBPA=d,[CHIP=e],[CONDITION=f];

a : BSC Number(00~11)

- b: BTS Number(00~47)
- c : RCP Number(0~5)

- d : DBPA Number(0~9)
- e : CHIP Number(0~1)

f: Select Option(conditional, unconditional)

Conditional : If there is call connected, wait until it is disconnected during timeout and then block it. If it is not disconnected until timeout, do not block it.

Unconditional : block a call regardless of their presence unconditionally. (The existing call is disconnected)

Reference : If an OverHead Channel is allocated, do not block a call unconditionally.

Input : BLK-DBPA:BSC=0,BTS=0,RCP=0,DBPA=1,CHIP=1,CONDITION=CONDITION;

Command Window	
MSC(0x00) 2001-06-14 21:08:50 M3310 BLOCK DBPA CHIP LOCATE = BSC00/BTS00/RCP00/DBPA01/CHIP01_CONDITION RESULT = BLOCKED COMPLETED	

Fig. 4.4-34 Result of BLOCK DBPA Command

4.4.5.4. UNBLOCK DBPA Command

Function to unblock DBPA

Command : UBLK-DBPA:BSC=a,BTS=b,RCP=c,DBPA=d,[CHIP=e],[CONDITION=f];

- a : BSC Number(00~11)
- b: BTS Number(00~47)
- c ∶ RCP Number(0~5)
- d : DBPA Number(0~9)
- e : CHIP Number(0~1)

Input : UBLK-DBPA:BSC=0,BTS=0,RCP=0,DBPA=1,CHIP=1;

Command Window	
MSC(0x00) 2001-06-14 21:09:35 M3320 UNBLOCK DBPA CHIP LOCATE = BSC00/BTS00/RCP00/DBPA01/CHIP01 RESULT = UNBLOCKED COMPLETED	
M	

Fig. 4.4-35 Result of UNBLOCK DBPA Command

4.4.5.5. BLOCK OverHead Channel Element Display Command

Function to block OverHead Channel Element Command : BLK-OVHD-CE:BSC=a,BTS=b,SECTOR=c,FA=d; a : BSC Number(00~11) b : BTS Number(00~47) c : SECTOR(ALPHA,BETA,GAMMA,DELTA,EPSILON,ZETA) d : FA Number(0~5)

Input : BLK-OVHD-CE:BSC=0,BTS=0,SECTOR=ALPHA,FA=0;

XINTERM		- D ×
	Command Window	
MSC(0x00) 2001 M3312 BL0 LOCATE SECTOR FA = 0 RESULT COMPLETED	1-06-14 21:01:18 DCK OVERHEAD CHANNEL ELEMENT = BSC00/BTS00 = ALPHA = BLOCKED	4



4.4.5.6. UNBLOCK OverHead Channel Element

Function to unblock OverHead Channel Element



Command : UBLK-OVHD-CE:BSC=a,BTS=b,SECTOR=c,FA=d;

- a : BSC Number(00~11)
- b : BTS Number(00~47)
- c: SECTOR(ALPHA,BETA,GAMMA,DELTA,EPSILON,ZETA)
- d : FA Number($0 \sim 5$)

Input : UBLK-OVHD-CE:BSC=0,BTS=0,SECTOR=ALPHA,FA=0;



Fig. 4.4-37 Result of UNBLOCK OverHead Channel Element

4.4.5.7. H/W RESET BTS Device Command

Function to reset BTS Device on H/W.Level

Command : RMT-BTS-DEV:BSC=a,BTS=b,PROC=c,DEV=d,[ID=e],CLS=f;

- a:BSC Number(00~11)
- b : BTS Number(00~47)
- c : Processor Name(BSP,BPP,CRP,RCP(00~05)
- d : Device Name(ARIA,DBPA,BUDA,HPA,PACA,BADA,RISA,BOTA,LICA)
- e : Device ID $(0 \sim 9)$
- f: Class(HARDRST, ISOLAT, UNISOL)

Input : RMT-BTS-DEV:BSC=0,BTS=0,PROC=CRP,DEV=LICA,ID=0,CLS=HARDRST;





Fig. 4.4-38 Result of H/W RESET BTS Device Command

4.4.5.8. H/W RESET LPA Device Command

Function to reset LPA Device on H/W.Level

Command : RMT-LPA:BSC=a,BTS=b,SECTOR=c,LPA=d,CLS=e;

- a : BSC Number(00~11)
- b : BTS Number(00~47)
- c : SECTOR(ALPHA,BETA,GAMMA,DELTA,EPSILON,ZETA)
- d : LPA Number(0~5)
- e : Class(HARDRST,ISOLAT,UNISOL)

Input : RMT-LPA:BSC=0,BTS=0,SECTOR=ALPHA,LPA=0,CLS=HARDRST;



Fig. 4.4-39 Result of H/W RESET LPA Device Command

4.4.5.9. H/W RESET LPA Combiner Command

Function to reset the LPA Combiner on $\ensuremath{\text{H/W}}$ Level

Command : RMT-LPA-COM:BSC=a,BTS=b,SHELF=c,COM=d,CLS=e;

- a : BSC Number(00~11)
- b: BTS Number(00~47)
- c: SHELF Number(0~5)
- d : Combiner Number($0 \sim 2$)
- e : Class(HARDRST,ISOLAT,UNISOL)

Input : RMT-LPA-COM:BSC=0,BTS=0,SHELF=0,COM=0,CLS=HARDRST;

X INTERM							
		С	omm	and	Window	,	
MSC(0x00) 20 M3332 H LOCAT CLASS RESUI COMPLETED	001-06-14 1/W RESE TE = BSC S = HARD LT = 0.K	4 21:40 T LPA CO DO/BTSOO RST	D:09 DMBINER D/BPP/SI	HELFOO/	'LPA_COMOO		4
M							

Fig. 4.4-40 Result of LPA Combiner H/W RESET Command

4.4.5.10. BTS SHELF POWER H/W RESET Command

Function to reset BTS SHELF POWER on H/W Level

Command : RMT-BTS-SHF:BSC=a,BTS=b,SHF=c,CLS=d;

```
a : BSC Number(00~11)
```

- b:BTS Number(00~47)
- c : SHELF Name(BANU,RCBU(00~05)
- d : Class(HARDRST,ISOLAT,UNISOL)

Input : RMT-BTS-SHF:BSC=0,BTS=0,SHF=RCBU00,CLS=HARDRST;

	×
Command Window	
MSC(0x00) 2001-06-14 21:42:59 M3350 BTS SHELF POWER H/W RESET LOCATE = BSC00/BTS00/RCBU00 CLASS = HARDRST RESULT = 0.K COMPLETED	

Fig. 4.4-41 Result of BTS SHELF POWER H/W RESET Command

4.4.6. Status Message Control

Codes	Definition	Description
S3002	CAN CNP/PNP Status Change	When Processor Status changes, it occurs.
	Display	
S3003	AMP Status Change Display	When Processor Status changes, it occurs.
S3004	ALP Status Change Display	When Processor Status changes, it occurs.
S3005	CCP Status Change Display	When Processor Status changes, it occurs.
S3006	SMP Status Change Display	When Processor Status changes, it occurs.
S3007	VMP Status Change Display	When Processor Status changes, it occurs.
S3008	PCP Status Change Display	When Processor Status changes, it occurs.
S3009	BSC-NCP Status Change Display	When Processor Status changes, it occurs.
S3010	BSP Status Change Display	When Processor Status changes, it occurs.
S3011	SCP Status Change Display	When Processor Status changes, it occurs.
S3012	BPP Status Change Display	When Processor Status changes, it occurs.
S3013	RCP Status Change Display	When Processor Status changes, it occurs.
S3017	CRP Status Change Display	When Processor Status changes, it occurs.
S3020	PMP Status Change Display	When Processor Status changes, it occurs.
S3201	CNP ASIA Status Change Display	Displayed upon device status change
S3202	CNP ASCA Status Change	Displayed upon device status change
	Display	
S3205	PNP ASIA Status Change Display	Displayed upon device status change
S3206	PNP ASCA Status Change	Displayed upon device status change
	Display	

Table 4.4-5 Status Message LIST

S3209	PCP	BCRA	Status	Change	Displayed	upon	device	status	change		
	Displa	у									
S3210	PMP	BCRA	Status	Change	Displayed	upon	device	status	change		
	Displa	у									
S3211	AM	P GPSR	Status Cl	nange	Displayed	upon	device	status	change		
		Dis	splay								
S3220	NCP A	SIA Statu	us Change	e Display	Displayed	upon	device	status	change		
S3221	NCP	ASCA	Status	Change	Displayed	upon	device	status	change		
	Displa	у									
S3222	NCP	ATSA	Status	Change	Displayed	upon	device	status	change		
	Displa	у									
S3224	ALP	ALMA	Status	Change	Displayed	upon	device	status	change		
	Displa	у									
S3230	BSP G	SPS Statu	s Change	Display	Displayed	upon	device	status	change		
S3501	ССР	Overload	I State	Change	Displayed	when	overlo	oad sta	tus is	generat	ed,
	Displa	у			released a	and c	hanged	owing	to load	change	in
					ССР						
S3502	BSP	Overload	State	Change	Displayed	when	overlo	oad sta	tus is	generat	ed,
	Displa	у			released a	and c	hanged	owing	to load	change	in
					BSP						

4.4.6.1. Inhibited Status Message Display Command

Function to display Status Message whose display to Outterm is inhibited Command : DIS-INH-STS;

Input : DIS-INH-STS;

XINTERM		<u>_ ×</u>
	Command Window	
MSC(0x00) 2001 M3400 DISI SN Completed	-06-14 21:46:23 PLAY INHIBITED STATUS = NONE	<u>Z</u>

Fig. 4.4-42 Result Inhibited Status Message Display Command

4.4.6.2. Status Message Display Inhibition Command

Function to inhibit Status Message display Command : INH-STS-MSG:SN=a; a: SN Number(ALL,S3002~S3020,S3201~S3230, S3501,S3502)

Input : INH-STS-MSG:SN=S3002;



Fig. 4.4-43 Result of Status Message Display Inhibition Command

4.4.6.3. Allow Inhibited Message Command

Function to allow display of Status Message whose display to Outterm is inhibited Command : ALW-STS-MSG:SN=a;

a: SN Number(ALL,S3002~S3020, S3201~S3230, S3501,S3502)

Input : ALW-STS-MSG:SN=S3002;

	<u>_ ×</u>
Command Window	
MSC(0x00) 2001-06-14 21:51:04 M3402 ALLOW STATUS MESSAGE : SN = S3002 RESULT = ALLOWED COMPLETED	KL N

Fig. 4.4-44 Result of Inhibited Message Display Allow Command

4.4.7. Overload Status Control

4.4.7.1. Processor Overload Status Display Command

Function to display processor Overload Status

Command : DIS-OVLD-STS:[BSC=a],[BTS=b],PROC=c;

- a : BSC Number(00~11)
- b:BTS Number(00~47)
- c : Processor Name(CCP,BSP)

Input : DIS-OVLD-STS:BSC=0,BTS=0,PROC=BSP;





Fig. 4.4-45 Result of Processor Overload Status Display Command

4.4.7.2. Overload Threshold Value Display Command

Function to display Overload Threshold Value Command : DIS-OVLD-THR:BSC=a,[BTS=b],PROC=c; a : BSC Number(00~11) b : BTS Number(00~47) c : Processor Name(CCP,BSP)

Input : DIS-OVLD-THR:BSC=0,BTS=0,PROC=BSP;



XINTERM								
			Com	man	d Wii	ndow		
MSC(0x00)	2001-0	6-14	21:57:16					
M3501	DISPL ATE =	AY UVE RSCOOZ	KLUAD IH RTSOOZRS	KESHULD D	VALUE			
BSC	BTS	PROC	CLASS	LEVEL	LOAD	ORG_PER	TER_PER	
00	00	BSP	NOR	0	100	100	100	
			MIN	1	100	96	100	
			MIN	2	100	88	100	
			MIN	- 3 - 4	100	80 72	100	
			MIN	5	100	64	100	
			MIN	6	100	56	100	
			MIN	7	100	48	100	
			MIN	8	100	40	100	
			МАЈ МАЛ	10	100	34	100	
			MAT	11	100	16	100	
			MAD	12	100	8	100	
			MAD	13	100	Ō	96	
			MAD	14	100	0	88	
			MAD	15	100	0	80	
			MAJ CRT	16	100	U	72	
			CRT	18	100	ň	56	
			ČRI	19	100	ŏ	48	
			CRI	20	100	0	40	
			CRI	21	100	0	32	
			CRI	22	100	0	24	
			CRT	23	100	ň	8	
COMPLETED			CHI		100		0	
1								

Fig. 4.4-46 Result of Overload Threshold Value Display Command

4.4.7.3. Overload Threshold Value Change Command

Function to change Overload Threshold Value.

Command:

```
CHG-OVLD-THR:BSC:a,[BTS=b],LEVEL=c,[LOAD=d], [ORG_PER=e],[TER_PER=f];
```

- a : BSC Number(00~11)
- b: BTS Number(00~47)
- c : LEVEL(0~100)
- d:LOAD(0~100)
- e : ORG_PER(0~100)
- $f: TER_PER(0~100)$

Input :

```
CHG-OVLD-THR:BSC:0,BTS=0,LEVEL=0,LOAD=100, ORG_PER=97,TER_PER=100;
```



Fig. 4.4-47 Result of Overload Threshold Value Change Command

4.4.7.4. Overload Generation Test Command

Function to generate Overload threshold value Command : STRT-OVLD-GEN:BSC=a,[BTS=b],PROC=c,LEVEL=d;

- a : BSC Number(00~11)
- b : BTS Number(00~47)
- c: Processor Name(CCP,BSP)
- d: LEVEL(0~24)

Input : STRT-OVLD-GEN:BSC=0,BTS=0,PROC=BSP,LEVEL=0;





4.4.7.5. Overload Generation Test STOP Command

Function to stop the Overload Generation Test

Command : STOP-OVLD-GEN:BSC=a,[BTS=b],PROC=c,LEVEL=d;

a : BSC Number(00~11)

b : BTS Number($00 \sim 47$)

c: Processor Name(CCP,BSP)

d: LEVEL(0~24)

Input : STOP-OVLD-GEN:BSC=0,BTS=0,PROC=BSP,LEVEL=0;



Fig. 4.4-49 Result of Overload Generation Test STOP Command

4.4.7.6. Command to Display Whether or not the Overload Generation Test is performed

Function to find out whether the Overload Display Test is performed Command : DIS-OVLD-GEN:BSC=a,[BTS=b],PROC=c,LEVEL=d;

a : BSC Number(00~11)
b : BTS Number(00~47)
c: Processor Name(CCP,BSP)
d: LEVEL(0~24)

Input : DIS-OVLD-GEN:BSC=0,BTS=0,PROC=BSP,LEVEL=0;



X INTERM	<u>_ 0 ×</u>
Command Window	
MSC(0x00) 2001-06-14 22:09:41 M3522 OVERLOAD TEST STATUS LOCATE = BSC00/BTS00/BSP INFORM = NO TEST FOR PROCESSOR OVERLOAD RESULT = 0.K COMPLETED	

Fig. 4.4-50 Command to Determine Whether to Perform the Overload Generation Test



4.5. Test Command

4.5.1. On-Line Test-related Command

On-Line Test is a function that allows a test to be performed automatically at a specific time on a specific day and includes the following: Vocoder, CE, BTS Markov test and VSWR test. The tests of CE, BTS Markov and VSWR find out presence of faults in hardware of Channel Elements in BTS and the test function for radio environment, and the Vocoder test finds out presence of faults in hardware of Vocoder in BSC. Because the tests of CE, Vocoder, BTS Markov, and VSWR seize call resources, the user designates the specific time (idlest time) during the time that only a test is allowed. The tests of CE, BTS Markov, and VSWR are conducted under the supervision of BSP while the Vocoder test is conducted under the supervision of CCP. If the user designates the day and start/ending time for the Online test, and the ID scope of the board to be tested, BSM becomes the designated time of the designated day and if the test is in the ALLOW status, it commands the corresponding Processor to start/end the test.

The performance of the On-Line test is decided by Command INH-ONL-TEST (Inhibit Test), ALW-ONL-TEST(Allow Test). Only when the status is designated as "ALLOW" by ALW-ONL-TEST Command, On-Line test is performed. The Command of INH-ONL-TEST inhibits the test. Besides these two Commands, there is a command to stop the test by each Test(CE : STS-CE-ONL, Vocoder : STS-VCE-ONL, BTS Markov : STS-MKV-ONL, VSWR : STS-VSWR-ONL). If the test was stopped by INHIBIT Command, the On-Line Test is not performed even if the designated time of the designated day of the week arrives because inhibit/allow status is changed to "INHIBIT". ,. However, if the Test was stopped by the above listed STOP Command, the On-Line Test that was performed on that day only comes to a halt and at the designated time of the next designated day of the week the On-Line Test is to be performed normally because DB Flag that indicates the status of INHIBIT/ALLOW is not changed

•

4.5.1.1. On-Line Test Inhibit Command

It is the function that inhibits On Line Test for CE, Vocoder, BTS Markov, and VSWR test. In the cases of CE, BTS Markov, and VSWR test, input the corresponding BTS number and in the case of Vocoder test, input the corresponding BSC number.

• Command INH-ONL-TEST:EXE=a, BSC=b,[BTS=c];

a: VCE/CE/MKV/VSWR

- b: BSC number (0~11)
- c: BTS number $(0 \sim 47)$
- Input/Output

Command Window	Þ
MSC(0x00) 2001-06-14 20:07:07	
M4100 INHIBIT UNLINE VCE TEST LOCATE : BSCO1 RESULT : INHIBIT OK COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] INH-ONL-TEST:EXE=VCE,BSC=1,BTS=0; ACCEPTED [jileea <lgicbsm>] [jileea <lgicbsm>]</lgicbsm></lgicbsm></lgicbsm>	

Fig. 4.5-1 Result of Test Inhibit Command Execution

4.5.1.2. On-Line Test Allow Command

Function that allows Online Test for CE, Vocoder, BTS Markov, and VSWR test For CE, BTS Markov and VSWR test, input the corresponding BTS number and for Vocoder test, input corresponding BSC number.

- Command ALW-ONL-TEST:EXE=a,BSC=b[BTS=c];
 - a: VCE/CE/MKV/VSWR
 - b: BSC number $(0 \sim 11)$
 - c: BTS number (0~47)
- Input/Output



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Command Window	
MSC(0x00) 2001-06-14 20:49:01 M4101 ALLOW ONLINE VCE TEST LOCATE : BSC01 RESULT : ALLOW OK COMPLETED	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
ACCEPTED [jileea <lgicbsm>] ALW-ONL-TEST:EXE=VCE,BSC=1; ACCEPTED [jileea <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.5-2 Result of Test Allow Command Execution

4.5.1.3. On-Line Test Inhibit Item Display Function

Function to display the inhibit of the on line test for the specific tests among the online tests by BSC and BTS. For Vocoder test, it displays Inhibit status. In case of CE, BTS Markov and in case of the VSWR test, it displays the status by BTS.

- Command DIS-INH-ONL:EXE=a,BSC=b[BTS=c];
 - a: VCE/CE/MKV/VSWR
 - b: BSC number (0~11)
 - c: BTS number (0~47)
- Input/Output

] ×
Command Window	
MSC(0x00) 2001-06-17 12:22:46 M4102 DISPLAY INHIBITTED ONLINE VCE TEST BSC STATUS 0 INHIBITTED 1 INHIBITTED 2 INHIBITTED 3 ALLOWED 4 INHIBITTED 5 INHIBITTED 6 INHIBITTED 7 INHIBITTED 9 INHIBITTED 10 INHIBITTED 11 INHIBITTED 11 INHIBITTED 13 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] DIS-INH-ONL:EXE=VCE; ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>	
3	T

Fig. 4.5-3 Result of Test Inhibit/Allow List Display Command Execution

4.5.2. Test-related to Channel Element

Channel Element Test is performed in BIT(Built In Test) for each chip. The types of

BIT Test are as follows:

- Bus Interrupt Test
- ChipX16 Test
- PP2S Test
- PCG Test

This BIT Test is normally performed on Chip with OVHD channel and Chip seized with a call and the result is reported to BSM.

Table 4.5-1CE	Test Result Message

On-Demand	DESCRIPTION
BIT_OK	NORMAL
BIT_BUS_FAIL	BUS INTERFACE TEST FAIL
BIT_INT_FAIL	INTERNAL INTERRUPT TEST FAIL
BIT_CHIPX16_FAIL BIT_PP2S_FAIL	CHIPX16 TEST FAIL
BIT_PCG_FAIL	PCG TEST FAIL
BIT_RAM_FAIL	MEMORY TEST FAIL
BIT_PROGRESS_FAIL	TEST PERFORMING FAIL

4.5.2.1. Channel Element Test Function

Function to perform BIT test by CHC and CHIP unit.

- Command TEST-CE :BSC=a ,BTS=b ,PROC=c [,CHC=d] [,CHIP=e];
 - a: BSC number (0~11)
 - b: BTS number (0~47)
 - c: RCP number (0∼9)
 - d: Channel Card Number (0~9)
 - e: Chip Number (0~1)
- Input/Output



Command Window MSC(0x00) 2001-06-15 11:38:20 M4200 ON-DEMAND CHANNLE ELEMENT TEST LOCATE : BSC00/BTS01/RCP00 RESULT : TEST START 0K CONTINUE MSC(0x00) 2001-06-15 11:38:21 M4200 ON-DEMAND CHANNLE ELEMENT TEST LOCATE : DSCOM/DTEMAND CHANNLE ELEMENT TEST LOCATE : DSCOM/DTEMAND CHANNLE ELEMENT TEST
MSC(0x00) 2001-06-15 11:38:20 M4200 ON-DEMAND CHANNLE ELEMENT TEST LOCATE : BSC00/BTS01/RCP00 RESULT : TEST START OK CONTINUE MSC(0x00) 2001-06-15 11:38:21 M4200 ON-DEMAND CHANNLE ELEMENT TEST LOCATE : DECOMPTON (DECOMPTON (DECOMPTON))
MSC(0x00) 2001-06-15 11:38:21 M4200 ON-DEMAND CHANNLE ELEMENT TEST
TARGET : NORMAL CHIP RESULT_0 : NORMAL CONTINUE
MSC(0x00) 2001-06-15 11:38:23 M4200 ON-DEMAND CHANNLE ELEMENT TEST LOCATE : BSC00/BTS01/RCP00/CHC02/CHIP01 TARGET : NORMAL CHIP RESULT_0 : NORMAL COMPLETED
AA
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
ACCEPTED [jileea <lgicbsm>] TEST-CE:BSC=0,BTS=1,PROC=0,CHC=2; ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>

Fig. 4.5-4 Result of CE BIT Test Execution


4.5.3. Vocoder Test Function

The types of Vocoder Tests are as follows:

- o DSP H/W Test
 - Determines the presence of problems in ROM, RAM, ALU of DSP in Vocoder
 - Diagnosis the presence of problems by checking Checksum, Read/Write, Flag Set, etc.
 - Caused by defective DSP Chip
- o Timing Module Test (Channel Test)
 - VCP generates Vocoder and Tx.Rx Timing by using MFP(Multiple-Function Processor) in order to exchange voice data with SLP every 20ms.
 - It judges the presence of problems in Vocoder counter by checking the generated Rx.Tx timing.
- o Loopback Test (Channel Test)
 - It is a test to decide whether or not the status of Loop used in Qcelp Algorithm test is normal and its Loop section has VLIA, VCMA and VCPA.
 All of them should be normal to execute the Qcelp Algorithm test. VCP generates Test Pattern and by checking the Loopbacked result, it decides an absence of failure.
- o QCELP Algorithm Test (Channel Test)
 - Due to intermittent problems in Vocoder and VCPA Card H/W, a phenomenon that a call is not heard bi-directionally occurs. In order to prevent this from happening, Qcelp Algorithm is tested. This test determines whether Vocoder is normal by testing whether status transition of Qcelp Algorithm is normally performed by the state.

Table 4.5-2Vocoder Test Result

On-Demand / On-Line Message	Description
-----------------------------	-------------



FLT_DSP_HW	DSP HW test Fault	
FLT_CLK	Timing Module test Fault	
FLT_VCPA_LOOPBACK	Vocoder Processor Loopback test Fault	
FLT_VCMA_LOOPBACK	Vocoder Master board Loopback test Fault	
FLT_VLIA_LOOPBACK	Vocoder Line interface Loopback test Fault	
FLT_QCELP_ST0	Qcelp Algorithm test Fault state0	
FLT_QCELP_ST1	Qcelp Algorithm test Fault state1	
FLT_QCELP_ST2	Qcelp Algorithm test Fault state2	
FLT_QCELP_ST3	Qcelp Algorithm test Fault state3	

4.5.3.1. On-Demand Vocoder Test Function

Function to test Vocoder DSP or Channel by VCP/SLAVE/CHIP unit.

```
• Command TEST-VCE: BSC=a ,VMP=b ,S_VCP=c [,E_VCP=d] [,SLAVE=e] [,CHIP=f]
```

[,VCE=g],TYPE=h [,OPTION=i] [,WAIT_T=j]

- a: BSC number (0~11)
- b: VMP number $(0 \sim 7)$
- c, d: VCP number (0~15)
- e: SLAVE number (0~1)
- f : CHIP number (0 ~ 3)

h: Test Type(DSP, Channel)

i: Test Option(SKIP, RELEASE, WAIT_THEN) => These are options used to test channels when a call is seized, and SKIP does not perform a test when a call is seized but skips. With option RELEASE, it disconnects a call when a call is seized and then performs a test. With option WAIT_THEN, it waits as long as j time is allowed and if a call is released within the designated time, then it goes on with a test. However, if a call is not released, it skips. J: Wait Time(5~300 sec)

• Input/Output





Fig. 4.5-5 Result of On-Demand Vocoder Test(Channel Type) Execution





Fig. 4.5-6 Result of On-Demand Vocoder Test(DSP Type) Execution

4.5.3.2. Parameter Change Command Related to On-Line Vocoder Test

Function to change On-Line Vocoder test start/ending time, test day and the scope of VMP/VCP to be tested.

- Command CHG-VCE-PARA :BSC=a ,S_VMP=b [,E_VMP=c] ,S_VCP=d [,E_VCP=e]
- [,STI ME=f] [,ETIME=g] ,WDAY=h;
 - a: BSC number (0~11)
 - b,c: VMP number(0~7)
 - d,e: VCP number(0~15)
 - f: On-Line test Start Time (0~23 Hour)
 - g: On-Line test End Time (1~24 Hour)
 - h: Test Day(Month ~ Day, Daily)
- Input/Output

Command Window
MSC(0x00) 2001-06-15 14:35:44 M4310 CHANGE VCE TEST PARAMETER LOCATE : BSC_00 START VMP : 00 END VMP : 00 START VCP : 00 END VCP : 02 START TIME : 02 END TIME : 04 TEST DAY : BEFORE MON CURRENT MON COMPLETED
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>
[jileea <lgicbsm>] CHG-VCE-PARA:BSC=0,S_VMP=0,E_VMP=0,S_VCP=0,E_VCP=2,ST ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>

Fig. 4.5-7 Result of On-Line Vocoder Test parameter Change Command Execution



Fig. 4.5-8 Result of On-Line Test at the time of On-Line Vocoder Test Execution

Parameter Change

4.5.3.3. Parameter Display Command related to On-Line Vocoder Test

Function to display On-Line Vocoder test start/ending time, test day, and the scope of VMP/VCPto be tested.

• Command DIS-VCE-PARA:BSC=a;

a: BSC number (0~11)

• Input/Output

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Command Window	
MSC(0x00) 2001-06-15 14:36:16 M4311 DISPLAY VCE TEST PARAMETER L0CATE : BSC_00 START VMP : 00 END VMP : 00 START VCP : 00 END VCP : 02 START TIME : 02 END TIME : 04 TEST DAY : MON COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] DIS-VCE-PARA:BSC=0; ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>	⊼ Z
AL	

Fig. 4.5-9 Result of On-Line Vocoder Test Parameter Display Command Execution

4.5.3.4. Display Command of On-Line Vocoder Test Result

Function to display the On-Line Vocoder Test Results which are stored in CCP to BSM by the VMP unit

- Command DIS-VCE-RSLT:BSC=a,VMP=b;
 - a: BSC number (0~11)
 - b: VMP number $(0 \sim 7)$

Input/Output

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Γ						(Comme	ana	Wind	ow				
	MSC()	0x00) M4312 Loc	2001 DIS	-06-' Play	17 08:12 ONLINE V RSC 00/VM	:04 CE T P 00	EST RESUL	.T						
		VCP	SLV	DSP	STS	VCE	STS	VCE	STS	VCE	STS	VCE	STS	
		0	0	0 1 2 3	0K 0K 0K 0K	00 00 00 00	RSVD_CH OK OK OK	01 01 01 01	ОК ОК ОК ОК	02 02 02 02	ОК ОК ОК ОК	03 03 03 03	0K 0K 0K 0K	
			1	0 1 2 3	OK NO_DATA NO_DATA NO_DATA	00 00 00 00	RSVD_CH NO_DATA NO_DATA NO_DATA	01 01 01 01	OK NO_DATA NO_DATA NO_DATA	02 02 02 02	OK NO_DATA NO_DATA NO_DATA	03 03 03 03 03	OK NO_DATA NO_DATA NO_DATA	
		1	0	0 1 2 3	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	00 00 00 00	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	01 01 01 01	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	02 02 02 02	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	03 03 03 03	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	
			1	0 1 2 3	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	00 00 00 00	NO_DATA NO_DATA NO_DATA NO_DATA	01 01 01 01	NO_DATA NO_DATA NO_DATA NO_DATA NO_DATA	02 02 02 02	NO_DATA NO_DATA NO_DATA NO_DATA	03 03 03 03	NO_DATA No_data No_data No_data No_data	
		2	0	0	NO_DATA	00	NO_DATA	01	NO_DATA	02	NO_DATA	03	NO_DATA	
					_				_	_				
	<u>I</u> 0	<u>L</u> DNG	<u>C</u> DM	<u>s</u> t	M <u>t</u> est	<u>N</u> 0.1	7 ST <u>A</u> T	AL <u>F</u> I	PREVIO	US				
	[ji] ACCE [ji]	eea <l PTED eea <l< th=""><th>GICB GICB</th><th>SM>] SM>]</th><th>dis-vce- I</th><th>RSLT</th><th>:BSC=0,VM</th><th>IP=0;</th><th></th><th></th><th></th><th></th><th></th><th></th></l<></l 	GICB GICB	SM>] SM>]	dis-vce- I	RSLT	:BSC=0,VM	IP=0;						
F														

Fig. 4.5-10 On-Line Vocoder Test Result Display Command

4.5.3.5. Display Command of On-Line Vocoder Test Status

Function to display the On-Line Vocoder Test performance status and to stop the test. If Item Field is designated as "DISPLAY", it displays On-Line Vocoder test status of the present corresponding BSC, and if it is designated as "END_ONL", it stops current On-Line test.

- Command STS-VCE-ONL:BSC=a,ITEM=b;
 - a: BSC number (0~11)
 - b: Execution ITEM (DISPLAY/END-ONL)
- Input/Output

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Command Window	
MSC(0x00) 2001-06-17 08:13:33 M4313 DISPLAY ONLINE VCE TEST STATUS L0CATE : BSC_00 RESULT : TESTING COMPLETED MSC(0x00) 2001-06-17 08:13:37 M4313 END ONLINE VCE TEST L0CATE : BSC_00 RESULT : OK COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
<pre>[jileea <lgicbsm>] STS-VCE-ONL:BSC=0,ITEM=DISPLAY; ACCEPTED [jileea <lgicbsm>] STS-VCE-ONL:BSC=0,ITEM=END_ONL; ACCEPTED</lgicbsm></lgicbsm></pre>	

Fig. 4.5-11 On-Line Vocoder Test End Command, Status Display Command Execution Result

4.5.4. Link Test Function

Link test is divided into PING test, IPC test, ATM Path test, and Trunk BER test. PING test checks the Link status of Application Level by using Ping command from Master Processor to each Target Processor. IPC test checks the presence of problems in LINK by conducting IPC Test to the mounted processors/devices. ATM Path test checks presence of faults in ATM LINK Level by conducting ATM Layer Test on the mounted Processor/Devices. Trunk BER test sends/receives the designated number of ATM Cells to the operator designated BTS Link to get Error Rate.

4.5.4.1. Trunk BER Test Function

Function to check the presence of problems in Trunk(16 E1) Link between BTS and BSC. Trunk BER Test analyzes performance per Link of each ALPA and LICA and the analyzed result is displayed in a certain format to BMS. One link is selected and while exchanging as many as the designated number of ATM Cells, test is conducted and Data Error Rate is displayed.

- Command TEST-LINK :BSC=a ,ALPA=b [,CNT=c] [,RATE=d];
 - a: BSC number (0~11)
 - b: ALPA number(0~9)
 - c: Send Cell Count(1 ~ 1,000,000)
 - d: Data Rate(1 ~ 90: Number of Cells transferred per a second)
- Input/Output

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		Com	mand	Window						
MSC(0x00) 3 M4483	2001-06-15 DISPLAY B	16:51:55 TS LINK BE	R TEST							
	RATE : 10 cps CONTINUE MSC(0x00) 2001-06-15 16:51:57									
M4483 LOC/	DISPLAY B ATE = BSCO	TS LINK BE	R TEST							
E1	SEND_CNT	RECV_CNT	LOSS_CNT	DELAY_CNT	CHKSUM_CNT					
0 1 2	20 20 20	20 20 0	0 0 20	0 0 0	0 0 0					
34	20 0	20 0	0	0 0	0 0					
5	0	0	0	0	0 0					
8	U 0 0	U 0 0	U 0 0	U 0 0	U () ()					
10 11	Ŭ O	ů O	Ö	Ŭ O	0 0					
12 13	0	0	0 0	0	0					
14 15	0 0	0 0	0 0	0 0	0 0					
<u>1</u> 0 <u>L</u> DNG	<u>C</u> DM <u>s</u> tm	<u>T</u> EST <u>N</u> O	.7 ST <u>A</u> T	AL <u>F</u> T PREV	TOUS					
ACCEPTED [jileea <l0 ACCEPTED [jileea <l0< td=""><td>GICBSM>] T GICBSM>]</td><td>EST-LINK:B</td><td>SC=0,ALPA=</td><td>0, CNT=20, RA</td><td>ιTE=10;</td><td></td></l0<></l0 	GICBSM>] T GICBSM>]	EST-LINK:B	SC=0,ALPA=	0, CNT=20, RA	ιTE=10;					

Fig. 4.5-12 Result of Trunk BER Test Performance

4.5.4.2. PING Test Function

Function to check Link Status for the Target Processor by the Master Processor that was input to MMI of BSM by using the Ping command

Ping test is divided into Point To Point Test and Point To Multi Test.

- Command TEST-PING:SRC=a,DST=b[,BSC=c][,BTS=d][,SMP=e][,VMP=f][,PCF=g];
 - a: Source Processor
 - b: Destination Processor(If there is input, it is PTP, if not, it is PTM)
 - c: BSC number(0~11)
 - d: BTS number(0~47)

- e: SMP number(0~5)
- f: VMP number(0~7)
- g: PCF number(0~2)

• Input/Output

X INTERM				. 🗆 ×
	Com	mand Wína	low	
MSC(0x00) 2001-06-17 M4480 ON-DEMAND MASTER : NCP RESULT : Point CONTINUE MSC(0x00) 2001-06-17 M4480 ON-DEMAND TARGET BSCO/ALP BSCO/CCP BSCO/CCP BSCO/CCP BSCO/CCP BSCO/SCP BSCO/SCP BSCO/SCP BSCO/SCP BSCO/SCP BSCO/SSP0 BSCO/SSP1 CAN-D/PNP COMPLETED	08:46:52 PING TEST t to Multi 08:47:03 PING NCP T DELAY(ms) 0.0 0.0 0.2 0.2 0.2 0.2 0.2 0.0 0.2 0.2	Test Start EST LOSS(Percent) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	RESULT ALIVE ALIVE ALIVE ALIVE ALIVE ALIVE ALIVE ALIVE ALIVE ALIVE	
A				
<u>I</u> O <u>L</u> DNG <u>C</u> DM <u>S</u> TM	<u>t</u> est <u>N</u> 0.	7 ST <u>A</u> T AL <u>F</u> T	PREVIOUS	
[jileea <lgicbsm>] T ACCEPTED [jileea <lgicbsm>] ĭ</lgicbsm></lgicbsm>	EST-PING:SR	C=NCP,BSC=0;		

Fig. 4.5-13 Result of PING Test Performance

4.5.4.3. IPC Test Function

Function to check the presence of problems in the Link status between Processor/Device by executing IPC command of Application Level .

IPC Test is divided into PTP test and PTM test.

• Command TEST-IPC :SRC=a [,DST=b] [,BSC=c] [,BTS=d] [,SMP=e] [,VMP=f] [,PCF=g];

- a: Source Processor
- b: Destination Processor(If input does exist, it is PTP and if not, it is PTM)
- c: BSC number(0~11)
- d: BTS number(0~47)

- e: SMP number(0~5)
- f: VMP number(0~7)
- g: PCF number(0~2)

• Input/Output

× INTERM				_ 🗆 ×
	Con	nmand Wi	ndow	
MSC(0x00) 2001-06-17 M4481 ON-DEMAND MASTER : NCP RESULT : Point CONTINUE	08:50:35 IPC TEST to Multi	5 i Test Start		
MSC(0x00) 2001-06-17 M4481 ON-DEMAND TARGET	08:50:54 IPC NCP T SEND REC	I Fest CV CHK_SUM(OK) CHK_SUM(NOK)	
BSCO/ALP BSCO/CCP BSCO/VMPD	4 4 4 4 4 4	4 4 4	0 0 0	
BSCO/SMPO CAN/CNP BSCO/SCP	4 4 4 4 4 4	4 4 4	0 0 0	
BSCO/BSPO BSCO/BSP1 CAN-D/PNP	4 4 4 4 4 4	4 4 4	0 0 0	
<u>IO LONG COM S</u> TM	<u>t</u> est <u>N</u> o).7 ST <u>A</u> t al <u>i</u>	ET PREVIOUS	
[jileea <lgicbsm>] TH ACCEPTED [jileea <lgicbsm>] [</lgicbsm></lgicbsm>	EST-IPC:SR	RC=NCP,BSC=0;		E E

Fig. 4.5-14 Result of IPC Test Performance

4.5.4.4. ATM Path Test Function

Function to check presence of problems in Link Status between Processor and Device by executing ATM CC, LB, PM command.

It is divided into PTP Test and PTM Test.

• Command TEST-

TM:SRC=a[,DST=b],CLASS=c,LEVEL=d[,BSC=e][,BTS=f][,SMP=g][,VMP=h][,PCF=i];

- a: Source Processor
- b: Destination Processor(If input does exist, it is PTP, and if not, it is PTM)
- c: Test class(CC: Continuity Check, LB: Loop Back, PM: Execution Monitor)

- d: Test Level(F4: VPC, F5: VCC)
- e: BSC number(0~11)
- f: BTS number(0~47)
- g: SMP number(0~5)
- h: VMP number(0~7)
- i: PCF number(0~2)
- Input/Output

XINTERM	١×				
Command Window					
MSC(0x00) 2001-06-17 08:48:02 M4482 ON-DEMAND ATM TEST MASTER : NCP RESULT : Point to Multi Test Start CONTINUE					
MSC(0x00) 2001-06-17 08:48:16 M4482 ON-DEMAND ATM NCP TEST CLASS : PM LEVEL : F4					
TARGET SEND RECV DELAY(tick)					
BSCO/ALP 10 10 0 BSCO/CCP 10 10 0 BSCO/VMPO 10 10 0 BSCO/VMPO 10 10 0					
BSCU/SMPU 10 10 0 CAN/CNP 10 10 0 BSC0/SCP 10 10 0 RSC0/RSP0 10 10 0					
BSCO/BSP1 10 10 0 CAN-D/PNP 10 10 0 COMPLETED					
 	I 🗄				
<u>IO LDNG CDM STM IEST NO.7 STAT ALET PREVIOUS</u>					
Image: Construct of the construction of the constructio					
	ł.				

Fig. 4.5-15 Result of ATM Path(PM) Test Performance



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Command Window	
MSC(0x00) 2001-06-17 08:48:49 M4482 ON-DEMAND ATM TEST MASTER : NCP RESULT : Point to Multi Test Start CONTINUE MSC(0x00) 2001-06-17 08:49:01 M4482 ON-DEMAND ATM NCP TEST CLASS - CC LEVEL - E4	
TARGET RESULT BSC0/ALP 0K BSC0/CCP 0K BSC0/SMPO 0K BSC0/SMPO 0K CAN/CNP 0K BSC0/SCP 0K BSC0/BSP0 0K BSC0/BSP1 0K CAN-D/PNP 0K COMPLETED 1	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jileea <lgicbsm>] TEST-ATM:SRC=NCP,CLASS=CC,LEVEL=F4,BSC=0; ACCEPTED [jileea <lgicbsm>] [</lgicbsm></lgicbsm>	

Fig. 4.5-16 Result of ATM Path(CC) Test Performance



XINTERM	_	
	Command Window	
MSC(0x00) 2001-06-17 M4482 ON-DEMAND MASTER : NCP RESULT : Point CONTINUE	08:49:54 ATM TEST t to Multi Test Start	
MSC(0x00) 2001-06-17 M4482 ON-DEMAND CLASS : LB	08:50:06 ATM NCP TEST LEVEL : F4	
TARGET	RESULT	
BSCO/ALP BSCO/CCP BSCO/VMPO BSCO/SMPO CAN/CNP BSCO/SCP BSCO/BSP0 BSCO/BSP1 CAN-D/PNP COMPLETED	OK OK OK OK OK OK OK OK OK	
TO LONG COM STM	TEST NO.7 STAT ALFT PREVIOUS	
[jileea <lgicbsm>] TE ACCEPTED [jileea <lgicbsm>] [</lgicbsm></lgicbsm>	EST-ATM:SRC=NCP,CLASS=LB,LEVEL=F4,BSC=0;	

Fig. 4.5-17 Result of ATM Path(LB) Test Execution



4.5.5. Command related to BSC Virtual Call Test

General mobile call is —made by MS, BSS(BSC, BTS), switching system interworking. Because the section between MS and BSS is the place where call environment setup function and modulation and demodulation of voice data are made including allocation of call resource necessary for radio communication, it is responsible for essential function in the mobile communication system. Testing call is the function to check the following: 1) the presence of faults in call processing made between BSS and MS that excludes the MSC function in the course of mobile call processing, and 2) the quality of voice. Since it excludes the MS function to, mutual calls such as M2L, L2M, or M2M cannot be made; however, , but cantest can be perfomed by selecting one between originating and terminating call. When a testing call was set up, BSS and MS generate markov data and exchange them and by checking if there is any loss of markov data, they calculate quality of voice. Terminating call inputs IMSI value of MS to be tested in BSM and trys paging to set up a call. And Originating call sets up a call by selecting testing call function of MS. (Originating call is not related to BSM function.)

• BSC Testing Call

It checks the following: 1) if there is any faults in call processing that is carried out between BSC(SLP) – BTS(CE) – MSs and 2) the quality of voice. If a Call is attempted in BSM by inputting the the information (i.e., the number of MS, BSC number, BTS number, sector, frequency resource, MS call setup data (Station Class Mark, Slot Cycle Index), Service Option(13K Markov, 8K Markov, 13K LoopBack, 8K LoopBack), and voice packet data rate(Full, half, Quarter, Eighth , Variable rate)), then Call Link between SLP-CE-MS is set up (setup of testing call) according to the data input. Once a Call is set up, SLP and MS calculate Frame Error Rate while exchanging markov data. When a Call is set up and a Call is released, BSM displays data of call resource, FER, and reason for release.

• Service Option

Service Options of present use for Testing call at BSC include 8K Markov, 13K Markov, 8K loopback, 13K loopback .

• Markov

Once Testing Call is set up, SLP and MS synchronizes their time, and generate identical data in sequence by Markov Algorithm. SLP and MS receive each other's data and compare them with their own to detect errors in the data frame. It can obtain Forward FER(measured at MS) and Reverse FER(measured at SLP).

LoopBack: When Testing Call is set up, SLP generates voice data and sends them to MS. MS performs lookback on this data as it is and SLP compares returned data with

the original data to validate the quality of voice. (FER measured at SLP)

• Data Rate

User can designate voice data rate to be used for testing call. The data rate is divided into the following: Full rate, Half rate, Quarter rate, Eighth rate, and Variable rate.

4.5.5.1. BSC Virtual Call Setup Function

Function to designate various options and setting up Call to a specific MS or all the MSs MS to be tested should be entered to BSM DB with INS-TEST-MS command. When TEST_KEY is not input, testing calls are set up for all the MSs stored in DB. Testing Call is attempted for 60 minutes at a maximum. Once a Call is set up, it is maintained for 60 minutes. If a call is disconnected due to the occurrence of faults, or when a user released the call at random, a message for the reason of call release is displayed.

• Command SET-TEST-CALL :BSC=a ,BTS=b ,SECTOR=c ,CDMA=d ,SCI=e ,SCM=f , OPTI=g ,DTYPE=h ,TIME=i [,TEST_KEY=j];

- a: BSC number (0~11)
- b: BTS number (0~47)
- c: sector (ALPHA/BETA/GAMMA/DELTA/ZETA/EPSILON)
- d: CDMA number (0~11)
- e: SLOT CYCLE Index (0~7)
- f: SLOT mode (SLOT_M, NON_SLT_M)
- g: option (MKV_13K/MKV_8K/LB_13K/LB_8K)
- h: Data Rate (VARIABLE/FULL/HALF/QUART/EIGHT)
- i: TIME(1~60min)
- j: Test Key(1 ~ 100)
- Input/Output



×INTERM	
Command Window	
MSC(0x00) 2001-06-17 08:37:57 M4500 MARKOV CALL TEST SETUP SINGLE REQUEST ACCEPT COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STA</u> T AL <u>F</u> T PREVIOUS	
[jileea <lgicbsm>] SET-BSC-CALL:BSC=0,BTS=0,SECTOR=ALPHA,CDMA=0,SCI=2,SC ACCEPTED [jileea <lgicbsm>]]</lgicbsm></lgicbsm>	AI V
	X

Fig. 4.5-18 BSC Virtual Call Setup Command INTERM Display



Fig. 4.5-19 Console Window Display at the setup of BSC Virtual Call



Fig. 4.5-20 Display at the Termination of BSC Virtual Call

4.5.5.2. Virtual Call Release Function

Function to release a call for a specific MS or all the MSs where a virtual call set up. The output resulting from virtual call release is displayed by "Release by MMC".

- Command REL-TEST-CALL :BSC=a ,BTS=b [,TEST_KEY=c];
 - a: BSC number (0~11)
 - b: BTS number (0~15)
 - c: TEST KEY(1~100)
- Input/Output

XINTERM	
Command Window	
MSC(0x00) 2001-06-17 08:38:24 M4501 MARKOV CALL TEST RELEASE SINGLE REQUEST ACCEPT COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] REL-TEST-CALL:BSC=0,BTS=0,TEST_KEY=1; ACCEPTED [jileea <lgicbsm>]]</lgicbsm></lgicbsm>	AI V
4	N

Fig. 4.5-21 Result of BSC Virtual Call Release Command Execution

4.5.5.3. Testing MS Display Function

Function to display MSs to be used for the BSC Virtual Call test.

- Command DIS-TEST-MS :BSC=a ,BTS=b;
 - a: BSC number (0~11)
 - b: BTS number (0~47)
- Input/Output



XINTERM	×
Command Window	
MSC(0x00) 2001-06-17 08:34:31 M4505 DISPLAY MOBILE STATION NUMBER LOCATE : BSC_00/BTS_00	Δ
KEY STATUS SECTOR DATA_RATE MOBILE 1 NORMAL ALL BLANK 0162907023 2 NORMAL ALL BLANK 0162907024 3 NORMAL ALL BLANK 0162907025	
TOT_EQP TESTING NORMAL A B G D E Z 3 0 3 0 0 0 0 0 COMPLETED	
A I	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] DIS-TEST-MS:BSC=0,BTS=0; ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>	N N
N N	

Fig. 4.5-22 Presently registered Testing MS Display

4.5.5.4. MS Supplementary Function

Function to register MS to be used for the BSC Virtual test.

For a test of virtual call, first register MS.

- Command INS-TEST-MS :BSC=a ,BTS=b ,TEST_KEY=c [,MSIN=d];
 - a: BSC number (0~11)
 - b: BTS number (0~47)
 - c: TEST KEY(1~100)
 - d: IMSI of MS
- Input/Output



	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 08:34:12 M4507 INSERT MOBILE STATION NUMBER MOB_ID : 0162907023 RESULT : ACCEPTED COMPLETED	
X	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] INS-TEST-MS:BSC=0,BTS=0,TEST_KEY=1,MSIN=0162907023 ACCEPTED</lgicbsm>	

Fig. 4.5-23 Testing MS Insert Result Display

4.5.5.5. MS Delete Function

Function to delete MS registered on the test list of BSC Virtual Call

- Command DEL-TEST-MS :TEST_KEY=a;
 - a: TEST_KEY of Mobile to be deleted
- Insert/Output





4.5.6. TRAFFIC PATH TESTING FUNCTION

it is a test enabling to decide if there is presence of failure with designated traffic path by setting a traffic path for BSC virtual call for a specific MS with the designated call resources and then measuring PER. The kinds of virtual call include Markov and Loop Back., There are 8K and 13K respectively by service option. Call resources that the user can designate are as follows: Selector Slave Processor, Trunk, and BTS channel Chip. In one BSC, 100 virtual calls can be set up simultaneously separated from testing function of virtual call. Once a call is set up, a message type which is identical to the message type coming from BSC virtual call setup process is displayed.

4.5.6.1. Traffic Path Testing Setup Function

Function to set up a Call for the designated Traffic Path

- Command SET-PATH-CALL : IMSI=a , BSC=b , SMP=c , SLP=d , SLPSLV = e [,TRK =
- f], BTS= g ,SECTOR = h ,CDMA = i ,RCP= j , MCPA=k , CHIP=I , SCI=m , SCM=n ,
- OPTI=o, DTYPE=p [,TIME=q];
- a : IMSI
- b : BSC_Number(0~11)
- c : SMP_Number(0~4)
- d : SLP_Number(0~19)
- e : SLP_Slave_Number(0~3)
- f:Trunk_Number(0~19)
- g:BTS_Number(0~47)
- h : Sector_Id(0~5)
- i : CDMA_Channel_Number(0~11)
- j:RCP_Number(0~9)
- k : MCPA_Number(0~9)
- I: CHIP_Number(0~1)
- m : Slot_Cycle_Index(0~7)
- n : Station_Class_Mark(SLOT_M, NON_SLT_M)
- o : Service_Option(MKV_13K, MKV_8K, LB_13k, LB_8K)
- p : Test_data_Type(VARIABLE, RATE_FULL, RATE_HALF, RATE_QUAR, RATE_EIGHT)
- q:TRACE_TIME(1~60min)
- Input/Output



XINTERM _	
Command Window	
MSC(0x00) 2001-06-18 11:14:25 M4510 PATH TEST CALL SETUP SINGLE REQUEST ACCEPT COMPLETED	
<u>IO LDNG CDM STM IEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] SET-PATH-CALL:IMSI=0162907023,BSC=0,SMP=0,SLP=0,SLPSU ACCEPTED [jileea <lgicbsm>]]</lgicbsm></lgicbsm>	
4	X

Fig. 4.5-25 Traffic Path Setup Command Execution Result Display(Interm Window)



Fig. 4.5-26 Traffic Path Setup Command Execution Result Display(Console Window)

4.5.6.2. Traffic Path Release Function

Function to release a Call to MS of a specific BSC, BTS where a virtual call is set up.

- Command REL-PATH-CALL :BSC=a ,BTS=b;
 - a: BSC number (0~11)
 - b: BTS number (0~15)
- Input/Output

	- 비스
Command Window	
MSC(0x00) 2001-06-18 11:16:45 M4511 PATH TEST CALL RELEASE SINGLE REQUEST ACCEPT COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] REL-PATH-CALL:BSC=0,BTS=0; ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>	

Fig. 4.5-27 Traffic Path Release Command Execution Result Display(Interm Window)



Fig. 4.5-28 Traffic Path Release Command Execution Result Display(Console Window)

4.5.7. Call Trace Testing Function

Mobile Call Trace Function traces the process of call setup for the MS, surveillance of the status after the call setup, the process of call release, and displays the content visibly to BSM after BSM of User's terminal designates a Mobile Station(MS) at random by using a value of MSIN(Mobile Station Identification Number). . The designated Mobile Station(MS) can trace the originating call or terminating call that was set up by the user and if necessary, it sets up Markov call(i.e., a terminating Markov call by paging) for tracing. Mobile call tracing can be performed to all kinds of calls(Voice Call, Data Call, HandOff Call) and can designate maximum two calls simultaneously for tracing. Information provided upon call tracing is as follows:

Tracing Information when Call is set

Resource of Call to be set

System Resource: BSC No, BTS No, Sector No, PN Offset

Attributes of Call: IMSI, ESN, Service Option, Call Type(Voice Call originating, Voice Call terminating, DATA Call initial set, DATA Call Reactivation by MS, DATA Call Reactivation by Network), Terminating number

BTS Resource: CDMA CH(Frequency) No, TC No(RCP#, MCPA#, CE#),

Code CH(Walsh code), Frame Offset

BSC Resource: SLP, VCE Number, CIC(Circuit Identifier Code)

Setup Process

Message display between processors during Call Setup: Visible Display of Call Flow During Call Setup, display RTD(Round Trip Delay) value and calculated distance by using this.

When Call Failure occurs, display reason value and its meaning Call Set Time by section

Tracing Information at the phase of calling after Call Setup: Tracing and Display at intervals of 1 ~ 5 seconds.

Elements of Communication Quality Forward FER(Frame Error Rate): Present FER, Total FER Backward FER(Frame Error Rate): Present FER, Total FER Power Control Parameter: TC Gain, Reverse Power Control Threshold Location Estimate Elements Present Active PN Offsets Aggregation : BSC No, BTS No, Sector, Cdma Ch, Walsh_ch, TC Id, RTD Present RTD Value and calculated distance by using this

Tracing Information at the phase of Call Release: Tracing it every time at the normal

release or abnormal release

- Reason for Call Release
- Reason for Call Release and its content: Display reason value and its meaning
- Quality Elements of Call
- Forward Total FER(Frame Error Rate)
- Backward Total FER(Frame Error Rate)

4.5.7.1. Call Trace Start/End Function

Function that starts/ends Call Trace with MS designated at random.

- Command TRC-CALL : IMSI=a, ITEM=b,BSC=c;
 - a: IMSI Number
 - b: Start / Stop
 - c: BSC ID
- Input/Output

Command Window	
MSC(0x00) 2001-06-17 08:17:28 M4700 CALL TRACE TEST M0B ID : 0162907023 RESULT : TRACE START at BSC00 CONTINUE	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jileea <lgicbsm>] TRC-CALL:IMSI=0162907023,ITEM=START,BSC=0; ACCEPTED</lgicbsm>	





Command Window
1997-11-18 16:17:48 M4480 CALL TRACE TEST M0B ID : 6194551003
1997-11-18 16:17:48 M4480 CALL TRACE TEST MOB ID : 6194551003 MESSAGE: PageMooRsp_B2C BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:48 M4480 CALL TRACE TEST M0B ID : 6194551003 MESSAGE: AssgnRsp_B2C CDMA_CH: 0 WALSH:0 FRAME_0FFSET:1 DCP: 0 MCPA:0 CE:12 CONTINUE
1997-11-18 16:17:48 M4480 CALL TRACE TEST M0B ID : 6194551003 MESSAGE: TcLinkReq_C2S BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:48 M4480 CALL TRACE TEST M0B ID : 6194551003 MESSAGE: AssgnAck_C2B BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:49 M4480 CALL TRACE TEST MOB ID : 6194551003 MESSAGE: mob_acq_ct1 BSC: 0 BTS: 02/0 CONTINUE
X X
IO LONG COM STM TEST NO.7 STAT ALFT PREVIOUS
ACCEPTED
ZI D

Fig. 4.5-30 Call Trace Display in the Process of Call Set

Command Window
1997-11-18 16:17:49 M4480 CALL TRACE TEST MOB ID : 6194551003 MESSAGE: ms_ack_order BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:49 M4480 CALL TRACE TEST MOB ID : 6194551003 MESSAGE: so_complete BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:49 M4480 CALL TRACE TEST MOB ID : 6194551003 MESSAGE: TcLinkAct_S2C BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:49 M4480 CALL TRACE TEST MOB ID : 6194551003 MESSAGE: Alerting_C2S BSC: 0 BTS: 02/0 CONTINUE
1997-11-18 16:17:50 M4480 CALL TRACE TEST FER REPORT MOB ID : 6194551003 FOR_FER :0.00 REV_FER :0.00 TOT_FOR_FER :0.00 TOT_REV_FER :0.00 RPC_THRESHOLD:2232 TC_GAIN:48 CUR_RTD:188, 0, 0 CONTINUE
1997-11-18 16:17:55 M4480 CALL TRACE TEST FER REPORT MOB ID : 6194551003 FOR_FER :4.80 REV_FER :2.80 TOT_FOR_FER :4.28 TOT_REV_FER :2.50 RPC_THRESHOLD:1320 TC_GAIN:59 CUR_RTD:220, 0, 0 CONTINUE
IO LDHG CDM STM TEST NO.7 STAT ALFT PREVIOUS
ACCEPTED
KI KI

Fig. 4.5-31 Display of Call Set Process and Elements of Communication Quality

INTERM	
Command Window	
1997-11-18 16:18:15 M4480 CALL TRACE TEST FER REPORT M0B ID : 6194551003 FOR_FER :0.80 REV_FER :19.20 TOT_FOR_FER :1.17 TOT_REV_FER :10.23 RPC_THRESHOLD:2900 TC_GAIN:40 CUR_RTD:247, 0, 0 CONTINUE 1997-11-18 16:18:20 M4480 CALL TRACE TEST FER REPORT M0B ID : 6194551003 FOR_FER :4.80 REV_FER :8.40 TOT_FOR_FER :1.76 TOT_REV_FER :9.93 RPC_THRESHOLD:2910 TC_GAIN:52 CUR_RTD:247, 0, 0	
1997-11-18 16:18:20 M4480 CALL TRACE TEST M0B ID : 6194551003 MESSAGE: Release BSC: 0 BTS: 02/0 CAUSE : Normal Call Clearing CONTINUE	
IO LDNG CDM STM TEST MO.7 STAT ALFT PREVIOUS	

Fig. 4.5-32 Display of Communication Quality and Release Reason





4.5.7.2. Call Trace MS Display Function

Function to display MS that currently uses the mobile call tracing function.

- Command DIS-TRC-MS;
- Input/Output

×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 08:18:21 M4701 DISPLAY TRACE MS IMSI BSC TRACE_TIME START_TIME 0162907023 0 5 8 : 17 COMPLETED	
<u>IO L</u> DNG <u>C</u> DM <u>S</u> TM <u>T</u> EST <u>N</u> 0.7 ST <u>A</u> T AL <u>F</u> T PREVIOUS	
[jileea <lgicbsm>] DIS-TRC-MS; ACCEPTED</lgicbsm>	

Fig. 4.5-34 Terminal Display in use of Call Trace

4.5.8. Number of Data Call User Display Function (Active/Dormant)

Function to display Number of Data Call by Active/Dormant

- Command DIS-DATA-CALL:PCP=a;
 - a: PCP Number(0~2)
- Input/Output

×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 08:52:02 M4740 ON-DEMAND DATA USER DISPLAY TEST LOCATE : PCP_00 PIPB_0 : PIP0 PIP1 PIP2 PIP3 PIP4 PIP5	
ACTIVE : 02 00 00 00 00 00 DORMANT : 00 00 00 00 00 00 00 TOTAL : 02 00 00 00 00 00 00 : PIP6 PIP7 PIP8 PIP9 PIP10 ACTIVE : 00 00 00 00 00 DORMANT : 00 00 00 00 00 TOTAL : 00 00 00 00 00	
PIPB_1 : PIP0 PIP1 PIP2 PIP3 PIP4 PIP5 ACTIVE : 00 00 00 00 00 00 DORMANT : 00 00 00 00 00 00 TOTAL : 00 00 00 00 00 00 : PIP6 PIP7 PIP8 PIP9 PIP10 ACTIVE : 00 00 00 00 DORMANT : 00 00 00 00	
PCF_0 : TOTAL ACTIVE CALLS : 2 : TOTAL DORMANT CALLS : 0 : TOTAL CALLS : 2 COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] DIS-DATA-CALL:PCP=0; ACCEPTED [jileea <lgicbsm>] [</lgicbsm></lgicbsm>	

Fig. 4.5-35 Result of the Number of DATA Call User Display

4.5.9. DATA Call User Status Display Function by IMSI

Command DIS-DATA-STS:PCP=a,IMSI=b;

- a: PCP Number(0~2)
- b: IMSI of MS
- Input/Output

×INTERM	- 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 08:55:05 M4741 ON-DEMAND DATA USER STATUS DISPLAY LOCATE : PCP_00 MOB_ID : 0162907023 STATUS : IDLE COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] DIS-DATA-STS:PCP=0,IMSI=0162907023; ACCEPTED [jileea <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.5-36 Result of DATA Call User Status Display by IMSI

4.5.10. IOS Message Display Function

This function is operated by inputting the IMSI of MS to trace, duration, etc in BSM and displays IOS trace information for all kinds of calls that MS attempts as visible information in text format in the BSM.

- Command TRC-IOS : MS=a, BSC=b, FLAG=c, DURATION=d
 - a: MS Number
 - b: BSC ID(0~11)
 - c: Start/Stop
 - d: Trace Duration Time
- Input/Output

×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 08:56:40 M4710 IOS MSG TRACE TEST MOB ID : 0162907023 RESULT : IOS TRACE START at BSCOO CONTINUE	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] TRC-IOS:IMSI=0162907023,ITEM=START,BSC=0; ACCEPTED [jileea <lgicbsm>]</lgicbsm></lgicbsm>	A T
<u> </u>	

Fig. 4.5-37 IOS Message Display START Result

Command Window		
MSC(0x00) 2001-06-18 16:27:17 M4710 IOS MSG TRACE TEST LOCATE : BSC 1 MOB ID : 0113000061 MSG TYPE : AsgnFail [0x 3] DATA[Hexa] 0 4 3 4 1 22 CONTINUE		
MSC(0x00) 2001-06-18 16:27:18 M4710 IOS MSG TRACE TEST LOCATE : BSC 1 MOB ID : 0113000061 MSG TYPE : ClearCmd [0x20] DATA[Hexa] 0 4 20 4 1 20 CONTINUE		
MSC(0x00) 2001-06-18 16:27:19 M4710 IOS MSG TRACE TEST LOCATE : BSC 1 MOB ID : 0113000061 MSG TYPE : ClearCmpl [0x21] DATA[Hexa] 0 1 21 CONTINUE		
MSC(0x00) 2001-06-18 16:27:25 M4710 IOS MSG TRACE TEST MOB ID : 0113000061 RESULT : TRACE END COMMAND ACCEPTED at BSC_ALL COMPLETED		
MSC(0x00) 2001-06-18 16:27:26 M4710 IOS MSG TRACE TEST MOB ID : 0113000061 RESULT : IOS MSG TRACE STOPPED BY MMC COMPLETED		
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>		

Fig. 4.5-38 IOS Display Message



×INTERM	_ 🗆 ×
Command Window	
MSC(0x00) 2001-06-17 08:57:29 M4710 IOS MSG TRACE TEST MOB ID : 0162907023 RESULT : TRACE END COMMAND ACCEPTED at BSCOO COMPLETED MSC(0x00) 2001-06-17 08:57:31 M4710 IOS MSG TRACE TEST M0B ID : 0162907023 RESULT : IOS MSG TRACE STOPPED BY MMC COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u>	
[jileea <lgicbsm>] TRC-IOS:IMSI=0162907023,ITEM=STOP,BSC=0; ACCEPTED [jileea <lgicbsm>]]</lgicbsm></lgicbsm>	×
4	

Fig. 4.5-39 STOP Result of IOS Message Display Function



4.5.11. POWER MONITORING Function

Function to check the BTS power control in BSM

- Command DIS-BTS-PWR:BSC=a,BTS=b,ITEM=c,[TIME=d]:
 - a: BSC ID(0~11)
 - b: BTS ID(0~47)
 - c: Stop/Start
 - d: Duration Time(1~100 min)

• Input/Ouput



Fig. 4.5-40 Power Monitoring START Result


×INTERM								_ [Rel
			Comma	nd Wind	łow				
MSC(0x00 M47 L G A T T) 2001-06-18 16 20 DISPLAY BTS P 0CATE = BSC01/BT PS TIME : Jun 18 CT_USERS X DIGITAL POWER X RF POWER	:50:05 OWER MONITO SOO 16:49:31 ALPHA : 0 : 0.17 : 0.176	BETA BETA 0 0.17 0.015	GAMMA 40 0.90 0.500	DELTA 0 0.00 0.015	EPSILON 0 0.00 0.000	ZETA 0 0.00 0.000	W	
N H CONTINUE	ORMAL FER AND OFF FER	: 0.00 : 0.00	0.00 0.00	8.33 8.00	0.00 0.00	0.00 0.00	0.00 0.00	Percent Percent	
MSC(0X00 M47 L G	20 DISPLAY BTS P OCATE = BSC01/BT PS TIME : Jun 18	0WER MONITOF SOO 16:49:33	RING DATA						
A	CT_USERS	ALPHA : 0	BETA	GAMMA 40	DELTA	EPSILON	ZETA		
T T N	X DIGITAL POWER X RF POWER ORMAL FER	: 0.18 : 0.178 : 0.00	0.18 0.015 0.00	0.90 0.500 8.33	0.00 0.015 0.00	0.00 0.000 0.00	0.00 0.000 0.000	₩ Percent	
	AND OFF FER	: 0.00	0.00	8.00	0.00	0.00	0.00	Percent	V

Fig. 4.5-41 Power Monitoring Result





4.6. No.7 Command

The function of BSM No.7 is to manage parameter information necessary for the operator to operate No.7. It is divided into the following function: 1) signaling link operation management and 2) the signaling link maintenance. The former is composed of the following: signaling point management function, signaling link set management function, signaling link management function, signaling terminal management function, signaling data management function, and SCCP management function. The latter is composed of signaling link and signaling terminal test, and signal link inhibit.

4.6.1. Related Command

4.6.1.1. Commands for Signaling Link Operation and Management Function

Table 4 6-1	Commands	related to	Signaling	Point Management
	Commanua		olghailig	i onn management

Commands	Description
DIS-SP-INFO	Signaling Point Information Display
CHG-OSP	Change of Intra-Switching Office Signaling Point
CHG-SP	Change of Signaling Point

Table 4.6-2 Commands related to Signaling Link Set

Commands	Description
ACT-LKS	Signaling Link Set Activation
DACT-LKS	Signaling Link Set Deactivation

Table 4.6-3 Commands related to Signaling Link

Commands	Description
CRET-SLK	Signaling Link Create
DEL-SLK	Signaling Link Deletion
ACT-SLK	Signaling Link Activation
DACT-SLK	Signaling Link Deactivation



DIS-SLK-INFO	Signaling Link Information Display
INH-SLK	Signaling Link Barring
ALW-SLK	Signaling Link Barring Release

Table 4.6-4 Commands related to Signaling Terminal

Commands	Description
CRTE-ST	Signaling Terminal Definition(Create)
DEL-ST	Signaling Terminal Deletion
DIS-ST-INFO	Signaling Terminal Information Display

Table 4.6-5 Commands related to Signaling Data Link

Commands	Description
CRTE-SDLK	Signaling Data Link Definition(Create)
DEL-SDLK	Signaling Data Link Deletion
DIS-SDLK-INFO	Signaling Data Link Information Display

Table 4.6-6 Display Commands for SCCP Data

Commands	Description
DIS-SCCP-NET	SCCP Network Configuration Data Display
DIS-SCCP-LSS	SCCP Intra-Switching Office Sub-system Status Data
	Display

Table 4.6-7 Signaling Link Status Display Commands

Commands	Description
DIS-SLK-STS	Signaling Link Status Display
DIS-ST-STS	Signaling Terminal Status Display
DIS-LKS-STS	Signaling Link Set Status Display

Table 4.6-8 Commands related to Timer

Commands	Description
DIS-MTP2-TMR	MTP L2 Timer Display
CHG-MTP2-TMR	MTP L2 Timer Change
DIS-MTP3-TMR	MTP L3 Timer Display
CHG-MTP3-TMR	MTP L3 Timer Change
DIS-SCCP-TMR	SCCP Timer Change
CHG-SCCP-TMR	SCCP Timer Change

4.6.1.2. Commands for Signaling Link Maintenance Function

Table 4.6-9 Test Related Commands	3
-----------------------------------	---

Commands	Description
TEST-SLK	Signaling Link Test
TEST-ST	Signaling Terminal Test
DIS-No7-CYC	Test Cycle Display
CHG-No7-CYC	Test Cycle Change

Table 4.6-10 Status Suppression Related Commands

Commands	Description
DIS-INH-NO7	Displayable Status Message Display
ALW-N07-MSG	Status Message Display Possible
INH-NO7-MSG	Status Message Display Suppression

4.6.2. Signaling Link Operation Management Function

4.6.2.1. Signaling Point Information Display

Display the Status of Signaling Point of local Switching Office and that of Intra-Switching Office.

Command DIS-SP-INFO:BSC_ID=a a = BSC Number (00 ~ 11) Input DIS-SP-INFO:BSC_ID=0;

	Command Window	
MSC(0x00) 2001-06-1 M5002 DISPLAY LOCATE : E NI SPC NAT 3 NAT 1295	14 15:40:12 Y SIGNALLING POINT INFORMATION SSCOO/SCP SP_TYPE DP_STS OWNSP ACCESS DEST ACCESS	
COMPLETED		



4.6.2.2. Change of Intra-Switching Office Signaling Point

It changes Information for Intra-Switching Office Signaling Point.

Command CHG-OSP:BSC=a,OSP_NUM=b;

a = BSC Number(00 ~ 11)

b= OSP Number ($00 \sim 65535$)

Input CHG-OSP:BSC_ID=0,OSP_NUM=5;

Output

X INT	ERM Command Window	2
	Communa minaon	12
MSCI COMI	0x00) 2001-06-18 11:02:54 M5000 CHANGE 0WN SIGNALLING P0INT LOCATE : BSC01/SCP OSP_NUM : 000000004 LETED	
ĪŪ	LONG COM STM TEST NO.7 STAT ALET PREVIOUS	
Ephe ACCI	<pre>iffer <lgicbsm>] CHG-OSP:BSC_ID=1,0SP_NUM=4; PTED </lgicbsm></pre>	
Epho ACCI Epho	iffer <lgicbsm>] CHG-OSP:BSC_ID=1,0SP_NUM=4; PTED iffer <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.6-2 Change of Intra-Switching Office Signaling Point

4.6.2.3. Change of Local Switching Office Signaling Point

It changes Signaling Point of Local Switching Office. Command CHG-SP-INFO:BSC_ID=a, SP_NUM=b

```
b = SP Number (00 \sim 65535)
```

Input CHG-SP-INFO:BSC_ID=0,SP_NUM=1038

Output

X INTI	ERM								_ 🗆 ×
				Ca	omm	and I	Wind	low	
MSC(COMP	0x00) M500 L S Leted	2001- 11 CHA .0CATE iP_NUM	06-15 NGE S : BS(: 000)	18:49 EGNALLI COO/SCI DO1038	9:30 ING POI ?	NT			
ĪO	LDNG	CDM	<u>s</u> tm	<u>T</u> EST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS	
ACCE Ephe ACCE Ephe	PTED iffer PTED iffer	<lgic< td=""><td>BSM>]</td><td>CHG-SI</td><td>P:BSC_1</td><td>D=0 , SP</td><td>_NUM=1(</td><td>038;</td><td>A</td></lgic<>	BSM>]	CHG-SI	P:BSC_1	D=0 , SP	_NUM=1(038;	A

Fig. 4.6-3 Change of Remote Switching Office Signaling Point

4.6.2.4. Activation of Signaling Link Set

Signaling Link Set indicates an aggregation of signaling links(Max 16 units). Activation of Signaling Link Set is to put all the defined Signaling Links in a in-service status.

```
Command ACT-LKS:BSC_ID=a
a = BSC Number ( 00 ~ 11)
Input ACT-LKS:BSC_ID=0;
Output
```





Fig. 4.6-4 Activation of Signaling Link Set

4.6.2.5. Deactivation of Signaling Link Set

Function that puts all the defined Signaling Links in out-of-service status

Command DACT-LKS:BSC_ID=a; a = BSC Number (00 ~ 01)

Input DACT-LKS:BSC_ID= a;

Output



Fig. 4.6-5 Deactivation of Signaling Link Set

4.6.2.6. Signaling Link Generation

Signaling Link is the concept combing Signaling Data Link with Signaling Terminal. It connects Intra-Switching Office Signaling Point to local switching Office Signaling Point, and can define 16 units of Signaling Link. The definition of Signaling Link is used to expand the number of Signaling Links. Before Signaling Link is defined, first of all, Signaling Terminal and Signaling Data Link to be defined as Signaling Link should have been defined.

Command CRTE-SLK:BSC=a, SLK=b, VMP=c, TRK=d, TS_NO = e, ST_ID =f;

- a = BSC Number ($00 \sim 11$)
- b = Signalling Link Code ($00 \sim 15$)
- c = VMP (00 \sim 07)
- d = Trunk Number ($00 \sim 16$)
- e = Time Slot Number ($00 \sim 31$)
- f = Signalling Terminal ($1 \sim 16$)

Input CRTE-SLK:BSC=0, SLK =5, VMP = 00, TRK = 5 TS_NO=16, ST_ID = 5; Output

				Co	omma	and I	Wind	low		
MSC COM	(0x00) M501 1 5 4 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	2001- 12 CRE LOCATE SLK /MP IRK IS_N0 ST_ID RESULT	06-15 ATE SI = BS(= 05 = 00 = 001 = 05 = 0K	18:57 EGNALLI 200 05	':09 ING LIN	ĸ				
<u>1</u> 0	LDNG	CDM	<u>s</u> tm	TEST	<u>N</u> 0.7	ST <u>A</u> T	AL <u>F</u> T	PREVIOUS		
ACC Ephi ACC Ephi	EPTED eiffer EPTED eiffer	<lgic <lgic< td=""><td>BSM>] BSM>]</td><td>crte-9 I</td><td>SLK : BSC</td><td>=0,SLC</td><td>=5,VMP</td><td>=0,TRK=5,1</td><td>S_NO=16,</td><td>ST_ID=5</td></lgic<></lgic 	BSM>] BSM>]	crte-9 I	SLK : BSC	=0,SLC	=5,VMP	=0,TRK=5,1	S_NO=16,	ST_ID=5

Fig. 4.6-6 Signaling Link Create

4.6.2.7. Signaling Link Deletion

Function to delete the defined Signaling Link.Before the Signaling Link is deleted, the corresponding Signaling Link should have been successfully deactivated.

Command DEL-SLK:BSC_ID=a, SLC =b;

a = BSC Number (00 ~ 11)

```
b = Signalling Link Code (00 \sim 15)
```

DEL-SLK:BSC=0, SLC =3; Input

Output

K∭.	TERM								
				C	omm	and	Wind	tow	
MSC COM	(0x00) ; M501; L0(SL(RE) PLETED	2001⊣ 3 DEL CATE C SULT	06-18 ETE SJ = BSCC = 3 = OK	09:41 IGNALLI JO	:46 ING LIN	к			
ĪO	LDNG	CDM	STM	IEST	<u>N</u> 0.7	STAT	ALET	PREVIOUS	
5 1937 - 1935	oiffor /	<lgic< td=""><td>BSM>1</td><td>DEL-SI</td><td>K:BSC_</td><td>ID=0,S</td><td>LC=3;</td><td></td><td></td></lgic<>	BSM>1	DEL-SI	K:BSC_	ID=0,S	LC=3;		

Fig. 4.6-7 Deletion of Signaling Link

4.6.2.8. Signaling Link Activation

Function to activate a defined Signaling Link. Before activated, the corresponding Signaling Link should have been successfully defined.

Command ACT-SLK:BSC_ID=a, SLC; a = BSC Number ($00 \sim 11$) b = Signalling Link Code ($00 \sim 15$) Input ACT-SLK:BSC_ID=0, SLC=2; Display





Fig. 4.6-8 Activation of Signaling Link

4.6.2.9. Signaling Link Deactivation

Function to deactivate the activated Signaling Link.

DACT-SLK:BSC=a, SLC=b; Command

a = BSC Number ($00 \sim 11$)

b = Signalling Link Code ($00 \sim 15$)

Input DACT-SLK:BSC_ID=0, SLC = 2;

Output





Fig. 4.6-9 Deactivation of Signaling Link

4.6.2.10. Signaling Link Information Display

Function to display information on Signaling Terminal, Signaling Data Link and Bit_Rate connected to Signaling Link. It can display information on entire Signaling Links and on Signaling Link designated.

Command DIS-SLK-INFO:BSC_ID=a; a = BSC Number (00 ~ 15)

Input DIS-SLK-INFO:BSC_ID = 0;

Output

		0	Comman	d Wind	ow		- 171
MSC(0x00) 2001 M5014 DI LOCATE SLC	-06-15 18: SPLAY SIGN/ = BSC00 ST_ID	54:41 ALLING LINK VMP_ID	INFORMATIO TRK_NO	IN TS_NO	BIT_RATE	
	0	1	0	0	16	64K	
	2	3	ŏ	2	16	64K	
COMP	3	4	0	3	16	64K	
COWF	LETED						
							M
a						2	
<u>I</u> 0	LONG COM	<u>STM</u> <u>T</u> es	T <u>N</u> 0.7 S	T <u>A</u> T AL <u>F</u> T	PREVIOUS		
ACCE	EDTED						12
Ephe	iffer <lgi< td=""><td>CBSM>] DIS</td><td>SLK-INFO:B</td><td>SC_ID=0;</td><td></td><td></td><td></td></lgi<>	CBSM>] DIS	SLK-INFO:B	SC_ID=0;			
ACCE	PTED	CDCHN1T					
the	affer stud	CR20511					Ę
21						C	5

Fig. 4.6-10 Signaling Link Information Display

4.6.2.11. Signaling Link Inhibition

It inhibits the use of presently defined Signaling Link.

```
Command INH-SLK:BSC_ID=0, SLC =0;

a = BSC Number ( 00 ~ 11 )

b = Signalling Link Code ( 00 ~ 15 )

Input INH-SLK:BSC_ID=0, SLC=0;

Output
```



Fig. 4.6-11 Signaling Link Inhibit

4.6.2.12. Signaling Link Allow

Signaling Link Allow is a function that allows the management of Signaling Link Status inhibited in the management of Signaling Link and then change to the status of availability.

```
Command ALW-SLK:BSC_ID=0, SLC =0;
```

a = BSC Number ($00 \sim 11$)

b = Signalling Link Code ($00 \sim 15$)

Input ALW-SLK:BSC_ID = 0, SLC = 0;

Output





Fig. 4.6-12 Signal Link Allow

4.6.2.13. Signal Terminal Addition

Signal Terminal indicates physical name of a Channel of STPA and can define 16 units of Signaling Terminal at a maximum. It is used to expand Signaling Terminal.

Command CRTE-ST:BSC=a, ST_ID=b;

a = BSC Number (00 \sim 15)

b = Signalling Terminal ($01 \sim 16$)

Input CRTE-ST:BSC_ID=0,ST_ID = 4;

Output



Fig. 4.6-13 Signaling Terminal Generation

4.6.2.14. Signaling Terminal Deletion

It is used when Signaling Terminal H/W has problems or other faults occur. Before Signaling Terminal is deleted, make sure that the corresponding Signaling Terminal is connected to Signaling Link.

```
Command DEL-ST:BSC=a, ST_ID =b;

a = BSC Number (00 ~ 11)

b = Signalling Terminal (01 ~ 16)

Input DEL-ST:BSC_ID=0,ST_ID = 5;

Output
```



Fig. 4.6-14 Signaling Terminal Deletion

4.6.2.15. Signaling Terminal Information Display

Function to display information about if it is linked with signaling link to a specific Signaling Terminal or all the Signaling Terminals.

Command DIS-ST-INFO:BSC_ID =a;

a = BSC Number (00 ~ 11)

Input DIS-ST-INFO:BSC_ID = 0;

Output

Command Window	
MSC(0x00) 2001-06-18 09:46:39 M5022 DISPLAY SIGNALLING TERMINAL INFORMATION LOCATE = BSCO0 ST_NO ST_TS STATE 01 0 ALOC 02 1 ALOC 03 2 ALOC 04 **** UNALOC COMPLETED	
<u>IO LDNG CDM STM TEST NO.7 STAT ALFT PREVIOUS</u> [pheiffer <lgicbsm>] DIS-ST-INFO:BSC_ID=0; ACCEPTED</lgicbsm>	

Fig. 4.6-15 Display of Signaling Terminal Information

4.6.2.16. Signaling Data Link Addition

Signaling Data Link indicates Trunk Number to allocate as Signaling Link and is used to expand Signaling Data Link.

```
Command CRTE-SDLK: BSC =a, VMP_ID=b, TRK= c ,TS_NO=d;
```

a = BSC Number (00 ~ 11)

b = VMP Number($00 \sim 07$)

c = TRUNK Number($00 \sim 16$)

d = Time Slot Number($00 \sim 31$)

Input CRTE-SDLK: BSC_ID=0, VMP_ID=1, TRK=0, TS_NO=16;

Output

XINTERM	
Command Window	
MSC(0x00) 2001-06-18 09:56:28 M5030 CREATE SIGNALLING DATA LINK LOCATE = BSC00 VMP = 01 TRK = 0000 TS_N0 = 16 RESULT = 0K COMPLETED	
4	
10 LONG COM STH TEST NO.7 STAT ALET PREVIO	US
<pre>[pheiffer <lgicbsm>] CRTE-SDLK:BSC=0,VMP=1,TRK=0,TS_M ACCEPTED [pheiffer <lgicbsm>]]</lgicbsm></lgicbsm></pre>	10-16;

Fig. 4.6-16 Signaling Data Link Generation

4.6.2.17. Signaling Data Link Deletion

It is used when Signaling Data Link H/W is defective or other problems occur.Before deleting the Signaling Data Link, make sure that corresponding Signaling Data Link is allocated to Signal Link and then delete.

Command DEL-SDLK;BSC=a, VMP=b, TRK=c, TS_NO=d; a = BSC Number (00 ~ 11) b = VMP Number(00 ~ 07) c = TRUNK Number(00 ~ 16) d = Timer Slot Number(00 ~ 31) Input DEL-SDLK:BSC=0, VMP=0, TRK= 5, TS_NO = 16;

Output

×IN	RM	_ 🗆 ×
	Command Window	
MSC COM	Dx00) 2001-06-18 09:53:08 M5031 DELETE SIGNALLING DATA LINK LOCATE = BSCO0 VMP = 00 TRK = 0005 TS_N0 = 0016 RESULT = 0K LETED	
10	LONG COM STH TEST NO.7 STAT ALET PREVIOUS	
Eph Acc Eph	ffer <lgicbsm>] DEL-SDLK:BSC=0,VMP=0,TRK=5,TS_NO=16; >TED ffer <lgicbsm>]]</lgicbsm></lgicbsm>	

Fig. 4.6-17 Signaling Data Link Deletion

4.6.2.18. Signaling Data Link Information Display

Function to display information on the defined Signaling Data Link and the status connected to Signaling Link.

Command DIS-SDLK-INFO: BSC_ID = a;

a = BSC Number ($00 \sim 11$)

Input DIS-SDLK-INFO: BSC_ID=0;

Output

l line	DCATE	PLAY SIGNA	LLING DA	TA LINK I	NFOR	MATION	
COMPLETE	//mp 10 10 10 10 10 11 10	TRK 0000 0001 0002 0003 0004 0005	TS_N0 16 16 16 16 16 16	ALOC_S ALOC ALOC ALOC ALOC UNALOC UNALOC	TS		
<u>IO L</u> DNC	: <u>c</u> dm	<u>S</u> TM <u>T</u> es	T <u>N</u> 0.7	ST <u>A</u> T A	. <u>F</u> T	PREVIOUS	

Fig. 4.6-18 Signaling Data Link Information Display

4.6.2.19. SCCP Network Configuration Data Display

Function to display SCCP Network Configuration Data for Intra-Switching Office Signaling Point, local Switching Office Signaling Point, and each sub-system.

Command DIS-SCCP-NET:BSC_ID=a;

a = BSC Number ($00 \sim 11$)

Input DIS-SCCP-NET:BSC_ID=0;

Output

XINTERM	×
Command Wir	dow
MSC(0x00) 2001-06-18 09:59:44 M5040 DISPLAY SCCP NETWORK CONFIGURATI LOCATE = BSC00 SPC SP_STATE SSN S 238-238-238 ALLOWED 252 P COMPLETED	DN S_STATE ROHIBITED
≺I <u>Io l</u> dng <u>c</u> dm <u>s</u> tm <u>t</u> est <u>N</u> 0.7 st <u>a</u> t al <u>f</u> 1	PREVIOUS
<pre>[pheiffer <lgicbsm>] DIS-SCCP-NET:BSC_ID=0; ACCEPTED [pheiffer <lgicbsm>]]</lgicbsm></lgicbsm></pre>	

Fig. 4.6-19 SCCP Network Configuration Data Display