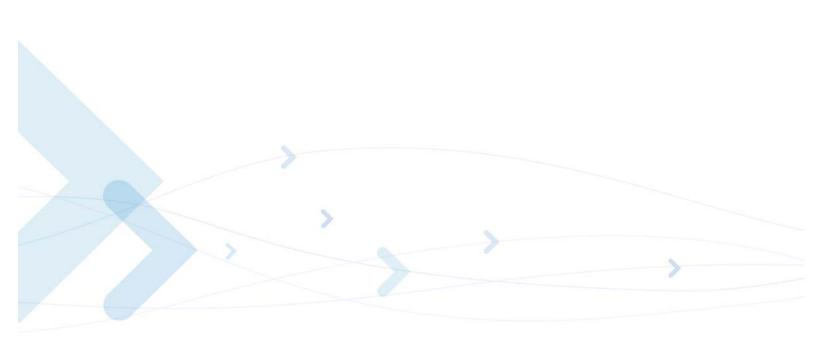


Technical Information





Motorola W24 Developer's Guide

Module Hardware Description

MARCH 31, 2008 6802984C95-A

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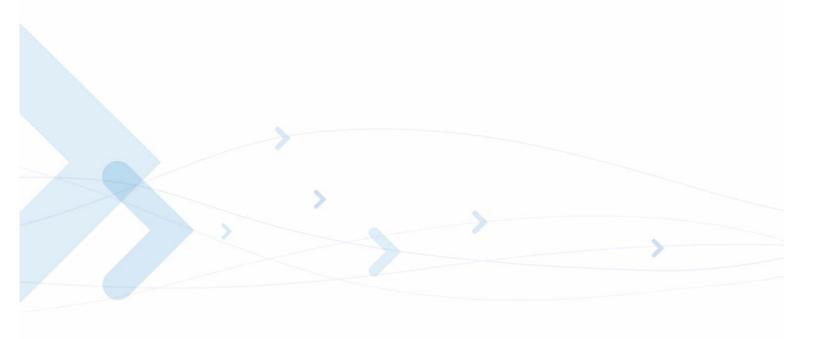
Table of Contents

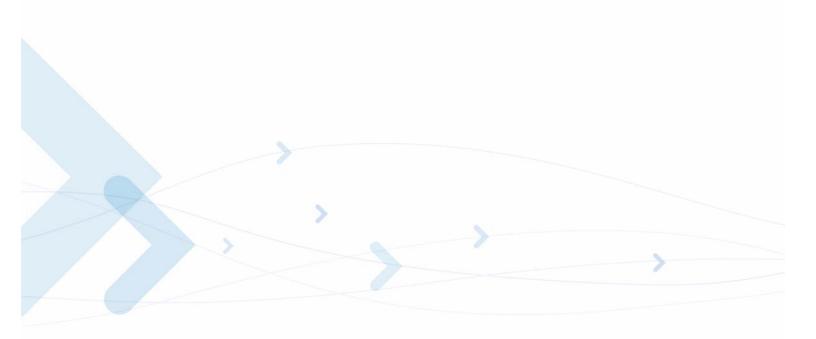
	Manual Scope	ii
	Target Audience	ii
	Manual Organization	ii
	Applicable Documents	
	Regulatory Requirements	
	Regulatory Statement (Safety).	
	Antenna and Transmission Safety Precautions	
	Standards	
	Contact Us	
	Text Conventions.	
	Field Service	
	General Safety	
	Caring for the Environment xii	
	Limitation of Liability xii	
	Warranty Notification	
	How to Get Warranty Service?	v
	Claiming x	v
	Conditions	v
	What is Not Covered by the Warrantyxv	vi 🛛
	Installed Dataxvi	
	Out of Warranty Repairs	
	Revision History	
Chapter 1:	Introduction	1
Chapter 1:		
Chapter 1:	Operation Setup	1
Chapter 1:	Operation Setup	1 1
Chapter 1:	Operation Setup	1 1 1
Chapter 1:	Operation Setup	1 1 1 2
Chapter 1:	Operation Setup Stand Alone Configuration Stacked Configuration Product Specifications Regulatory Approvals	1 1 1 2 4
Chapter 1:	Operation Setup Stand Alone Configuration Stacked Configuration Product Specifications Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements	1 1 1 2 4 4
Chapter 1:	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user	1 1 2 4 4 4 4
Chapter 1:	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247	1 1 2 4 4 4 4 4 4
Chapter 1:	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209	1 1 2 4 4 4 4 4 4 4
Chapter 1:	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247	1 1 2 4 4 4 4 4 4 4
	Operation SetupStand Alone ConfigurationStacked ConfigurationProduct SpecificationsRegulatory ApprovalsCFR 47 Part 15.19 specifies Label RequirementsCFR 47 Part 15.21 Information to userCFR 47 Part 15.247CFR 47 Part 15.247CRF 47 Part 15.247 & 15.205 & 15.209CFR 47 Part 15.107 & part 15.109	1 1 2 4 4 4 4 4 4 4 4
Chapter 1: Chapter 2:	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109	1 1 2 4 4 4 4 4 4 4 4 7
	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109	1 1 2 4 4 4 4 4 4 4 4 7 7
	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109	1 1 2 4 4 4 4 4 4 4 4 4 7 7 7
	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109 Hardware Interface Description Architecture Overview Digital Block Power Management Block	1 1 2 4 4 4 4 4 4 4 4 7 7 7 8
	Operation Setup Stand Alone Configuration Stacked Configuration Product Specifications Product Specifications Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109 Hardware Interface Description Architecture Overview Digital Block Power Management Block RF Block State St	1 1 2 4 4 4 4 4 4 4 4 7 7 7 8 8
	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications. Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CRF 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109 Hardware Interface Description Architecture Overview Digital Block Power Management Block RF Block Operating Modes	1 1 2 4 4 4 4 4 4 4 4 4 7 7 7 8 8 8 9
	Operation SetupStand Alone ConfigurationStacked ConfigurationProduct SpecificationsRegulatory ApprovalsCFR 47 Part 15.19 specifies Label RequirementsCFR 47 Part 15.21 Information to userCFR 47 Part 15.247CRF 47 Part 15.247 & 15.205 & 15.209CFR 47 Part 15.107 & part 15.109Hardware Interface DescriptionArchitecture OverviewDigital BlockPower Management BlockRF BlockOperating ModesPower Supply	1 1 2 4 4 4 4 4 4 4 7 7 8 8 9 0
	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications Product Specifications Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CFR 47 Part 15.247 Stacked State CFR 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109 Hardware Interface Description Architecture Overview Digital Block Power Management Block RF Block Operating Modes Power Supply 10 Power Supply 10	1 1 2 4 4 4 4 4 4 4 7 7 8 8 9 0 0
	Operation SetupStand Alone ConfigurationStacked ConfigurationProduct SpecificationsRegulatory ApprovalsCFR 47 Part 15.19 specifies Label RequirementsCFR 47 Part 15.21 Information to userCFR 47 Part 15.247CRF 47 Part 15.247 & 15.205 & 15.209CFR 47 Part 15.107 & part 15.109Hardware Interface DescriptionArchitecture OverviewDigital BlockPower Management BlockRF BlockOperating ModesPower Supply	1 1 2 4 4 4 4 4 4 4 7 7 8 8 9 0 0
	Operation Setup. Stand Alone Configuration Stacked Configuration Product Specifications Product Specifications Regulatory Approvals CFR 47 Part 15.19 specifies Label Requirements CFR 47 Part 15.21 Information to user CFR 47 Part 15.21 Information to user CFR 47 Part 15.247 CFR 47 Part 15.247 Stacked State CFR 47 Part 15.247 & 15.205 & 15.209 CFR 47 Part 15.107 & part 15.109 Hardware Interface Description Architecture Overview Digital Block Power Management Block RF Block Operating Modes Power Supply 10 Power Supply 10	1 1 2 4 4 4 4 4 4 4 7 7 8 8 9 0 0 1

	Turning the W24 On	12
	Turning the W24 On Using ON_N	12
	Turning the W24 On Using IGN	
	Turning the W24 Off	
	Turning the W24 Off Using ON_N	14
	Turning the W24 Off Using IGN	14
	Power Loss shut down	15
	Turning the W24 Off Using AT+iMRST	15
	Low Power Mode.	16
	Activating Low Power Mode	16
	Serial Interface During Low Power Mode	
	Terminating Low Power Mode	
	Temporary Termination of Low Power Mode	
	Permanent termination of Low Power Mode	
	Deep Sleep Mode	
	Serial Interfaces	
	Primary UART (UART1)	
	Secondary UART (UART2)	
	USB Device Interface	
	USB Host Interface (Stacked configuration only)	
	SIM Interface	
	Audio Interface	
	A/D Converter Interface.	
	Unused/Reserved Signals.	
	Controls and Indicators Interface.	
	Reset	
	VREF Reference Regulator	27
	Wakeup	28
	Antenna Detection	
	GPRS/EGPRS Detection	28
	Transmission Indicator	29
	Wi-Fi Indicator	29
	Mode Select	29
	General Purpose I/O.	30
	Antenna Interface.	31
Chapter 3:	Electrical and Environmental Specifications	
	Absolute Maximum Ratings	
	Environmental Specifications	
	Application Interface Specifications	34
Chapter 4:	Mechanical Specifications	
	Board Dimensions	
	Interface Connector Specifications	
	RF Connector Specifications	
	Mating Connector	
	W24 Mounting	
	W24 Stand Alone Mounting	
	W24 + G24 Stacked Mounting	50
Chanter 5	Summer Convice and Troublesheeting	E 4
Chapter 5:	Support, Service and Troubleshooting	
	Support	
	Who to Contact?	
	Required Query Information	51

Service	
Who to Contact?	
Required Query Information	
Testing a Standalone Unit	
Test Setup	

Acronyms and Abbreviations Index







List of Figures

1-1	W24 Stand Alone Configuration	. 1
1-2	W24 + G24 Stacked Configuration	. 2
2-1	W24 Block Diagram	7
2-1		
	Transmission Power Drops.	
2-3	ON_N Power On Timing	
2-4	IGN Power On Timing	
2-5	ON_N Power Off Timing	14
2-6	IGN Power Off Timing.	15
2-7	WKUPI N Signal Operation	
2-8	WKUPO_N Signal Operation.	
2-9	Serial Interface Data	
2-10	Serial Interfaces Block Diagram.	
2-10	UART1 Interface Signals	
2-11	UARTI Interface Signals	20
4-1	W24 Mechanical Characteristics	45
4-2	W24 Stand Alone Model	
4-3	MMCX Connector Dimensions	
4-4	Optional MMCX Cable Assembly	
4-5		
	W24 Mounting Area	
4-6	W24 + G24 Stacked Mounting.	
4-7	W24 + G24 Stacked Mounting Spacer Dimentions	50
	>	



List of Tables

1-1	Product Specifications	. 2
2-1	W24 Operating Modes	9
2-2	Power Supply Signals.	
2-3	W24 Current Ratings	
2-4	Secondary UART Interface Signals	
2-5	USB Device Interface Signals (at Host connector)	
2-6	USB Host Interface Signals (at G24 connector).	
2-7	SIM Interface Signals	
2-8	Analog Audio Interface Signals	
2-9	Digital Audio Interface Signals	
2-10	ADC Interface Signals	
2-11	Internal Interface Signals	
2-12	Controls and Indicators.	
2-13	VREF Specifications	
2-14	GPIO Interface Signals	
2-15	Antenna Interface Specifications	
3-1	Maximum Ratings	
3-2	Environmental Ratings	
3-3	Host Interface Specifications	
3-4	G24 Interface Specifications.	
4-1	W24 host interface connector options	46
4-2	W24 cellular interface connector options.	
4-3	RF Connector Specifications	

Preface

Manual Scope

This manual provides the electrical, mechanical and environmental requirements for properly integrating the W24 module in a host application.

This manual gives a complete set of hardware features and functions that may be provided by W24. The availability of any feature or function, which is described in this manual, depends on the hardware revision and software version of a specific W24 model.

The parameters and values provided in this manual are defined under typical conditions. These values may vary when subject to different conditions, such as SW version, network status, application settings and environmental conditions.

Target Audience

This manual is intended for all members of the integration team who are responsible for integrating the W24 into the host application, including representatives from hardware, software and RF engineering disciplines.

Manual Organization

This manual contains the following chapters:

- Chapter 1—introduces the W24 unit and provides important safety instructions.
- **Chapter 2**—provides a detailed hardware description of the blocks and components comprising the W24.
- Chapter 3—describes the pin assignments for W24 connectors.
- Chapter 4—describes W24 mechanical specifications and requirements.
- **Chapter 5**—provides contact information for Motorola Service Support and Customer Assistance.

Applicable Documents

- Motorola W24 Developer's Guide-W24 Developer's Kit 6802985C05
- Motorola W24 Developer's Guide-W24 AT+i Commands 6802985C10
- Motorola G24 Developer's Guide-Module Hardware Description 68089192V27
- Motorola G24 Developer's Guide-AT Commands Reference Manual 6889192V28

Regulatory Requirements

The Federal Communications Commission (FCC) requires application for certification of digital devices in accordance with CFR Title 47, Part 15 Sub Part C ,section 15.247 and Part 15 Sub Part B sections 15.107,15.109. This includes MPE calculation. As the W24 modem is not a standalone transceiver but is an integrated module, the W24 cannot be tested by itself for EME certification. It is, however, the integrator's responsibility to have the completed device tested for EME certification.

Caution: Unauthorized repairs or modifications could result in permanent damage to the equipment and void your warranty and your authority to operate this device under Part 15 of the FCC Rules.

Regulatory Statement (Safety)

The following safety precautions must be observed during all phases of the operation, usage, service or repair of any cellular terminal or mobile incorporating the W24 module. Manufacturers of the cellular terminal are advised to convey the following safety information to users and operating personnel, and to incorporate these guidelines into all manuals supplied with the product. Failure to comply with these precautions violates safety standards of design, manufacture and intended use of the product. Motorola assumes no liability for customer failure to comply with these precautions.

- The W24 must be operated at the voltages described in the technical documentation
- The W24 must not be mechanically nor electrically changed. Use of connectors should follow the guidance of the technical documentation
- The W24 is designed to meet the EMC requirements of ETS 300328
- When integrating the W24 into a system, Motorola recommends testing the system to ETS300328
- The W24 meets the safety requirements of EN60950
- Systems using the W24 are subject to mandatory EMC testing under directive 89/336/EEC (see link below). Other directives, such as the LVD directive 73/23/EE, may also apply to a system using the W24 module http://ac.auropa.au/anterprise/pawapproach/standardization/harmstdc/reflist.html

http://ec.europa.eu/enterprise/newapproach/standardization/harmstds/reflist.html

Host Label Requirement

A readily visible label is required on the outside of the host that state i.e. "FCC ID: IHDT56DB2/IC ID: 109O-DB2 is contained within", provided the host is within the limits of the module grant. You must not remove the label from embedded module.

FCC Notice to Users

Motorola has not approved any changes or modifications to this device by the user. Any changes or modifications could void the user's authority to operate the equipment. See 47 CFR Sec. 15.21. This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept

any interference received, including interference that may cause undesired operation. See 47 CFR Sec. 15.19(3).

If your mobile device or accessory has a USB connector, or is otherwise considered a computer peripheral device whereby it can be connected to a computer for purposes of transferring data, then it is considered a Class B device and the following statement applies:

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and the receiver.
- Connect the equipment to an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- 1. This device may not cause harmful interference.
- 2. This device must accept any interference received, including interference that may cause undesired operation.

Precautions

Interface connector and some of the module circuits are not shielded. Be sure to take appropriate precautionary measures in order to avoid ESD while handling the module. ESD can damage the W24/G24 module.

Antenna and Transmission Safety Precautions

User Operation

Do not operate your unit when a person is within eight inches (20 centimeters) of the antenna. A person or object within 8 inches (20 centimeters) of the antenna could impair call quality and may cause the unit to operate at a higher power level than necessary, as well as expose that person to RF energy in excess of that established by the FCC RF Exposure Guidelines.

Important:	The unit must be installed in a manner that provides a minimum separation distance
	of 8 inches (20 centimeters) or more between the antenna and persons and must not
	be co-located or operate in conjunction with any other antenna or transmitter in
	order to satisfy FCC RF exposure requirements for mobile transmitting devices.

Antenna Installation

- A minimum separation distance of 8 inches (20 centimeters) must be maintained between the antenna and all persons.
- When using G24 module: The combined cable loss and antenna gain must not exceed +7.5 dBi (850MHz band). The combined cable loss and antenna gain must not exceed +2.5 dBi and total system output must not exceed 2.0W EIRP in the PCS (1900) band in order to comply with the EIRP limit of 24.232 (b). OEM installers must be provided with antenna installation instruction and transmitter operating conditions for satisfying RF exposure compliance.
- When using W24 module for meeting FCC the combined cable loss and antenna gain must not exceed 6 dBi (2400 MHz Band) and the total system output must not exceed 1.0W peak output power 15.247(b) or/and for meeting the EN 300 328 standard Maximum EIRP spectral density 10 dBm/MHz. The combined cable loss and antenna gain must not exceed 3 dBi.

Section 15.203 - Antenna Requirement

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions of this Section. The manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited. This requirement does not apply to carrier current devices or to devices operated under the provisions of Sections 15.211, 15.213, 15.217, 15.219, or 15.221. Further, this requirement does not apply to intentional radiators that must be professionally installed, such as perimeter protection systems and some field disturbance sensors, or to other intentional radiators which, in accordance with Section 15.31(d), must be measured at the installation site. However, the installer shall be responsible for ensuring that the proper antenna is employed so that the limits in this Part are not exceeded.

Standards

- FCC_47cfr15.247
- FCC_47cfr15.209 & 15cfr15.205 (radiated spurious emission restricted band)
- EN_300 328
- IEEE Std 802.11b
- IEEE Std 802.11g

Important: To comply with the FCC RF exposure limits and to satisfy the categorical exclusion requirements for mobile transmitters, the requirements described in the following section, "Antenna Installation", must be met.

- EN 60950 Safety
- RSS-210 Issue 7 (IC Cannada)
- EN 301 489-1
- EN 301 489-17

Contact Us

We at Motorola want to make this guide as helpful as possible. Keep us informed of your comments and suggestions for improvements.

For general contact, technical support, report documentation errors and to order manuals, use this email address:

M2M.CustomerCare@motorola.com

Motorola appreciates feedback from the users of our information.

Text Conventions

The following special paragraphs are used in this guide to point out information that must be read. This information may be set-off from the surrounding text, but is always preceded by a bold title in capital letters:

Note

Note: Presents additional, helpful, noncritical information that you can use.

Warning

Warning: Presents information to warn you of a potentially hazardous situation in which there is a possibility of personal injury.

Important

Important: Presents information to help you avoid an undesirable situation or provides additional information to help you understand a topic or concept.

Caution

Caution: Presents information to identify a situation in which damage to software, stored data, or equipment could occur, thus avoiding the damage.

Field Service

For Field Service requests, use this email address: M2M.CustomerCare@motorola.com

General Safety

Remember!... safety depends on you!

The following general safety precautions must be observed during all phases of operation, service, and repair of the equipment described in this manual. Failure to comply with these precautions or with specific warnings elsewhere in this manual violates safety standards of design, manufacture, and intended use of the equipment. Motorola, Inc. assumes no liability for the customer's failure to comply with these requirements. The safety precautions listed below represent warnings of certain dangers of which we are aware. You, as the user of this product, should follow these warnings and all other safety precautions necessary for the safe operation of the equipment in your operating environment.

Do not operate in an explosive atmosphere

Do not operate the equipment in the presence of flammable gases or fumes. Operation of any electrical equipment in such an environment constitutes a definite safety hazard.

Do not service or adjust alone

Do not attempt internal service or adjustment unless another person, capable of rendering first aid is present.

Keep away from live circuits

Operating personnel must:

- not remove equipment covers. Only Factory Authorized Service Personnel or other qualified maintenance personnel may remove equipment covers for internal subassembly, or component replacement, or any internal adjustment
- not replace components with power cable connected. Under certain conditions, dangerous voltages may exist even with the power cable removed
- always disconnect power and discharge circuits before touching them

Do not substitute parts or modify equipment

Because of the danger of introducing additional hazards, do not install substitute parts or perform any unauthorized modification of equipment. Contact Motorola Warranty and Repair for service and repair to ensure that safety features are maintained.

Dangerous procedure warnings

Warnings, such as the example below, precede potentially dangerous procedures throughout this manual. Instructions contained in the warnings must be followed. You should also employ all other safety precautions that you deem necessary for the operation of the equipment in your operating environment.

Warning example:

Warning: Dangerous voltages, capable of causing death, are present in this equipment. Use extreme caution when handling, testing, and adjusting.

Caring for the Environment

The following information is provided to enable regulatory compliance with the European Union (EU) Directive 2002/96/EC Waste Electrical and Electronic Equipment (WEEE) when using Motorola equipment in EU countries.

Disposal of Motorola equipment in EU countries



Please do not dispose of Motorola equipment in landfill sites.

In the EU, Motorola in conjunction with a recycling partner will ensure that equipment is collected and recycled according to the requirements of EU environmental law.

Please contact the Customer Network Resolution Center (CNRC) for assistance. The 24 hour telephone numbers are listed at http://mynetworksupport.motorola.com

Select Customer Network Resolution Center contact information.

Alternatively if you do not have access to CNRC or the internet, contact the Local Motorola Office.

Disposal of Motorola equipment in non-EU countries

In non-EU countries, dispose of Motorola equipment in accordance with national and regional regulations.

Limitation of Liability

The Products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body; in other applications intended to support or sustain life; for the

planning, construction, maintenance, operation or use of any nuclear facility; for the flight, navigation, communication of aircraft or ground support equipment; or in any other application in which the failure of the Product could create a situation where personal injury or death may occur. If CUSTOMER should use any Product or provide any Product to a third party for any such use, CUSTOMER hereby agrees that MOTOROLA is not liable, in whole or in part, for any claims or damages arising from such use, and further agrees to indemnify and hold MOTOROLA harmless from any claim, loss, cost or damage arising from such use.

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The preceding states MOTOROLA's entire liability for MOTOROLA's breach or failure to perform under any provision of this Agreement.

Warranty Notification

Motorola guarantees to you, the original purchaser, the OEM module and accessories which you have purchased from an authorized Motorola dealer (the "Products"), to be in conformance with the applicable Motorola specifications current at the time of manufacture for a term of [1] year from date of purchase of the Product(s) (Warranty Term).

You must inform Motorola of the lack of conformity to the applicable specifications of any of the Products within a period of two (2) months from the date on which you detect a defect in material, workmanship or lack of conformity and in any event within a term not to exceed the Warranty Term, and must immediately submit the Product for service to Motorola's Authorized Repair or Service Center. Motorola shall not be bound by Product related statements not directly made by Motorola nor any warranty obligations applicable to the seller.

A list of the Motorola Call Center numbers is enclosed with this Product.

During the Warranty term, Motorola will, at its discretion and without extra charge, as your exclusive remedy, repair or replace your Product which does not comply with this warranty; or failing this, to reimburse the price of the Product but reduced to take into account the use you have had of the Product since it was delivered. This warranty will expire at the end of the Warranty Term.

This is the complete and exclusive warranty for a Motorola OEM module and accessories and in lieu of all other warranties, terms and conditions, whether express or implied.

Where you purchase the product other than as a consumer, Motorola disclaims all other warranties, terms and conditions express or implied, such as fitness for purpose and satisfactory quality.

In no event shall Motorola be liable for damages nor loss of data in excess of the purchase price nor for any incidental special or consequential damages* arising out of the use or inability to use the Product, to the full extent such may be disclaimed by law.

This Warranty does not affect any statutory rights that you may have if you are a consumer, such as a warranty of satisfactory quality and fit for the purpose for which products of the same type are normally used under normal use and service, nor any rights against the seller of the Products arising from your purchase and sales contract.

(*)including without limitation loss of use, loss of time, loss of data, inconvenience, commercial loss, lost profits or savings.

How to Get Warranty Service?

In most cases the authorized Motorola dealer which sold and/or installed your Motorola OEM module and original accessories will honor a warranty claim and/or provide warranty service. Alternatively, for further information on how to get warranty service please contact either the customer service department of your service provider or Motorola's service centers, listed in Chapter 5.

Claiming

In order to claim the warranty service you must return the OEM module and/or accessories in question to Motorola's Authorized Repair or Service Center in the original configuration and packaging as supplied by Motorola. Please avoid leaving any supplementary items like SIM cards. The Product should also be accompanied by a label with your name, address, and telephone number; name of operator and a description of the problem.

In order to be eligible to receive warranty service, you must present your receipt of purchase or a comparable substitute proof of purchase bearing the date of purchase. The phone should also clearly display the original compatible electronic serial number (IMEI) and mechanic serial number [MSN]. Such information is contained with the Product.

You must ensure that all and any repairs or servicing is handled at all times by a Motorola Authorized Service Center in accordance with the Motorola Service requirements.

In some cases, you may be requested to provide additional information concerning the maintenance of the Products by Motorola Authorized Service Centers only, therefore it is important to keep a record of any previous repairs, and make them available if questions arise concerning maintenance.

Conditions

This warranty will not apply if the type or serial numbers on the Product has been altered, deleted, duplicated, removed, or made illegible. Motorola reserves the right to refuse free-of-charge warranty service if the requested documentation can not be presented or if the information is incomplete, illegible or incompatible with the factory records.

Repair, at Motorola's option, may include reflashing of software, the replacement of parts or boards with functionally equivalent, reconditioned or new parts or boards. Replaced parts, accessories, batteries, or boards are warranted for the balance of the original warranty time period. The Warranty Term will not be extended. All original accessories, batteries, parts, and OEM module equipment that have been replaced shall become the property of Motorola. Motorola does not warrant the installation, maintenance or service of the products, accessories, batteries or parts.

Motorola will not be responsible in any way for problems or damage caused by any ancillary equipment not furnished by Motorola which is attached to or used in connection with the Products, or for operation of Motorola equipment with any ancillary equipment and all such equipment is expressly excluded from this warranty.

When the Product is used in conjunction with ancillary or peripheral equipment not supplied by Motorola, Motorola does not warrant the operation of the Product/peripheral combination and Motorola will not honor any warranty claim where the Product is used in such a combination and it is determined by Motorola that there is no fault with the Product. Motorola specifically disclaims any responsibility for any damage, whether or not to Motorola equipment, caused in any way by the use of the OEM module, accessories, software applications and peripherals (specific examples include, but are not limited to: batteries, chargers, adapters, and power supplies) when such accessories, software applications and peripherals are not manufactured and supplied by Motorola.

What is Not Covered by the Warranty

This warranty is not valid if the defects are due to damage, misuse, tampering, neglect or lack of care and in case of alterations or repair carried out by unauthorized persons.

The following are examples of defects or damage not covered by this product warranty

- 1. Defects or damage resulting from use of the Product in other than its normal and customary manner.
- 2. Defects or damage from misuse, access to incompatible sources, accident or neglect.
- 3. Defects or damage from improper testing, operation, maintenance, installation, adjustment, unauthorized software applications or any alteration or modification of any kind.
- 4. Breakage or damage to antennas unless caused directly by defects in material or workmanship.
- 5. Products disassembled or repaired other than by Motorola in such a manner as to adversely affect performance or prevent adequate inspection and testing to verify any warranty claim.
- 6. Defects or damage due to range, coverage, availability, grade of service, or operation of the cellular system by the cellular operator.
- 7. Defects or damage due to moist, liquid or spills of food.
- 8. Control unit coil cords in the Product that are stretched or have the modular tab broken.
- 9. All plastic surfaces and all other externally exposed parts that are scratched or damaged due to customer normal use.

Depending on operating conditions and your usage habits, wear and tear might take place of components including mechanical problems related to Product housing, paint, assembly, sub-assemblies, displays and keyboards and any accessories which are not part of the Product's in-box configuration. The rectification of faults generated through wear and tear and the use of consumable items like batteries beyond their Optimum Performance Time as indicated in the product manual is considered to be your responsibility and therefore Motorola will not provide the free Warranty repair service for these items.

Installed Data

Please make and retain a note of all data you have inserted into your Product for example names, addresses, phone numbers, user and access codes, notes etc. before submitting your Product for a Warranty service as such data may be deleted or erased as part of the repair or service process.

Please note if you have downloaded material onto your product, these may be deleted or erased as part of the repair process or testing process. Motorola shall not be responsible for such matters. The repair or testing process should not affect any such material that was installed by Motorola on your Product as a standard feature.

Out of Warranty Repairs

If you request Motorola to repair your Product any time after the Warranty term or where this warranty does not apply due to the nature of the defect or fault, then Motorola may in its discretion carry out such repairs subject to you paying Motorola its fees for such a repair or it may refer you to an authorized third party to carry out such repairs.

Revision History

Manual Number

6802984C95-A

Manual Title

Module Hardware Description

Version Information

The following table lists the manual version, date of version, and remarks about the version.

Revision History

Version	Date Issue	Remarks	
Α	March 31, 2008	Initial Release	

Chapter 1: Introduction

W24 is a self contained Wi-Fi module, supporting IEEE 802.11b/g systems in 2.4GHz band. W24 enables IP connectivity to any host device. It is ideal for enabling M2M applications to achieve secure, high speed throughput and access to IP networks via 802.11b/g wireless LANs or even via Cellular network when connected to G24 GSM/GPRS module family.

W24 integrates an operating system, drivers and communication stack, wide assortment of proven security protocols, NATs for management purposes, built-in web server for managing the device via web and inherent built-in device firewall.

Operation Setup

The W24 supports two operating setup configurations:

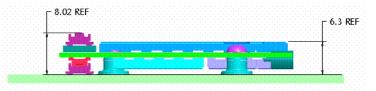
- Stand Alone configuration
- Stacked assembly configuration (with G24).

Stand Alone Configuration

Stand Alone configuration consists of a W24 as a stand alone Wi-Fi module that is controlled by the host.

In this configuration, the W24 70 pin host connector (bottom connector) is used for interfacing with the W24.

Figure 1-1 below illustrates the W24 Stand Alone configuration.





Stacked Configuration

Stacked configuration consists of a G24 that is mounted on top of the W24 in a piggy-back configuration. In this configuration, the W24 70-pin host connector (bottom connector) is used for interfacing with the host board, while the G24 is connected to the W24 cellular 70-pin interface connector (top connector).

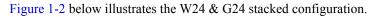




Figure 1-2: W24 + G24 Stacked Configuration

Product Specifications

oortant:	page viii, "Regulat	ons and requirements, see "Regulatory Requirements" on ory Statement (Safety)" on page viii and "Antenna and y Precautions" on page ix in "Preface".
ote: Mo	torola reserves the r	ight to change the specifications without prior notice.
	<u>Ta</u>	ble 1-1: Product Specifications
Produ	ct Features	
	~	Adds Wi-Fi capability to G24 platforms WLAN (802.11.b/g)
		Operates in the 2.4GHz ISM band Ad-Hoc WLAN Support Local WEB server
Physic	cal Characteristics	
Size:		45.2 x 24.4 x 7.25 mm 45.2 x 24.4 x 5.XX mm (stand alone model)
Mount	ting:	Two Ø2.4 mm holes
Weigh	it:	10 grams

Environmental	
Operational temperature:	-20°C to +60°C
Storage temperature:	-40°C to +85°C
Performance	
Operating voltage:	3.3 - 4.2 V
Current consumption:	250mA
Maximum Tx peak output	25 dBm according to 47 CFR 15.247 (b) (3)& RSS-210
power:	Section A8.4
Interfaces	
Connectors:	70-pin, board-to-board (host interface) 70-pin, board-to-board (G24 interface) not available on al models MMCX RF Connector
Serial Ports:	USB: USB full-speed device port, Rev. 2.0 USB full-speed host port, Rev. 2.0 Host UART Interface: BR from 2400 bps to 230.4 kbps Extended bit rates, up to 3Mbps Modem UART Interface: BR from 2400 bps to 230.4 kbps Extended bit rates, up to 3Mbps
Data Features	
Data Throughput:	Via UART:
	TBD kbps upload / TBD kbps download (security dis- abled) TBD kbps upload/ TBD kbps download (security enabled) Via USB: TBD kbps upload / TBD kbps download (security dis- abled) TBD kbps upload/ TBD kbps download (security enabled)
Control/Status Indicators	
Wakeup	
Antenna Detect	
Reset (Ready)	
TX enable	(Available only when coupled with G24)
Features over RS232	
Embedded TCP/IP stack	
AT Command Set	
	AT+i TM commands (ConnectOne's AT+i TM) is an exten-
	sion to the standard AT command set that allows program ming, monitoring, communication activation and control.
Accessories	sion to the standard AT command set that allows program

Table 1-1: Product Specifications (Cont.)

Regulatory Approvals

The W24 module has been tested and approved under the standards and regulations listed below:

- FCC
- CE (DOC)
- IC
- EMC
- Safety
- **R&**TTE

Important: The following paragraphs must be addressed by the integrator to ensure their host is in compliance to the W24 FCC grant and/or the FCC grant of the host device and the G24 (where applicable).

CFR 47 Part 15.19 specifies Label Requirements

The following text may be on the product, user's manual, or container.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

CFR 47 Part 15.21 Information to user

The user's manual or instruction manual for an intentional or unintentional radiator shall caution the user that changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment. In cases where the manual is provided only in a form other than paper, such as on a computer disk or over the Internet, the information required by this section may be included in the manual in that alternative form, provided the user can reasonably be expected to have the capability to access information in that form.

CFR 47 Part 15.247

Operation within the bands of 2400 to 2483.5 MHz.

CRF 47 Part 15.247 & 15.205 & 15.209

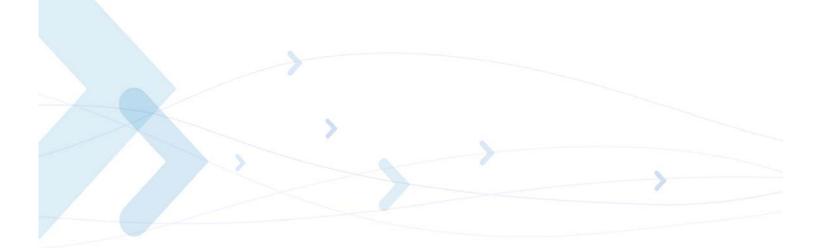
Spurious Radiated Emissions, Restricted Bands.

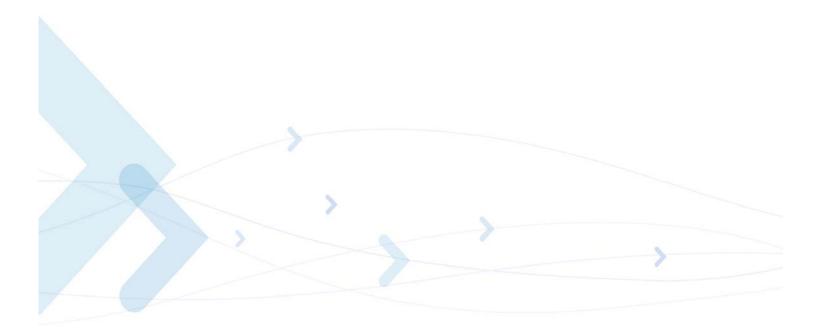
CFR 47 Part 15.107 & part 15.109

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide

reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult with your local dealer or with an experienced radio/TV technician for help.





Chapter 2: Hardware Interface Description

The following paragraphs describe in details the hardware requirements for properly interfacing and operating the W24 module.

Architecture Overview

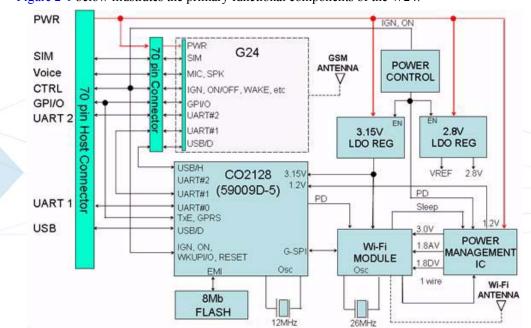


Figure 2-1 below illustrates the primary functional components of the W24.

Figure 2-1: W24 Block Diagram

The W24 consists of the following blocks:

Digital Block

- Micro-controller Unit (MCU) for system and application code execution.
- 1MByte Flash.
- Serial communications interfaces.
- USB Device interface.
- USB Host interface (available on cellular connector only).

- UART1.
- UART2 (when stacked with G24).
- General purpose IO signals.
- 12MHz crystal.

Power Management Block

- Power-up/down control circuit.
- Power Management IC (PMIC).
- 2 LDO regulators.

RF Block

- Wireless LAN Module which includes:
 - Power Amplifier
 - RF Transceiver
 - RF switch
 - Balun
 - MAC Processor
 - MAC EEPROM
- 26 MHz crystal.
- RF Antenna connector.

Operating Modes

W24 incorporates several operating modes. Each operating mode is different in the active features and interfaces.

Table 2-1 summarizes the general characteristics of the W24 operating modes and provides general guidelines for operation.

Mode	Description	Features		
Not Powered	VCC supply is disconnected.	The W24 is Off. Any signals connected to the interface con- nector must be set low or tri-state.		
Powered off Valid VCC supply. All W24 voltage regulators are disabled.		The W24 Interfaces and circuits are powered down. Only the power up/down control circuit is monitoring the IGN and ON_N signals. Any signals connected to the interface connector must be set low or tri-stated.		
Off-mode	Valid VCC supply. W24 powered down by ON_N, while IGN set high. All W24 voltage regulators are active.	The W24 Interfaces and circuits are powered down. The CPU is running at 32kHz and monitoring IGN and ON_N signals. Any sig nals connected to the interface connector must be set low or tri-stated.		
Sleep	RESET_N signal is high. CTS_N signal is disabled.	The W24 is in low power mode. The application interfaces are disabled, but, W24 continues to monitor the Wi-Fi network.		
Deep Sleep RESET_N signal is high. CTS_N signal is disabled.		The W24 enters deep sleep mode if it cannot find an AP that it can register to. It wakes up periodically to rescan and try to register. If the AP is found, it exits the deep sleep mode. If the AP cannot be found/registered to, it goes back to deep sleep mode - during that time it disables the RF section and does not monitor the Wi-Fi network.		
ldle	RESET_N signal is disabled (high). CTS_N and DSR_N signals are enabled (low).	The W24 is fully active, connected to the Wi-Fi network and ready to communicate. This is the default power-up mode.		
Airplane	RF block disabled.	The W24 interfaces are fully active, but its RF section is disabled.		
Тх	RESET_N signal is high. A Wi-Fi data transmission is in prog TXEN_N signal is toggling. When data transmission terminates, returns to the last operating state (Id Sleep).			

Table 2-1: W24 Operating Modes

Power Supply

The W24 power supply must be a single external DC voltage source of 3.3V to 4.2V. In order to support the stacked configuration, i.e. when G24 cellular module is coupled on the W24 module, the power supply must be able to sustain the required voltage level during a GSM transmit burst current surge, which may reach 2.0A.

The W24 interface connector has 8 contacts for the main power supply, as described in Table 2-2. All these contacts must be used for proper operation.

Pin #	Signal Name	Description			
1-4	GND	Main ground connection for W24 module.			
5-8	VCC	DC supply input for W24 module. $V_{IN} = 3.3 V \text{ to } 4.2 V$ $I_{RMS} = 250 \text{ mA during transmission}$ sion (Stand alone) $I_{RMS} = 900 \text{ mA during multi-slot}$ transmission (with G24) $I_{MAX} = 2.5 \text{ A during transmit}$ bursts			

Table 2-2: Power Supply Signals

Power Supply Design

Note: The power design conciderations show maximum requirements for stacked configuration.

Special care must be taken when designing the power supply of the W24 in stacked configuration. The single external DC power source indirectly supplies all the digital and analog interfaces, but also directly supplies the G24 RF power amplifier (PA). Therefore, any degradation in the power supply performance, due to losses, noises or transients, will directly affect the G24 performance.

The burst-mode operation of the GSM transmission and reception, draws instantaneous current surges from the power supply, which causes temporary voltage drops of the power supply level.

The transmission bursts consume the most instantaneous current, and therefore cause the largest voltage drop. If the voltage drops are not minimized, the frequent voltage fluctuations may degrade the W24 and G24 performance.

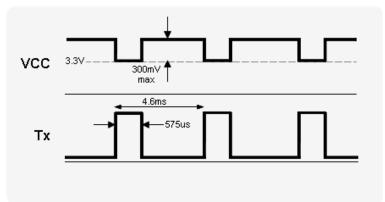


Figure 2-2 illustrates the power supply behavior during GSM transmission (Stacked configuration).

Figure 2-2: Transmission Power Drops

Power Consumption

Table 2-3 specifies typical W24 current consumption ratings in various operating modes. The current ratings refer to the overall W24 current consumption over the VCC supply.

Parameter	Description	Conditions	Min	Тур	Мах	Unit
I _{OFF}	Powered off	3.3V <vcc<4.2v< td=""><td></td><td>10</td><td>100</td><td>μA</td></vcc<4.2v<>		10	100	μA
I _{OFF MODE}	Powered off with IGN high	3.3V <vcc<4.2v IGN<0.8V</vcc<4.2v 		4	6	mA
IDEEPSLEEP		3.3V <vcc<4.2v< td=""><td></td><td>4</td><td>6</td><td>mA</td></vcc<4.2v<>		4	6	mA
I _{SLEEP}	Idle mode	3.3V <vcc<4.2v< td=""><td></td><td>9</td><td>12</td><td>mA</td></vcc<4.2v<>		9	12	mA
I _{IDLE}	Idle mode	3.3V <vcc<4.2v< td=""><td></td><td>125</td><td>150</td><td>mA</td></vcc<4.2v<>		125	150	mA
I _{AIRPLANE}	Airplane mode	3.3V <vcc<4.2v< td=""><td></td><td>45</td><td>60</td><td>mA</td></vcc<4.2v<>		45	60	mA
I _{TX}		3.3V <vcc<4.2v< td=""><td></td><td>190</td><td>250</td><td>mA</td></vcc<4.2v<>		190	250	mA

Table 2-3: W24 Current Ratings

Power On/Off Operation

The W24 power on consists of two primary phases:

Initial power up is hardware controlled by the Power-up/down circuit which monitors two power-up control signals: ON_N and IGN. If any of these two signals is asserted, the Power-up/down circuit will enable Voltage regulators and the power management IC. They will provide valid supply voltages to the processor which will start to boot.

The second phase is SW controlled. Processor checks the state of ON_N and IGN signals and if any of them is asserted, the CPU takes control over the voltage regulators and power management IC and holds them enabled.

Power down is controlled by SW - Once valid power-down condition occurs, CPU will release the signal which keeps the regulators and power management enabled, thus causing the W24 to power down. If this happened while IGN signal is still asserted, the regulators will remain active, and the unit will enter "Off mode".

W24 state is indicated at the interface connector by the hardware output signals RESET_N. The RESET_N signal indicates whether W24 is powered on or off. When the RESET_N is low, W24 is powered-off. When it is high, W24 is powered-on.

Important: Do not operate the W24 out of its electrical or environmental limits. Refer to the specifications chapter for details of these limits.

Turning the W24 On

When the W24 power supply is stable above the minimum operating level and W24 is powered off, all of its circuits are disabled.

When W24 is turned on, by any of the methods described below, it will first perform an automatic initialization. When the initialization has completed, W24 resumes normal operation (stand alone mode), or turns on the G24 module (stacked modules) and resumes normal operation.

During the internal system-test process W24 may toggle several interface signals, which are visible to the application. These signals do not represent any valid state or data, and should be ignored by the customer application until the system-test has completed.

Valid Turn-on reasons are:

- ON_N asserted low for 0.5s
- IGNITION asserted

Note: During FW upgrades (reflashing) make sure to power-up the W24 by IGN.

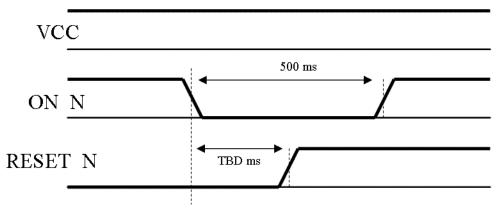
Turning the W24 On Using ON_N

The ON_N input signal is set high by an internal pull-up resistor whenever a power supply is applied to W24. Therefore, it is recommended to operate this signal using an open collector/drain circuit connection.

Asserting the ON_N signal low for a minimum of 500 milliseconds (0.5 seconds) and a maximum of 1.5 seconds will cause the W24 to turn-on.

Asserting the ON_N signal low for more than 1.5 seconds may cause the W24 to interpret the signal as a power-off command, and turn off immediately after turning on.

Figure 2-3 illustrates the power-on process using the ON_N signal.



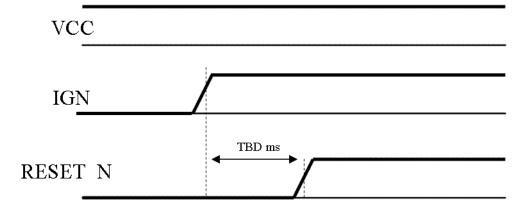


Turning the W24 On Using IGN

The IGN input signal must be set low when not used. To turn on W24, this signal must be asserted high. The IGN signal must remain high for the duration of the W24 operation. W24 powers down when the IGN signal is returned to its low state.

Important: It is recommended to place a pull-down resistor in the customer application, on the IGN signal. A 100 kohm resistor, or less, is acceptable.

Figure 2-4 illustrates the power-on process using the IGN signal.





Turning the W24 Off

There are several ways to turn the W24 off:

- Asserting the ON_N signal low for a minimum of 2 seconds.
- Setting the IGN signal low.
- Turning the W24 Off using AT+iMRST.

Note: If the unit was powered up by ON_N signal, W24 will ignore attempt to turn it off by setting the IGNITION to low.

Turning the W24 Off Using ON_N

The ON_N signal is internally pulled-up when power is applied to W24.

Asserting the ON_N signal low for a minimum of 2 seconds will turn W24 off. This will initiate a normal power-off process, which includes G24 power down (stacked configuration) and disabling of all applications interfaces (UART, USB, etc.).

Figure 2-5 illustrates the power-off timings when using the ON_N signal.

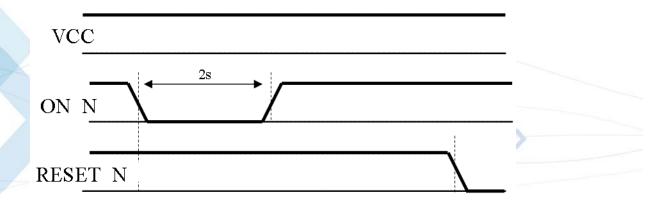


Figure 2-5: ON_N Power Off Timing

Note: If the unit was powered up by IGN, and it's being turned off by ON_N, the unit will enter "Off mode", and not power off.

Turning the W24 Off Using IGN

The IGN signal may be used to power off W24 if the unit was powered-up by the IGN signal. When the IGN signal is set low, W24 will turn off. This will initiate a normal power-off process, which includes G24 power down (stacked configuration) and disabling of all applications interfaces (UART, USB, etc.).

The IGN signal will not power off W24 before 30 seconds have elapsed since W24 was powered-on. This delay mechanism is implemented to protect W24 from unexpected transients on the IGN line during power up, particularly when applying vehicle cranking waveforms.

Figure 2-6 illustrates the power-off timings when using the IGN signal.

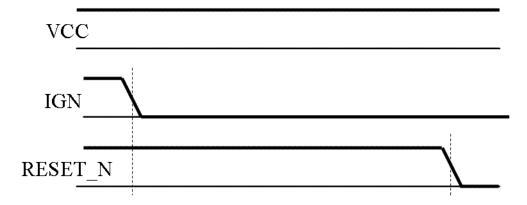


Figure 2-6: IGN Power Off Timing

Power Loss shut down

A low power shut down occurs when W24 senses that the external power supply is below the minimal operating limit. The module will respond by powering down automatically without notice.

This form of power-down is not recommended for regular use since the unexpected power loss may result in loss of data.

Turning the W24 Off Using AT+iMRST

The AT+iMRST command initiates a W24 system reset operation, which powers off the W24. This command emulates the ON_N signal operation for W24 power off.

Note: In Stacked configuration, AT+iMRST powers down W24 only. It does not power down the G24 or any similar module.

Low Power Mode

The W24 supports optional low power modes, called Sleep Mode and Deep Sleep Mode, in which it operates with minimum functionality, and therefore draws significantly less current. During Sleep Mode the W24 network connection is not lost. W24 continues to monitor the network constantly for any incoming data.

During low power mode, all of the W24 interface signals are inactive and are kept in their previous state, prior to activating low power mode. To save power, all the W24 internal clocks and circuits are shut down, and therefore serial communications are disabled.

In the cases where no Access Point can be found at W24 power-up, W24 will enter Deep Sleep Mode. During Deep Sleep Mode, network connection does not exist. W24 will periodically come out of Deep Sleep Mode, to attempt to locate APs. If the search is not successful, the module will return to Deep Sleep Mode.

Activating Low Power Mode

By default, the W24 powers on in Idle mode. In this mode, all the W24 interfaces and features are functional and the module is fully active. Sleep mode is activated by setting the AT+iPSE>0.

When the WAKEUP_N signal is disabled (high) and no data is present on the serial interface or incoming from the Wi-Fi network, and no internal system task is running, W24 will enter Sleep mode.

When in Sleep mode, the W24 disables all its serial interfaces (USB and UARTs), and the VBUS output signal of the cellular connector (stacked configuration).

The W24 continues to respond to both external and internal interrupts.

The following conditions must be met for the W24 to enter Sleep mode:

- WKUPI_N signal is disabled (high).
- AT+ iPSE and AT+ iS102 parameter values are not 0.
- For example:
 - AT+ iPSE = 1 activates low power mode after 1 second of inactivity.
 - AT+ iPSE = 5 activates low power mode after 5 seconds of inactivity.
 - AT+ iPSE = 0 disables low power mode (default).
- AT+ iWLPS parameter value is not 0, so the Wi-Fi chip is in automatic power save mode.
- No host command in currently being processed.
- No WPA process is currently active. For example, scanning or handshake.
- Carrier lost handling is not in process.
- No data is waiting to be transmitted over any of the active sockets.
- No data is waiting to be sent in SerialNET mode.

For more information about AT+ iS100 and AT+ iS102 parameters, see the following sections.

Important: W24 will not enter Sleep mode in any case when data is present on the serial interface or incoming from the Wi-Fi network, or an internal system task is running. Only when processing of a pending external or internal system task has completed, W24 will enter Sleep mode according to the AT+iPSE command settings.

Serial Interface During Low Power Mode

During low power mode the W24 serial interfaces are disabled.

The W24 wakes up periodically from low power mode to monitor the network for data. After this short paging is completed, W24 returns to low power mode. During this short awake period, the serial interfaces are enabled and communications with the module is possible.

Terminating Low Power Mode

To bring W24 back from the sleep mode, WAKEUP_N needs to be asserted (set low). The W24 Sleep mode is enabled or disabled by the AT+iPSE command. To permanently terminate the W24 Sleep mode, the AT+iPSE=0 command is used. W24 does not enter Sleep mode until an AT+iPSE>0 command is sent.

Important: During power save mode the W24 internal clocks and circuits are disabled, in order to minimize power consumption. When terminating the power saving mode, and switching to Idle mode, W24 requires a minimal delay time to reactivate and stabilize its internal circuits before it can respond to application data. This delay is typically 5 milliseconds. The delay guarantees that data on the serial interface is not lost or misinterpreted.

Temporary Termination of Low Power Mode

Temporary termination of low power mode occurs when W24 switches from Sleep mode to Idle mode for a defined period, and then returns automatically to Sleep mode.

Low power mode may be terminated temporarily by several sources, some of which are user initiated and others are initiated by the system.

Using the WKUPI_N signal

The WKUPI_N signal is an active low input, that is set high by default. By asserting this signal low the application can wake-up W24 from low power mode and switch to Idle mode.

W24 will remain in Idle mode, awake and fully active, as long as WKUPI_N signal remains low. When this signal is disabled and set high again, W24 will return to Sleep mode automatically, according to the AT+iPSE settings (see Figure 2-7).

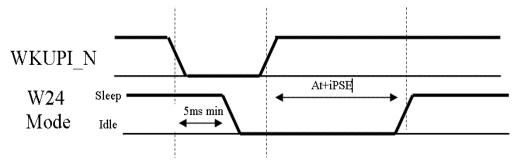


Figure 2-7: WKUPI_N Signal Operation

The WKUPI_N signal is the recommended method to temporarily wake-up W24 from low power mode. It provides the application full control of the W24 operating mode and guarantees that data on the serial interface will not be lost or misinterpreted.

Using the WKUPO_N signal

The WKUPO_N signal is an active-low W24 output that is asserted high by default. By asserting this signal low, the host can be waked up by W24. WKUPO_N alerts the host that W24 exits from Sleep mode into Idle mode due to network activity (see Figure 2-8).

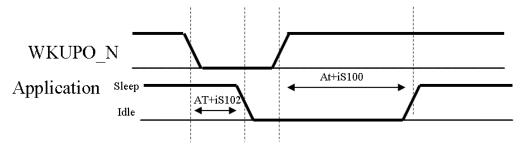


Figure 2-8: WKUPO_N Signal Operation

The AT+iS102 parameter defines the delay time, in milliseconds, that W24 waits when in SerialNET mode, after asserting the WKUPO_N signal, and before sending data on the host interface. This delay is required to allow the application enough time to re-activate from low power mode and switch to normal mode. If S102=0, which is the default value, the WKUPO_N signal and mechanism is disabled. In other words, W24 will never assert the WKUPO_N signal. AT+iS100 defines the minimal time interval, in milliseconds, that W24 waits before asserting WKUPO_N after de-asserting it. In other words, W24 will not assert the WKUPO_N signal if the time that had passed from the previous de-assertion of this signal is not at least the duration specified by the S100 parameter. This time interval is required to avoid frequent unnecessary wakeup events and consequent S102 delays.

The S100 parameter is relevant only if S102>0, which enables the WKUPO_N signal operation.

Incoming Network Data

During low power mode, W24 continues monitoring the Wi-Fi network for any incoming data.

When W24 receives an indication from the network that data is available, it automatically wakes up from low power mode to alert the application. When W24 has completed to process all the tasks related to the incoming data, it will automatically return to low power mode according to the AT+iEPS settings.

Depending on the type of network indication and the application settings, W24 may operate in several methods, which are configurable by AT commands, to alert the application of the incoming data:

- Enable the WKUPO_N signal to wake-up the application from low power.
- Send data to the application over the serial interface.

Data on the Serial interface

While W24 is temporarily awake in Idle mode, data may be transmitted on the serial interface. In case data is being transmitted in any direction, W24 will not return to low power mode. This is

regardless of the original wake-up reason or source. W24 will remain awake while data is transferred.

Only when the serial interface transfer is completed and the data has been processed, W24 will return to low power mode automatically, according to the AT+iPSE settings (see Figure 2-9).

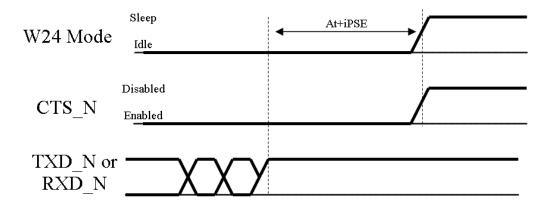


Figure 2-9: Serial Interface Data

Permanent termination of Low Power Mode

The W24 low power mode is enabled and disabled by the AT+iPSE command.

To permanently terminate the W24 low power mode, the AT+iPSE = 0 command must be used. Setting AT+iPSE = 0 disables the currently active low power mode and switches W24 to Idle mode.

W24 will not return to low power mode until an AT+iPSE > 0 commands is set again.

This command can be sent only when the serial interface is active.

In case the serial interface is disabled, it must first be activated before sending this command. To reactivate the serial interface, a temporary termination of the low power mode is required, as described in "Temporary Termination of Low Power Mode" on page 17.

Following the temporary low power mode termination, the serial interface will activate and the AT+iPSE = 0 command can be received by W24.

Deep Sleep Mode

In case that W24 cannot locate an active access point or cannot register to it, W24 will enter the Deep Sleep Mode.

During Deep Sleep Mode, network connection does not exist. W24 will periodically come out of the Deep Sleep Mode, to attempt to locate APs. If the search is not successful, the module will return to the Deep Sleep Mode.

The time between consecutive attempts to locate an AP is set by the AT+iWLSI command.

Serial Interfaces

W24 includes three completely independent serial communications interfaces, which may be used by the application for several purposes (see Figure 2-10).

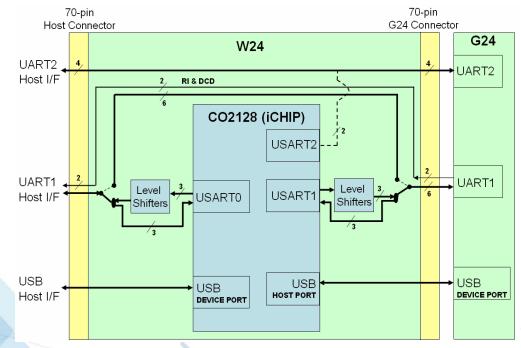


Figure 2-10: Serial Interfaces Block Diagram

Primary UART (UART1)

The W24 primary UART is a standard 8-signal bus. The primary UART is used for all the communications with W24 - AT+i commands interface, Wi-Fi data, and G24- AT command Interface (stacked configuration), programming and software upgrades.

The UART signals are active low CMOS level signals. For standard RS232 communications with a PC, an external transceiver is required.

W24 is defined as a DCE device, and the user application is defined as the DTE device. These definitions apply for the UART signals naming conventions, and the direction of data flow, as described in Figure 2-11.

UART]		W24 70	pin interface - DCE
	TXD	┣───►	21	TXD_N	Transmit Data
	RXD	<	11	RXD_N	Receive Data
	RTS	┝───▶	9	RTS N	Request To Send
	CTS	4	15	CTS_N	Clear To Send
DTE	DTR		19	DTR_N	Data Terminal Ready
	DSR	<u>،</u>	13	DSR_N	Data Set Ready
	DCD	•	17	DCD_N	Carrier Detect
	RI	4	23	RI_N	Ring Indicator

Figure 2-11: UART1 Interface Signals

The W24 primary UART supports baud rates 2400, 4800, 9600, 19200, 38400, 57600, 115200, 230400, as well as the extended high speed rates: 1Mbps, 1.5Mbps and 3Mbps. Auto baud rate detection is supported for baud rates up to 115200 bps. All flow control handshakes are supported: hardware, software, or none. Parity bit and Stop bit definitions are also supported. The UART default port configuration is 8 data bits, 1 stop bit and no parity, with hardware flow control and auto baud rate detect enabled.

Primary UART is defined for 3 different HW models: standard, stand alone and JAVA models.

In the standard model, Primary UART is connected directly to W24 processor. 'AT+i' standard command will be executed by the W24, while 'AT' commands that are for the G24 primary UART (stacked configuration), will be diverted by the W24 processor to the G24 interface connector, and will be executed by the G24.

In the JAVA model, where the G24 JAVA model function as W24 Host application, all UART commands will be diverted to the G24 interface connector (by different HW wiring), and the G24 will execute them.

Important: The W24 UART will not send data over the serial interface in case the DTR_N and/or RTS_N input signals are disabled (set high). Therefore, regardless of the handshake method, it is still required to enable these signals for proper operation, by asserting them low.

Note: Add a pull-up resistors (22-100K) on all UART input signals that can be left in an undefined state (Hi-Z). The resistors should pull to the VREF voltage level.

Secondary UART (UART2)

W24 incorporates Secondary UART interface intended to support G24 UART2 interface, in stacked configuration. The interface consists of the following pins (see Table 2-4):

Pin #	Pin Name	Description
29	RXD2	Receive Data
31	TXD2	Transmit Data
33	RTS2	Request To Send
35	CTS2	Clear To Send

Table 2-4: Secondary UART Interface Signals

The above signals are routed from 70-pin host connector to G24 70-pin connector via W24. They are not internally connected to W24 circuits. If the W24 will be used as stand alone only, the above pins should be left open.

For more details on implementing a secondary interface for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description", PN 6889192V27.

USB Device Interface

W24 incorporates a standard Universal Serial Bus (USB) interface.

The interface consists of the following pins (see Table 2-5):

Pin # Pin Name		Description	
10	USB_VBUS	USB bus power	
12	USB_DP	USB bus serial data	
14	USB_DN	USB bus serial data	

Table 2-5: USB Device Interface Signals (at Host connector)

The W24 USB electrical interface and protocol conform to the USB 2.0 full-speed specifications. W24 is defined as a USB device on the 70-pin Host Connector. This interface does not support hub or host functionality.

USB may be used for standard communications with W24, as done through the UART interface.

USB Host Interface (Stacked configuration only)

W24 incorporates USB Host interface intended to support G24 USB interface, in stacked configuration. The interface consists of the following pins (see Table 2-6):

Pin #	Pin Name	Description
10	USB_VBUS	USB bus power
12	USB_DP	USB bus serial data
14	USB_DN	USB bus serial data

Table 2-6: USB Host Interface Signals (at G24 connector)

The W24 USB Host electrical interface and protocol conform to the USB 2.0 full-speed specifications.

USB may be only used for standard communications between W24 and G24 (stacked configuration only), as done through the UART interface. Setting USB Host communication is enabled by the AT+iHIF=5 command.

Important: W24 relies on the host to provide the USB_VBUS voltage, for both USB device and USB host interfaces. Therefore, if only USB host interface is to be used, or if host interface is going to be used when the device USB interface is inactive, make sure to provide the valid USB_VBUS voltage on the Host Connector.

For more details on setting a USB interface for use with G24, please refer to the "Motorola W24 Programmer's Guide - At+i Programmers Manual Description" PN 6802985C10.

Note: The maximum length of the W24 USB signals is 1m.

SIM Interface

W24 incorporates SIM interface intended to support G24 SIM interface, in stacked configuration. The interface consists of the following pins (see Table 2-7):

Pin #	Pin Name	Description
44	SIM_RST_N	Active low SIM reset signal
46	SIM_CLK	SIM Clock
48	SIM_VCC	SIM Voltage Supply
50	SIM_PD_N	Active low SIM card presence detection
52	SIM_DIO	Serial input and output data

Table 2-7:	SIM	Interface	Signals

The above signals are routed from 70-pin host connector to G24 70-pin connector via W24. They are not internally connected to W24 circuits. If the W24 will be used as stand alone only, the above pins should be left open.

For more details on implementing a SIM interface for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description" PN 6889192V27

Audio Interface

W24 incorporates Audio interface intended to support G24 Audio interface, in stacked configuration. The interface consists of the following pins (see Table 2-8 for analog audio and Table 2-9 for digital audio):

Pin #	Pin Name	Description
55	HDST_INT_N	Headset detect interrupt
57	HDST_MIC	Headset microphone input
59	AGND	Audio ground
61	MIC	Microphone input
63	ALRT_N	Alert speaker inverted
65	ALRT_P	Alert speaker
67	SPKR_N	Speaker inverted
69	SPKR_P	Speaker

Table 2-8: Analog Audio Interface Signals

Table 2-9: Digital Audio Interface Signals

Pin #	Pin Name	Description	
18	PCM_DIN	Digital audio receive	
20	PCM_DOUT	Digital audio transmit	
22	PCM_CLK	Digital audio clock	7
24	PCM_FS	Digital audio clock	

The above signals are routed from 70-pin host connector to G24 70-pin connector via W24. They are not internally connected to W24 circuits. If the W24 will be used as stand alone only, the above pins should be left open.

For more details on implementing a Audio interface for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description" PN 6889192V27

A/D Converter Interface

W24 incorporates Analog to Digital Converter interface intended to support G24 ADC interface, in stacked configuration. The interface consists of the following pins (see Table 2-10):

Pin #	Pin Name	Description
37	ADC1	General purpose A/D
43	ADC2	General purpose A/D
47	ADC3	General purpose A/D

Table 2-10: ADC Interface S	ignals
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The above signals are routed from 70-pin host connector to G24 70-pin connector via W24. They are not internally connected to W24 circuits. If the W24 will be used as stand alone only, the above pins should be left open.

For more details on implementing a ADC interface for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description" PN 6889192V27

Unused/Reserved Signals

W24 provides an interface to unused and reserved G24 signals, to enable their usage in the future. The signals are directly routed from 70-pin host connector to the 70-pin cellular connector. The above pins should be left open. Here is the summary interface consists of the following pins (see Table 2-11):

Pin #	Pin Name	Description
54	LCD_CS	N.C
56	LCD_SD	N.C
58	LCD_CLK	N.C
70	SPI_CS	N.C
62	SPI_INT_N	N.C
64	SPI_DIN	N.C
68	SPI_DOUT	N.C
66	SPI_CLK	N.C

Table 2-11: Internal Interface Signals

Controls and Indicators Interface

The W24 incorporates several interface signals for controlling and monitoring the module's operation. The following paragraphs describes these signals and their operation.

Table 2-12 gives a description of the controls and indicators signals.

Pin #	Pin Name	Description
25	RESET_N	W24 system readiness output indicator. When high, W24 is operating.
27	VREF	2.75V regulated output. Supplies up to 100mA to the external circuits.
26	WKUPO_N	Host application wake-up signal indicator.
16	WKUPI_N	Device application wake-up sig- nal control.
41	ANT_DET	Antenna physical connection detect indicator.
39 💊	TXEN_N	Transmission burst indication.
49	GPRS	GPRS/EGPRS coverage indica- tor.
60	WIFI_LED_LCD_RS	Wi-Fi Activity LED indicator.
45	MSEL_UID	Mode Select.

Table 2-12: Controls and Indicators

Reset

The RESET_N output signal indicates the W24's operating status. This signal is set high after power up, when W24 is operating. It is set low when W24 is powered off.

When the RESET_N signal is low, the W24 interface signals are disabled and do not represent any valid data or state. Furthermore, any input signals connected to the W24 interface must be disabled (tri-state) or set low when RESET_N is low.

VREF Reference Regulator

The W24 incorporates a regulated voltage output, VREF. The regulator provides a 2.8V output for use by the customer application. This regulator can source up to 100 mA of current to power any external digital circuits. **VREF voltage is present whenever the W24 is powered up, regardless of it's mode of operation**.

Important:	The VREF regulator is powered from the W24's main power supply, and therefore
	any current sourced through this regulator originates from the W24 VCC supply.
	The overall VCC current consumed by W24 is directly affected by the VREF
	operation. The W24 current consumption rises with respect to the current drawn
	from VREF.

Table 2-13 gives the VREF specifications.

			nouno		
Parameter	Conditions	Min	Тур	Max	Unit
V _{OUT}	I _{OUT} = 150 mA	2.7	2.8	2.9	V
I _{OUT}	Active mode		-	100	mA
Load regula- tion	I _{OUT} = 1.0 mA to 100 mA		2	15	mV
Line regula- tion	Vin = Vcc I _{OUT} = 1.0 mA		1	10	mV

Table 2-13: VREF Specifications

Wakeup

For a detailed description regarding Wake up signal interface, please refer to "Low Power Mode" on page 16, paragraphs "Using the WKUPI N signal" and "Using the WKUPO N signal".

Antenna Detection

The W24 incorporates an internal antenna detection circuit, which senses the physical connection and removal of a Wi-Fi antenna, as well as the cellular antenna, in case that a cellular module like G24 is stacked on the W24.

In case of stacked configuration (W24 + G24), a removal of a single or both of the modules antennas, will cause the antenna detect indicator to be set Low.

The antenna detection state is reported to the application through the ANT_DET output signal, and may also be queried by the AT+iRP19 command.

The detection circuit senses DC resistance to ground on the G24 antenna connector.

A DC resistance below 100kohm (+10%) is defined as a valid antenna connection, and the ANT_DET output signal is set high.

GPRS/EGPRS Detection

This signal is routed from the 70-pin host connector to G24 70-pin connector via W24. It is not internally connected to W24 circuits. If the W24 will be used as stand alone only, this signal pin should be left open.

For more details on implementing a GPRS/EGPRS signal for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description" PN 6889192V27

Transmission Indicator

This signal is routed from the 70-pin host connector to G24 70-pin connector via W24. It is not internally connected to W24 circuits. If the W24 will be used as stand alone only, the signal pin should be left open.

For more details on implementing a GPRS/EGPRS signal for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description" PN 6889192V27

Wi-Fi Indicator

Wi-Fi LED Indicator is an open-drain output, which indicates the Wi-Fi activity.

Mode Select

This signal sets the W24 mode of operation. It should be left open for normal operation. If pulled to ground at power up for 3s, it forces a module in boot mode.

General Purpose I/O

W24 incorporates 8 General Purpose I/O interface. The interface consists of the following pins (see Table 2-14):

Pin #	Pin Name	Description
28	GPIO1	General purpose I/O
30	GPIO2	General purpose I/O
32	GPIO3	General purpose I/O
34	GPIO4 / BT_PRIORITY	General purpose I/O / Bluetooth coexistence indication
36	GPIO5 / BT_FREQ	General purpose I/O / Bluetooth coexistence indication
38	GPIO6 / BT_STATE	General purpose I/O / Bluetooth coexistence indication
40	GPIO7 / WL_ACTIVE	General purpose I/O, Wi-Fi indi- cation
42	GPIO8	General purpose I/O

Table 2-14: GPIO Interface Signals

In Stand alone configuration, GPIO pins 1 to 3 act as general purpose inputs. GPIO pins 4 to 7 function as Bluetooth coexistence control/indicators for the W24 to use with a Bluetooth interface in order to avoid RF collision between Bluetooth and Wi-Fi transmission. GPIO8 is not used, and should be left open.

In stacked configuration, all GPIO signals are routed from G24 to host, and are used as GPIO signals for G24 user application.

For more details on implementing a GPIO interface for use with G24, please refer to the "Motorola G24 Developer's Guide - Module Hardware Description" PN 6889192V27

Antenna Interface

The W24 antenna connector is the RF interface to the WLAN network.

The antenna interface consist of an MMCX type connector, which has impedance matched to 50Ω at the relevant WLAN 802.11 b/g frequencies.

The antenna or antenna application must be installed properly to achieve best performance.

Table 2-15 gives the antenna interface specifications.

Parameter	Specifications
WLAN 802.11b/g band	2400 MHz - 2483.5MHz
Gain	0 dBi
Impedance	50Ω
VSWR	Less than: 2.5:1

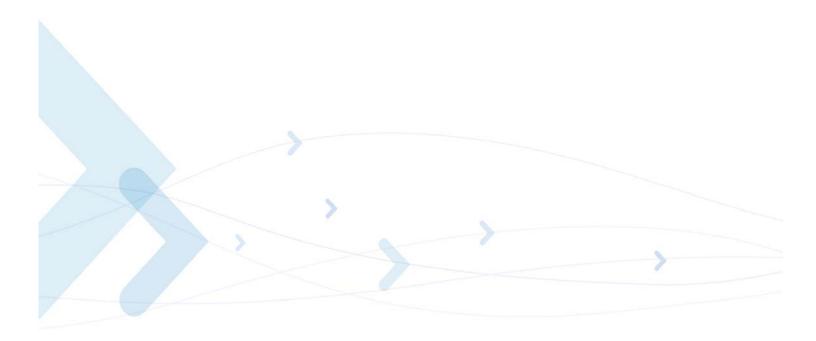
Table 2 To: Antenna Internace Opcontoations	Table 2-15:	Antenna	Interface	Specifications
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It is the Integrator's responsibility to design the antenna or antenna assembly used with the W24.

This will highly affect the RF performance of the W24 (signal quality, current consumption etc.).

The following guidelines should be followed:

- Make sure that the antenna or antenna assembly matches the Antenna Interface Specifications.
- Use low loss RF cable and connectors keeping cable runs to a minimum.
- In order to prevent mutual interference between the GSM and the WLAN antennas in stacked configuration, a minimum distance that provides a 30dB isolation is required. To achieve the required 30dB isolation between the antennas, a distance of about 7cm from each other should be kept when using a monopole isotropic antenna with 0dBi and appropriate return loss.



Chapter 3: Electrical and Environmental Specifications

Absolute Maximum Ratings

Table 3-1 gives the maximum electrical characteristics of the W24 interface signals.

Caution: Exceeding the values may result in permanent damage to the module.

Parameter	Conditions	Min	Max	Unit								
VCC Supply		-0.3	4.5	V								
Digital Input Signals (Except for IGN, VBUS, USB_DP, USB_DN)	W24 powered on	-0.3	3.6	V								
Analog Input Signals (Audio, A/D inter- faces)	W24 powered on	-0.3		V								
All Input Signals (Except for IGN, VBUS, USB_DP, USB_DN)	W24 powered off	-0.3		V								
IGN signal		-0.3	16	V								
VBUS signal		-0.3	5.25	V								
USB_DP, USB_DN		-0.3	3.6	V								

Table 3-1: Maximum Ratings

Environmental Specifications

Table 3-2 gives the environmental operating conditions of the W24 module.

Caution: Exceeding the values may result in permanent damage to the module.

Parameter	Conditions	Min	Max	Unit
Ambient Operating Temperature		-20	+60	°C
Storage Temperature		-40	+85	°C
ESD	At antenna connector Contact Air	-8 -15	+8 +15	kV

Table 3-2: Environmental Ratings

Application Interface Specifications

Table 3-3 summarizes the DC electrical specifications of the host application interface connector signals.

Table 3-4 summarizes the DC electrical specifications of the cellular modem (e.g. G24) interface connector signals.

Important:	Interface signals that are not used by the customer application must be left
	unconnected. W24 incorporates the necessary internal circuitry to keep unconnected
	signal in their default state. Do not connect any components to, or apply any voltage
	on, signals that are not used by the application.

Important: It is recommended to place a pull-down resistor in the customer application, on the IGN signal. A 100 kohm resistor, or less, is acceptable.

Important:	Signals that are defined as "Do Not Use", or DNU, must remain externally
	unconnected in any case. These signals are reserved for future use.

	Signal			Activo	Internal			Level			
Pin #	Signal Name	Description	I/O	H/L	PU/PD	Parameter	Conditions	Min	Тур	Max	Units
Power	:										
1											
2	GND	Ground									
3											
4											
5						V _{IN}		3.3	3.6	4.2	V
6	VCC	DC power	Ι			I _{MAX}	VCC = 3.6 V		190	250	mA
7		supply				I _{OFF}			10	100	μΑ
8											
Contro	ol:			r.					1		
27	VREF	Reference regulator out-	0			V _{OUT}	$I_{OUT} < 100 \text{mA}$	2.7	2.8	2.9	V
1		put		5							
16	WKUPI_N	W24 wakeup	Ι	L 🦉	15K PU	V _{IH}		2.0		3.15	V
1		input				V _{IL}		-0.3	0	0.8	
26	WKUPO_ N	Host wakeup output	0	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
25	RESET_N	W24 Ready indication output	0	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
53	ON_N	On/Off switch	Ι	L	100K PU	V _{OH} V _{OL}		2		0.4	V
51	IGN	Ignition input	Ι	Н	80K PD	V _{IH} V _{IL}					V
39	TXEN_N	Transmit indi- cator	0	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
41	ANT_DET	Antenna pres- ence indicator	0	Н	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
49	GPRS	GPRS/ EGPRS cov- erage indica- tor	0	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V

												Level			
Pin #	Signal Name	Description	I/O	Active H/L	Internal PU/PD	Parameter	Conditions	Min	Тур	Мах	Units				
UART	1:			<u> </u>											
21	TXD_N	UART1 TXD	Ι	L	15K PU	V _{IH}	$I_{OUT} < 8 \text{ mA}$	2		3.15	V				
11	RXD_N	UART1 RXD	0	L											
9	RTS_N	UART1 RTS	Ι	L		V _{IL}		-0.3	0	0.8					
15	CTS_N	UART1 CTS	0	L			$I_{OUT} < 8 \text{ mA}$								
19	DTR_N	UART1 DTR	Ι	L							V				
13	DSR_N	UART1 DSR	0	L		V _{OL}				0.4					
17	DCD_N	UART1 DCD	0	L		V _{OH}		2.575							
23	RI_N	UART1 RI	0	L											
UART	2:														
29	RXD2	UART2 RXD	0	L	15K PU	17				2.15					
31	TXD2	UART2 TXD	Ι	L	15K PU	V _{IH}		2		3.15					
33	RTS2	UART2 RTS	Ι	L	15K PU	V _{IL}	I _{OUT} < 8 mA	-0.3	0	0.8	V				
35	CTS2	UART2 CTS	0	L	15K PU		1001 0 1111				•				
2					6	V _{OL}	>	2.75		0.4					
			>			V _{OH}			0		>				
USB:			I												
10	USB_VBUS	USB bus power	Ι	Н	_				5.0	5.25	V				
12	USB_DP	USB bus serial data	I/O	Н							V				
14	USB_DN	USB bus serial data	I/O	L							V				
SIM Ca	ard:														
50	SIM_PD_N	SIM presence detect	Ι												
48	SIM_VCC	SIM supply	0			Refer to G24									
44	SIM_RST_ N	SIM reset	0			Hardwre Description- 6889192V27	Stacked Confoguration								
52	SIM_DIO	SIM serial data	I/O			6889192V27									
46	SIM_CLK	SIM clock	0												

	Signal			Activo	Internal			Level					
Pin #	-	Description	I/O	H/L	PU/PD	Parameter	Conditions	Min	Тур	Max	Units		
Digital	Digital Audio:												
18	PCM_ DIN	Digital audio receive	Ι										
20	PCM_ DOUT	Digital audio transmit	0			Refer to G24 Hardwre Description- 6889192V27	Stacked						
22	PCM_CLK	Digital audio clock	0				~	Confoguration					
24	PCM_FS	Digital audio frame sync.	0										

									Le	vel	
Pin #	Signal Name	Description	I/O	Active H/L	Internal PU/PD	Parameter	Conditions	Min	Тур	Max	Units
GPIO:					L						L
28	GPIO1	General pur- pose I/O	I/O								
30	GPIO2	General pur- pose I/O	I/O								
32	GPIO3	General pur- pose I/O	I/O								
34	GPIO4	General pur- pose I/O	I/O								
36	GPIO5	General pur- pose I/O	I/O								
38	GPIO6	General pur- pose I/O	I/O								
40	GPIO7	General pur- pose I/O	I/O	>							
42	GPIO8	General pur- pose I/O	I/O								
Audio											
67	SPKR_N	Speaker inverted	0		\land						>
69	SPKR_P	Speaker	0								
63	ALRT_N	Alert speaker inverted	0								
65	ALRT_P	Alert speaker	0			Refer to G24					
61	MIC	Microphone input	Ι			Hardwre Description- 6889192V27	Stacked Confoguration				
59	AGND	Audio ground									
57	HDST_ MIC	Headset microphone input	Ι								
55	HDST_ INT_N	Headset detect inter- rupt	Ι								

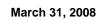
	Cianal			Active					Le	vel	
Pin #	Signal Name	Description	I/O	H/L	Internal PU/PD		Conditions	Min	Тур	Max	Units
A/D:											
37	ADC1	General pur- pose A/D	Ι								
43	ADC2	General pur- pose A/D	Ι			Refer to G24 Hardwre	Stacked				
47	ADC3	General pur- pose A/D	Ι			Description- 6889192V27	Confoguration				
45	MSEL_UI D	Do Not Use									
Displa	y:			1							
54	CLI_CS	Do Not Use									
60	LCD_RS	Do Not Use									
56	LCD_ DATA	Do Not Use	>								
58	LCD_CLK	Do Not Use									
Data L	ogger:										
70	SPI_CS	Do Not Use		<i>.</i>							
62	SPI_IRQ_ N	Do Not Use		\geq						>	
64	SPI_DIN	Do Not Use									
68	SPI_ DOUT	Do Not Use									
66	SPI_CLK	Do Not Use									

	0				1				Le	vel	
Pin #	Signal Name	Description	I/O	Active H/L	Internal PU/PD	Parameter	Conditions	Min	Тур	Max	Units
Power	:							•			
1											
2	GND	Ground									
3											
4											
5						V _{IN}		3.3	3.6	4.2	V
6	VCC	DC power	0			I _{MAX}	VCC = 3.6 V		220	490	mA
7						I _{OFF}			10	100	μA
8											
Contro				1				[[
27	G24_VRE F	Reference regulator out- put	NC	N/A	5						/
16	G24_WK UPI_N	W24 wakeup input	0	L	15K PU	V _{IH} V _{IL}	>	2	0	3.15 0.8	V
26	G24_WK UPO_N	Host wakeup output	I	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	v 🧪
25	G24_RES ET_N	Reset signal output	Ι	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
53	G24_ON_ N	On/Off switch	0	L	15K PU	V _{OH} V _{OL}		2.75		0.4	V
51	G24_IGN	Ignition input	0	Н	15K PU	V _{IH} V _{IL}		2		0.4	V
39	TXEN_N	Transmit indi- cator	Ι	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
41	G24_ANT _DET	Antenna pres- ence indicator	Ι	Н	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V
49	GPRS	GPRS/ EGPRS cov- erage indica- tor	Ι	L	15K PU	V _{OH} V _{OL}	I _{OUT} < 8 mA	2.75		0.4	V

Table 3-4: G24 Interface Specifications

Pin # UART1 21	Signal Name	Description			Internal						
		•	I/O	H/L	PU/PD	Parameter	Conditions	Min	Тур	Max	Units
21	:			1	. <u> </u>						
~ I	TXD_N	UART1 TXD	0	L							
11	RXD_N	UART1 RXD	Ι	L		V _{IH}		2		3.15	
9	RTS_N	UART1 RTS	0	L		V _{IL}		-0.3	0	0.8	
15	CTS_N	UART1 CTS	Ι	L			_				
19	DTR_N	UART1 DTR	0	L	15K PU		I _{OUT} < 8 mA				V
13	DSR_N	UART1 DSR	Ι	L		V _{OL}				0.4	
17	DCD_N	UART1 DCD	Ι	L		V _{OH}		2.575			
23	RI_N	UART1 RI	Ι	L		• OH					
UART2	:			-							
29	RXD2	UART2 RXD	I	L		17		•		2.15	
31	TXD2	UART2 TXD	0	L		V _{IH}		2		3.15	
33	RTS2	UART2 RTS	0	L	15K PU	V _{IL}	I _{OUT} < 8 mA	-0.3	0	0.8	V
35	CTS2	UART2 CTS	Ι	L	151110		1001 < 0 mm				-
						V _{OL}	5			0.4	
10					-	V _{OH}		2.75		5	
USB:											
10	USB_VBUS	USB bus power	0	Н							
12	USB_DP	USB bus serial data	I/O	Н							
14	USB_DN	USB bus serial data	I/O	L							
SIM Ca	rd:			1	<u> </u>					1	
50	SIM_PD_N	SIM presence detect	0								
48	SIM_VCC	SIM supply	Ι			Refer to G24					
44	SIM_RST_ N	SIM reset	Ι			Hardwre Description- 6889192V27	Stacked Confoguration				
52	SIM_DIO	SIM serial data	I/O			0007172 ¥ 2 /					
46	SIM_CLK	SIM clock	Ι								

	Signal			Activo	Internal				Le	vel		
Pin #	-	Description	I/O	H/L	PU/PD	Parameter	Conditions	Min	Тур	Мах	Units	
Digital	Digital Audio:											
18	PCM_ DIN	Digital audio receive	0									
20	PCM_ DOUT	Digital audio transmit	Ι			Refer to G24 Hardwre	Stacked					
22	PCM_CLK	Digital audio clock	Ι			Description- 6889192V27	Confoguration					
24	PCM_FS	Digital audio frame sync.	Ι									



	Signal			Active	Internel				Le	vel	
Pin #	Signal Name	Description	I/O	Active H/L	Internal PU/PD	Parameter	Conditions	Min	Тур	Max	Units
GPIO:											
28	GPIO1	General pur- pose I/O	I/O								
30	GPIO2	General pur- pose I/O	I/O								
32	GPIO3	General pur- pose I/O	I/O								
34	GPIO4	General pur- pose I/O	I/O								
36	GPIO5	General pur- pose I/O	I/O								
38	GPIO6	General pur- pose I/O	I/O								
40	GPIO7	General pur- pose I/O	I/O								
42	GPIO8	General pur- pose I/O	I/O							/	/
Audio	:										
67	SPKR_N	Speaker inverted	Ι	\geq	->					>	
69	SPKR_P	Speaker	Ι							_	
63	ALRT_N	Alert speaker inverted	Ι								
65	ALRT_P	Alert speaker	Ι			Refer to G24					
61	MIC	Microphone input	0			Hardwre Description- 6889192V27	Stacked Confoguration				
59	AGND	Audio ground									
57	HDST_ MIC	Headset microphone input	0								
55	HDST_ INT_N	Headset detect inter- rupt	0								

									Le	vel	
Pin #	Signal Name	Description	I/O	Active H/L	Internal PU/PD	Parameter	Conditions	Min	Тур	Max	Units
A/D:				·							
37	ADC1	General pur- pose A/D	0								
43	ADC2	General pur- pose A/D	0			Refer to G24 Hardwre	Stacked				
47	ADC3	General pur- pose A/D	0			Description- 6889192V27	Confoguration				
45	UID	Do Not Use									
Displa	y:										
54	CLI_CS	Do Not Use									
60	LCD_RS	Do Not Use									
56	LCD_ DATA	Do Not Use		5							
58	LCD_CLK	Do Not Use	/								
Data L	ogger:										
70	SPI_CS	Do Not Use			>						
62	SPI_IRQ_ N	Do Not Use	>				>				
64	SPI_DIN	Do Not Use		\geq						_	1
68	SPI_ DOUT	Do Not Use									
66	SPI_CLK	Do Not Use									

Chapter 4: Mechanical Specifications

Board Dimensions

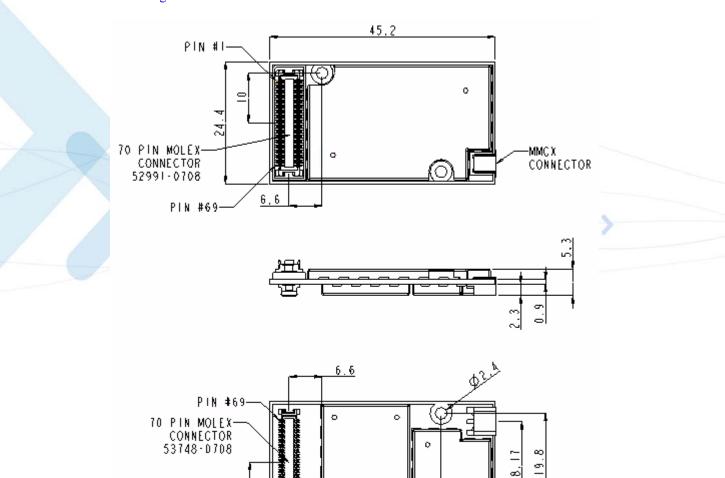


Figure 4-1 describes the W24 mechanical characteristics.

Figure 4-1: W24 Mechanical Characteristics

PIN #1

10.5

ο

0

23.9

o

2.2

A Stand Alone model of the W24 is shown in Figure 4-2.

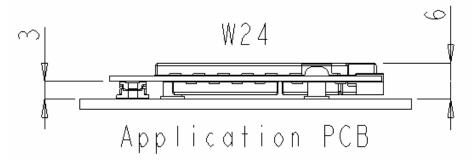


Figure 4-2: W24 Stand Alone Model

Interface Connector Specifications

The W24 uses a single 70-pin, 0.5 mm pitch, board to board connector for the host interface, as described in Table 4-1.

Table 4-1: W24 host interface connector options

W24 Connector	Mating Connector	Stacking Height
Molex 53748-0708	Molex 52991-0708	3.0 mm

Table 4-2 describes the cellular interface connector options.

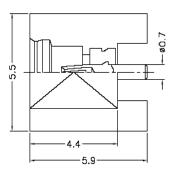
Table 4-2: W24 cellular interface connector options

W24 Connector	Mating Connector	Stacking Height
Molex 52991-0708	Molex 53748-0708	4.5 mm

RF Connector Specifications

The W24 uses a standard MMCX receptacle connector for the radio interface. The connector is manufactured by Amphenol, PN MMCX6251S5.

Figure 4-3 shows the MMCX connector dimensions.



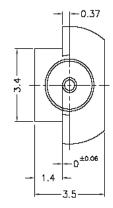


Figure 4-3: MMCX Connector Dimensions

Table 4-3 describes the W24 RF connector characteristics.

Parameter	Specifications
Impedance	50 Ω
Rated Voltage	335 V _{RMS}
Contact Resistance	5 mΩ center contact 2.5 mΩ outer contact
Insulation Resistance	1000 ΜΩ
Insertion Force	3.4 lbs maximum
Withdrawal Force	4.5 lbs maximum
Contact Retaention Force	4 lbs maximum
Durability	500 mated cycles maximum

Table 4-3: RF Connector Specifications

Mating Connector

The RF mating connector should be a standard MMCX plug connector or cable assembly, which corresponds to the W24 MMCX connector specifications.

Any standard MMCX connector or application from different manufacturers may be mated with W24.

musuateu m Figur	6 4-4.			
	Item description	11_MMCX-50-1- 1/111_OH		
	Connector Description	Straight cable plug for flexible cable		
	Series / Pattern Highlights	MMCX – Type / Pattern 11 SUHNER full crimp cable attachement	N. S.	
	ltem number	22651666		
	Data sheet	11_MMCX-50-1-1/111_0		
	Outline drawing	11_MMCX-50-1-1/111_0		
	Catalogue drawing	11_MMCX-50-1-1/111_0		
		B Assembly Instruction		
	2002/95/EC (RoHS)	dcompliant		
	Interface Standards	SUHNER-MMCX		
	Mechanical data		Jump to [Top]	
	Cable Entry Centre	Contact	crimped	
	Cable Entry Outer		crimped	
	Engangement Force max		15 N	
	Disengangement F min		6 N	
	Disengangement F max		15 N	
	Dielectric Size		1	
	Electrical data		Jump to [Top]	
	Impedance		50 Q	
	Interface Freq max		≪6 GHz	
	Environmental a	nd general data	Jump to [Top]	>
	Operating Temp min		-55 °C	
	Operating Temp max		155 °C	
	Weight		0.0009 kg	
	Number of Matings		500	

Such a cable assembly example is the Huber-Suhner PN 11_MMCX-50-1-2/111_OH, which is illustrated in Figure 4-4.

Figure 4-4: Optional MMCX Cable Assembly

W24 Mounting

The W24 incorporates 2 mechanical holes for installing the module onto the application board. The holes are 2.4 millimeters in diameter, which accommodates several types of mechanical elements.

Several mechanical approaches may be applied to mount and fasten W24 to the application board. Using M2 screws with suitable washers to mount the module onto spacers, a bracket or chassis is a recommended design.

Special attention must be paid to the area surrounding the W24 mounting holes. A grounding pad of 4.4 millimeters in diameter surrounds these holes. The diameter and area of this pad must not be exceeded by any mechanical or electrical element.

Several electrical components, which are not shielded, are located near the holes.

These components must not be in contact with the mounting elements or with other parts of the application board, and care must be taken to avoid any damage.

Figure 4-5 depict the W24 mounting area.

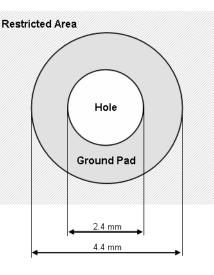


Figure 4-5: W24 Mounting Area

W24 Stand Alone Mounting

The holes are used for mechanical mounting of W24 to the application board but also for grounding support. Using conductive elements to install W24, significantly improves the overall grounding of the module and therefore improves the W24 performance and stability.

It is required to use screws or other mechanical elements to fasten W24 to the application board, but it is highly recommended to use conductive elements to improve the module's performance.

The preferred mounting screw head types are:

- "Allen" head with a champer the best choice.
- "Star" head good.
- "Philips" head may cause damage to nearby components.

Recommended screw type for static products: M2x4, PAN, SST

For portable products it's recommended put also washer M2 (max. dia. 4.0mm)

Recommended screw tightening force - 2.5 Lb*In

W24 + G24 Stacked Mounting

When G24 module is stacked on W24 and mounted on the host PCB, it is required to use hexagonal spacer (x2) between the two PCB's, see Figure 4-6.

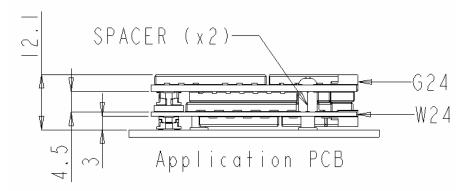


Figure 4-6: W24 + G24 Stacked Mounting

Recommended spacer dimensions are described in Figure 4-7.

Recommended spacer tightening force - 3.0 Lb*In.

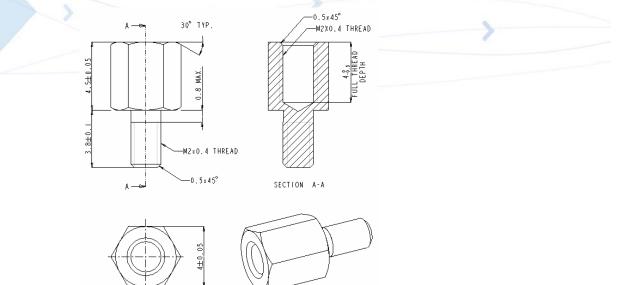


Figure 4-7: W24 + G24 Stacked Mounting Spacer Dimentions

Chapter 5: Support, Service and Troubleshooting

Support

This section provides contact information for any possible queries that may arise, for example:

- Have questions?
- Having trouble getting the Developer Board set up?
- Technical questions?
- Configuration questions/problems?
- Technical operating problems?
- Need documentation?

Who to Contact?

The M2M Data Module Customer Support Center is ready to assist you on integration issues.

Direct Customer. Use this following email address to contact customer assistance: M2M.CustomerCare@motorola.com

Note: The support services provided by Motorola are subject to the agreement between the customer and Motorola and may be at an additional charge to the customer. Motorola will inform the customer in advance of any such charge.

Indirect Customer. Send requests to your distributor and not to Motorola help desk.

Required Query Information

Every new call/problem report, directly from a Direct Customer or from a distributor, should be directed to the help desk email address noted above in "Who to Contact?". It is recommended to report each individual issue in a separate email. The following information is required when reporting a problem:

- Customer name and address
- Customer contact information for this request, including:
 - Name
 - Telephone
 - Fax number
 - Mobile number
 - Email address
- Product name (W24)

- Software version of the unit (AT+i3 command) or model number
- PCB version (located on the PCB near the RF connector)
- Severity of the problem
- Problem description, including:
 - Setup Configuration (such as Developer Board, handset, host, connections, and so on)
 - Detailed scenario from startup
 - Log of all the commands and the responses, beginning from startup
- Answers to the following questions:
 - Was the same scenario tested on the Developer Board and the PC to reproduce the problem?
 - How many units do you have, and how many of them have this problem?
 - How often does the problem recur?

In addition to the information requested above, send the following AT commands and the HyperTerminal log with the responses:

- AT+iXXXX // to get the signal strength (RX level)
- AT+iXXXX // to read the unit MSN number
- AT+i3 // to get the software version of the W24

Service

The M2M field service department is ready to support you in following cases:

- 1. To analyze the root cause of a special defect.
- 2. To replace defected units within the 15??? months warranty period.
- 3. To perform a mass reflash of the W24.

Who to Contact?

Direct Customer. Use this following email address to contact customer assistance:

n2csfs01@motorola.com

Indirect Customer. Contact your distributor and not Motorola field service department.

Required Query Information

To apply for analysis or replacement of modules within the warranty period, the following information must be provided:

- 1. Model.
- 2. MSN (Motorola Serial Number).
- 3. Defect/Symptom for every unit (deeper details required for analysis).
- 4. Requester full information (Name, company, phone, email).

Populate all the above into an excel file and email it to the Field Service Department listed above.

If all the required data will be provided, an RMA will be issued to the customer within one business day.

The RMA will specify exactly to what destination the customer should send the units to and other references to be used for follow up.

Testing a Standalone Unit

This section describes how to perform a W24 functionality test, whose purpose is to:

- Introduce the user to the W24
- Explain how to work with the W24 unit
- Describe how to evaluate basic W24 features

The test setup provides a wide platform through which a W24 unit can be evaluated. The specific test procedure described below covers only a few of the W24's many features. Using this setup, you can perform several additional tests on the W24.

The test requires knowledge about the operation of the M2M Developer's Kit, terminal applications and AT and AT+i commands. Refer to relevant documentation for assistance.

To perform the test, you need the following:

- A W24 OEM cellular engine unit
- A W24 Developer's Kit
- A desktop or laptop computer, which includes:
 - A free serial communications port (RS232)
 - A connected and active line modem (internal or external)
 - A terminal application (such as HyperTerminal)

Test Setup

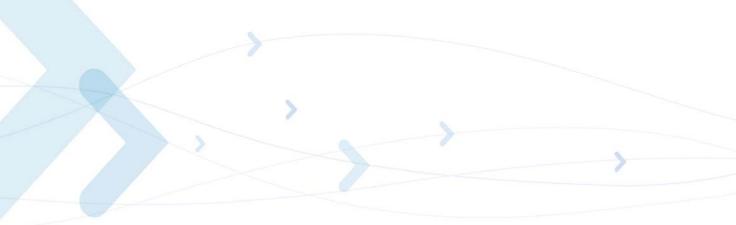
Acronyms and Abbreviations

Abbreviatio	on Full Name
Α	
AMR	Adaptive Multi Rate
AOC	Adaptive Multi Kate
	Advice of Charge
B	
BR	Baud Rate
bps	Bits Per Second
C	
C CSD	Circuit Switched Data
C CTS	Clear to Send
D	>
DCD	Data Carrier Detect
DCE	Data Communication Equipment (such as modems)
DCS	Digital Cellular System (GSM in the 1800MHz band)
DOC	Department of Communications (Canada)
DRX	Discontinuos Reception
DSP	Digital Signal Processor
DSR	Data Set Ready
DTE	Data Terminal Equipment (such as terminals, PCs and so on)
DTMF	Dual Tone MultiFrequency
DTR	Data Terminal Ready
DTX	Discontinuos Transmission
Е	
EFR	Enhanced Full Rate
EGPRS EGSM	Enhanced General Packet Radio Service
EGSIV	Extended Global System for Mobile Communications

Abbreviatio	n Full Name	
EMC	Electromagnetic Compatibility	
EOTD	Enhanced Observed Time Difference	
EPOS	Electronic Point of Sale	
ERP	Effective Radiated Power	
ESD	Electrostatic Discharge	
ETSI	European Telecommunication Standards Institute	
FCC	Federal Communications Commission (U.S.)	
FR	Full Rate	
FTA	Full Type Approval	
•		
GCF	GSM Certification Forum	
GPIO	General Purpose Input/Output	
G GPRS	General Packet Radio Service	
G GSM	Global System for Mobile Communications	
HR	Half Rate	
	Hall Rate	
IC	Integrated Circuit	
LNA	Low-noise Amplifier	
1		
MMCX	Miniature Micro Coax	
MO	Mobile Originated	
MT	Mobile Terminated	
OEM	Original Equipment Manufacturer	
PCB	Printed Circuit Board	
PCL	Power Class Level	
PCM	Pulse Code Modulation	
PCS	Personal Communication System (also known as GSM 1900)	
PD	Pull Down	

	Abbreviation	Full Name
	PDU	Packet Data Unit
	PLL	Phase-locked Loop
	PTCRB	PCS-1900 Type Certification Review Board (GSM North America)
	PU	Pull Up
R		
	R&TTE	Radio and Telecommunications Terminal Equipment
	RMS	Root Mean Square
	RI	Ring Indicator
	RTS	Request To Send
S		
	SAR	Specific Absorption Rate
	SIM	Subscriber Identity Module
	SMS	Short Message Service
	SPI	Serial Peripheral Interface
T		
	TDMA	Time Division Multiple Access
	TIS	Transmitter Isotropic Sensitivity
	TRP	Transmitter Radiated Power

	Abbreviation	Full Name
U		
	UART	Universal Asynchronous Receiver Transmitter
	USB	Universal Serial Bus
	USSD	Unstructured Supplementary Services Data
V		
	vcc	Voltage Common Collector
	VSWR	Voltage Standing Wave Ratio



Index

A

Antenna Installation, x Approvals Regulatory, 4

В

Block Diagram Description, 7

Е

Environmental Specifications, 3

0

Organization of Manual, xviii

Ρ

Physical Specifications, 2 Precautions, ix Product Specifications, 3

R

Regulatory Approvals, 4

S

Safety Precautions, ix

Specifications, 2 Environmental, 3 Physical, 2 Standards, x Support Center, 51

Т

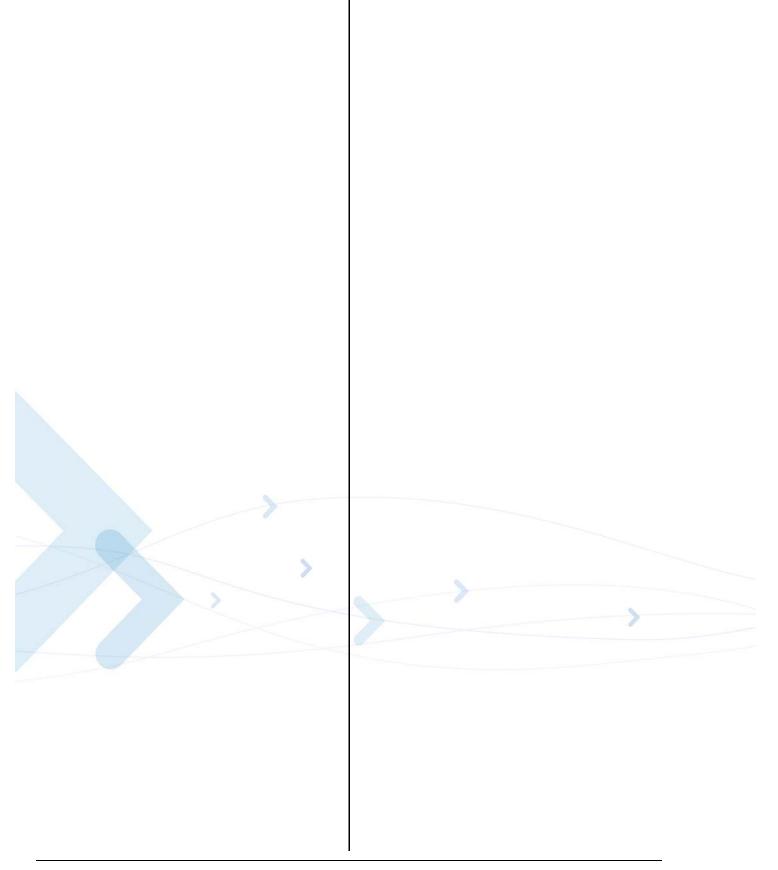
Terms, 2 Testing, 54

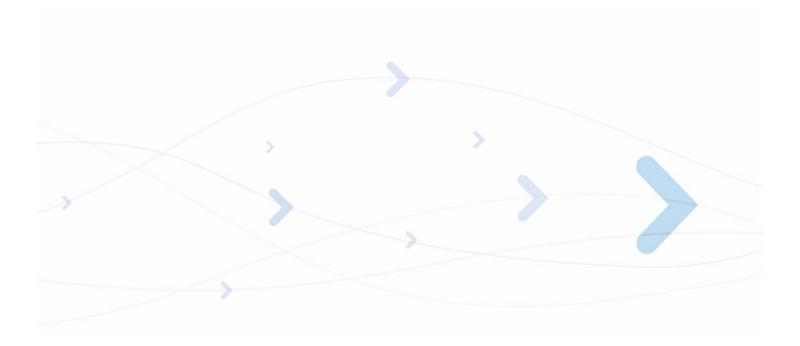
U

User Operation, ix

W

-		
W24		
Abbreviations, 2		
Antenna Installation, x		
Block Diagram Description, 7		
Product Specifications, 2		
Regulatory Approvals, 4		
Safety Precautions, ix		
Standards, x		
Terms and Abbreviations, 2	S	
Testing, 54	7	
User Operation, ix		







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