Reader/Writer RC-S480C Users Manual (Version 1.50)

30 November 1999

Contents

List of Table

List of Figures

1 Definition

In this document, following words are used in the following sense.

Controller - An external computer, a gate controller or equivalent equipment directly connected to Reader/Writer through specified wired interface.

Reader/Writer - The equipment, specified in this document, for Sony Remote Card System: *FeliCa*

Card - A Contactless Smart Card for Sony Remote Card System: *FeliCa*

2 General

2.1 Basic Specification

Reader/Writer consists of 3 parts – an *antenna board*, an *RF/control board* and the *cable* that connects the *antenna board* and the *RF/control board*.

2.1.1 Dimensions

The dimension of each type is described in Chapter 6.

The tolerance is ±0.5*mm*.

2.1.2 Specification Table

Communication with Card

data transfer rate : 250*kbps* or 211.875*kbps*
carrier frequency : 13.56*MHz* (±50*ppm)*
modulation : Receive - ASK, Manchester Coding
: Transmit - ASK(12±2%), Manchester Coding
modulation bandwidth : ±300*kHz* (-30dB relative to carrier level)
radiation level : less than 2000*μV/m* at 10*m* distance (C-antenna)
: less than 300*μV/m* at 10*m* distance (B-antenna)
Communication method : half-duplex, CRC16

Power Supply (see Chapter 6)

Digital Processing Unit (DPU)

IC CXD8680R (designed by Sony)
CXD8680R has 32 *bit* CPU core, 8 *kbyte* ROM, 3 *kbyte* SRAM, 2 *kbyte* Block Erasable Flash Memory and 4x32 *kbyte* Flash Memory inside.

Controller Interface (serial interface)

number of ports : 1 (one)
signal level : CMOS logic level
baud rate for operation : 7.2*kbps* ~ 1.2288*Mbps*
(7.2, 8.192, 9.6, 14.4, 16.384, 19.2, 28.8, 32.768, 38.4, 57.6, 65.536, 76.8, 115.2, 131.072, 153.6, 230.4, 262.144, 460.8, 524.288, 921.6, 1048.576, 1228.8 *kbps*)
communication method : half-duplex, check sum

2.1.3 Encryption Technique

All communications to and from the *Card*, with the exception of Polling Command, are encrypted.

All the communications to and from the *Controller*, with the exception of the Attention Command, are encrypted.

3 kinds of Encryption algorithm, Single-DES 56*bit*, Triple-DES 56*bit* and SEAC (Sony Encryption Algorithm for Card) 64*bit*, can be applied.

3 *Reader/Writer* Operation

3.1 Connection with *Controller*

The pin assignment is described in Chapter 6.

3.2 Transaction Overview

3.2.1 Communication Protocol

There are two communication protocols, namely, data link level and application level protocol.

Either ACK or NACK packet is returned in every packet transfer. ACK indicates a successful transfer, NACK means unsuccessful.

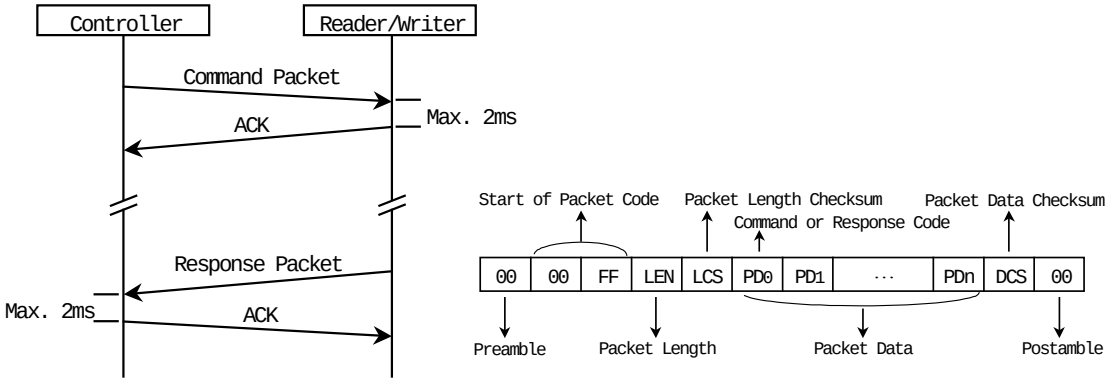

Fig. 1 : Communication Protocol and Packet Structure

3.2.2 Re-sending Protocol

Reader/Writer supports the re-sending procedure in *Controller* interface. Re-sending handling is triggered or required in the two cases below :

1. NACK packet is returned.
2. Neither ACK nor NACK response comes. (time out)

Controller should re-send the same packet within 5*ms* after receiving NACK or detecting time out.

3.2.3 ACK / NACK Packet

ACK / NACK packet format is as follows :
ACK : LEN = 00h & LCS = ffh without Packet Data part and DCS
NACK : LEN = ffh & LCS = 00h without Packet Data part and DCS

3.2.4 Mutual Authentication

Mutual Authentication is a process that is performed between *Controller* and *Reader/Writer* to authenticate each other mutually in order to avoid the fraud usage.

This authentication is based on the following elements :

1. two 8*byte* (64*bit*) keys
2. three pass authentication model (ISO 9758)
3. cryptography algorithm mentioned in section 3.2.5.

3.2.5 Encryption and Decryption

The cryptography algorithm is used for the following three stages :

1. mutual authentication between *Controller* and *Reader/Writer*
2. mutual authentication between *Reader/Writer* and *Card*
3. commands and data encryption through *Reader/Writer*

4 Communication Commands

4.1 General

In this section, the communication commands between *Controller* and *Reader /Writer* are described.

Table 1-3 shows all commands between *Controller* and general *Reader/Writer*. All commands are categorized into 3 groups as follows :

1. *Reader/Writer* internal operation command
2. *Card* operation command
3. *Card* management command

Card management command group is for issue *Reader/Writer* and not for general *Reader/Writer* .

4.2 *Reader/Writer* Internal Operation Command

4.2.1 Attention Command

Attention Command enables *Reader/Writer* and *Controller* to recognize each other and is the only command available before the authentication completes. The main purpose of this Attention Command is to recognize its partner before authentication and to force *Reader/Writer* to be in the idle state.

4.2.2 Authentication 1 Command

Authentication 1 Command enables *Controller* to authenticate *Reader/Writer*.

4.2.3 Authentication 2 Command

Authentication 2 Command enables *Reader/Writer* to authenticate *Controller*.

4.2.4 Disconnect Command

Disconnect Command is for *Controller* to terminate the communication with *Reader/Writer*.

Command	Command Code	Response Code
Attention	00h	01h
Authentication 1	02h	03h
Authentication 2	04h	05h

Disconnect	06h	07h
Change Reader/Writer Access Key	20h	21h
Self-Diagnosis	40h	41h
Check Firmware Version	44h	45h
Change Communication Mode	46h	47h
Kill Module	4ah	4bh
Reader/Writer Reset	4ch	4dh
Firmware Maintenance	52h	53h

Table 1 : *Reader/Writer* Internal Operation Command

Command	Command Code	Response Code
Polling	80h	81h
Request Service	82h	83h
Request Response	84h	85h
Mutual Authentication	86h	87h
Read Block	88h	89h
Write Block	8ah	8bh
Release	8eh	8fh
Read Without Encryption	98h	99h
Write Without Encryption	9ah	9bh

Table 2 : *Card* Operation Command

Command	Command Code	Response Code
Register Issue ID	c0h	c1h
Register Area	c2h	c3h
Register Service	c4h	c5h
Register Manufacture ID	e0h	e1h
Card Self-Diagnosis	f0h	f1h

Table 3 : *Card* Management Command

4.2.5 Change *Reader/Writer* Access Key Command

Change *Reader/Writer* Access Key Command performs alteration of the access keys, which are stored in *Reader/Writer* for the mutual authentication between *Controller* and *Reader/Writer*.

4.2.6 Self-Diagnosis Command

Self-Diagnosis Command activates self-diagnosis test of *Reader/Writer*. After the Diagnosis completion, *Reader/Writer* sends back the test result.

4.2.7 Check Firmware Version Command

Check Firmware Version Command is used for checking the version of *Reader/Writer* firmware.

4.2.8 Change Communication Mode Command

Change Communication Mode Command performs the following changes : baud rate, logic level interface(non-inverted or inverted), encryption on/off, time-out.

4.2.9 Kill Module Command

Kill Module Command is used for killing the specified module within *Reader/Writer*.

4.2.10 *Reader/Writer* Reset Command

Reader/Writer Reset Command executes the initialization routine.

4.2.11 Firmware Maintenance Command

Firmware Maintenance Command is used for updating the firmware within *Reader/Writer*.

4.3 *Card* Operation Command

4.3.1 Polling Command

Polling Command enables *Reader/Writer* to give a call to *Card* and to detect existence of *Card* by the response from *Card*.

4.3.2 Request Service Command

Request Service Command enables *Reader/Writer* to check whether the specified service code is registered to *Card* or not. In case that the specified service is registered to *Card*, key version of the service is available by the response from *Card*.

4.3.3 Request Response Command

Request Response Command enables *Reader/Writer* to check which mode *Card* is in. There are 4 modes, ‘before Authentication’, ‘after Authentication 1’, ‘after Authentication 2’ and ‘after Register xxx Command (see 4.4 *Card* Management Command)’.

4.3.4 Mutual Authentication Command

Mutual Authentication Command activates the mutual authentication procedure between *Reader/Writer* and *Card*.

4.3.5 Read Block Command

Read Block Command activates *Reader/Writer* to read the specified block data from *Card* after the mutual authentication between the *Reader/Writer* and *Card* has been established successfully.

Read Block Command enables a Service Provider with successful mutual authentication to read blocks that this Service Provider has the right to access.

With one Read Block Command, up to 8 block can be read simultaneously. If more than 8 blocks should be read, more than one Read Block Command shall be called separately.

The response of Read Block Command is a 1*byte* Read result and Block Data that has been read. In the Read result, a 1*byte* consequence of one block Read is given in 1*bit*. As there are up to 8 blocks, 1*byte* is enough to record all the Read consequence.

4.3.6 Write Block Command

Write Block Command activates *Reader/Writer* to write the specified block data to *Card* as new data after the mutual authentication between *Reader/writer* and *Card* has been established successfully.

With one Write Block Command, up to 8 block can be written simultaneously. If more than 8 blocks should be written, more than one Write Block Command shall be called separately.

The response of Write Block Command is a 1*byte* Write result. In the Write result, a 1*byte* consequence of one block Write is given in 1*bit*. As there are up to 8 blocks, 1*byte* is enough to record all the Write consequence.

The purse operation can be performed with Write Block Command.

4.3.7 Release Command

Release Command enables *Card* to be released from established communication sequence with *Reader/Writer*.

4.3.8 Read Without Encryption Command

Read Without Encryption Command activates Reader/Writer to read the specified block data from *Card* without the mutual authentication between the *Reader/Writer* and *Card*. Read Without Encryption Command can be applied only to blocks which service code is registered security-free.

The number of blocks that can be read simultaneously and the response of Read Without Encryption Command are the same as those for Read Block Command (see 4.3.5).

4.3.9 Write Without Encryption Command

Write Without Encryption Command activates Reader/Writer to write the specified block data to *Card* without the mutual authentication between the *Reader/Writer* and *Card*, however can be applied only to blocks which service code is registered security-free.

The number of blocks that can be write simultaneously and the response of Write Without Encryption Command are the same as those for Write Block Command (see 4.3.6).

4.4 *Card* Management Command

The commands described in this section are for issue *Reader/Writer* and not for general *Reader/Writer* .

4.4.1 Register Issue ID Command

Register Issue ID Command makes it possible to register Issue ID(IDi) , Issue Parameter(PMi), System Code and Area 0000 Key to *Card*, and also makes it to erase other services and to initialize Memory Allocation Information in *Card*.

4.4.2 Register Area Command

Register Area Command makes it possible to register new Area and the parameters of new Area to *Card*, which are Service Code Range, Available Block Number and Area Key.

4.4.3 Register Service Command

Register Service Command makes it possible to register new Service and the parameters of new Service to *Card*, which are Service Code, Block Number and Service Key.

4.4.4 Register Manufacture ID Command

Register Manufacture ID Command makes it possible to register Manufacture ID(IDm) , Manufacture Parameter(PMm), System Code, System Key and Area 0000 Key to *Card*, and also makes it to clear Issue ID of *Card*.

In order to execute Register Manufacture ID Command, Manufacture ID must be all 00h. This means that Register Manufacture ID Command is effective to *Card* once for all.

4.4.5 Card Self-Diagnosis Command

Card Self-Diagnosis Command activates self-diagnosis test of *Card*. After the Diagnosis completion, *Card* sends back the test result.

5 Fundamental Specifications

5.1 Operating Temperature

-20 to 70°C

Compliant to IEC 68-2-2 Part 2 Test Bd and IEC 68-2-1 Part 2 Test Ad, with duration 16 hours.

5.2 Storage Temperature

-40 to 80°C

Compliant to IEC 68-2-2 Part 2 Test Bb and IEC 68-2-1 Part 2 Test Ab, with duration 72 hours.

5.3 Humidity

Compliant to IEC 68-2-3 Part 2 Test Ca, with duration 4 days.

5.4 Cold and Heat Shock

Compliant to IEC 68-2-1 Part 2 Test Aa, with temperature -20°C and duration 2 hours, and IEC 68-2-2 Part 2 Test Ba, with temperature 70°C and duration 2 hours.

5.5 Shock

Compliant to IEC 68-2-27 Part 2 Test Ea, severity 60*g*, duration 11*ms*.

5.6 Vibration

Compliant to IEC 68-2-6 with respect to the following parameters :

- Frequency Range : 10*Hz* to 500*Hz*
- Vibration Severity : 3*gn*
- Sweep rate : 1 octave per minute
- Endurance by sweeping : 20 sweep cycles for each X,Y and Z axis.

5.7 Spurious Emissions

Compliant to MPT 1339 clause 4.1.3 ; FCC Regulation Part 15.225 and Part 15.209 ; and EC(EU) I-ETS 300 330 clause 7.2.1.3.

5.8 Bump

Compliant to IEC 68-2-29 Part 2 Test Eb, severity 40*gn*, duration 6*ms* and 4000 bumps.

5.9 Electrostatic Discharge

Compliant to IEC 801-2, with severity level 3 (Test Voltage Air discharge : 8*kV*).

5.10 Multiple Card

The *Reader/Writer* allows addressing and authentication of up to four *Cards* in the communication area. Each *Card* will only respond to commands intended for it and only one *Card* can be addressed in a single command.

5.11 Anti-Collision

Reader/Writer supports anti-collision of multiple cards.

6 *Reader/Writer* Specification Table

6.1 B-antenna *Reader/Writer*

6.1.1 B-antenna *Reader/Writer* Specification Table

- Model No. : RC-S480B

General model No. "RC-S480" is printed on the RF/Control board (see Fig.2). "B-antenna" means antenna type shown in Fig.6 and it is marked on "B" as in Fig.2. A mark on numerals 1-5 (it is 1 in Fig.2) indicates the installed software version.

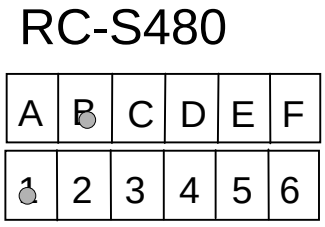

Fig. 2 : Printed Model No. (B-antenna)

- Operating Distance : 30*mm*
  - The *Reader/Writer* is adjusted by the combination of the RF/Control board and the antenna board, both of which shall have the same serial number labels to maximize the operating distance, and therefore, the operating distance cannot be assured by the different combination of the boards.
  - The antenna board and the RF/Control board shall be placed more than 15*mm* apart.
  - In the effective operating scope of the antenna, there shall be nothing that generates electric or magnetic noise. Neither shall there be an electric or magnetic shield or reflection.
  - Emission from another *Reader/Writer* affects the operating distance. In operating plural *Reader/Writer*s simultaneously, it is desirable for antenna boards to be separated more than 30*cm* each other in order to keep the 30*mm* operating distance. However, *Reader/Writers* can be operated under less than 30*cm* separation each other with no problem except for decrease of the operating distance.

- Dimensions (see Fig.5 and 6)

antenna : 50*mm* x 25*mm* x 6*mm* (PCB thickness 1*mm*, connector height 5*mm*)
RF/Control board : 50*mm* x 60*mm* x 15*mm* (PCB thickness 1*mm*)
cable : length 120*mm*
The RF/Control board can be attached to external board by the interface connector CN2 as is shown in Fig.3.

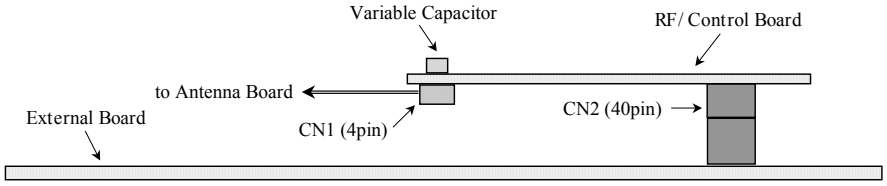

Fig.3 : RF/Control Board Attached to External Board

- Mounting Holes (see Fig.5,6)

  antenna : (no mounting hole)
  RF/Control board : M3 x 2, clearance 8*mm*

- Interface Connector : HKP-40M1 (Honda Tsushin Kogyo Co., Ltd.)

  The interface connector is a 40-pin double row C-antennaonnector located at the lower edge of the RF/Control board which is shown in the schematic drawing of Fig.5. The pitch of connector pins is 2.54mm.Connector pins are gilded.
  The left end of the bottom row of connector pins is No. 1. Pin assignment of the connector (CN2) is shown in Fig. 4 and Table 4.

  12V :DC input port (for RF power amplifier)
  5V :DC input port
  GND :Ground port
  SI, SO :Serial Data I/O (CMOS 5*V*)
  DA0-7 :Parallel Data I/O (CMOS 5*V*)
  DA0-7 are connected to PG0-7 of DPU IC CXD8680R. In order to control DA0-7, it is necessary to load other program code to RF/Control board. (see "*Reader/Writer* Programmer's Reference Manual")
  DB0-7 :Parallel Data I/O (CMOS 5*V*)
  DB0-7 are connected to PH0-7 of DPU IC CXD8680R. In order to control DB0-7, it is necessary to load other program code to RF/Control board. (see "*Reader/Writer* Programmer's Reference Manual")
  HS0-3 :Parallel Data I/O (CMOS 5*V*)
  HS0-2 are connected to PF4-6 of DPU IC CXD8680R, and HS3 is connected to both PF7 and PF2. In order to control HS0-3, it is necessary to load other program code to RF/Control board. (see "*Reader/Writer* Programmer's Reference Manual")
  Mode :No connection in usual operation.
  Only in loading program, high level voltage (5*V*) shall be applied to the pin before RF/Control board is powered on

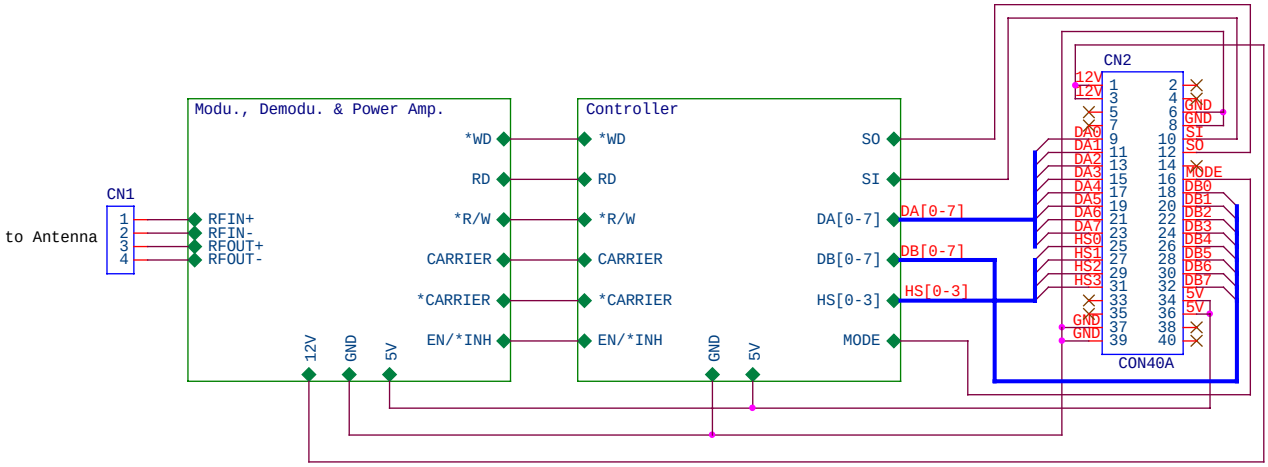

Fig.4 : Pin Assignment of Connectors CN1-2

Pin No.	Pin Assignment	Pin Assignment	Pin No.
1	12V	No Connection	2
3	12V	No Connection	4
5	No Connection	GND	6
7	No Connection	GND	8
9	DA0	SI	10
11	DA1	SO	12
13	DA2	No Connection	14
15	DA3	MODE	16
17	DA4	DB0	18
19	DA5	DB1	20
21	DA6	DB2	22
23	DA7	DB3	24
25	HS0	DB4	26
27	HS1	DB5	28
29	HS2	DB6	30
31	HS3	DB7	32
33	No Connection	5V	34
35	No Connection	5V	36
37	GND	No Connection	38
39	GND	No Connection	40

Table 4 : Pin Assignment of Interface Connector CN2

- Power

voltage :DC12*V* (CN2 pin1 and 3)
:DC5*V* (CN2 pin34 and 36)
voltage allowance :DC12*V* +4*V*, -0.5*V*
:DC5*V* ±0.5*V*
current :DC12*V* ≈ 90mA
:DC5*V* ≈150mA
voltage ripple allowance :100*mV* (peak-to-peak)

The *Reader/Writer* can be operated in the voltage range of 5 - 11.5*V* and the voltage ripple > 100*mV* for DC12*V*, however operating distance cannot be assured in such condition.

- External Interface

serial 1 port : CMOS logic level (5*V*)
software selectable, non-inverted or inverted, and baud rate
(default setting : non-inverted, 115.2*kbps*)
parallel 20bit I/O port : CMOS logic level (5*V*)

6.1.2 B-antenna *Reader/Writer* Schematic Drawing

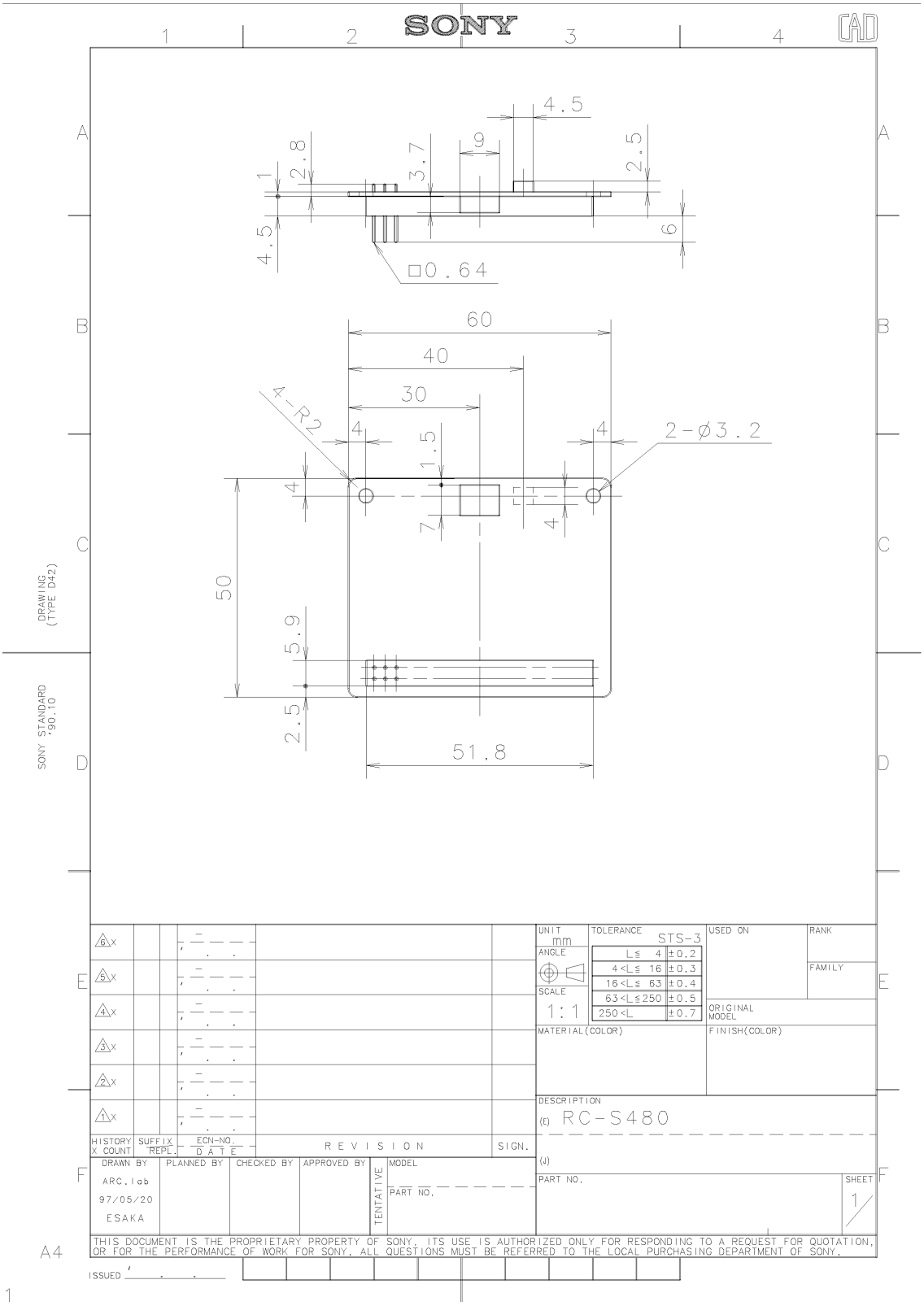

Fig.5 : RF/Control Board (B-antenna & C)

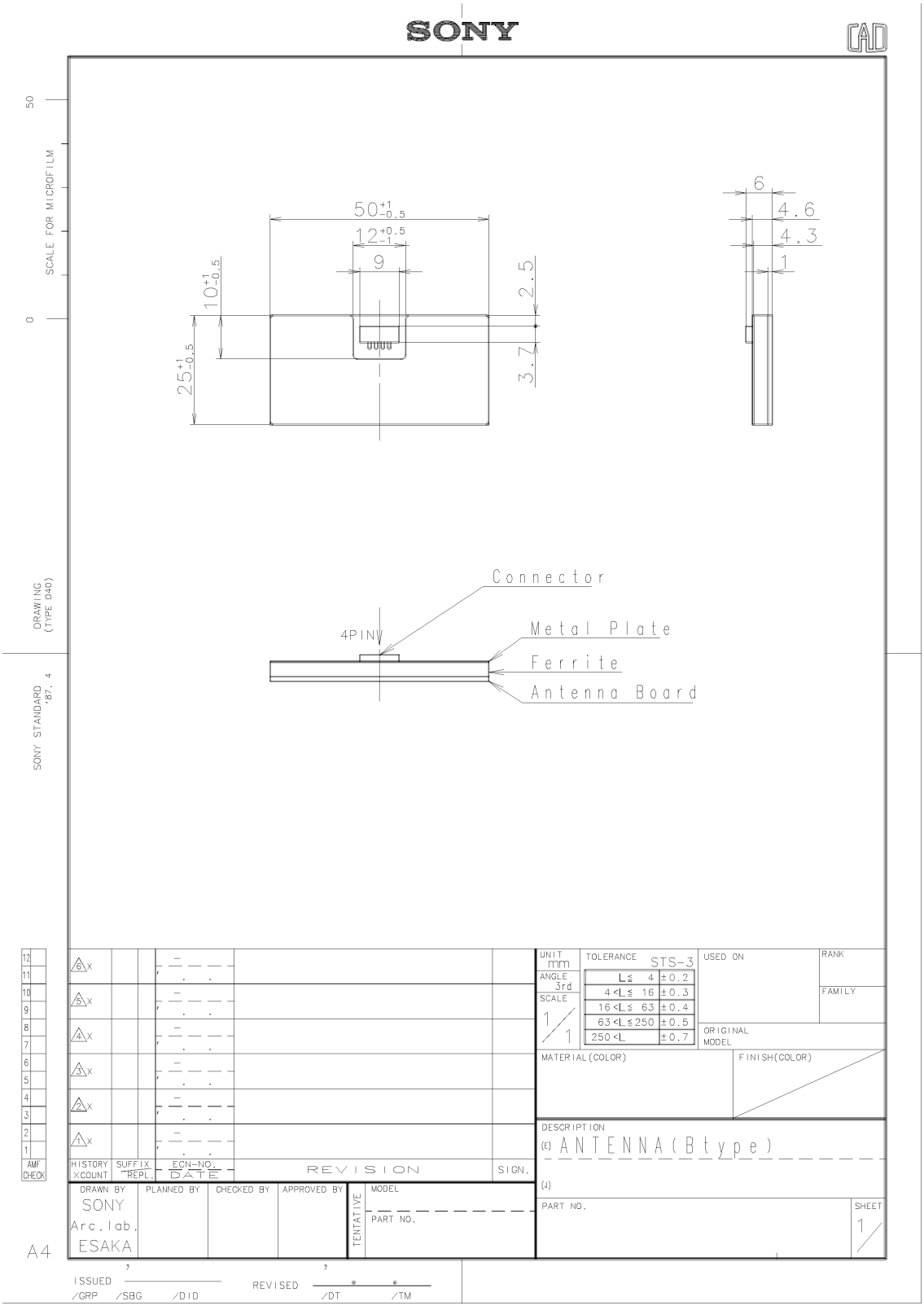

Fig.6 : Antenna Board (B-antenna)

6.2 C-antenna *Reader/Writer*

6.2.1 C-antenna *Reader/Writer* Specification Table

- Model No. : RC-S480C

  General model No. "RC-S480" is printed on the RF/Control board (see Fig.7). "C-antenna" means antenna type shown in Fig.8 and it is marked on "C" as in Fig.7. A mark on numerals 1-5 indicates (it is 1 in Fig.7) the installed software version.

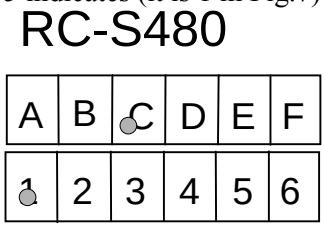

Fig. 7 : Printed Model No. (C-antenna)

- Operating Distance : 100*mm*
  - The *Reader/Writer* is adjusted by the combination of the RF/Control board and the antenna board, both of which shall have the same serial number labels to maximize the operating distance, and therefore, the operating distance cannot be assured by the different combination of the boards.
  - The antenna board and the RF/Control board shall be placed more than 15*mm* apart.
  - In the effective operating scope of the antenna, there shall be nothing that generates electric or magnetic noise. Neither shall there be an electric or magnetic shield or reflection.
  - Emission from another *Reader/Writer* affects the operating distance. In operating plural *Reader/Writer*s simultaneously, it is desirable for antenna boards to be separated more than 70*cm* each other in order to keep the 20*mm* operating distance. However, *Reader/Writers* can be operated under less than 70*cm* separation each other with no problem except for decrease of the operating distance.

- Dimensions (see Fig.5 and 8)

  antenna :104*mm* x 67*mm* x 6*mm* (PCB thickness 1*mm*, connector height 5*mm*)
  RF/Control board : 50*mm* x 60*mm* x 15*mm* (PCB thickness 1*mm*)
  cable : length 120*mm*
  The RF/Control board can be attached to external board by the interface connector CN2 as is shown in Fig.3.

- Mounting Holes (see Fig.5 and 8)

  antenna : M3 x 4, clearance 8*mm*
  RF/Control board : M3 x 2, clearance 8*mm*

- Interface Connector : HKP-40M1 (Honda Tsushin Kogyo Co., Ltd.)

The interface connector is a 40-pin double row C-antennaonnector located at the lower edge of the RF/Control board which is shown in the schematic drawing of Fig.5. Connector pins are gilded. The left end of the bottom row of connector pins is No. 1. Pin assignment of the connector (CN2) is shown in Fig. 4 and Table 4.

12V :DC input port (for RF power amplifier)
5V :DC input port
GND :Ground port
SI, SO :Serial Data I/O (CMOS 5*V*)
DA0-7 :Parallel Data I/O (CMOS 5*V*)
DA0-7 are connected to PG0-7 of DPU IC CXD8680R. In order to control DA0-7, it is necessary to load other program code to RF/Control board. (see "*Reader/Writer* Programmer's Reference Manual")
DB0-7 :Parallel Data I/O (CMOS 5*V*)
DB0-7 are connected to PH0-7 of DPU IC CXD8680R. In order to control DB0-7, it is necessary to load other program code to RF/Control board. (see "*Reader/Writer* Programmer's Reference Manual")
HS0-3 :Parallel Data I/O (CMOS 5*V*)
HS0-2 are connected to PF4-6 of DPU IC CXD8680R, and HS3 is connected to both PF7 and PF2. In order to control HS0-3, it is necessary to load other program code to RF/Control board. (see "*Reader/Writer* Programmer's Reference Manual")
Mode :No connection in usual operation.
Only in loading program, high level voltage (5*V*) shall be applied to the pin.

- Power

voltage :DC12*V* (CN2 pin1 and 3)
:DC5*V* (CN2 pin34 and 36)
voltage allowance : DC12*V* +4*V*, -0.5*V*
:DC5*V* ±0.5*V*
current :DC12*V* ≈ 150mA
:DC5*V* ≈ 150mA
voltage ripple allowance :100*mV* (peak-to-peak)

The *Reader/Writer* can be operated in the voltage range of 5 - 11.5*V* and the voltage ripple > 100*mV* for DC12*V*, however operating distance cannot be assured in such condition.

- External Interface

serial 1 port : CMOS logic level(5*V*)
software selectable, non-inverted or inverted, and baud rate
(default setting : non-inverted, 115.2*kbps*)
parallel 20bit I/O port : CMOS logic level (5*V*)

6.2.2 C-antenna *Reader/Writer* Schematic Drawing

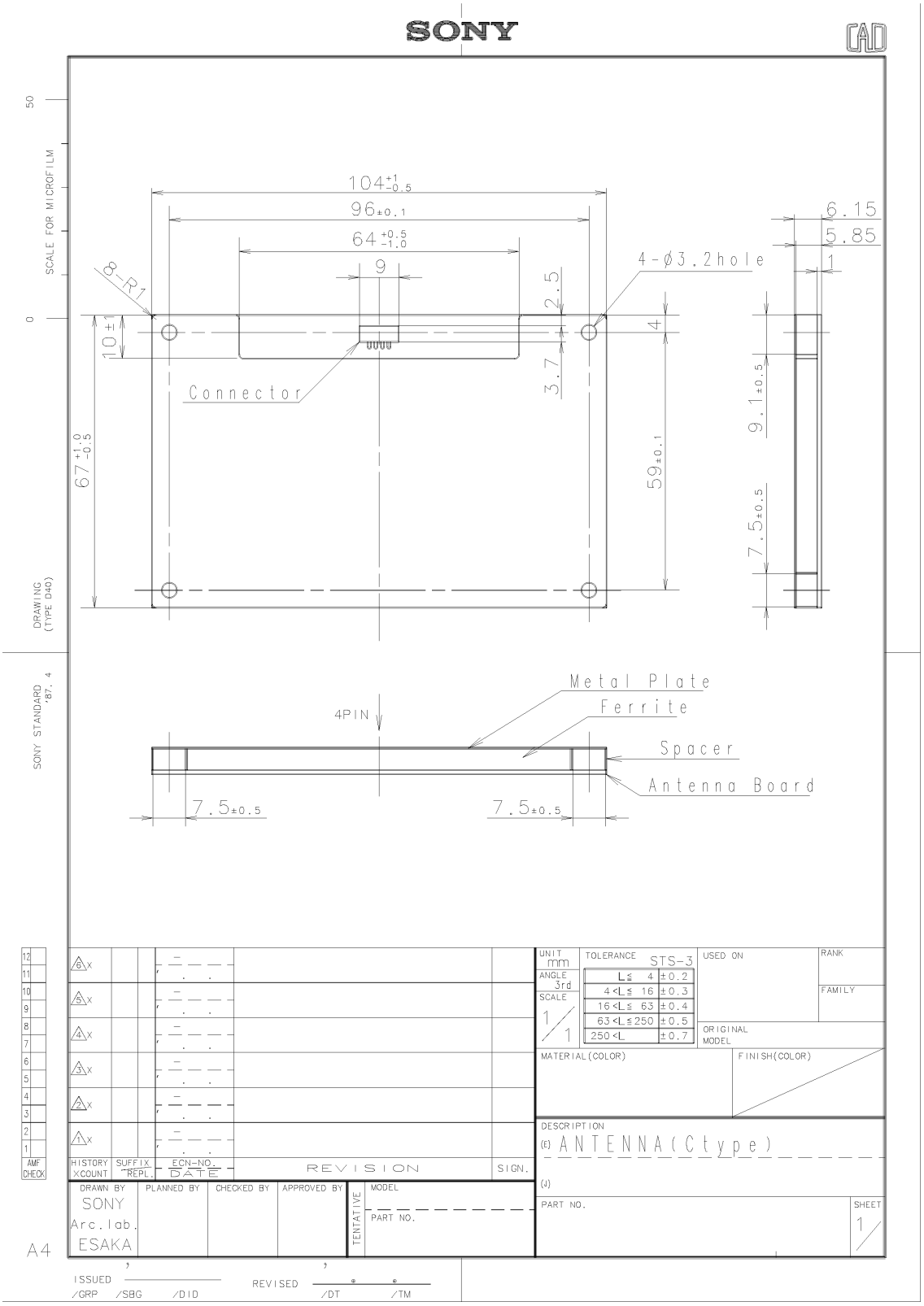

Fig.8 : Antenna Board (C-antenna)