

# 11a/b/g/n IoT WiFi module

## DNESA-144

## User Manual

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## Revision History

Rev.	History	Date	Author
V0.3	- Initial version	2015/2/26	TT
V0.4	- Update the mechanical dimension	2015/11/27	TT
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## Federal Communication Commission Interference Statement

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

FCC Caution: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

### IMPORTANT NOTE:

#### Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

Country Code selection feature to be disabled for products marketed to the US/CANADA

#### This device is intended only for OEM integrators under the following conditions:

- 1) The antenna must be installed such that 20 cm is maintained between the antenna and users, and
- 2) The transmitter module may not be co-located with any other transmitter or antenna,
- 3) For all products market in US, OEM has to limit the operation channels in CH1 to CH11 for 2.4G band by supplied firmware programming tool. OEM shall not supply any tool or info to the end-user regarding to Regulatory Domain change.

As long as 3 conditions above are met, further transmitter test will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed

### IMPORTANT NOTE

In the event that these conditions can not be met (for example certain laptop configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID can not be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

### End Product Labeling

This transmitter module is authorized only for use in device where the antenna may be installed such that 20 cm may be maintained between the antenna and users. The final end product must be labeled in a visible area with the following: "Contains FCC ID: NKR-DNSA144".

### Manual Information to the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.

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# 1. Key Features

- QCA4004 IoE solution
- Internal PA and LNA, with external RF Switch for Antenna Diversity
- 2.4GHz and 5GHz Dual band IEEE 802.11a/b/g/n, single stream 1x1
- 11n support HT20 and HT40 Bandwidth
- 1 On board dual-band printed Antenna and optional U.FL antenna connectors.
- GPIO voltage is flexible, can be between 1.8~3.3V
- Single power supply: 3.3V, Low Power Consumption, also support Green Tx and very low power sleep mode for mobile battery operation application. Can be wake up and go to sleep quickly.
- 42mm x 20mm size, 2-layer, single side component
- Operation temperature: -40~+85 degrees C(Industry Class Version); 0~+85 degrees C(Commercial Class Version)
- TCP/IP offload, include Encryption
- QCA4004 has Hardware Wake Up Manager allow chip go to sleep and wake up later automatically. This feature can reduce normal operation power consumption.
- SPI Slave interface to the MCU.
- DNSA-144 also can be host-less mode(stand-alone). Allow user to run tiny size application.

### 3. Electrical Specification

- General DC electrical characteristics

These conditions apply to all DC characteristics unless otherwise specified:

Temp = 25 °C, VDD33= 3.3 V

Table 4: DC electrical characteristics for digital I/Os

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>IH</sub>	High level I voltage	–	1.8	–	3.6	V
V <sub>IL</sub>	Low level I voltage	–	-0.3	–	0.3	V
V <sub>OH</sub>	High level O voltage	–	2.2	–	3.3	V
V <sub>OL</sub>	Low level O voltage	–	0	–	0.4	V
I <sub>IL</sub>	Low level I current	–	–	–	0.1	μA
I <sub>IH</sub>	High level I current	–	–	–	0.1	μA
I <sub>OH</sub>	High level O current for GPIO0 to GPIO13	–	–	–	8	mA
	High level O current for GPIO18 to GPIO25	–	–	–	20	
I <sub>OL</sub>	High level O current for GPIO0 to GPIO13	–	–	–	20	mA
	High level O current for GPIO18 to GPIO25	–	–	–	20	
C <sub>IN</sub>	I capacitance for GPIO0 to GPIO13	–	–	5	–	pF
	I capacitance for GPIO18 to GPIO25	–	–	3	–	

- Power Consumption

V<sub>cc</sub>=3.3V, Room temperature

Condition	Average	Unit
2.4G 11g continue Tx@6M_17dBm	900	mWatt
2.4G 11n continue Tx@HT40MCS7_11dBm	630	
2.4G 11b/g and 11n HT20 continue Rx	314	
2.4G 11n HT40 continue Rx	340	
5.18G 1a continue Tx@6M_14dBm	947	
5.18G 11n continue Tx@HT40MCS7_6dBm	660	
5.18G 11a and 11n HT20 continue Rx	320	
5.18G 11n HT40 continue Rx	342	

Note A: External IO voltage (DVDD\_GPIO) can be 1.8~3.3V.

Note B: QCA4004 need 3.3V and 1.2V supply. For 1.2V supply there are 2 methods: Switching and LDO. Inside the QCA4004, there is embedded a switching regulator to convert 3.3V to 1.2V for its chip core voltage supply. Also it has a LDO driver to act as 3.3V->1.2V LDO driver (might need external transistor). In

this table we consider to use switching regulator method to have better power efficiency. The tradeoff is we have to add a power inductor near QCA4004 chip. Also shielding height has to be caution to prevent the inductor magnetic field interaction with the iron material in the shielding.

■ Absolute Rating

Symbol	Description	Range	Unit
VDDIO	All IO supply voltage	-0.3~+4.0	Volt
3.3V	All 3.3V net	-0.3~+4.0	Volt
Min Digital IO	All Digital IO pins	-0.3	Volt
Max Digital IO	All Digital IO pins	(VDDIO+0.3)	Volt
RFin	Max Input RF signal	10	dBm
Tstorage	Storage Temperature	-40~+135	°C
ESD-HBM	Human Body Model, all pins		Volt
ESD-CDM-nonRF	Charged Device Model, non-RF pins	500	Volt
ESD-CDM-RF	Charged Device Model, RF pins	400	Volt

Note: This table is the level that could permanent cause damage to the module. For normal operating, please use below table for design consideration.

■ Recommend Operating Condition

Symbol	Description	Range	Unit
VDDIO_GPIO	All IO supply voltage	1.8~3.4	Volt
3.3V	All 3.3V net	3.15~3.45	Volt
Min Digital IO	All Digital IO pins	-0.2	Volt
Max Digital IO	All Digital IO pins	(VDDIO+0.2)	Volt

■ SPI slave interface electrical timing diagram

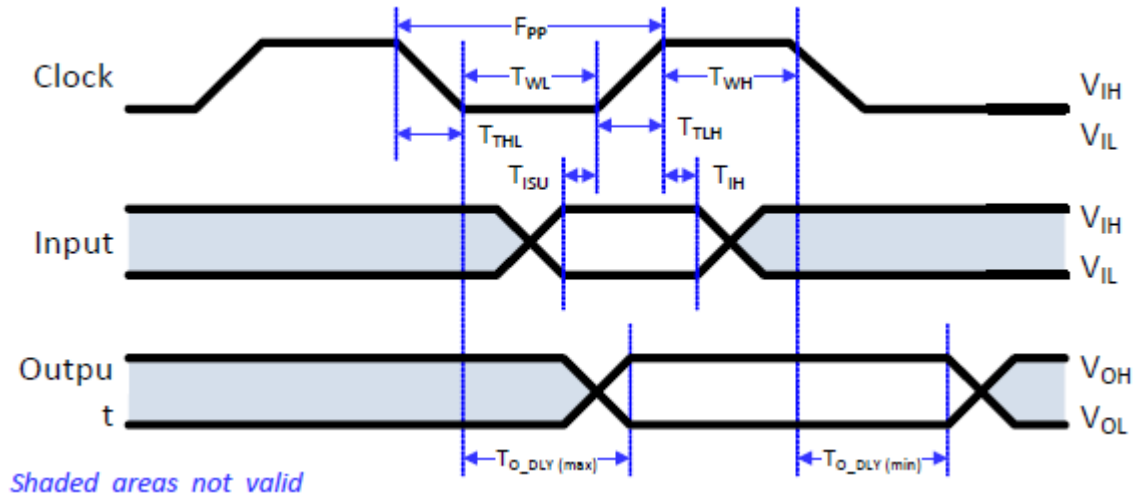


Fig.4: SPI slave timing

Table 4: SPI slave port timing (48 MHz reference clock)

Parameter	Description	Min	Max	Unit
$f_{PP}$	Clock frequency	0	48	MHz
$t_{WL}$	Clock low time	8.3	–	ns
$t_{WH}$	Clock high time	8.3	–	ns
$t_{TLH}$	Clock rise time	–	2	ns
$t_{THL}$	Clock fall time	–	2	ns
$t_{ISU}$	Input setup time	5	–	ns
$t_{IH}$	Input hold time	5	–	ns
$t_{O\_DLY}$	Output delay	0	5	ns

■ RF path:

- There is a SPDT RFSW to do the diversity between 2 RF ports during normal mode.
- For Port0, there are 2 options, U.FL0 or printed antenna by a jumper capacitor.
- If using on board printed antenna: Please do not cover or put anything close to the printed antenna otherwise the radiation efficiency will be degraded. Also keep clean underneath the printed antenna area.
- For Port1, there are 2 options as well, U.FL1 or RF PAD by another jumper capacitor.
- RF PAD: Ideally, this can bridge the RF signal to the device outside the board. Application board designer must be caution on the impedance control of the RF pad and 50 Ohm trace at platform board.
- DNSA-144 MP version might remove the unused U.FL connector.

Fig. 9: Module Land Footprint [unit:mm]



- Soldering Reflow Profile: Using WNC’s internal SMT line profile. Solder paste: SAC305.

Profile Type Selection		Lead Free Solder	
		(Sn3.0Ag0.5Cu)	
		Lead Free	
		Temperature	Time
Profile Parameter Setting	Max. Rising Slope	<3 °C/sec	
	Soaking Time	140~190 °C	70~105sec
	Wetting Time	217 °C	70~90sec
	Peak Temperature	230~250 °C	
	Over Wetting Time	230 °C	40~60sec

Table 5: Reference soldering profile for customer to mount the module.

## 4. Regulatory & Environment

- Pre-certified FCC
- Regulatory Compliance:
- Environment policy: All material used inside this module is Lead Free, RoHS compliant.

## 5. Package Information

80 pcs DNSA-144 \* 10 Trays = 800 pcs/SET  
 800 pcs \* 3 SETS(33 Trays) = 2,400 pcs/CARTON  
 PS Tray Max. Temp is 75°C

## 6. Order Information

Model	Description
DNSA-144(I)	I-temp standard, SPI, Printing ANT ver.
DNSA-144(I_UFL)	I-temp standard, SPI, U.FL antenna conn. ver.

## 7. Driver/Utility Installation

The driver should have been installed before the TV is shipped from the manufacturer. You can start using its network function without installing driver or utility.