

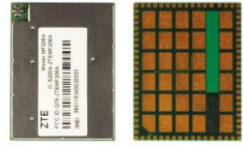


Product Spec List

Product Name: MF206A

Version:1.2

Date:2013-05-31

MF206A Spec List					
Item	Feature	Description	Complia	Notes	
	Product Name	MF206A			
Artwork	Dimensions	36mm * 26mm * (2.5+/-0.2)mm			
	Weight	About 5.2g			
	Picture				
	Form Factor	LGA package type II(108 Pin)			
	USB Interface Type	Other		Provide USB Interface	
	Design guide	Hardware & Software Design guide	Yes		
Solution	Chipset supplier	Qualcomm			
Baseband	Processor	ARM 9			
	Processor speed (Apps)	MDM6270:High-performance ARM926EJ-S running at up to 184 MHz for 3.6 Mbps HSDPA			
	USIM/SIM	Support Standard 6 PIN SIM card		3V SIM card and 1.8V SIM card	
	Memory(SDRAM/NAND)	32MByte/128MByte		Cost Related	
	MICRO SD Card		Optional		
	USB Version	USB 2.0 HIGH SPEED			
	Interface	LGA			
	Active power consumption	about 170mA		W2100, Average Power 0dBm	
	Sleep Mode	about 3.9mA			
	Minimum power consumption	≤75mA		-75db ,DRX =640	
	Power supply	3.8V(3.4~4.2V)			
LED control	3 LED pins				
RF	Receive Diversity		No		
	Main Antenna	External		Provide Antenna Interface or pad	
	Receive Diversity Antenna	N/A			
	GSM Band	EDGE/GPRS/GSM:1900/1800/900/850MHz		Cost Related	
	UMTS Band	WCDMA/HSDPA:2100/1900/850(900)MHz		Cost Related	
	RxDiv Band	N/A			
	Max. transmitter power	UMTS2100/1900/850(900): Power Class 3 (+24dBm +1/-3dBm) GSM/GPRS 850MHz/900MHz: Power Class 4 (+33dBm ±2dBm) GSM/GPRS 1800MHz/1900MHz: Power Class 1 (+30dBm ±2dBm) EDGE 850MHz/900MHz: Power Class E2 (+27dBm ±3dBm) EDGE 1800MHz/1900MHz: Power Class E2 (+26dBm -4/+3dBm)			
Technical Standard	GSM/EDGE/WCDMA	GSM CS: UL 9.6kbps/DL 9.6kbps			
		GPRS: Multi-slot Class 10			
		EDGE: Multi-slot Class 12			
	HSDPA/HSUPA/HSPA+	WCDMA CS: UL 64kbps/DL 64kbps			
		WCDMA PS: UL 384kbps/DL 384kbps			
		HSDPA: DL 3.6Mb/s(Category 6)			
	3GPP Release	R99,R5			
	OS	Window	Windows XP (SP2 and later)	Yes	
			Windows Vista (32bit)	Yes	
			Windows Vista (64bit)	Yes	
			Windows 7	Yes	
		WinCE	WinCE 5.0	Yes	CPU:X86、 ARM
			WinCE 6.0	Yes	CPU:X86、 ARM
Android		1.6	Yes		
		2.0	Yes		
		2.1	Yes		
		2.2	Yes		
	2.3	Yes			
4.0	Yes				
Linux	kernel 2.6.20 and later	Yes			
GPRS Class	Class B				
Application	DATA	RAS	Yes		
		NDIS	Optional	Windows	
		ECM	Optional	Linux	
	GPS		Yes		
	SMS		Yes		
	MMS		Yes	not support build-in MMS protocol stack	
	STK		Yes		
	USSD		Yes		
	PHONEBOOK		Yes		
	NETWORK LOCK		Optional		
	POWER SUPPLY	RF Switch		Yes	
Standby & Hibernation			Yes		
USB Selective Suspend			Yes		
Remote Wake-up			Yes		
Approvals & Certification	CE		Yes		
	GCF		Optional		
	FCC		Yes		
	ROHS		Yes		
	PTCRB		Yes		
	CCC		Optional		
Others	IC、WEEE		Yes		
Environment	Operating Temperature	-30 ~ 75° C			
	Limited Temperature	-40 ~ 85° C			
	Storage Temperature	-45 ~ 90° C			
	Humidity	5%~ 95%			
Commercial Details	Engineering samples Date (yyyy/mm/dd)	2011-1-20			
	Final samples Date (yyyy/mm/dd)	2011-3-20			
	Product Market launch Date (yyyy/mm/dd)	2011-5-20			

MF206A PIN Define

Pin	Pin Name	Description	I/O	Remark
1	ANT_MAIN	Main antenna pad	AIO	
2	GND	Ground		
3	JTAG_RESOUT_N	JTAG reset signal	DI	Reserved for ZTEWelink debug
4	PON_RST_N	Module reset	DI	1.8V
5	POWER_ON	Power on/off	DI	1.8V
6	AP_READY	Module query AP sleep status	DI	1.8V; L: Active H: Sleep
7	I2C_SCL	I2C clock	DO	1.8V
8	I2C_SDA	I2C data	DIO	
9	MODULE_READY	AP query module sleep status	DO	1.8V; L: Active H: Sleep
10	AP_WAKEUP_MODULE	AP wake up module	DI	1.8V; L: valid H: invalid
11	MODULE_WAKEUP_AP	Module wake up AP	DO	1.8V; L: valid H: invalid
12	GND	Ground		
13	RESERVED			
14	GND	Ground		
15	NC			
16	NC			
17	NC			
18	NC			
19	MODULE_POWERON	Module status indicator	DO	1.8V; L: Shut down H: Power on
20	LED_GREEN	LED Indicator pins	AI	Valid Current: 5/10/15/20/25/30/35/40mA
21	LED_RED		AI	
22	LED_BLUE		AI	
23	VPH_PWR	Power supply	AI	3.8V(3.4~4.2V)
24	VPH_PWR			
25	VPH_PWR			
26	VPH_PWR			
27	UART_CTS	UART Clear To Send signal	DI	1.8V
28	UART_RFR	UART Request To Send signal	DO	
29	UART_TXD	UART Transmit Data output	DO	
30	UART_RXD	UART Receive Data input	DI	
31	GND	Ground		
32	RESERVED			
33	RESERVED			
34	RESERVED			
35	RESERVED			
36	GND	Ground		
37	ADC	Analog voltage detect	AI	0~2.1V
38	GND	Ground		
39	SPI_CS_N	SPI chip-select; not mandatory in a	DIO	1.8V
40	SPI_CLK	SPI clock	DIO	
41	SPI_DATA_ML_SO	SPI master out/slave in data	DIO	
42	SPI_DATA_MO_SI	SPI master in/slave out data	DIO	
43	USB_VBUS	Monitors the external USB supply voltage	AI	USB slave High-speed
44	GND	Ground		
45	USB_DP	The positive USB differential signal	AIO	
46	USB_DM	The negative USB differential signal	AIO	
47	GND	Ground		
48	VREG_RUIM	Power for SIM	AO	SIM card type: 1.8/3.0V Supprot UICC
49	UIM_DATA	Data for SIM	DIO	
50	UIM_CLK	Clock for SIM	DO	
51	UIM_RST	Reset for SIM	DO	
52	UIM_DP	Be used for UICC	AIO	
53	UIM_DM	Be used for UICC	AIO	
54	GND	Ground		
55	VREG_SDCC	SD CARD power supply	AO	
56	SDCC_CMD	Command bit for SD device	DIO	
57	SDCC_CLK	Output clock for SD device	DO	
58	SDCC_DATA3	SD device data bit3	DIO	
59	SDCC_DATA2	SD device data bit2	DIO	
60	SDCC_DATA1	SD device data bit1	DIO	
61	SDCC_DATA0	SD device data bit0	DIO	
62	SD_DET_N	SD device detect	DI	1.8V
63	GND	Ground		
64	RESERVED			
65	RESERVED			
66	GND	Ground		
67	RESERVED			
68	RESERVED			
69	GND	Ground		
70	GPS_ANT	GPS antenna	AI	
71	GND	Ground		
72	JTAG_TRST_N	JTAG reset		Reserved for ZTEWelink debug
73	JTAG_RTCK	JTAG return clock		
74	JTAG_TCK	JTAG clock input		
75	JTAG_TDO	JTAG test data output		
76	JTAG_TDI	JTAG test data input		
77	JTAG_TMS	Test mode select input		
78	GND	Ground		
79-108	GND_PAD	Thermal Pads Ground		

Notes:

1. DI:Digital input, DO:Digital output,DIO:Digital output & Digital output
2. AI:Analog input, AO:Analog output,AIO:Analog output & Analog output