

RSTS PROFESSIONAL

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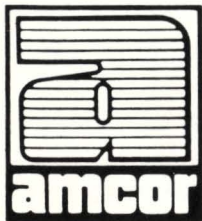
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From the editors...

waiting . . .

Carl Marbach

Version 7.0 was in its time a neat piece of software. It had all kinds of good new things; big (better, faster) FIP, Data caching, QSTATS, lots of new MODES, ANSI magtape routines and lots more. It was a definite improvement over V6C. Sure, it had its problems, but these would be fixed in the next release — wasn't that just a year away?

In the Golden, Olden days, when you purchased a one year support contract for RSTS it guaranteed you at least one new release. We became used to a release cycle of about 1 year. Okay, it slipped once and a while to 18 months, but the clear inference was that we could expect releases at about 1 year intervals. In 1975 we were running V6A. In 1980 we were through 6B, 6C and 7.0 giving us 4 versions in 5 years; pretty close to the cycle we were describing above.

December will mark two (2) years since V7.0 hit the streets and the best DEC GUESS is that the next version is still about 1 more year away! Three years! What's going on? Hardware hasn't exactly been quiet over these 2 years; RM05, 11/24, immediate delivery (!) on LS120's and VT100's, MOS memories and more.

Maybe V7.0 is so solid it doesn't need a new release. But what about the 11/70 small buffer problem, the 11/34 task building problems (it's still task building!), the RM05 support (it costs extra), 11/24's are anyone's guess, Stats that don't work and worse (they give erroneous figures) and more. Why is it that RSX seems to get the new bells and whistles first; DECNET, 2780 support, FORTRAN 4 PLUS.

Of course the commercial marketplace has made RSTS one of the most active operating systems around. There is Word Processing, List processing, Data bases, Queuing systems, backup packages, magazines, disk structuring packages, modeling systems, languages and editors — all from sources other than DEC. Just look through this magazine for all the good people working for your money.

Insiders tell me that all this time is being spent figuring out the BEST solution to these problems. That they will be worth waiting for. How do all of you out there feel about paying for three years for support and not seeing a new release? We think that although two women can't make a baby in less than nine months, two RSTS developers could produce a new version of RSTS twice as fast as one. Message: get off INDENT, GIGI and frills; make RSTS work the way it should.

What are you waiting for?

Andy Riebs spelling the small buffer relief. ▶

DECUS MIAMI The Tide Has Turned

Dave Mallery

The big news from Miami is that two years of stone-walling has ended. DEC is talking to us again! One is tempted to attribute the thaw to the effect of tropical breezes and sunny skies on those inhabitants of the frozen north, but I'd rather think that there have been some fundamental changes in policy.

This was obvious right from the start. As soon as the opening salvos of what I had predicted would be "Buffer Wars" were fired, the development folks informed us that they were promising relief in the next release and would tell us more at a later session. The next morning, at a session entitled "Building a RSTS Monitor", Andy Riebs from the development team disclosed two approaches that were in the works to provide the relief.

First, a new memory pool would be established to hold WCB's and FCB's. Secondly, selected code segments would be re-worked to utilize "I and D" space—a hardware feature never before used by RSTS. Basically, this presents the developer with another set of memory mapping registers to use for buffer pools and the like. Please be very clear that nothing in this article, as well as nothing said at Miami represents a firm commitment by DEC. It is imperative that we accept this information in the spirit in which it was given.

There was also some bad news. The next release is more than six months away.

This symposium was highlighted by many excellent user papers. Mike Mayfield from Northwest Digital Software delivered a six hour marathon on Monitor Tables to large, late night audiences.

Mark Diebert from Squibb gave an excellent paper entitled "So Your Disk Is Irrevocably Corrupt" shedding a great deal of light on one of the more ominous init error messages.

Joyce Hayes and Steve Stepanek gave a three session TECO wonderland tour. It's amazing how some things never die. I have attended more funerals for RSTS and TECO than I care to remember. I heard about one site, in Rochester, NY, that uses about forty

... continued on page 28



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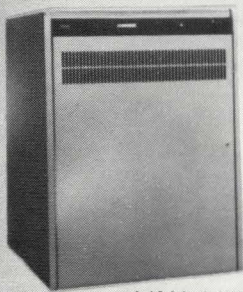
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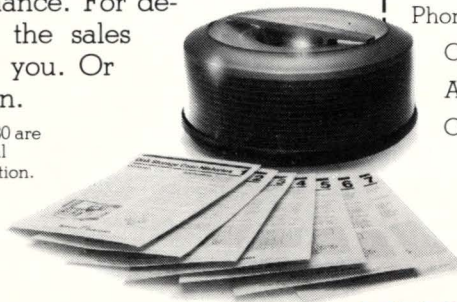
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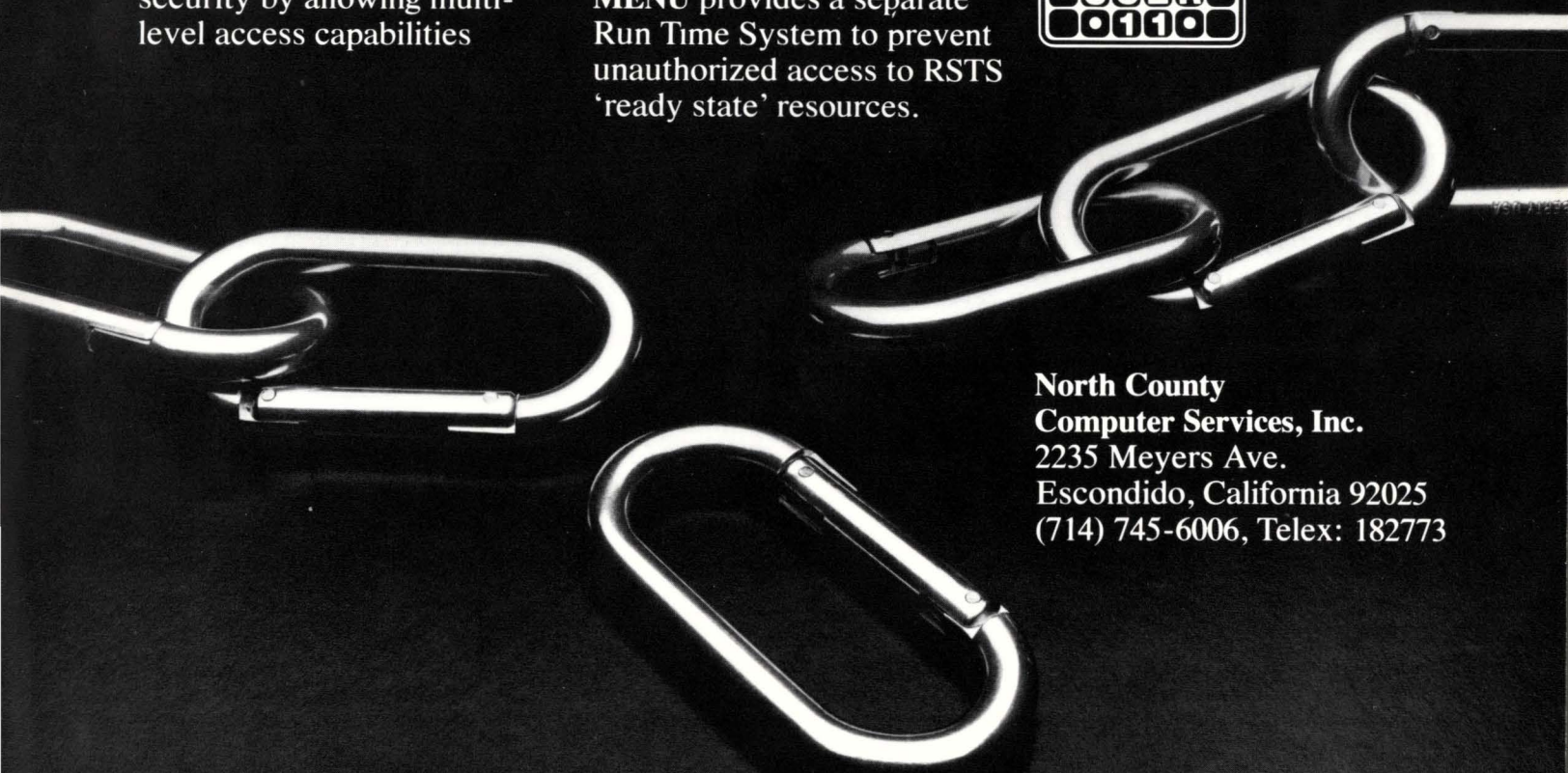
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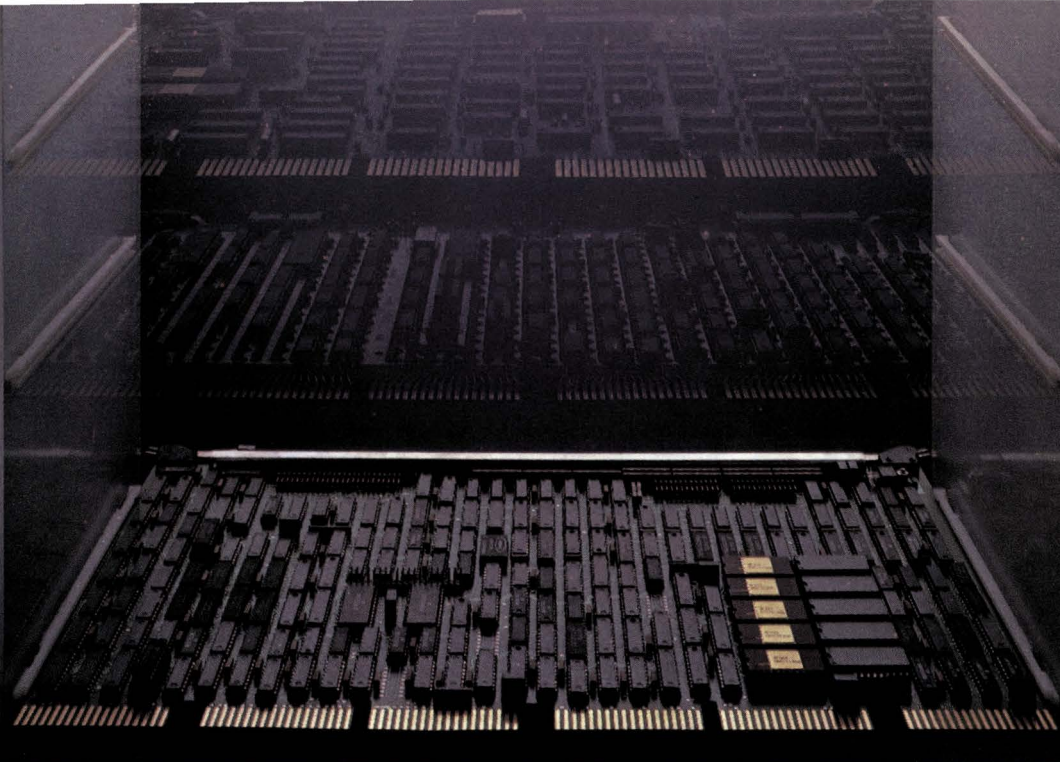
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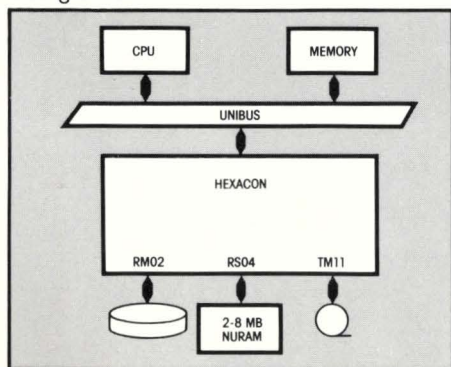
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"I (for once) was speechless."

-- Dave Mallery, March, 1981 issue of
RSTS PROFESSIONAL

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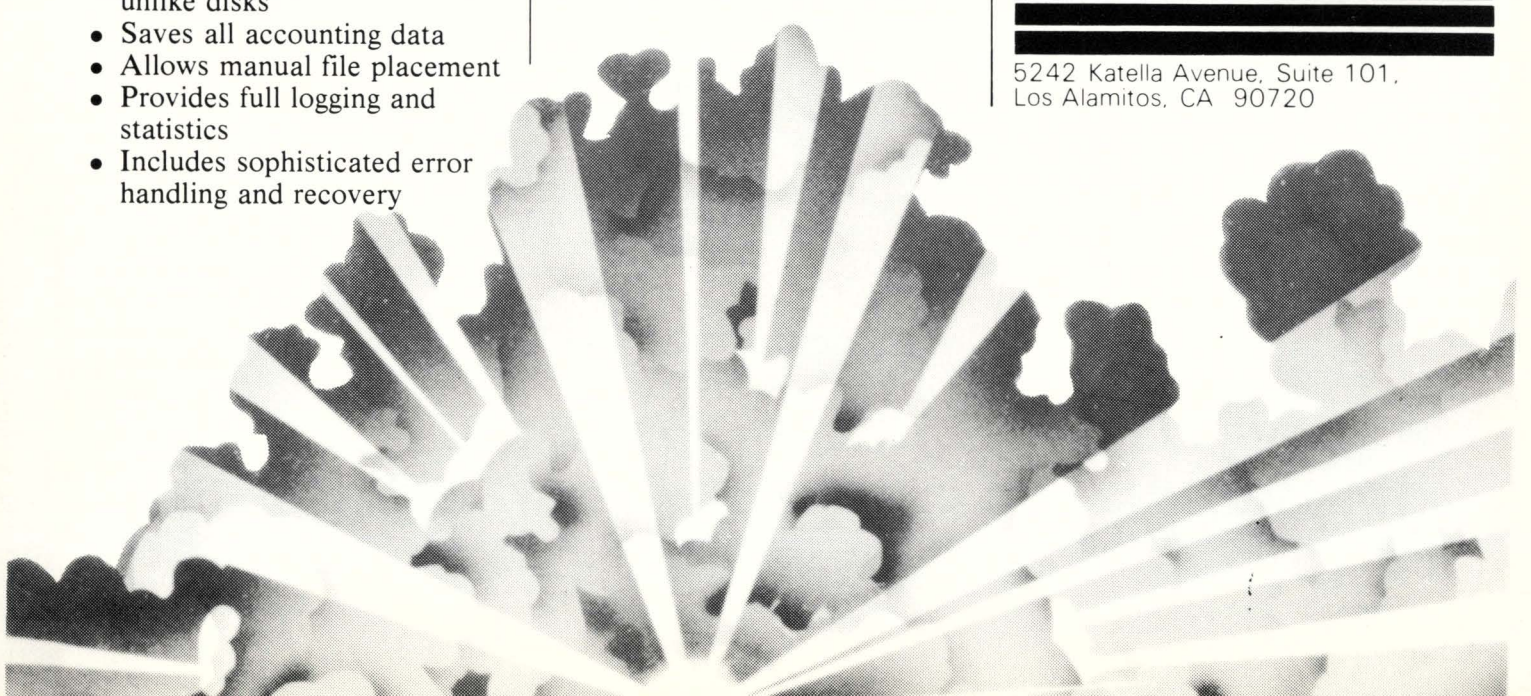
DISKIT, Dave says, "...is the 'final solution' to structured disks, eliminating all of the time and complexity and reducing the job to one of a SAVRES."

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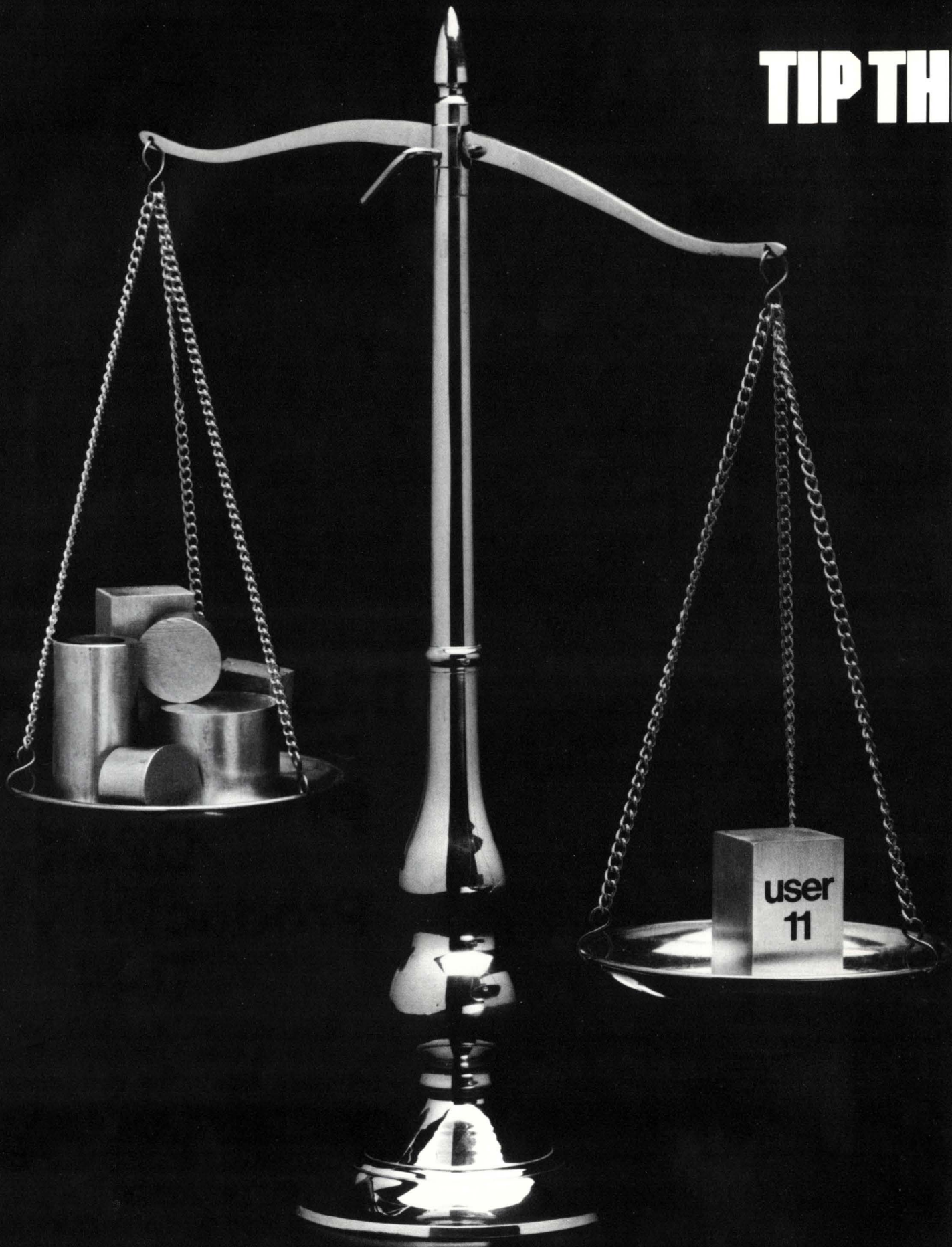
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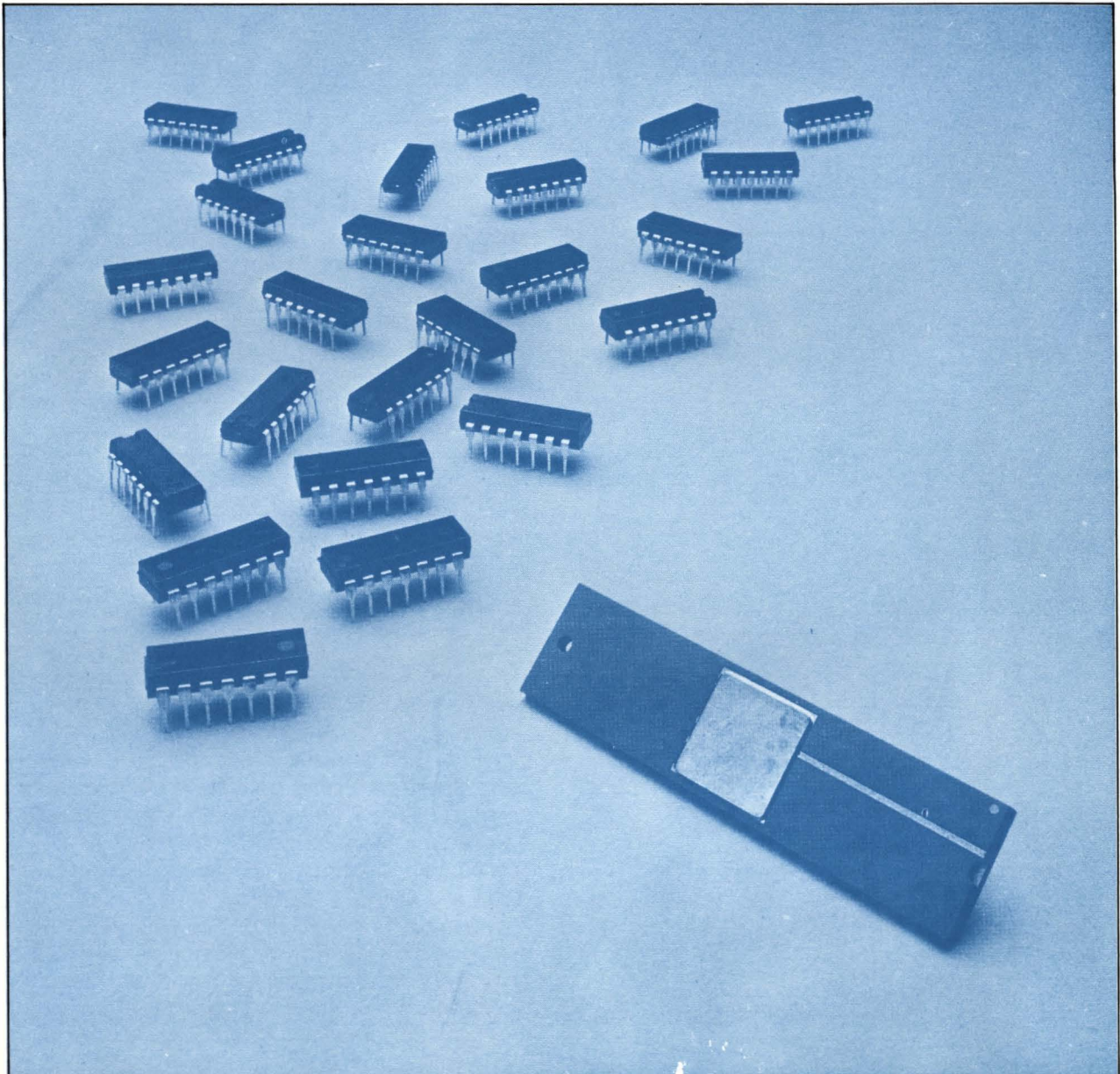
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The VAX-SCENE

Number 3

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June 1981



INSIDE:

- CONPAX: Conversion of PDP-11 Assembly Code to VAX-11 Native Mode**
- VAX News**



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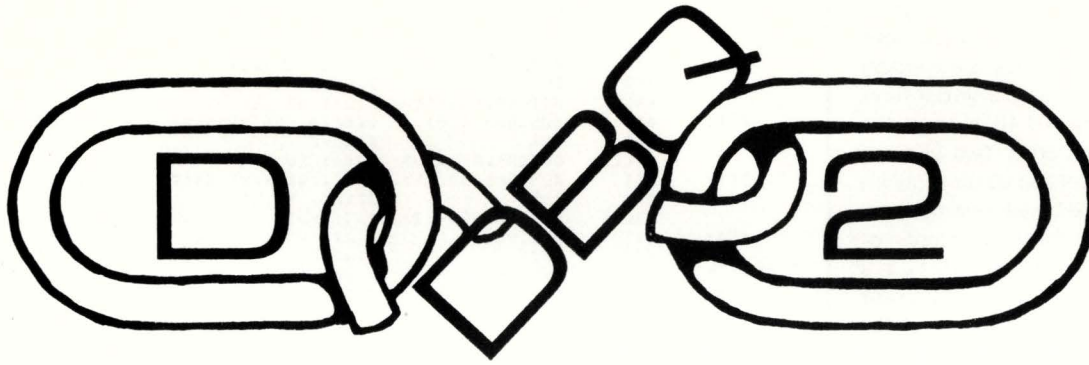
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which operates exactly like the matching register in the TU 10. Also, the unibus address register, which really is a memory address register, operates in a similar function to the one in the TU 10, even to the degree that the high order two bits are contained in the control and status register; one in order to give 18 bit addressing capability. While spacing forward records or characters was controlled in the TU 10 in a different fashion, the TU 16 has a different frame count register which contains a twos compliment number of records to be spaced over, characters to be written or characters that have been read. There is a second control and status register. This register contains some additional information including bit 8 which is the mass I/O bus data bus parity error. This is the error that gets set on a parity error indication on the high-speed bus. The drive status register contains similar information, some additional peripheral information. One of the additional capabilities that the TU 16 has on an optional basis is to write in phase encoded 1600 bits per inch. There is an error register in which each of the 16 bits contains a certain particular error. An attention summary register in the controller enables the programmer to determine immediately which of the 8 drives has interrupted rather than looking at the status of all 8 drives of the sequence. A character check register contains the cyclic redundancy check character that is used on the TU 16. There is the same data buffer register as well as a drive type register which allows the program to find out what type of drive this is. There are also serial number registers containing the serial number of the drive and a tape control register to control the density and format of the data being written. So, although the TU 16 has a few more registers than the TU 10, most of the additional data available is better error checking, increased error diagnostic aids and little, if any of it, has to do with the difference in architecture between the way the data is transferred via a high-speed controller. Thus, the fact that the TU 16 can operate via a mass bus and a high-speed controller is transparent to the programmer who has to program applications for it. ♥

AUTHORS!!!

SEND YOUR ARTICLES TO THE RSTS PROFESSIONAL ON MAG TAPE, IN EITHER RNO, PIP OR WORD-11 FORMAT.

Eighty percent of this issue of the RSTS Professional was transmitted via telecommunications from author's mag tapes to phototypesetting equipment and was not retyped.

```

BIT      #US.NOK,(SP)    ;;PERMANENT FILE?
BEQ      10$             ;;NOPE
CALLX    OUTCHR,R5,<P>   ;;SAY SO
10$:     BIT      #US.NOX,(SP)    ;;CONTIGUOUS?
        BEQ      20$             ;;NOPE
        CALLX    OUTCHR,R5,<C>   ;;SAY SO
20$:     BIT      #US.UPD,(SP)    ;;OPEN FOR UPDATE?
        BEQ      30$             ;;NOPE
        CALLX    OUTCHR,R5,<U>   ;;SAY SO
30$:     BIT      #US.UPD!US.WRT,(SP) ;;WE HAVE WRITE PRIVELEGES?
        BNE      40$             ;;YES, SO IGNORE
        CALLX    OUTCHR,R5,<R>   ;;IF NOT, SAY READ ONLY
40$:     BIT      #US.PLC,(SP)    ;;ARE WE A PLACED FILE
        BEQ      50$             ;;NOPE
        CALLX    OUTCHR,R5,<L>   ;;SAY SO
50$:     TST      (SP)+         ;;GET RID OF STATUS
        RETURN                    ;;AND GO BACK

.DSABL   LSB

.SBTTL   PRINT OUT SINGLE OR DOUBLE INTEGER
;+
;DBLNUM  - PRINT OUT DOUBLE PRECISION INTEGER
;        WITH OR WITHOUT LEFT JUSTIFICATION
;
;        SP -> LSB OF INTEGER, MSB OF INTEGER
;
;        CALL    DBLNUM
;
;        R2 - R5 RANDOM
;-
.ENABL   LSB

DBLNUM:  MOV      (SP)+,R5        ;;SNAG RETURN ADDRESS
        CLR      R2              ;;CLEAR A HIGH ORDER
        BISB    (SP)+,R2        ;;GET THE HIGH ORDER OF THE INTEGER
        MOV      (SP)+,R3        ;;GET THE LOW ORDER
        MOV      R5,-(SP)        ;;REPLACE THE RETURN ADDRESS
        DIV     #10000,R2        ;;SPLIT THE NUMBER
        MOV      R3,-(SP)        ;;SAVE LOW ORDER
        TST     R2              ;;IS THERE A HIGH ORDER?
        BEQ     10$             ;;NO HIGH ORDER
        MOV     R2,R3           ;;SET TO OUTPUT IT
        CALL    20$            ;;DO IT
10$:     SEC      20$           ;;LOW ORDER SHOULDNT SUPPRESS LEADING ZEROES
        MOV     (SP)+,R3        ;;DO LOW ORDER NOW
        CALL    20$            ;;OUTPUT THE LOW PART
        RETURN                    ;;AND GO BACK

20$:     CALL    DODIVS,R5,30$   ;;GO DO THE DIVISIONS, PRINTING ASCII DIGITS
        _WORD  1000,100,10.    ;;EXIT
        RETURN

30$:     MOV     50$(R2),R2      ;;GET THE NUMERIC
40$:     CALLRX  CHOUTE         ;;OUTPUT THE CHARACTER

.ASCII  "$.?"
50$:     _ASCII "0123456789"    ;;NUMBERS

        _EVEN

;+
;FILNAM  - PRINT OUT A FILENAME
;
;        R5 -> TO THREE FILENAME WORDS
;
;        CALL    FILNAM,R5
;-

FILNAM:  MOV     #70$,-(SP)      ;;PUSH FOR EXTENSION
        MOV     PC,-(SP)        ;;BACK HERE ONCE
60$:     MOV     (R5)+,R3        ;;GET THE RAD50 WORD
        SEC      20$           ;;DON'T SUPPRESS LEADING ZEROES
        CALL    DODIVS,R5,80$   ;;GO DO THE DIVIDES
        _WORD   50*50,50
        RETURN                    ;;EXIT

70$:     MOV     #"-",R2        ;;SET TO OUTPUT A "-"
        CALLX   CHOUTE         ;;AND CALL ROUTINE TO DO IT
        BR      60$            ;;AND CONTINUE

80$:     TST     R2             ;;CHECK FOR A SPACE
        BEQ     90$            ;;OUTPUT A SPACE
        ADD     #A-1,R2        ;;ADJUST FOR ALPHABETICS
        CMP     R2,#33+<A-1>    ;;IS IT IN FACT ALPHABETIC?
        BLO    40$            ;;YES, GO USE IT
        SUB     #36+<A-1>,R2    ;;ADJUST FOR NUMERIC (ZERO = 0)
        BR      30$            ;;GO DO IT

90$:     MOV     #" ",R2        ;;SET SPACE AS CHARACTER
        BR      40$            ;;AND OUTPUT IT

;+
;OUTSIZ  - PRINT A NUMBER 0-255 WITH LEADING ZERO SUPPRESSION.
;
;        SP -> NUMBER (HIGH ORDER BYTE IGNORED), ...

```


nice. Why here? Well, telephone charges are minimized because it is very close to the center of the U.S. But they admit that it is easier recruiting people to the quality of life available in the Rocky Mountain foothills. There is also unlimited expansion available on the large site they occupy. The DDC shares the facility with the telephone support group and a disk manufacturing facility. Growth has been so rapid that there are signs of people being moved all over; new desks, new terminals (some in boxes), new walls, and new people.

I met Al in Denver and we drove the one and one half hours to 'the springs'. The DDC itself is spectacular, it sits alone on what appears to be miles of land mostly covered by low vegetation. At the end of the land are the foothills of the Rockies and the Rockies themselves with Pikes Peak standing out like a sore thumb that isn't sore; just pretty to look at. I suppose they get used to it, but guys, it sure is nice looking.

Once inside I met Bob Ross, fugitive from Detroit who is PDP-11 family manager (see corporate chart). Bob suggested that before we went into detail about the DDC itself that I might like a tour of the building and the disk manufacturing facility it contains. Warren Shubert was our guide through a plant that manufactures RK07's, pseudo-manufactures RM03's and RM05's (really CDC) and RP06's (memorex). They are also making the disk packs for the RK07's in a 'clean room'. In another 'clean' room they are making winchester media, this room is 1000 times cleaner than the RK07 room! The RP07 winchester (available for VAX) looks like a state of the art drive, with a monthly service cost less than the RP06 which holds less. The manufacturing system is RSTS of course (we're moving to VAX. . .) and tracks the process from receiving to final production and testing. Testing is done en masse with many smaller 11's, and hundreds of drives seeking forever (on a clear disk you can seek forever). Warren showed us the new packaging and shipping area that had recently been overhauled. They had done a study and found that this area was the bottleneck in production. A new system of wrapping, stacking, and moving these large packages around was installed and . . . now there is another bottleneck somewhere else. Slick this is, a small person (female even) can move huge amounts of disk packs around in a flash. All this confirms how little I know about manufacturing, and at the same time how similar manufacturing is to a complicated program. Maybe if I figured out where the bottleneck was in my trial balance I could. . .well, thanks to Warren and we went back upstairs.

Bob and I sat down and went over some of the goals of the DDC. The main objective of the DDC was to:

- Reach a diagnostic conclusion with a recommendation within 1 hour
- Achieve this in 90% of the cases

```

@S"TI$CTT:" L -1W
@I"TI$CFF:" ;HANDLE CONTROL/F (OPEN FILES)
" -1W
@S"BNE 40$" @S";;" -1W K
@I"NOPE, IT IS CONTROL/T OR CONTROL/F
" -1W
@S"40$:" -1W @I" CMP R2,#F-100 ;CONTROL/F MAYBE?
" BEQ 60$ ;YEP, HANDLE IT
" L @I"50$:" 3L @I"
60$: CALL MAPPED,R5,TTOPNF ;GO TRY FOR OPEN FILES
BR 50$ ;DO SAME AS CONTROL/T
" -1W @S"TTSYST:" 3L -1W @I" TMPORG TTOPNF
TTOPNF: SEC ;SAY ILLEGAL UNLESS OVERLAID
RETURN ;AND EXIT
UNORG

" -1W @N".WORD '6-100" OL -1W
@I".WORD 'F-100 ;CONTROL/F (OPEN FILES)
" -1W @N".WORD TI$CCG" OL -1W @I" .WORD TI$CFF ;CONTROL/F
" -1W
ET&512 "N 155^T 72^T 155^T 74^T
@^A"CLOSING FILE OUT ..." 13^T 10^T EC
ET&512 "N 155^T 72^T 155^T 74^T
@^A"FINISHED..." 13^T 10^T EX

```

- Continue to work the problem after 1 hour upon branch request

After these the DDC also:

- Can perform/help in PM
- Installation check-outs
- Branch Demo's (on request)

According to Bob, they are getting close to achieving the 90% effectiveness that they are shooting for. To you and me this means that they will make the correct recommendation to the branch for repair within 1 hour of your call about 90% of the time. Great.

In addition there is a 'hot call' list. 'Hot calls' are for VIP's, loud yellers, intermittent problems, and continuing problems. You can be put on this list by your branch, who will then agree on a plan of action with the DDC. You should be an integral part of this plan; if they don't ask you, then ask them. Remember, it's your machine. Once you are flagged on their data base as a 'hot call', the DDC will continue to follow up on the problem, i.e. if they don't hear from the branch or you for a week they will call to find out what's happening.

Why does the DDC work? Mostly because of the people who actually do the diagnosis (they get help from the computer of course). Jim Porter gave us a demonstration of exactly how the diagnosis is done. The computer is an 11/70 (they have four of them) running RSTS (we're moving to VAX). Jim sat at a VT100 and paged through their data base showing me various installations including mine. All necessary data is stored including your configuration, telephone number, contact, contact number, and DDC history. The engineer can look at all this to determine how to proceed. After looking up your configuration, Jim asks the computer (DDC) to connect to the remote location; connection and dial-out (Vadic auto-dialers) is automatic. Once connected he can instruct the computer to run through several 'scripts' of diagnostic sessions. There is a general check-out script as well as many specialized ones. Jim and his fellow engineers know these systems inside and out, you are guaranteed to get an expert. Nice is a keyword here, but then when they go to lunch they look out at Pikes Peak and that is bound to help even my disposition.

The calls are initially taken by the phone answering

ODT.DOC

By Bob "MACRO MAN" Meyer

Insert for page 74, RSTS PROFESSIONAL, June 1981.

Dear Readers:

We're pleased to present [HOT OFF THE PRESS], Figure 1.

.title	demo	
one::	.word	7
two::	.word	6
demo::	mov	one,r0
	add	two,r0
	bpt	
	.end	demo

FIGURE 1.

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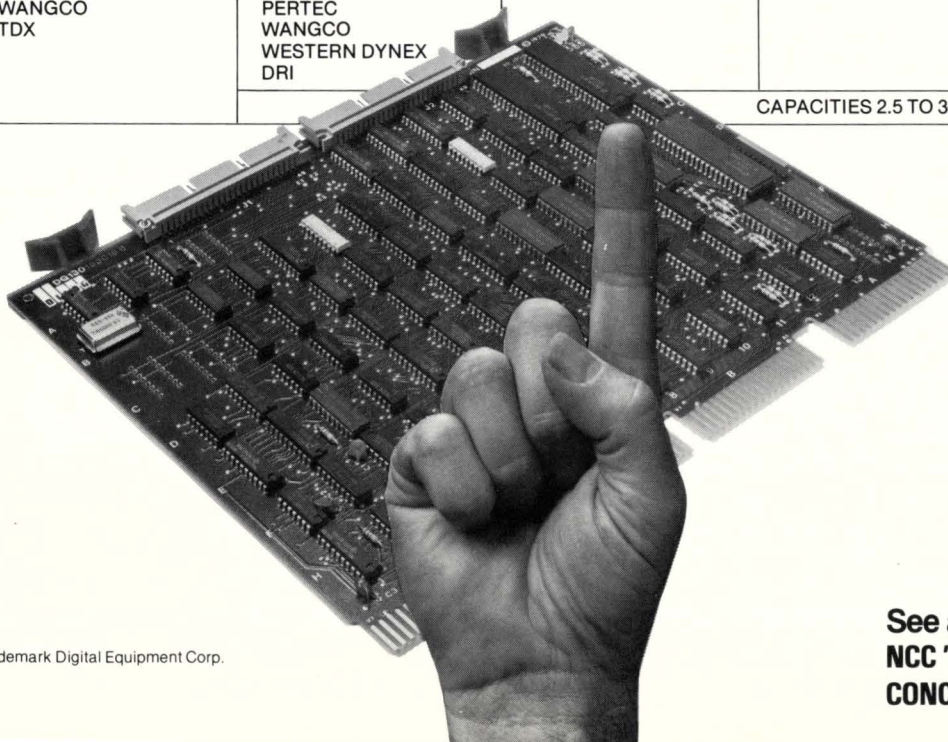
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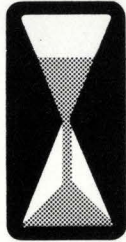
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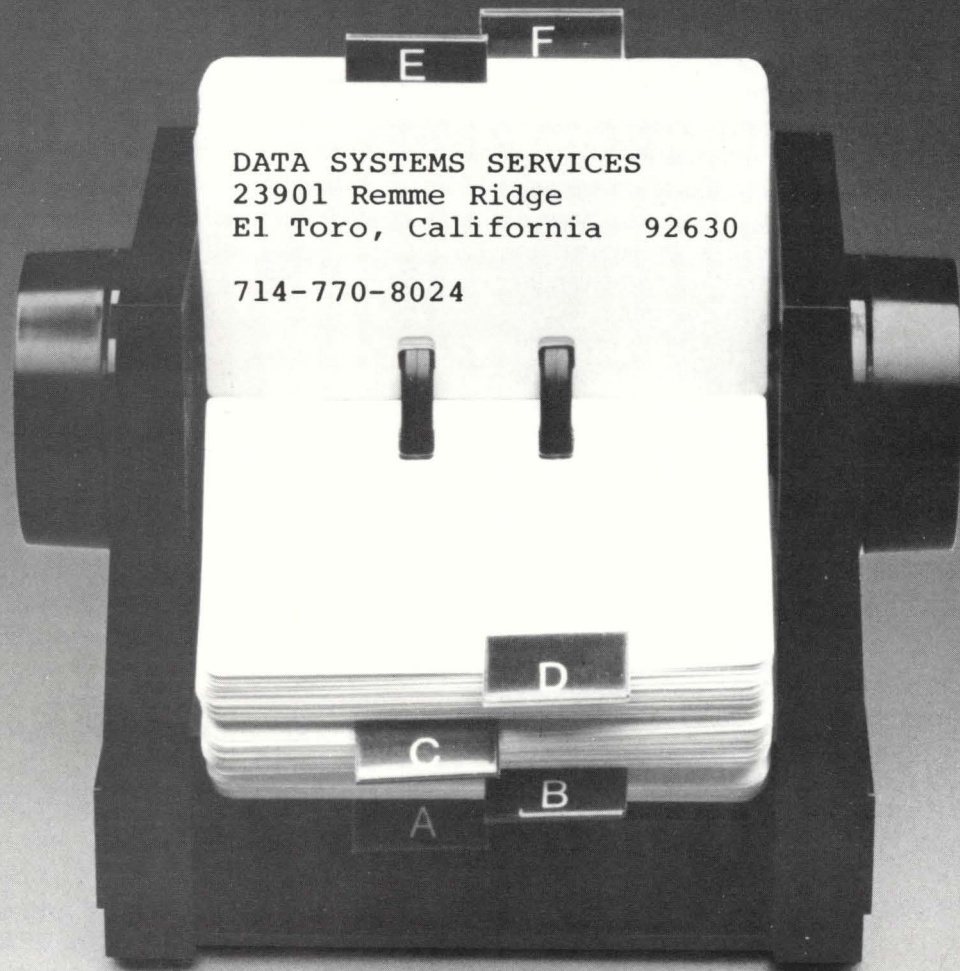
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LCK, IN	The entry is in the process of swapping in.
LCK	The entry is not available for swapping out.
OUT	The entry is not currently in memory and does not desire to be made resident.

2.2 MEMLST — RESIDENT MEMORY LIST

All of the memory on a system is defined by the resident memory list, MEMLST. As memory is divided among several different usages the memory control sub-blocks for each usage are linked into MEMLST in ascending order. Thus, by following the links between each MCB we have seen all available memory in the order it is allocated.

The memory control list is based at the location MEMLST. This location is the address of the first entry in the memory control list, rather than a pointer to the first entry as in most other linked lists. The first entry describes the memory used by the monitor and any free memory following it.

The memory control list always contains at least three entries. These are the root MCB, the system default runtime system, and the tail MCB. The root is actually the monitor MCB. The tail terminates the list and shows the highest memory location addressable on the system.

2.2.1 Root Memory Control Sub-Block

The first entry in the resident memory list is the root MCB. This entry starts at location MEMLST and describes the memory used by the monitor and any free memory following it.

The format of the root MCB is as follows:

Offset	Symbol	Description
0	M.PPRV	The link to the previous entry is 0 since this is the first entry in MEMLST.
2	M.PNXT	Pointer to the next entry in MEMLST.
4	M.TSIZ	The total of the monitor's size plus any free memory following the monitor.
6	M.SIZE	This byte contains the size of the monitor in K-words.
7	M.CTRL	The status bit LCK is set to show that the monitor's memory is not available for other uses.
8	M.PHYA	The starting physical address is 0 since the monitor always starts at location 0.

2.2.2 Tail Memory Control Sub-Block

The tail MCB is the last entry in MEMLST. It terminates the list and defines the highest memory address available on the system. The format of the tail MCB is:

Offset	Symbol	Description
0	M.PPRV	The backwards link points to the previous entry in MEMLST at its M.PNXT entry.

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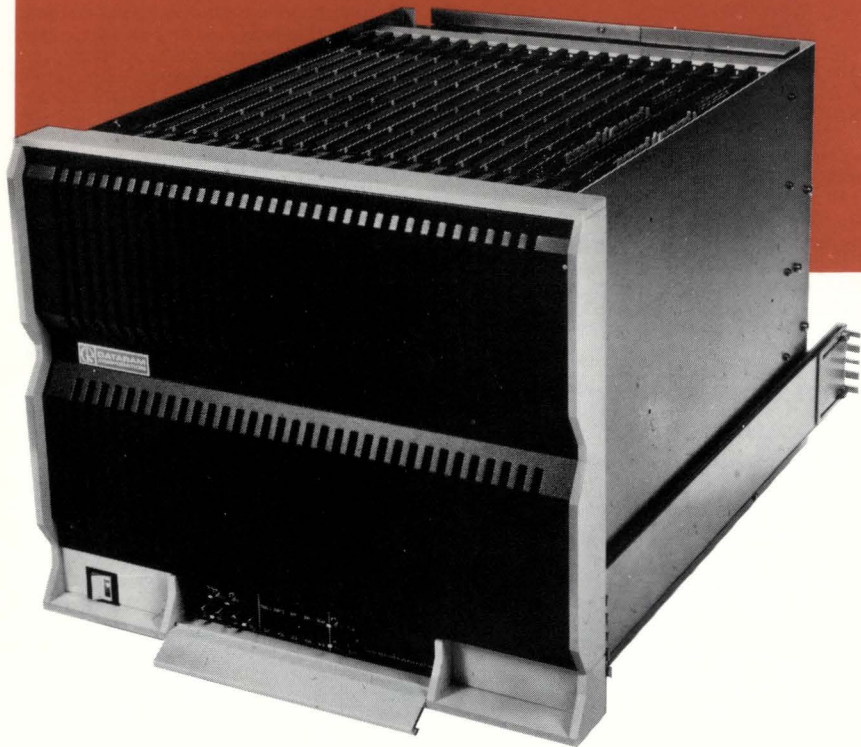


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Offset	Symbol	Description	Offset	Symbol	Description
0	R.LINK	This word contains the address of the next resident library descriptor block. If this entry is the last LIB block in the list it will contain a 0.	22	R.FILE	These three bytes contain the FIP block number of the block that contains the UFD name entry for this resident library. It is used to close the resident library file when it is removed.
2	R.NAME	These two words contain the resident library name in RAD50.	25		This byte is the offset to the name entry of the RTS file within the directory block specified by R.FILE divided by 2.
4	L.PPN	This word contains the account number (PPN) of the resident library file. The project number is in the high byte. The programmer number is in the low byte.	26	R.CNT	This byte contains a count of the number of jobs currently attached to this resident library.
6	R.MCTL	These five words are the memory control sub-block for the resident library (see 2.1).	27		This byte contains a count of the number of jobs using this resident library which are currently resident in memory. If the residency count is 0 the resident library is eligible for "swapping out". If a resident library is loaded with the /STAY switch, the high bit of this byte is set, ensuring that the residency count will never be 0 and the resident library will always remain in memory.
14	R.KSIZ	This word (within the memory control sub-block) contains the size of the resident library in K-words.	28	L.STAT	This byte is used to differentiate between an RTS block and a LIB block. If bit 7 (symbolically, LS.LIB) is set this is a LIB block, otherwise it is an RTS block. LS.LIB is the only bit currently defined for L.STAT.
18	R.DATA	These three bytes contain the FIP block number of the first block of the resident library image. When a resident library is loaded into memory it is accessed on disk by this block number. Byte 20 is the most significant byte of the block number.	29	L.PROT	This byte is the library protection code. The protection code is used to control access to the memory space of a res-
21		This byte is the FIP unit number for the disk containing the resident library. It is used when loading the resident library image and when closing the resident library file when the resident library is removed.			

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DATA RATES: 7 selectable rates for any of the 4 lines (150-9600). **ELECTRICAL:** EIA standard RS232C with modem control. **VECTOR/ADDRESS SELECTION:** 16 continuous word address for Vector or Address—starting values selected on any boundary.

QUADRACALL™ (4-LINE DN11 REPLACEMENT)

INSTALLS IN: 1 SPC slot, 4 lines at 1 bus load.
PERFORMANCE: Interfaces up to 4 Bell 801 ACU's with Unibus for autodial link-ups. **INPUT/OUTPUT:** 5-input signals from ACU are handled by EIA RS-232 receivers. 6-output signals are transmitted using EIA RS232 drivers. **VECTOR/ADDRESS SELECTION:** Allows selection of device address and vector by use of pencil switches.

ABLE DV/16 (8, 16, 24 or 32-LINE DV11 REPLACEMENT)

INSTALLS IN: <half DV11 space providing byte protocol handling for sync/async communications such as DEC DDMCP, IBM BISYNC, etc. **OPERATING ADVANTAGE:** User may mix sync and async lines in combinations of 4 or 8 lines by on-board switch selection with modem control. Fully software compatible with all DV11 performance features.

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