MacDermid Alpha Releases Systek ETS 1200 Pattern Plating Metallization for Embedded Trace Substrates

(Waterbury, CT USA) – August 6th, 2020 – MacDermid Alpha Electronics Solutions, a global leader in specialty materials for electronics, announces the release of Systek ETS 1200, the latest addition to the Systek family of IC Substrate manufacturing solutions.

Systek ETS 1200 is an advanced DC acid copper pattern plating process specifically formulated to plate fine lines and pads in embedded trace substrates. With the dimensions utilized in panel-level packaging growing ever finer and more electrically demanding, embedded trace technology provides the tight tolerances required to route the circuitry on the organic substrate. Systek ETS 1200 excels at plating the difficult to achieve trace profile tolerances and coplanarity requirements of the die interface outerlayer of the IC substrate. The process is production-proven with dimensions as fine as 7/7 µm lines and spaces and has capability to accommodate further density increases should substrate designers require them. Systek ETS 1200 plates traces with sharp cornered square trace profiles that are encased in build-up film for superior electrical isolation with very low impedance. Copper deposits plated with the Systek ETS 1200 bath exhibit very low internal stress and exceed IPC Class III standards for tensile strength and elongation, creating a durable interconnection with no warpage. Chemical process control is achieved with simple CVS analysis of a 3-component system, ensuring quality and ease of use.

MacDermid Enthone's Systek UVF 100 process for 2 in 1 RDL plating can be combined with the Systek ETS 1200 embedded trace technology, to create extremely high-density coreless buildup layers for panel-level packaging.

For more information on Systek ETS 1200, please visit MacDermidAlpha.com

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