

Errata to MPC184 Security Co-Processor User's Manual— PCI Interface, Rev. 2

This errata describes corrections to the *MPC184 Security Co-Processor User's Manual*, Revision 2. For convenience, the section number and page number of the errata item in the reference manual are provided. Items in bold are new since the last revision of this document.

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Section, Page	Changes
3.1, 3-2	In Table 3-2, change the Identification register address from 01024 to 01020.
8.1.6, 8-9	In Figure 8-9, change the address for the ID Register from 0x01024 to 0x01020.
B.1.6, B-8	In Figure B-9, change the address for the ID Register from 0x01024 to 0x01020.

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Section, Page No.

Changes

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