



TC1100 | TC3 I/O

Using TwinCAT I/O, cyclic data can be collected by different fieldbuses in process images. Cyclic tasks drive the corresponding fieldbuses. Various fieldbuses can be operated with different cycle times on one CPU. Applications can directly access the process image. The fieldbuses and the process images are configured in TwinCAT Engineering.

- provides variable-oriented linkage of I/O devices to tasks
- tasks are variable-oriented among each other
- the smallest unit is one bit
- supports both synchronous and asynchronous relationships
- consistent exchange of data areas and process images
- online display in the directory tree
- online watch window
- "Force and Write" for commissioning and for testing task variables and I/O devices
- supported fieldbuses:
 - EtherCAT
 - Lightbus
 - PROFIBUS DP (master and slave)
 - Interbus
 - CANopen
 - SERCOS interface
 - DeviceNet
 - Ethernet
 - USB
 - SMB (System Management Bus)

Technical data	TC1100
Target system	Windows 7/8/10, Windows CE

Ordering information	
TC1100-0v20	TC3 I/O, platform 20 (Economy)
TC1100-0v30	TC3 I/O, platform 30 (Economy Plus)
TC1100-0v40	TC3 I/O, platform 40 (Performance)
TC1100-0v50	TC3 I/O, platform 50 (Performance Plus)
TC1100-0v60	TC3 I/O, platform 60 (Mid Performance)
TC1100-0v70	TC3 I/O, platform 70 (High Performance)
TC1100-0v80	TC3 I/O, platform 80 (Very High Performance)
TC1100-0v81	TC3 I/O, platform 81 (Many-core 5...8 Cores)
TC1100-0v82	TC3 I/O, platform 82 (Many-core 9...16 Cores)
TC1100-0v83	TC3 I/O, platform 83 (Many-core 17...32 Cores)
TC1100-0v84	TC3 I/O, platform 84 (Many-core 33...64 cores)
TC1100-0v90	TC3 I/O, platform 90 (Other)
TC1100-0v91	TC3 I/O, platform 91 (Other 5...8 Cores)
TC1100-0v92	TC3 I/O, platform 92 (Other 9...16 Cores)
TC1100-0v93	TC3 I/O, platform 93 (Other 17...32 Cores)
TC1100-0v94	TC3 I/O, platform 94 (Other 33...64 Cores)