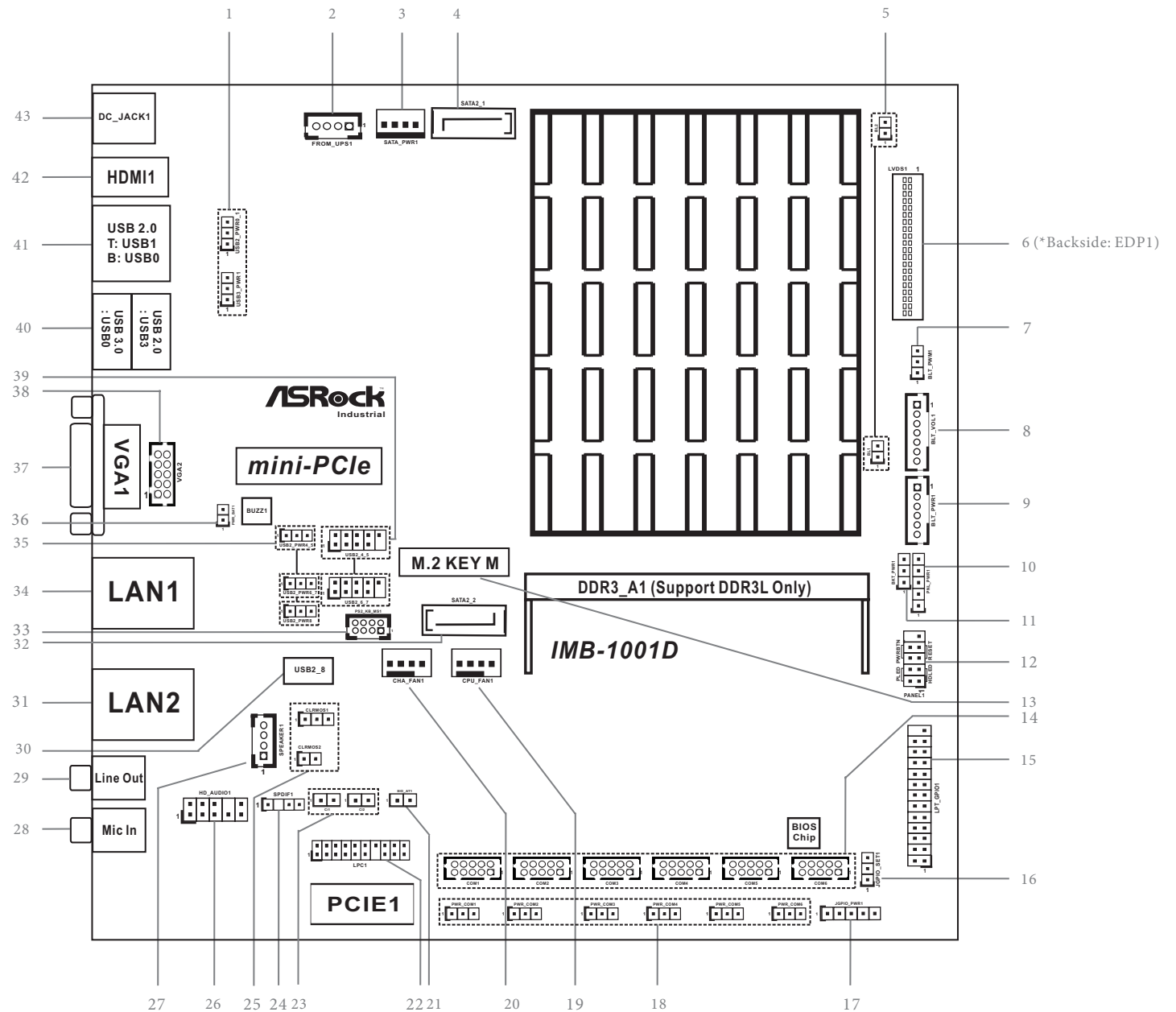


IMB-1001D

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1 : USB Power Setting Jumpers

(USB3_PWR1 (For USB3_0_USB2_3))
(USB2_PWR0_1 (For USB2_0_1))

1-2: +5V
2-3: +5VSB

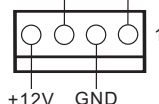


2 : 4-pin DC-in PWR Connector (Input +12V) & UPS Module

Power Output Connector
Pin1 and Pin4: GND
Pin2 and Pin3: DC Input



3 : SATA Power Output Connector



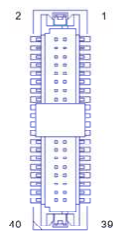
4 : SATA2 Connector (SATA2_1)

5 : BL1, BL2



6 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
2	LCD_VCC	1	LCD_VCC
4	LDLC_CLK	3	+3.3V
6	LVDS_A_DATA0#	5	LDLC_DATA
8	PD (Panel Detection)	7	LVDS_A_DATA0
10	LVDS_A_DATA1	9	LVDS_A_DATA1#
12	LVDS_A_DATA2#	11	GND
14	GND	13	LVDS_A_DATA2
16	LVDS_A_DATA3	15	LVDS_A_DATA3#
18	LVDS_A_CLK#	17	GND
20	GND	19	LVDS_A_CLK
22	LVDS_B_DATA0	21	LVDS_B_DATA0#
24	LVDS_B_DATA1#	23	GND
26	GND	25	LVDS_B_DATA1
28	LVDS_B_DATA2	27	LVDS_B_DATA2#
30	LVDS_B_DATA3#	29	DPLVDD_EN
32	NC	31	LVDS_B_DATA3
34	LVDS_B_CLK	33	LVDS_B_CLK#
36	CON_LBKLT_EN	35	GND
38	LCD_BLT_VCC	37	CON_LBKLT_CTL
40	LCD_BLT_VCC	39	LCD_BLT_VCC



PIN	Signal Name
40	NA
39	LCD_BLT_VCC
38	LCD_BLT_VCC
37	LCD_BLT_VCC
36	LCD_BLT_VCC
35	NA
34	NA
33	CON_LBKLT_CTL
32	CON_LBKLT_EN
31	GND
30	GND
29	GND
28	GND
27	eDP_HPD_CON
26	GND
25	GND
24	GND
23	GND
22	NA
21	LCD_VCC
20	LCD_VCC
19	LCD_VCC
18	LCD_VCC
17	GND
16	eDP_AUX#_CON
15	eDP_TX#_CON
14	GND
13	eDP_TX#0_CON
12	eDP_TX#0_CON
11	GND
10	eDP_TX#1_CON
9	eDP_TX#1_CON
8	GND
7	eDP_TX#2_CON
6	eDP_TX#2_CON
5	GND
4	eDP_TX#3_CON
3	eDP_TX#3_CON
2	GND
1	NA

* PD (Panel Detection): Connect this pin to LVDS Panel's Ground pin to detect Panel detection.

* eDP Connector (Optional)
(on the Backside of PCB)



7 : Backlight Control Level (BLT_PWM1)

1-2: From eDP PWM to CON_LBKLT_CTL
2-3: From LVDS PWM to CON_LBKLT_CTL



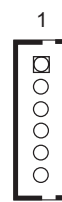
8 : Backlight Volume Control (BLT_VOL1)

PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	BRIGHTNESS_UP
5	BRIGHTNESS_DW
6	GND
7	GND



9 : Backlight Power Connector (BLT_PWR1)

PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC



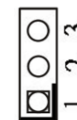
10 : Panel Power Select (LCD_VCC) (PNL_PWR1)

1-2: LCD_VCC: +3V
2-3: LCD_VCC: +5V
4-5: LCD_VCC: +12V



11 : Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)

1-2: LCD_BLT_VCC: +5V
2-3: LCD_BLT_VCC: +12V

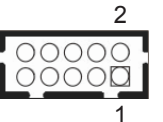


12 : System Panel Header

GND	RESET#
PWRBTN#	GND
PLED-	HDLED-
PLED+	HDLED+

13 : M.2 KEY M (M2)

Pin#	Signal#	Signal#	Pin#
1#	GND#	+3.3V#	2#
3#	GND#	+3.3V#	4#
5#	NA#	NA#	6#
7#	NA#	NA#	8#
9#	GND#	NA	10#
11#	NA#	+3.3V#	12#
13#	NA#	+3.3V#	14#
15#	GND#	+3.3V#	16#
17#	NA#	+3.3V#	18#
19#	NA#	NA#	20#
21#	GND#	NA#	22#
23#	NA#	NA#	24#
25#	NA#	NA#	26#
27#	GND#	NA#	28#
29#	NA#	NA#	30#
31#	NA#	NA#	32#
33#	GND#	NA#	34#
35#	NA#	NA#	36#
37#	NA#	NA	38#
39#	GND#	SMB_CLK#	40#
41#	SATA_RX	SMB_DATA#	42#
43#	SATA_RX#	NA#	44#
45#	GND#	NA#	46#
47#	SATA_TX#	NA#	48#
49#	SATA_TX	NA	50#
51#	GND#	NA	52#
53#	NA	NA	54#
55#	NA	NA#	56#
57#	GND#	NA#	58#
67#	NA#	NA	68#
69#	GND	+3.3V#	70#
71#	GND#	+3.3V#	72#
73#	GND#	+3.3V#	74#
75#	GND#	#	#

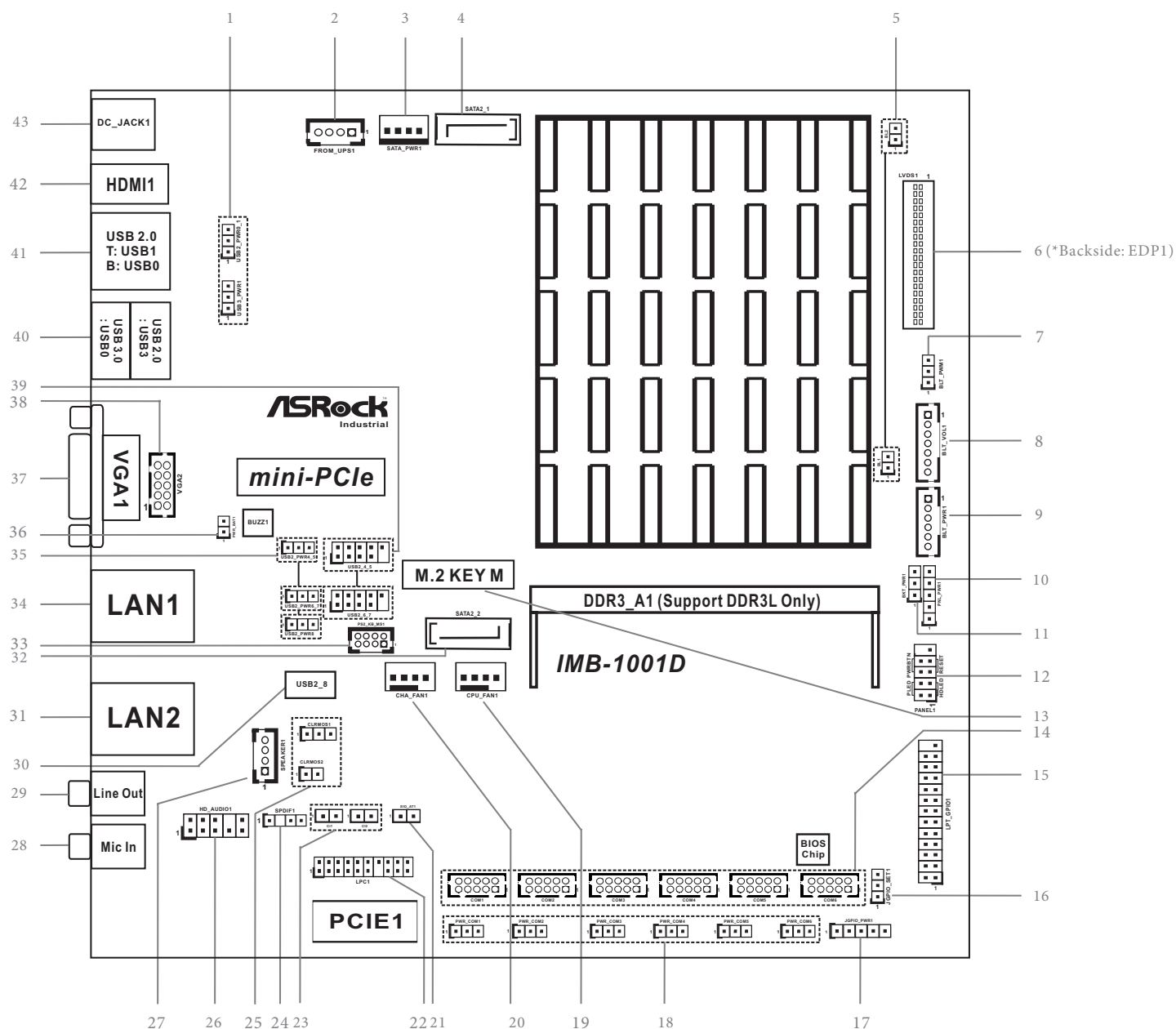
14 : Internal COM Port Headers
COM3 (RS232/422/485)
COM1, 2, 4, 5, 6 (RS232)

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	N/A	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	PWR	7	RRTS#	5	GND	3	TTXD	1	DDCD#

* This motherboard supports RS232/422/485 on COM3 port. Please refer to below table for the pin definition. In addition, COM3 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

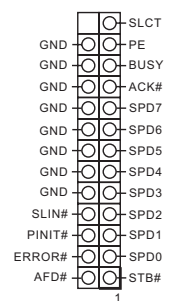
COM3 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	PWR	PWR	PWR
10	N/A	N/A	N/A



15 : Printer Port / GPIO Header (LPT_GPIO1)

Printer Port:



GPIO:

PIN	Signal Name	PIN	Signal Name
26	NC	25	NA
24	GND	23	SIO_GP30
22	GND	21	SIO_GP31
20	GND	19	SIO_GP32
18	GND	17	SIO_GP33
16	GND	15	SIO_GP34
14	GND	13	SIO_GP35
12	JGPIOPWR	11	SIO_GP36
10	JGPIOPWR	9	SIO_GP37
8	SIO_GP43	7	SIO_GP40
6	SIO_GP44	5	SIO_GP41
4	SIO_GP45	3	SIO_GP42
2	SIO_GP46	1	SIO_GP47

* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).

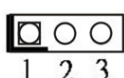
16 : Digital Input / Output Default Value Setting (JGPIO_SET1)
1-2: Pull-High
2-3: Pull-Low



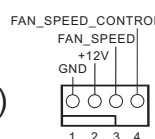
17 : Digital Input / Output Power Select (JGPIOPWR) (JGPIO_PWR1)
1-2: +12V
2-3: +5V
4-5: GND



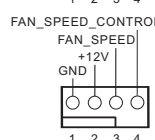
18 : COM Port Pin9 PWR Setting Jumpers
PWR_COM1 ~ 6 (For COM Port1 ~ 6)
1-2: +5V
2-3: +12V



19 : 4-Pin CPU FAN Connector (+12V)



20 : 4-Pin Chassis FAN Connector (+12V) (Optional)

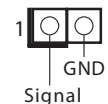


21 : SIO_AT1
Open: ATX Mode
Short: AT Mode

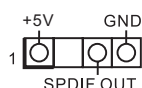


22 : LPC Header

23 : Chassis Intrusion Headers
CI1:
Close: Active Case Open
Open: Normal
CI2:
Close: Normal
Open: Active Case Open

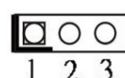


24 : SPDIF Header
1-2: +5V
2-3: SPDIF OUT



25 : Clear CMOS Headers

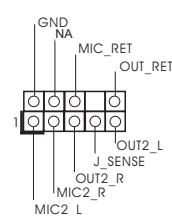
CLRMOS1 :
1-2: Normal
2-3: Clear CMOS
CLRMOS2 :
Open: Normal
Short: Auto Clear CMOS (Power Off)



26 : Front Panel Audio Header

27 : 3W Audio AMP Output Wafer

PIN	Signal Name
4	OUTRN
3	OUTRP
2	OUTLP
1	OUTLN



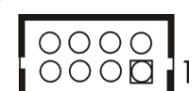
28 : Audio Output: Pink – Mic In
29 : Audio Output: Green – Line Out

30 : USB2.Port (USB2_8)

31 : RJ45 LAN Port (LAN2)

32 : SATA2 Connector (SATA2_2)
(Shared with M.2 KEY M (M2))

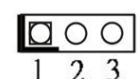
33 : PS2 Keyboard/Mouse Header



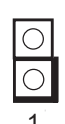
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
8	GND	6	GND	4	USB2_PWR6_7	2	USB2_PWR6_7
7	MSCLK	5	MSDATA	3	KBDATA	1	KBCLK

34 : RJ45 LAN Port (LAN1)

35 : USB Power Setting Jumpers
(USB2_PWR4_5 (For USB2_4_5))
(USB2_PWR6_7 (For USB2_6_7))
(USB2_PWR8 (For USB2_8))
1-2: +5V
2-3: +5VSB



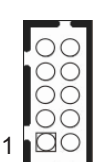
36 : PWR_BAT1
Open: Normal
Short: Charge Battery



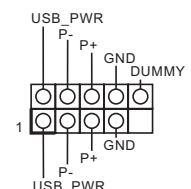
37 : VGA Port

38 : VGA Header (VGA2)

PIN	Signal Name	PIN	Signal Name
9	DDC_CLK	10	DDC_DATA
7	HSYNC	8	VSYNC
5	BLUE	6	GND
3	GREEN	4	GND
1	RED	2	GND



39 : USB2.0 Connectors
(USB2_4_5, USB2_6_7)



40 : Top : USB2.0 Port (USB2_3)
Bottom : USB3.0 Port (USB3_0)
41 : USB2.0 Ports (USB2_0_1)
42 : HDMI Port
43 : DC Jack