



Thank you for the opportunity to review your EM35xx hardware design. In order for us to provide a timely and complete review of your design, please fill out the following questionnaire.

Design Questions:

1. Have you reviewed our EM35xx reference designs? ☐ Yes ☐ No
 - o If yes, which designs?

EM358x, USB, 4-layer, Inverted-F Antenna	EM35xx, 4-layer, Ceramic Balun	RD-0001-0201, EM3588, Wi-Fi/Eth Gateway
EM35xx, 2-layer, Chip Antenna	EM35xx, 4-layer, SE2432L FEM	RD-0002-0201, EM3588, Virtual Gateway
EM35xx, 2-layer, Inverted-F Antenna	EM35xx, 4-layer, RF6525 FEM	RD-0020-0601, EM357, Lighting
EM359x, USB, 4-layer, Ceramic Balun	RD-0035-0601, EM3585, Lighting	RD-0030-0201, EM3587, Contact Sensor
RD-0039-0201, EM3581, Capsense Dimmer	☐ Other (name of reference design) _____	
 - o Name of Silicon Labs contact or partner who provided reference design _____
2. Have you reviewed [AN698 \(PCB Design with EM35x\)](#)? ☐ Yes ☐ No
3. Which EM35xx device(s) are you using in this design? _____
4. Which ZigBee application is this design (HA, SE, ZCL, RF4CE, etc.)? _____
5. Is this design using an OEM module or is this a discrete design? ☐ OEM Module ☐ Discrete
6. Is your design using the EM35xx device as a system-on-chip (SoC), or as a network co-processor (NCP) with a host MCU?
☐ SoC ☐ NCP (EZSP SPI) ☐ NCP (EZSP UART) ☐ NCP over USB
 - o If NCP, what is the part number of the host which is connected to the EM35xx? _____
 - o Are you using customizable extended NCP (xNCP)? ☐ Yes ☐ No
7. Does your design include a front end module (FEM) with PA and/or LNA? ☐ Yes ☐ No
 - o If yes, which FEM is connected to the EM35xx? _____
8. What region(s) of the world is this product intended to be sold/deployed (check all that apply)?
☐ USA ☐ Europe ☐ China ☐ India ☐ Japan ☐ Other _____
9. What compliance/regulatory standards does your design have to meet? ☐ FCC ☐ CE ☐ ETSI ☐ Other _____
10. Are there other radios used in this design (Wi-Fi, BTLE, sub-GHz ZigBee, etc.)? ☐ Yes ☐ No
 - o If yes, please list the frequency bands (169/434/868/915MHz, 2.4GHz, etc.) _____
11. How are you using the serial controllers?

☐ SC1	☐ SPI	☐ UART	☐ I2C/TWI	SC1 Peripheral: _____
☐ SC2	☐ SPI	☐ I2C/TWI	☐ USB	SC2 Peripheral: _____
☐ SC3 (EM359x)	☐ SPI	☐ UART	☐ I2C/TWI	SC3 Peripheral: _____
☐ SC4 (EM359x)	☐ SPI	☐ I2C/TWI		SC4 Peripheral: _____
12. What other peripherals are connected to the EM35x GPIO (ADC, timer, LED, button, sensor, etc.)?

13. Is your design powered by battery or line powered? ☐ Battery Powered ☐ Line Powered
 - o If battery powered, please list type of battery _____
14. Are you using any other Silicon Labs devices in this design? ☐ Yes ☐ No
 - o If yes, please list the devices _____
15. Please provide any additional information about your design that will help aid our team with the review.

Deliverable Format:

Please note that the following file formats are requested for design reviews:

- Schematic Review – PDF
- Layout Review – Gerbers (including paste mask and solder mask layers)

Note: An update of an existing design which has already been reviewed should be submitted to the same portal case as the original design in order for proper traceability and comparison between designs.