

DDR3 / DDR3L CONTROLLER

MDQ_00	M3
MDQ_01	J4
MDQ_02	N4
MDQ_03	K3
MDQ_04	N3
MDQ_05	G3
MDQ_06	L3
MDQ_07	H3
MDQS_0	J3
MDQS_0	M4
MDM_0	
	H2
MDQ_08	L1
MDQ_09	G1
MDQ_10	M2
MDQ_11	H1
MDQ_12	N1
MDQ_13	J2
MDQ_14	M1
MDQ_15	J1
MDQS_1	K1
MDQS_1	L2
MDM_1	
	R4
MDQ_16/MECC_0	V4
MDQ_17/MECC_1	R3
MDQ_18/MECC_2	W4
MDQ_19/MECC_3	T4
MDQ_20/MECC_4	Y2
MDQ_21/MECC_5	P3
MDQ_22/MECC_6	V3
MDQ_23/MECC_7	T3
MDQS_2	U3
MDQS_2	W3
MDM_2	
	R2
MDQ_24	V2
MDQ_25	P1
MDQ_26	W1
MDQ_27	R1
MDQ_28	Y1
MDQ_29	P2
MDQ_30	U2
MDQ_31	T1
MDQS_3	U1
MDQS_3	V1
MDM_3	
	D3
MA_00	D7
MA_01	B6
MA_02	B3
MA_03	A2
MA_04	C7
MA_05	A5
MA_06	A3
MA_07	C6
MA_08	B5
MA_09	D1
MA_10	A4
MA_11	D5
MA_12	A6
MA_13	C3
MA_14	A7
MA_15	
	C4
MBA_0	C5
MBA_1	D4
MBA_2	
	E2
MODT0	E1
MODT1	E3
MCKE_0	C1
MCKE_1	B1
MCS_0	C2
MCS_1	A8
MCS_2	B8
MCS_3	
	F4
MWE	F1
MRAS	F2
MCAS	
	G4
MCK	F3
MCK	
	D8
MDIC_0	C8
MDIC_1	
	J7
MVREF	

## IFC/eSDHC/ULPI

IFC_AD_00/CFG_SYS_PLL_0	P21
IFC_AD_01/CFG_SYS_PLL_1	R22
IFC_AD_02/CFG_SYS_PLL_2	T23
IFC_AD_03/CFG_CORE_PLL_0	T22
IFC_AD_04/CFG_CORE_PLL_1	U23
IFC_AD_05/CFG_CORE_PLL_2	U22
IFC_AD_06/CFG_CORE_SPEED	V23
IFC_AD_07/CFG_DDR_PLL_0	V22
IFC_AD_08/CFG_DDR_PLL_1	R21
IFC_AD_09/CFG_IFC_PB_0	R20
IFC_AD_10/CFG_IFC_PB_1	T21
IFC_AD_11/CFG_IFC_PB_2	T20
IFC_AD_12/CFG_SRDS_REFCLK	U21
IFC_AD_13/CFG_IO_PORTS_0	V21
IFC_AD_14/CFG_IO_PORTS_1	V20
IFC_AD_15/CFG_IFC_ADM	W20
IFC_ADDR_16/SDHC_CLK/USB_CLK/IFC_CS_2	W23
IFC_ADDR_17/SDHC_CMD/USB_D_0/DMA_DREQ_1	W21
IFC_ADDR_18/SDHC_DATA_0/USB_D_1/DMA_DACK_1	Y23
IFC_ADDR_19/SDHC_DATA_1/USB_D_2/DMA_DDONE_1	Y22
IFC_ADDR_20/SDHC_DATA_2/USB_D_3	AA23
IFC_ADDR_21/SDHC_DATA_3/USB_D_4	Y21
IFC_ADDR_22/SDHC_WP/USB_D_5	AB23
IFC_ADDR_23/SDHC_CD/USB_D_6	AA22
IFC_ADDR_24/USB_D_7	AC23
IFC_CS_0	AA21
IFC_CS_1	AB21
IFC_CLK_0	Y18
IFC_CLK_1/USB_NXT/IFC_CS_3	AC18
IFC_BCTL/CFG_BOOT_SEQ_0	AA19
IFC_AVD/CFG_DRAM_TYPE	AC22
IFC_CLE/CFG_HOST_AGT_0	AA20
IFC_OE/CFG_HOST_AGT_1	AB20
IFC_WP	AC20
IFC_WE/CFG_IFC_FLASH_MODE	AC21
IFC_RB	Y19
IFC_PAR_0/USB_STP	AC19
IFC_PAR_1/CFG_PLAT_SPEED	AB18
IFC_PERR/USB_DIR	AA18

SD1

SD1_TX_0	AB9
SD1_TX_0	AC9
SD1_TX_1	Y10
SD1_TX_1	AA10
SD1_TX_2	Y14
SD1_TX_2	AA14
SD1_TX_3	AB15
SD1_TX_3	AC15
SD1_RX_0	Y8
SD1_RX_0	AA8
SD1_RX_1	AB11
SD1_RX_1	AC11
SD1_RX_2	AB13
SD1_RX_2	AC13
SD1_RX_3	Y16
SD1_RX_3	AA16
SD1_REF_CLK	AA12
SD1_REF_CLK	Y12
SD1_PLL_TPD	U11
SD1_PLL_TPA	U13
SD1_IMP_CAL_TX	U14
SD1_IMP_CAL_RX	U10

SD2

SD2_TX_0	C14
SD2_TX_0	D14
SD2_TX_1	C10
SD2_TX_1	D10
SD2_RX_0	A13
SD2_RX_0	B13
SD2_RX_1	A11
SD2_RX_1	B11
SD2_REF_CLK	C12
SD2_REF_CLK	D12
SD2_PLL_TPD	G13
SD2_PLL_TPA	G11
SD2_IMP_CAL_TX	G10
SD2_IMP_CAL_RX	G14

eTSEC/IEEE1588

TSEC1\_TXD\_0/1588\_ALARM\_OUT1/CFG\_ROM\_LOC[0]  
 TSEC1\_TXD\_1/1588\_ALARM\_OUT2/CFG\_ROM\_LOC[1]  
 TSEC1\_TXD\_2/1588\_PULSE\_OUT1/CFG\_ROM\_LOC[2]  
 TSEC1\_TXD\_3/1588\_PULSE\_OUT2/CFG\_ROM\_LOC[3]  
 TSEC1\_TX\_EN/CFG\_SVR  
 TSEC1\_GTX\_CLK/DMA\_DDONE\_0

AC5  
 AC1  
 AA5  
 AA4  
 AC6  
 AA3

TSEC1\_RXD\_0/1588\_TRIG\_IN1  
 TSEC1\_RXD\_1/1588\_TRIG\_IN2/GPIO\_12  
 TSEC1\_RXD\_2/1588\_CLK\_IN  
 TSEC1\_RXD\_3/1588\_CLK\_OUT  
 TSEC1\_RX\_DV/DMA\_DREQ\_0\_B/GPIO\_13  
 TSEC1\_RX\_CLK/DMA\_DACK\_0/GPIO\_14

AC4  
 AB4  
 AB3  
 AC3  
 AB6  
 Y6

TSEC1\_GTX\_CLK125/GPIO\_15

Y5

MANAGEMENT INTERFACE

EC\_MDIO  
 EC\_MDC/CFG\_CPU\_BOOT

AC2  
 AA6

	SCAN_MODE	H21
		H22
	MISCELLANEOUS	TDI
		TDO
		TCK
JTAG		TMS
		TRST
		IRQ_0
		IRQ_1
		IRQ_2/TRIG_IN
		IRQ_3/SRESET/TMP_DETECT
		GPIO_0/IRQ_4/DRVVBUS/MDVAL
IRQ		GPIO_1/IRQ_5/VBUSPWRFAULT/MSRCID_0
		GPIO_2/IRQ_6/CKSTP_IN/MSRCID_1
		GPIO_3/IRQ_7/CKSTP_OUT/MSRCID_2
		GPIO_4/IRQ_8/MCP/MSRCID_3/CLK_OUT
		GPIO_5/IRQ_9/UDE/MSRCID_4
		READY/TRIG_OUT/ASLEEP
		IRQ_OUT
		SYSCLK
		RTC
CLK		USBPHY_CLK
		HRESET
		HRESET_REQ/CFG_SB_DIS
		SPI_MOSI/GPIO_6
		SPI_MISO/GPIO_7
		SPI_CLK/GPIO_8
		SPI_CS0/GPIO_9
		IIC1_SDA
		IIC1_SCL
		IIC2_SDA
		IIC2_SCL
		UART_SIN_0
		UART_SOUT_0
		UART_CTS_0
		UART_RTS_0/CFG_IFC_ECC_0
		UART_SIN_1
		UART_CTS_1/GPIO_10/TDM_TX_CLK/IRQ_10
		UART_SOUT_1/CFG_IFC_ECC_1
		UART_RTS_1/GPIO_11/IRQ_11/TDM_RX_CLK
		CAN1_TX/UART_SOUT_2/TDM_TX_DATA/CFG_BOOT_SEQ_1
		CAN1_RX/UART_SIN_2/TDM_RX_DATA
		CAN2_TX/UART_SOUT_3/TDM_TFS
		CAN2_RX/UART_SIN_3/TDM_RFS
		UDP
		UDM
		NC_N23
		VBUSCLMP
		IBIAS_REXT

U?F			
T17	BVDD1	AVDD_DDR	N17
U16	BVDD2		
U17	BVDD3	AVDD_PLAT	P17
U20	BVDD4		
AB19	BVDD5	AVDD_CORE	H9
G17		SD1_AVDD	U12
H17	BVDD_VSEL_0		
	BVDD_VSEL_1	SD2_AVDD	G12
G7			R17
H7	GVDD1	VDD23	H8
K7	GVDD4	VDD3	J8
L7	GVDD5	VDD8	K8
M7	GVDD6	VDD11	L8
N7	GVDD7	VDD14	M8
P7	GVDD8	VDD16	N8
R7	GVDD9	VDD18	P8
T7	GVDD10	VDD20	R8
G8	GVDD11	VDD33	T8
G9	GVDD2	VDD24	T9
	GVDD3	VDD25	U9
		VDD30	H10
U7	LVDD1	VDD4	T10
U8	LVDD2	VDD26	H11
AB5	LVDD3	VDD5	H14
		VDD6	T14
		VDD27	G15
C17	OVDD1	VDD1	H15
C22	OVDD2	VDD7	T15
D20	OVDD3	VDD28	U15
G20	OVDD4	VDD31	G16
J20	OVDD5	VDD2	H16
		VDD32	J16
		VDD9	K16
T13	S1VDD1	VDD12	L16
Y7	S1VDD2	VDD15	M16
Y13	S1VDD3	VDD17	N16
AA11	S1VDD4	VDD19	P16
AA17	S1VDD5	VDD21	R16
AB7	S1VDD6	VDD22	T16
AB10	S1VDD7	VDD29	J17
AB12	S1VDD8	VDD10	K17
AB14	S1VDD9	VDD13	
A10	S2VDD1		L17
A12	S2VDD2	USBVDD1_0_1	M17
C11	S2VDD3	USBVDD1_0_2	
C15	S2VDD4		J21
H13	S2VDD5	USBVDD1_8_1	K21
		USBVDD1_8_2	
T11	X1VDD1		
Y9	X1VDD2	USBVDD3_3_1	K20
Y15	X1VDD3	USBVDD3_3_2	L20
AB8	X1VDD4		
AB16	X1VDD5		
A14	X2VDD1		
B9	X2VDD2		
C9	X2VDD3		
C13	X2VDD4		

**POWER1**

<Value>

U?G			
Y11	S1VSS1	VSS70	AB22
Y17	S1VSS2	VSS66	W22
AB17	S1VSS3	VSS73	G22
AC10	S1VSS4	VSS8	B22
AA13	S1VSS5	VSS68	Y20
AC14	S1VSS6	VSS54	P20
AA7	S1VSS7	VSS7	B19
AC12	S1VSS8	VSS72	AC17
		VSS6	B17
		VSS61	R15
B10	S2VSS1	VSS79	P15
B12	S2VSS2	VSS47	N15
D11	S2VSS3	VSS39	M15
D15	S2VSS4	VSS34	L15
		VSS26	K15
T12	SD1_AVSS	VSS18	J15
H12	SD2_AVSS	VSS4	A15
		VSS60	R14
J23	USBVSS1	VSS78	P14
K22	USBVSS2	VSS46	N14
L22	USBVSS3	VSS77	M14
M21	USBVSS4	VSS33	L14
M23	USBVSS5	VSS25	K14
		VSS17	J14
		VSS59	R13
AA9	X1VSS1	VSS53	P13
AA15	X1VSS2	VSS45	N13
AC8	X1VSS3	VSS76	M13
AC16	X1VSS4	VSS32	L13
		VSS24	K13
		VSS16	J13
B14	X2VSS1	VSS58	R12
B15	X2VSS2	VSS52	P12
D9	X2VSS3	VSS44	N12
D13	X2VSS4	VSS38	M12
		VSS31	L12
		VSS23	K12
		VSS15	J12
		VSS57	R11
		VSS51	P11
		VSS43	N11
		VSS37	M11
AA2	SENSEVDD	VSS30	L11
		VSS22	K11
Y3	SENSEVSS	VSS14	J11
		VSS56	R10
		VSS50	P10
AB1	NC_AB1	VSS42	N10
AA1	NC_AA1	VSS36	M10
		VSS29	L10
		VSS21	K10
		VSS13	J10
		VSS55	R9
		VSS49	P9
		VSS41	N9
		VSS35	M9
		VSS28	L9
		VSS20	K9
		VSS75	J9
		VSS2	A9
		VSS71	AC7
		VSS5	B7
		VSS10	D6
		VSS67	Y4
		VSS64	U4
		VSS48	P4
		VSS27	L4
		VSS74	H4
P22	POVDD1	VSS11	E4
N20	POVDD2	VSS3	B4
N21	FA_VDD	VSS69	AB2
		VSS65	W2
		VSS63	T2
		VSS40	N2
		VSS19	K2
		VSS12	G2
N22	NC_N22	VSS9	D2
B2	NC_B2	VSS1	A1
			R23

<Value>