

Results of the IEC 61508 Functional Safety Assessment

Project:

2051 Pressure Transmitter with 4-20mA HART Device Label SW 1.0.0-1.4.x

Customer:

Emerson Automation Solutions (Rosemount, Inc.)
Shakopee, MN
USA

Contract No.: Q18/11-012 Report No.: ROS 11/07-062 R005 Version V4, Revision R0, August 26, 2019 Dave Butler



Management Summary

The Functional Safety Assessment of the Rosemount 2051 Pressure Transmitter with 4-20mA HART development project, performed by *exida* consisted of the following activities:

- exida assessed the development process used by Rosemount through an audit and review
 of a detailed safety case against the exida certification scheme which includes the relevant
 requirements of IEC 61508. The assessment was executed using subsets of the IEC 61508
 requirements tailored to the work scope of the development team.
- *exida* reviewed and assessed a detailed Failure Modes, Effects, and Diagnostic Analysis (FMEDA) of the devices to document the hardware architecture and failure behavior.
- exida reviewed field failure data to verify the accuracy of the FMEDA analysis.
- exida reviewed the manufacturing quality system in use at Rosemount.

The functional safety assessment was performed to the SIL 3 requirements of IEC 61508:2010. A full IEC 61508 Safety Case was created using the *exida* Safety Case tool, which also was used as the primary audit tool. Hardware and software process requirements and all associated documentation were reviewed. Environmental test reports were reviewed. The user documentation and safety manual also were reviewed.

The results of the Functional Safety Assessment can be summarized by the following statements:

The audited development process, as tailored and implemented by the Rosemount 2051 Pressure Transmitter with 4-20mA HART development project, complies with the relevant safety management requirements of IEC 61508 SIL 3.

The assessment of the FMEDA, done to the requirements of IEC 61508, has shown that the 2051 Pressure Transmitter with 4-20mA HART can be used in a low demand safety related system in a manner where the PFD_{AVG} is within the allowed range for SIL 3 according to table 2 of IEC 61508-1.

The assessment of the FMEDA also shows that the 2051 Pressure Transmitter with 4-20mA HART meets the requirements for architectural constraints of an element such that it can be used to implement a SIL 2 safety function (with HFT = 0) or a SIL 3 safety function (with HFT = 1), per Route1_H for models where the SFF \geq 90% and Route 2_H for all models.

This means that the 2051 Pressure Transmitter with 4-20mA HART is capable for use in SIL 2 and SIL 3 applications in Low demand mode when properly designed into a Safety Instrumented Function per the requirements in the Safety Manual and when using the versions specified in section 3.1 of this document.



The manufacturer is entitled to use these Functional Safety Logos.







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1 Purpose and Scope

This document describes the IEC 61508 functional safety assessment of the:

2051 Pressure Transmitter with 4-20mA HART

by *exida* according to the accredited *exida* certification scheme which includes the requirements of IEC 61508:2010.

The purpose of the assessment was to evaluate the compliance of:

- the 2051 Pressure Transmitter with 4-20mA HART with the technical IEC 61508-2 and -3 requirements for SIL 3 and the derived product safety property requirements

and

- the 2051 Pressure Transmitter with 4-20mA HART development processes, procedures and techniques as implemented for the safety-related deliveries with the managerial IEC 61508-1, -2 and -3 requirements for SIL 3.

and

- the 2051 Pressure Transmitter with 4-20mA HART hardware analysis represented by the Failure Mode, Effects and Diagnostic Analysis with the relevant requirements of IEC 61508-2.

The assessment has been carried out based on the quality procedures and scope definitions of *exida*.

The results of this assessment provide the safety instrumentation engineer with the required failure data per IEC 61508 / IEC 61511 and confidence that sufficient attention has been given to systematic failures during the development process of the device.

1.1 Tools and Methods used for the assessment

This assessment was carried out using the *exida* Safety Case tool. The Safety Case tool contains the *exida* scheme which includes all the relevant requirements of IEC 61508.

For the fulfillment of the objectives, expectations are defined which builds the acceptance level for the assessment. The expectations are reviewed to verify that each single requirement is covered. Because of this methodology, comparable assessments in multiple projects with different assessors are achieved. The arguments for the positive judgment of the assessor are documented within this tool and summarized within this report.

The assessment was planned by *exida* agreed with Rosemount (see [R2]).

All assessment steps were continuously documented by *exida* (see [R1])



2 Project Management

2.1 exida

exida is one of the world's leading accredited Certification Bodies and knowledge companies, specializing in automation system safety and availability with over 500 years of cumulative experience in functional safety. Founded by several of the world's top reliability and safety experts from assessment organizations and manufacturers, exida is a global company with offices around the world. exida offers training, coaching, project-oriented system consulting services, safety lifecycle engineering tools, detailed product assurance, cyber-security and functional safety certification, and a collection of on-line safety and reliability resources. exida maintains a comprehensive failure rate and failure mode database on process equipment based on 250 billion hours of field failure data.

2.2 Roles of the parties involved

Emerson Automation Solutions Manufacturer of the 2051 Pressure Transmitter with 4-20mA

(Rosemount, Inc.) HART

exida Performed the hardware assessment [R3]

exida Performed the Functional Safety Assessment [R1] per the

accredited exida scheme.

Rosemount contracted *exida* with the IEC 61508 Functional Safety Assessment of the above-mentioned devices.

2.3 Standards / Literature used

The services delivered by *exida* were performed based on the following standards / literature.

[N1]	IEC 61508 (Parts 1 – 7):	Functional Safety of Electrical/Electronic/Programmable
	2010	Electronic Safety-Related Systems

2.4 Reference documents

2.4.1 Documentation provided by Rosemount

[D1]	{D01}	Functional Safety Management Plan	
[D2]	{D02a}	CM Plan checklist from EDP 400-300	
[D3]	{D07}	Project Plan	
[D4]	{D08}	Project Defined Process Documents	
[D5]	{D10}	DOP 1810 Training Procedures	
[D6]	{D100}	Integration Test Results	
[D7]	{D11}	Safety Competencies	
[D8]	{D110}	EMC Test Results	
[D9]	{D111}	Validation Test Results	
[D10]	{D111a}	ROS Validation Testing Checklist	



D111 D112 D113 Temperature test results D131 D12 EDP 400-502 Peer Safety Review D141 D13 Training and Competency Matrix D151 D144 D13 Safety Instrumented Systems Training Program D161 D161 DOP 7 Rosemount Product Development Process D171 D160a Product Safety Manual for 2051 D181 D161a WA0007 Safety Manual Checklist D191 D167 Product Approvals D201 D168 Product Release Version Desscription D211 D16a RMD G7.3-0001 Product Realization: Project Management Process D221 D17 DOP 415 Product Design and Development Process D222 D17 DOP 440 Engineering Change Procedure D231 D24 D25 D20 ISO 9001:2008 Certificate D25 D20 ISO 9001:2008 Certificate D26 D27 D22 DP-50111-16 Field Return Analysis Procedure D28 D29 D24 EDP 400-300 Configuration and Change Control Management Plan D301 D25 EDP 400-300 Configuration Anagement Plan D303 D26 D06 D0P 660 Supplier Corrective Action D331 D28 D09 EDP400-600 Quality_Assurance_Procedure D332 D28 D09 EDP400-600 Quality_Assurance_Procedure D333 D30 Safety Integrity Requirements Specification D334 D35 Safety Validation Test Plan D35 D400 Safety Validation Plan Review D36 D37 Safety Validation Plan Review D39 D35 Validation Test Plan D40 D400 Architecture Design Description Document D41 D400 Architecture Design Description Document D41 D400 CTT Platform Electronics Architecture D41 D400 CTT Platform Electronics Architecture D44 D400 System Requirements		I	·
D13	[D11]	{D112}	Humidity Test results
D14	[D12]	{D113}	Temperature test results
D15	[D13]	{D12}	EDP 400-502 Peer Safety Review
D16	[D14]	{D13}	Training and Competency Matrix
D17	[D15]	{D14}	Safety Instrumented Systems Training Program
D161a D161a WA0007 Safety Manual Checklist D19 (D167) Product Approvals Product Release Version Desscription D168 Product Release Version Desscription RMD_G7.3-0001 Product Realization: Project Management Process D17 D0P 415 Product Design and Development Process D18 D0P 440 Engineering Change Procedure D19 D0P 1110 Metrology Procedure D19 D19 D19 D19 D1110 Metrology Procedure D19	[D16]	{D16}	DOP 7 Rosemount Product Development Process
D19	[D17]	{D160a}	Product Safety Manual for 2051
D20	[D18]	{D161a}	WA0007 Safety Manual Checklist
D21 {D16a}	[D19]	{D167}	Product Approvals
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[D43] {D40a} C/T Platform Electronics Architecture	[D41]	{D38}	Master Test Plan
	[D42]	{D40}	Architecture Design Description Document
[D44] {D40b} System Requirements	[D43]	{D40a}	C/T Platform Electronics Architecture
	[D44]	{D40b}	System Requirements



[D45]	{D41}	Integration Test Plan
[D46]	{D50}	Detailed Design Description
[D47]	{D53}	Fault Injection Test Plan/Results
[D48]	{D55}	Schematics
[D49]	{D56}	BOM
[D50]	{D57}	HW Component Derating analysis
[D51]	{D58}	HW Verification
[D52]	{D59}	BOM history
[D53]	{D60}	HW Design Guidelines for Test and Manufacture
[D54]	{D61}	HW Requirements Review
[D55]	{D62}	Assembly Drawing
[D56]	{D69}	Hardware Design Phase Verification Checklist
[D57]	{D71}	Detailed Software Design Specification
[D58]	{D73}	SIRS-SW Design Traceability
[D59]	{D78}	SW Architecture Design Review
[D60]	{D79}	Software Architecture and Design Phase Review Log (with review of sw architecture and design checklist)
[D61]	{D81}	WA0007 SIS Checklists
[D62]	{D82}	Software Tools Analysis
[D63]	{D83}	PIU Assessment; IAR Compiler
[D64]	{D90}	PC Lint Configuration file
[D65]	{D90a}	PC Lint resolution example
[D66]	{D90b}	Code Review example
[D67]	{D90c}	PC Lint Results
[D68]	{D91}	Unit Test Records - HW
[D69]	{D92}	Unit Test - SW test plan
[D70]	{D92a}	SW unit test results
[D71]	{D92b}	Test objectives in header file
[D72]	{D92c}	Test objectives in source file
[D73]	{D92d}	Test Techniques to use to develop test plans
[D74]	{D93}	sw module_size_justification
[D75]	{D94}	sw module_test_coverage
[D76]	{D97}	Software DVT Test Plan
[D77]	{D97a}	SW test descriptions
[D78]	{D99a}	Action Items
[D79]	{D127}	Sprint_backlog



[D80]	{D169}	SHA-1 Hash Code for 2051 Pressure Transmitter

2.4.2 Documentation generated by exida

[R1]	Rosemount Pressure Transmitter 2051	Detailed Safety Case documenting results of assessment (internal document, updated)
[R2]	Q19-05-140 r1 Recertification 2051 3051 3051S 3051SDA2 Proposal	Assessment Plan
[R3]	ROS 1105075 R001 V1R3 Remote Seal FMEDA_Rosemount.doc or later	Rosemount 1199 Remote Seal FMEDA Report; April 29, 2013
[R4]	ROS 1304008 R001 V1R0 Primary Elements FMEDA_Rosemount or later	Rosemount Primary Elements FMEDA Report; June 16, 2013

2.5 Assessment Approach

The certification audit was closely driven by requirements of the *exida* scheme which includes subsets filtered from IEC 61508.

The assessment was planned by *exida* and agreed with Rosemount.

The following IEC 61508 objectives were subject to detailed auditing at Rosemount:

- FSM planning, including
 - Safety Life Cycle definition
 - Scope of the FSM activities
 - Documentation
 - Activities and Responsibilities (Training and competence)
 - Configuration management
 - Tools and languages
- Safety Requirement Specification
- Change and modification management
- Software architecture design process, techniques and documentation
- Hardware architecture design process, techniques and documentation
- Hardware design / probabilistic modeling
- Hardware and system related V&V activities including documentation, verification
 - Integration and fault insertion test strategy
- Software and system related V&V activities including documentation, verification
- System Validation including hardware and software validation
- Hardware-related operation, installation and maintenance requirements

The project teams, not individuals were audited.



3 Product Description

The 2051 Pressure Transmitter with 4-20mA HART is available in a wide configuration of solutions to meet the most demanding application needs.

The major components of the 2051 Pressure Transmitter are the sensor module and the electronics housing. The sensor module contains the oil filled sensor system and the sensor electronics. The sensor electronics are installed within the sensor module. The electrical signals from the sensor module are transmitted to the output electronics in the electronics housing and ultimately to the terminal block for connection to the host system. The basic block diagram of the Rosemount differential Coplanar measurement type is shown in Figure 1.

The 2051 Pressure Transmitter with 4-20mA HART series includes the following measurement configurations:

- 2051 Pressure Transmitter with 4-20mA HART: Differential and Gage Coplanar
 The 2051 utilizes capacitance sensor technology for differential Coplanar measurements.
- 2051 Pressure Transmitter with 4-20mA HART: Coplanar Absolute, In-Line Gage and Absolute

Piezoresistive sensor technology is used for the absolute Coplanar and In-Line measurements.

2051 Level Transmitter with 4-20mA HART

A 2051 Pressure Transmitter is available as a Level assembly. The 2051 Level transmitter can be used to measure level on virtually any liquid level vessel. 2051 transmitters and seal systems are designed to offer a flexible solution to meet the performance, reliability, and installation needs of nearly any level measurement application.

2051 4-20mA HART Flowmeter

A 2051 Pressure Transmitter can be combined with primary elements to offer fully assembled flowmeters. The direct mount flowmeter capability eliminates troublesome impulse lines associated with traditional installations. With multiple primary element technologies available, 2051 flowmeters offer a flexible solution to meet the performance, reliability, and installation needs of nearly any flow measurement application. The flowmeters covered for this assessment are based on the Rosemount 1195, 405, and 485 primary elements. Excluded from the assessment are models with Flo-Tap, remote mount, or temperature input options.



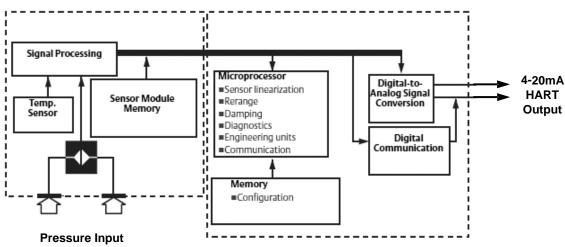


Figure 1 - Rosemount 2051 Pressure Transmitter Block Diagram

For safety instrumented systems usage it is assumed that the 4 - 20 mA output is used as the primary safety variable. No other output variants are covered by this assessment

The 2051 Pressure Transmitter with 4-20mA HART is classified as a Type B¹ device according to IEC 61508, having a hardware fault tolerance of 0.

The 2051 Pressure Transmitter with 4-20mA HART can be connected to the process using an impulse line, depending on the application the clogging of the impulse line needs to be accounted for, see section 5.1 of the FMEDA reports [R3] and [R4].

3.1 Product Versions

This assessment includes all versions of the models of 2051 Pressure Transmitter with 4-20mA HART with option code QT.

¹ Type B device: "Complex" component (using micro controllers or programmable logic); for details see 7.4.4.1.3 of IEC 61508-2.



4 IEC 61508 Functional Safety Assessment Scheme

exida assessed the development process used by Rosemount for this development project against the objectives of the exida certification scheme. The results of the assessment are documented in [R1]. All objectives have been considered in the Rosemount development processes for the development.

exida assessed the set of documents against the functional safety management requirements of IEC 61508. This was done by a pre-review of the completeness of the related requirements and then a spot inspection of certain requirements, before the development audit.

The safety case documents the fulfillment of the functional safety management requirements of IEC 61508-1 to -3.

The detailed development audit (see [R1]) evaluated the compliance of the processes, procedures and techniques, as implemented for the 2051 Pressure Transmitter with 4-20mA HART, with IEC 61508.

The assessment was executed using the exida certification scheme which includes subsets of the IEC 61508 requirements tailored to the work scope of the development team.

The result of the assessment shows that the 2051 Pressure Transmitter with 4-20mA HART is capable for use in SIL 3 applications, when properly designed into a Safety Instrumented Function per the requirements in the Safety Manual.

4.1 Product Modifications

The modification process has been successfully assessed and audited, so Rosemount may make modifications to this product as needed.

As part of the *exida* scheme a surveillance audit is conducted prior to renewal of the certificate. The modification documentation listed below is submitted as part of the surveillance audit. *exida* will review the decisions made by the competent person in respect to the modifications made.

- List of all anomalies reported
- List of all modifications completed
- Safety impact analysis which shall indicate with respect to the modification:
 - The initiating problem (e.g. results of root cause analysis)
 - The effect on the product / system
 - The elements/components that are subject to the modification
 - The extent of any re-testing
- List of modified documentation
- Regression test plans



5 Results of the IEC 61508 Functional Safety Assessment

exida assessed the development process used by Rosemount during the product development against the objectives of the exida certification scheme which includes IEC 61508 parts 1, 2, & 3 [N1]. The development of the 2051 Pressure Transmitter with 4-20mA HART was done per this IEC 61508 SIL 3 compliant development process. The Safety Case was updated with project specific design documents.

5.1 Lifecycle Activities and Fault Avoidance Measures

Rosemount has an IEC 61508 compliant development process as assessed during the IEC 61508 certification. This compliant development process is documented in [D22].

This functional safety assessment evaluated the compliance with IEC 61508 of the processes, procedures and techniques as implemented for the product development. The assessment was executed using the *exida* certification scheme which includes subsets of IEC 61508 requirements tailored to the SIL 3 work scope of the development team. The result of the assessment can be summarized by the following observations:

The audited development process complies with the relevant managerial requirements of IEC 61508 SIL 3.

5.1.1 Functional Safety Management

5.1.2 Safety Lifecycle and FSM Planning

The functional safety management of any Rosemount Safety Instrumented Systems Product development is governed by [D22]. This process requires that Rosemount create a project plan [D3] which is specific for each development project. The Project Plan defines all of the tasks that must be done to ensure functional safety as well as the person(s) responsible for each task. These processes and the procedures referenced herein fulfill the requirements of IEC 61508 with respect to functional safety management.

5.1.3 Training and competence recording

Competency is ensured by the creation of a competency and training matrix for the project [D14]. The matrix lists all of those on the project who are working on any of the phases of the safety lifecycle. Specific competencies for each person are listed on the matrix which is reviewed by the project manager. Any deficiencies are then addressed by updating the matrix with required training for the project.

5.1.4 Configuration Management

All documents are under version control as required by [D30].

Formal configuration control is defined and implemented for Change Authorization, Version Control, and Configuration Identification. A documented procedure exists to ensure that only approved items are delivered to customers. Master copies of the software and all associated documentation are kept during the operational lifetime of the released software.



5.1.5 Tools (and languages)

There are no on-line support tools.

Off-line support tools are documented, including tool name, manufacturer name, version number, use of the tool on this project. This includes validation test tools.

All off-line support tool criticalities have been classified and documented. Tools in classes T2 and T3 have a specification or product manual which clearly defines the behavior of the tool and any instructions or constraints on its use. List all T3 and T2 tools along with a reference (file name, document number) to the specification or product manual. The information required for safely using T2 or T3 tools has been documented.

For each tool in class T3, evidence that the tool conforms to its manual through a combination of confidence from use and tool validation. For each tool in class T3, if tool validation was performed, the results of the validation were documented.

5.2 Safety Requirement Specification

As defined in [D22] a safety requirements specification (SRS) is created for all products that must meet IEC 61508 requirements. For the 2051 Pressure Transmitter with 4-20mA HART, the safety integrity requirements specification (SIRS) [D36] contains a system overview, safety assumptions, and safety requirements sections. During the assessment, *exida* reviewed the content of the specification for completeness per the requirements of IEC 61508: 2010.

Requirements are tracked throughout the development process by the creation of a series of traceability matrices which are included in the following documents: [D36], [D39], [D58], and [D79]. The system requirements are broken down into derived hardware and software requirements which include specific safety requirements. Traceability matrices show how the system safety requirements map to the hardware and software requirements, to hardware and software architecture, to software and hardware detailed design, and to validation tests.

Requirements from IEC 61508-2, Table B.1 that have been met by Rosemount include project management, documentation, structured specification, inspection of the specification, and checklists.

Requirements from IEC 61508-3, Table A.1 that have been met by Rosemount include backward traceability between the safety requirements and the perceived safety needs.

5.3 Change and modification management

Modifications are performed per the Rosemount's change management process as documented in [D23] and [D29]. Impact analyses are performed for all changes once the product is released for integration testing. The results of the impact analysis are used in determining whether to approve the change. The standard development process as defined in [D22] is then followed to make the change. The handling of hazardous field incidents and customer notifications is governed by [D26]. This procedure includes identification of the problem, analysis of the problem, identification of the solution, and communication of the solution to the field. This meets the requirements of IEC 61508 SIL 3. Measures to verify and validate the change are developed following the normal design process.



Requirements from IEC 61508-3, Table A.8 that have been met by the Rosemount modification process include impact analysis, reverify changed software modules, reverify affected software modules, revalidate complete system or regression validation, software configuration management, data recording and analysis, and forward and backward traceability between the software safety requirements specification and the software modification plan (including reverification and revalidation)

The modification process has been successfully assessed and audited, so Rosemount may make modifications to this product as needed. An impact analysis is performed for any change related to functional safety.

5.4 System Design

System or subsystem design has been partitioned into subsystems, and interfaces between subsystems are clearly defined and documented. The System Architecture Design clearly identifies the SIL of all components in the design.

The System Architecture Design describes that the behavior of the device when a fault is detected is to take an action which will achieve or maintain a safety state.

Formal design reviews are held, and the results recorded; action items are identified, assigned, and resolved.

5.5 Hardware Design and Verification

Hardware design, including both electrical and mechanical design, is done according to [D22]. The hardware design process includes creating a hardware architecture specification, a peer review of this specification, creating a detailed design, a peer review of the detailed design, component selection, detailed drawings and schematics, a Failure Modes, Effects and Diagnostic Analysis (FMEDA), electrical unit testing, fault injection testing, and hardware verification tests.

Requirements from IEC 61508-2, Table B.2 that have been met by Rosemount include observance of guidelines and standards, project management, documentation, structured design, modularization, use of well-tried components, checklists, semi-formal methods, computer aided design tools, simulation, and inspection of the specification. This meets the requirements of SIL 3.

Verification activities are built into the standard development process as defined in [D22]. Verification activities include the following: Fault Injection Testing, integration testing, FMEDA, peer reviews and both hardware unit testing. In addition, safety verification checklists are filled out for each phase of the safety lifecycle. This meets the requirements of IEC 61508 SIL 3.

Requirements from IEC 61508-2, Table B.3 that have been met by Rosemount include functional testing, project management, documentation, and black-box testing.

5.5.1 Hardware architecture design

Hardware architecture design [D045] has been partitioned into subsystems, and interfaces between subsystems are defined and documented. Design reviews are used to discover weak design areas and make them more robust. Measures against environmental stress and over-voltage are incorporated into the design.

The FSM Plan, development process and guidelines define the required verification activities related to hardware including documentation, verification planning, test strategy and requirements tracking to validation test.



5.5.2 Hardware Design / Probabilistic properties

To evaluate the hardware design of the 2051 Pressure Transmitter with 4-20mA HART, a Failure Modes, Effects, and Diagnostic Analysis was performed by *exida* for each component in the system. The FMEDA was verified using Fault Injection Testing as part of the development, and as part of the IEC 61508 assessment.

A Failure Modes and Effects Analysis (FMEA) is a systematic way to identify and evaluate the effects of different component failure modes, to determine what could eliminate or reduce the chance of failure, and to document the system in consideration. An FMEDA (Failure Mode Effect and Diagnostic Analysis) is an FMEA extension. It combines standard FMEA techniques with extension to identify online diagnostics techniques and the failure modes relevant to safety instrumented system design.

Failure rates are listed in the FMEDA reports for each important failure category. Refer to the FMEDA ([R3] to [R4]) for a complete listing of the assumptions used and the resulting failure rates.

These results must be considered in combination with PFD_{AVG} of other devices of a Safety Instrumented Function (SIF) in order to determine suitability for a specific Safety Integrity Level (SIL). The Safety Manual states that the application engineer should calculate the PFD_{AVG} for each defined safety instrumented function (SIF) to verify the design of that SIF.

The FMEDA analysis shows that most of the reviewed 2051 models have a Safe Failure Fraction > 90% (if the logic solver is programmed to detect over-scale and under-scale currents) and therefore those models meet Route $1_{\rm H}$ hardware architectural constraints for up to SIL 2 as a single device and SIL 3 with Hardware Fault Tolerance of 1.

The failure rate data used for this analysis meets the *exida* criteria for Route 2_H and the diagnostic coverage is \geq 60%. Therefore, all the reviewed 2051 models meet the Route 2_H hardware architectural constraints for up to SIL 2 as a single device when the listed failure rates are used.

If the 2051 Pressure Transmitter with 4-20mA HART is one part of an element, the architectural constraints should be determined for the entire sensor element.

The architectural constraint type for the 2051 Pressure Transmitter with 4-20mA HART Series is B. The required SIL determine the level of hardware fault tolerance that is required per requirements of IEC 61508 or IEC 61511. The SIS designer is responsible for meeting other requirements of applicable standards for any given SIL as well.

The analysis shows that the design of the 2051 Pressure Transmitter with 4-20mA HART meets the hardware requirements of IEC 61508, SIL 2 @HFT=0 and SIL 3 @ HFT=1.

5.6 Software Design

Software (firmware) design is done according to [D22]. The software design process includes software architecture design and peer review, detailed design and peer review, critical code reviews, static source code analysis and unit test.

Requirements from IEC 61508-3, Table A.2 that have been met by Rosemount include fault detection, error detecting codes, failure assertion programming, diverse monitor techniques, stateless software design, retry fault recovery mechanisms, graceful degradation, forward and backward traceability between the software safety requirements specification and software architecture, semi-formal methods, event-driven, with guaranteed maximum response time, static resource allocation, and static synchronization of access to shared resources.



Requirements from IEC 61508-3, Table A.3 that have been met by Rosemount include suitable programming language, strongly typed programming language, language subset, and increased confidence from use for the tools and translators.

Requirements from IEC 61508-3, Table A.4 that have been met by Rosemount include semi-formal methods, computer aided design tools, defensive programming, modular approach, design and coding standards, structured programming, forward traceability between the software safety requirements specification and software design.

This meets the requirements of SIL 3.

5.7 Software Verification

Verification activities are built into the standard development process as defined in [D22]. Verification activities include the following: Fault Injection Testing, code reviews, static source code analysis, module testing, integration testing. In addition, safety verification checklists are filled out for each phase of the safety lifecycle. This meets the requirements of IEC 61508 SIL 3.

Requirements from IEC 61508-3, Table A.5 that have been met by Rosemount include dynamic analysis and testing, data recording and analysis, functional and black box testing, performance testing, interface testing, and test management and automation tools.

Requirements from IEC 61508-3, Table A.6 that have been met by Rosemount include functional and black box testing, performance testing, and forward traceability between the system and software design requirements for hardware/software integration and the hardware/software integration test specifications

Requirements from IEC 61508-3, Table A.9 that have been met include static analysis, dynamic analysis and testing, forward traceability between the software design specification and the software verification plan.

This meets the requirements of SIL 3.

5.8 Safety Validation

Validation Testing is done via a set of documented tests. The validation tests are traceable to the Safety Requirements Specification [D36] in the validation test plan [D39]. The traceability matrices show that all safety requirements have been validated by one or more tests. In addition to standard Test Specification Documents, third party testing is included as part of the validation testing. All non-conformities are documented in a change request and procedures are in place for corrective actions to be taken when tests fail as documented in [D22].

Requirements from IEC 61508-2, Table B.5 that have been met by Rosemount include functional testing, functional testing under environmental conditions, interference surge immunity testing, fault insertion testing, project management, documentation, static analysis, dynamic analysis, and failure analysis, expanded functional testing and black-box testing.

Requirements from IEC 61508-3, Table A.7 that have been met by Rosemount include process simulation, functional and black box testing, and forward and backward traceability between the software safety requirements specification and the software safety validation plan. This meets SIL 3.



5.9 Safety Manual

Rosemount created a safety manual for the 2051 Pressure Transmitter with 4-20mA HART [D17] which addresses all relevant operation and maintenance requirements from IEC 61508. This safety manual was assessed by *exida*. The final version is compliant with the requirements of IEC 61508.

Requirements from IEC **61508-2, Table B.4** that have been met by Rosemount include operation and maintenance instructions, maintenance friendliness, project management, documentation, and limited operation possibilities.

This meets the requirements for SIL 3.



6 2019 IEC 61508 Functional Safety Surveillance Audit

6.1 Roles of the parties involved

Emerson Automation Solutions (Rosemount, Inc.)	Manufacturer of the 2051 Pressure Transmitter
exida	Performed the IEC 61508 Functional Safety Surveillance Audit per the accredited <i>exida</i> scheme.

Rosemount contracted *exida* in June 2019 to perform the surveillance audit for the above 2051 Pressure Transmitter with 4-20mA HART. The surveillance audit was conducted remotely.

6.2 Surveillance Methodology

As part of the IEC 61508 functional safety surveillance audit the following aspects have been reviewed:

- Procedure Changes Changes to relevant procedures since the last audit are reviewed to determine that the modified procedures meet the requirements of the exida certification scheme.
- Engineering Changes The engineering change list is reviewed to determine if any of the changes could affect the safety function of the 2051 Pressure Transmitter with 4-20mA HART.
- Impact Analysis If changes were made to the product design, the impact analysis
 associated with the change will be reviewed to see that the functional safety requirements for
 an impact analysis have been met.
- Field History Shipping and field returns during the certification period will be reviewed to
 determine if any systematic failures have occurred. If systematic failures have occurred
 during the certification period, the corrective action that was taken to eliminate the systematic
 failure(s) will be reviewed to determine that said action followed the approved processes and
 was effective.
- Safety Manual The latest version of the safety manual will be reviewed to determine that it meets the IEC 61508 requirements for a safety manual.
- FMEDA Update If required or requested the FMEDA will be updated. This is typically done if there are changes to the IEC 61508 standard and/or changes to the *exida* failure rate database.
- Evaluate use of the certificate and/or certification mark Conduct a search of the applicant's
 web site and document any misuse of the certificate and/or certification mark. Report any
 misuse of the certificate and/or certification mark to the exida Managing Director.
- Recommendations from Previous Audits If there are recommendations from the previous audit, these are reviewed to see if the recommendations have been implemented properly.



6.3 Documentation provided by Rosemount

Ref	Description	
[E1]	Configuration and Change Management	
[E2]]	Control of Monitoring and Measuring Equipment	
[E3]	Corrective Action - Preventive Action Process	
[E4]	Customer Notification Process	
[E5]	Document and Record Management Process	
[E6]	Engineering Change Order (ECO) Process	
[E7]	Failure Analysis Process	
[E8]	How to Write and Assemble a Failure Analysis Laboratory Summary	
[E9]	Peer Review Work Instruction	
[E10]	Product Design and Development Process	
[E11]	Quality Manual	
[E12]	Safety-related Systems Verification Checklists	
[E13]	Supplier Quality Manual	
[E14]	Supply Chain Supplier Corrective Action Process Description	
[E15]	Safety Manual	
[E16]	Compatibility Test results	

6.4 Surveillance Documentation generated by exida

[R1]	Surveillance Audit Checklist - Rosemount 2051_3051.xlsx	IEC 61508 Surveillance Case for 2051 Pressure Transmitter with 4-20mA HART
1	ROS 18-11-012 V1R0 Field Failure Analysis - 2051_3051.xlsx	Field Failure Analysis for 2051 Pressure Transmitter with 4-20mA HART

6.5 Surveillance Results

6.5.1 Procedure Changes

Changes were made to procedures documented in 6.3. Changed procedures were reviewed and found to be consistent with the requirements of IEC 61508.

6.5.2 Engineering Changes

There were no significant design changes to these products during the previous certification period. Engineering changes and impact analyses for minor changes were reviewed and all documentation was found to be acceptable.

6.5.3 Impact Analysis

There were no safety-related design changes during the previous certification period.



6.5.4 Field History

The field histories of these products were analyzed and found to be consistent with the failure rates predicted by the FMEDA.

6.5.5 Safety Manual

The updated safety manual was reviewed and found to be compliant with IEC 61508:2010.

6.5.6 FMEDA Update

No FMEDA changes were needed for this Surveillance Audit.

6.5.7 Evaluate use of certificate and/or certification mark

The Rosemount website was searched and no misleading or misuse of the certification or certification marks was found.

6.5.8 Previous Recommendations

There were no previous recommendations to be assessed by this assessment.



7 Terms and Definitions

Fault tolerance Ability of a functional unit to continue to perform a required function in the

presence of faults or errors (IEC 61508-4, 3.6.3)

FIT Failure In Time (1x10⁻⁹ failures per hour)

FMEDA Failure Mode Effect and Diagnostic Analysis

HFT Hardware Fault Tolerance

Low demand mode Mode where the demand interval for operation made on a safety-related

system is greater than twice the proof test interval.

High demand mode Mode where the demand interval for operation made on a safety-related

system is less than 100x the diagnostic detection/reaction interval, or where

the safe state is part of normal operation.

PFD_{AVG} Average Probability of Failure on Demand
PFH Probability of dangerous Failure per Hour

SFF Safe Failure Fraction - Summarizes the fraction of failures, which lead to a

safe state and the fraction of failures which will be detected by diagnostic

measures and lead to a defined safety action.

SIF Safety Instrumented Function

SIL Safety Integrity Level

SIS Safety Instrumented System – Implementation of one or more Safety

Instrumented Functions. A SIS is composed of any combination of sensor(s),

logic solver(s), and final element(s).

HART Highway Addressable Remote Transducer

Type A element "Non-Complex" element (using discrete components); for details see 7.4.4.1.2

of IEC 61508-2

Type B element "Complex" element (using complex components such as micro controllers or

programmable logic); for details see 7.4.4.1.3 of IEC 61508-2



8 Status of the document

8.1 Liability

exida prepares reports based on methods advocated in International standards. Failure rates are obtained from a collection of industrial databases. *exida* accepts no liability whatsoever for the use of these numbers or for the correctness of the standards on which the general calculation methods are based.

8.2 Version History

Contract Number	Report Number	Revision Notes
Q18/11-012	ROS 11-07-062 R005 V4R0	Surveillance Audit; DEB; July 26, 2019
Q14/12-011	ROS 11-07-062 R005 V3R2	Updated per customer comments; May 2015; TES
Q14/12-011	ROS 11-07-062 R005 V3R1	Recertification; added surveillance section; April 2015; TES
Q13/04-008	ROS 11-07-062 R005 V2R2	Updated ROS 11-07-062 R003 V2R2 2051 FMEDA; Dec 13, 2013; TES
Q13/04-008	ROS 11-07-062 R005 V2R1	Updated comments from customer; Aug 20 th , 2013; TES
Q13/04-008	ROS 11-07-062 R005 V2R0	Updated to new 2010 standard and 2 _H ; June 25 th , 2013; TES
Q11/07-062	ROS 11-07-062 R005 V1R2	Updated to include reference to hash code for software; May 30, 2012
Q11/07-062	ROS 11-07-062 R005 V1R1	Updated based on review comments; March 8, 2012
Q11/07-062	ROS 11-07-062 R005 V0R1	Draft; March 7, 2012

Review: V3, R0: Loren Stewart; August 26, 2019

Status: Released

8.3 Future Enhancements

At request of client.

8.4 Release Signatures

David Butler, CFSE, exidaCSP, Evaluating Assessor

Loren L. Stewart, CFSE, Certifying Assessor