

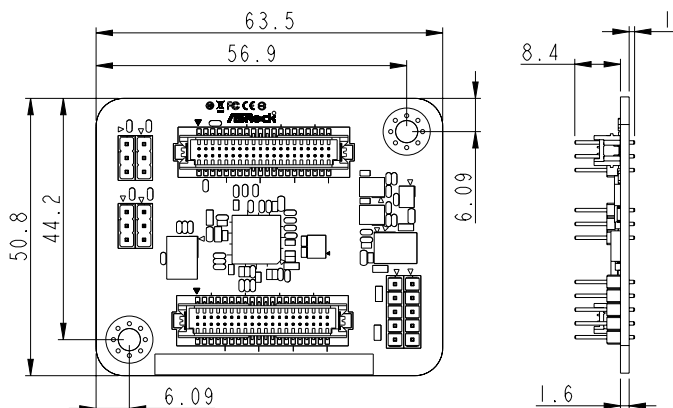
eDP to LVDS

Package Contents

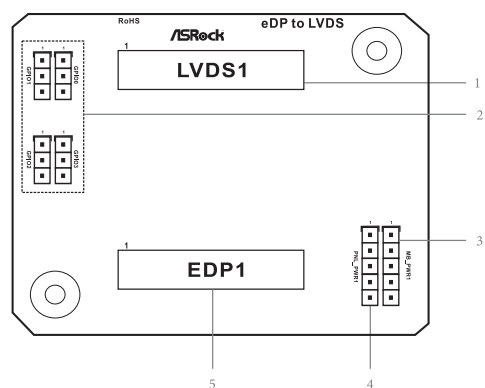
- 2 x Screws (M3*4)
- 1 x eDP Cable
- 1 x M/B Power Cable

* Make sure to connect two ends of M/B Power Cable to MB_PWR1 and PNL_PWR1 headers properly.

Mechanical Drawing



Card Layout



Jumpers/Headers Definition

1. LVDS Panel Connector (LVDS1)

PIN	Signal Name	PIN	Signal Name
1	LCD_HIT_VCC	39	LCD_HIT_VCC
2	CON_LBKLT_CTL	40	LCD_HIT_VCC
3	CON_LBKLT_EN	41	LCD_HIT_VCC
4	LCD_VCC	42	LCD_HIT_VCC
5	LCD_VCC	43	LCD_HIT_VCC
6	LCD_VCC	44	LCD_HIT_VCC
7	LCD_VCC	45	LCD_HIT_VCC
8	LCD_VCC	46	LCD_HIT_VCC
9	LCD_VCC	47	LCD_HIT_VCC
10	LCD_VCC	48	LCD_HIT_VCC
11	LCD_VCC	49	LCD_HIT_VCC
12	LCD_VCC	50	LCD_HIT_VCC
13	LCD_VCC	51	LCD_HIT_VCC
14	LCD_VCC	52	LCD_HIT_VCC
15	LCD_VCC	53	LCD_HIT_VCC
16	LCD_VCC	54	LCD_HIT_VCC
17	LCD_VCC	55	LCD_HIT_VCC
18	LCD_VCC	56	LCD_HIT_VCC
19	LCD_VCC	57	LCD_HIT_VCC
20	LCD_VCC	58	LCD_HIT_VCC
21	LCD_VCC	59	LCD_HIT_VCC
22	LCD_VCC	60	LCD_HIT_VCC
23	LCD_VCC	61	LCD_HIT_VCC
24	LCD_VCC	62	LCD_HIT_VCC
25	LCD_VCC	63	LCD_HIT_VCC
26	LCD_VCC	64	LCD_HIT_VCC
27	LCD_VCC	65	LCD_HIT_VCC
28	LCD_VCC	66	LCD_HIT_VCC
29	LCD_VCC	67	LCD_HIT_VCC
30	LCD_VCC	68	LCD_HIT_VCC
31	LCD_VCC	69	LCD_HIT_VCC
32	LCD_VCC	70	LCD_HIT_VCC
33	LCD_VCC	71	LCD_HIT_VCC
34	LCD_VCC	72	LCD_HIT_VCC
35	LCD_VCC	73	LCD_HIT_VCC
36	LCD_VCC	74	LCD_HIT_VCC
37	LCD_VCC	75	LCD_HIT_VCC
38	LCD_VCC	76	LCD_HIT_VCC
39	LCD_VCC	77	LCD_HIT_VCC
40	LCD_VCC	78	LCD_HIT_VCC
41	LCD_VCC	79	LCD_HIT_VCC
42	LCD_VCC	80	LCD_HIT_VCC
43	LCD_VCC	81	LCD_HIT_VCC
44	LCD_VCC	82	LCD_HIT_VCC
45	LCD_VCC	83	LCD_HIT_VCC
46	LCD_VCC	84	LCD_HIT_VCC
47	LCD_VCC	85	LCD_HIT_VCC
48	LCD_VCC	86	LCD_HIT_VCC
49	LCD_VCC	87	LCD_HIT_VCC
50	LCD_VCC	88	LCD_HIT_VCC
51	LCD_VCC	89	LCD_HIT_VCC
52	LCD_VCC	90	LCD_HIT_VCC
53	LCD_VCC	91	LCD_HIT_VCC
54	LCD_VCC	92	LCD_HIT_VCC
55	LCD_VCC	93	LCD_HIT_VCC
56	LCD_VCC	94	LCD_HIT_VCC
57	LCD_VCC	95	LCD_HIT_VCC
58	LCD_VCC	96	LCD_HIT_VCC
59	LCD_VCC	97	LCD_HIT_VCC
60	LCD_VCC	98	LCD_HIT_VCC
61	LCD_VCC	99	LCD_HIT_VCC
62	LCD_VCC	100	LCD_HIT_VCC

2. Digital Input / Output Select (GPIO0~3)

- 1-2 : High
- 2-3 : Low



3. Motherboard Power Select (MB_PWR1)

- Pin1 : +3V
- Pin2 : LCD_VCC
- Pin3 : +5V
- Pin4 : LCD_VCC
- Pin5 : +12V



4. Panel Power Select (PNL_PWR1)

- 1-2 : LCD_VCC : +3V
- 2-3 : LCD_VCC : +5V
- 4-5 : LCD_VCC : +12V



5. eDP Connector (EDP1)



PIN	Signal Name
1	NA
2	GND
3	eDP_TX#3_CON
4	eDP_TX3_CON
5	GND
6	eDP_TX#2_CON
7	eDP_TX2_CON
8	GND
9	eDP_TX#1_CON
10	eDP_TX1_CON
11	GND
12	eDP_TX#0_CON
13	eDP_TX0_CON
14	GND
15	eDP_AUX_CON
16	eDP_AUX#_CON
17	GND
18	LCD_VCC
19	LCD_VCC
20	LCD_VCC
21	LCD_VCC
22	NA
23	GND
24	GND
25	GND
26	GND
27	eDP_HPD_CON
28	GND
29	GND
30	GND
31	GND
32	CON_LBKLT_EN
33	CON_LBKLT_CTL
34	SMB_DATA
35	SMB_CLK
36	LCD_BLT_VCC
37	LCD_BLT_VCC
38	LCD_BLT_VCC
39	LCD_BLT_VCC
40	NA

Specifications

Function

- eDP to LVDS

Form Factor

- Customizes

Controller / Major IC

- CHRONTEL CH7511B

Input I/F

- 2 lanes eDP input
- +3V, +5V, +12V input to select Panel power

Output I/F

- 2 channel LVDS signals output
- LVDS resolution up to 1920 x 1080

Output connector

- 40pin 1.25mm pitch

Dimensions (W x L x H/ mm)

- 50 x 63 x 2

Operating Temperature

- 0°C ~ 60°C

Storage Temperature

- -40° C ~ 85° C

Supported Panel Types

Item	GPIO Setting [GPIO3:GPIO2:GPIO1:GPIO0]	Panel Type	Tested Panel Model
1	1111	1920x1080/24-bit/2-ch/LED	M215HW01 VB
2	1110	1440x900/24-bit/2-ch/LED	MT190AW02 V.4
3	1101	1024x768/24-bit/1-ch/CCFL	M150XN07
4	1100	1280x1024/24-bit/2-ch/CCFL	M170EG01 VD
5	1011	1366x768/24-bit/1-ch/CCFL	M185XW01 V8
6	1010	1440x900/24-bit/2-ch/CCFL	M190PW01 V7
7	1001	1024x600/18-bit/1-ch/LED	CLAA102NA2CCN
8	1000	800x600/18-bit/1-ch/CCFL	SJ084MA03
9	0111	1280x1024/24-bit/2-ch/LED	G170EG01 V1
10	0110	1024x768/24-bit/1-ch/LED	G150XG01.3
11	0101	1600x900/18-bit/2-ch/LED	BI73RW01 V0
12	0100	1366x768/24-bit/1-ch/LED	CALL185WA04 M185XW01 VF
13	0011	1366x768/18-bit/1-ch/LED	HSD121PHW1-A01
14	0010	800x600/24-bit/1-ch/LED	G084SN05 V.8
15	0001	640x480/24-bit/1-ch/LED	G065VN01V2
16	0000	1024x768/18-bit/1-ch/LED	CLAA104XA02CW

*For the updated information, please visit www.asrockind.com.
*The Chronitel F/W only can be flashed via ASRock Industrial M/B.